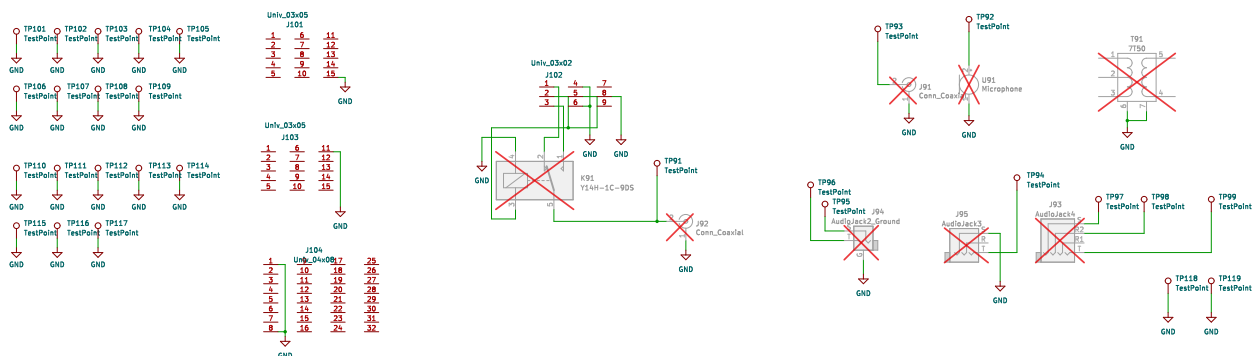


### Reserve circuit



### Reference circuit

