A10 Development Plan

1. Develop AVX2 code gen on new machine
2. Install OpenCl on new machine
3. Write test routine in clsimd.c that tests operation of the shared virtual memory operations
4. Experiment with multiple work groups
5. Write ilcg CL.m4 package that will translate a single for loop into a Clfor whilst translating expressions and assignments using something similar to the existing C machine desc
6. Prototype running the whole vector pascal stack inside a shared virtual memory segment.
7. Run tests on hand written kernels operating on pascal stack arrays
8. Make Clfor have a simplify that plants code for call to launch a named kernel, and also sends the body of the loop to CLCG for translation to a named openCL kernel
9. Extend CLSIMD to invoke the CL package on each for loop before matching it. Do this by overloading cgFor method. Add in the stuff in the code generator that is needed to link in any temporary C and CL files.