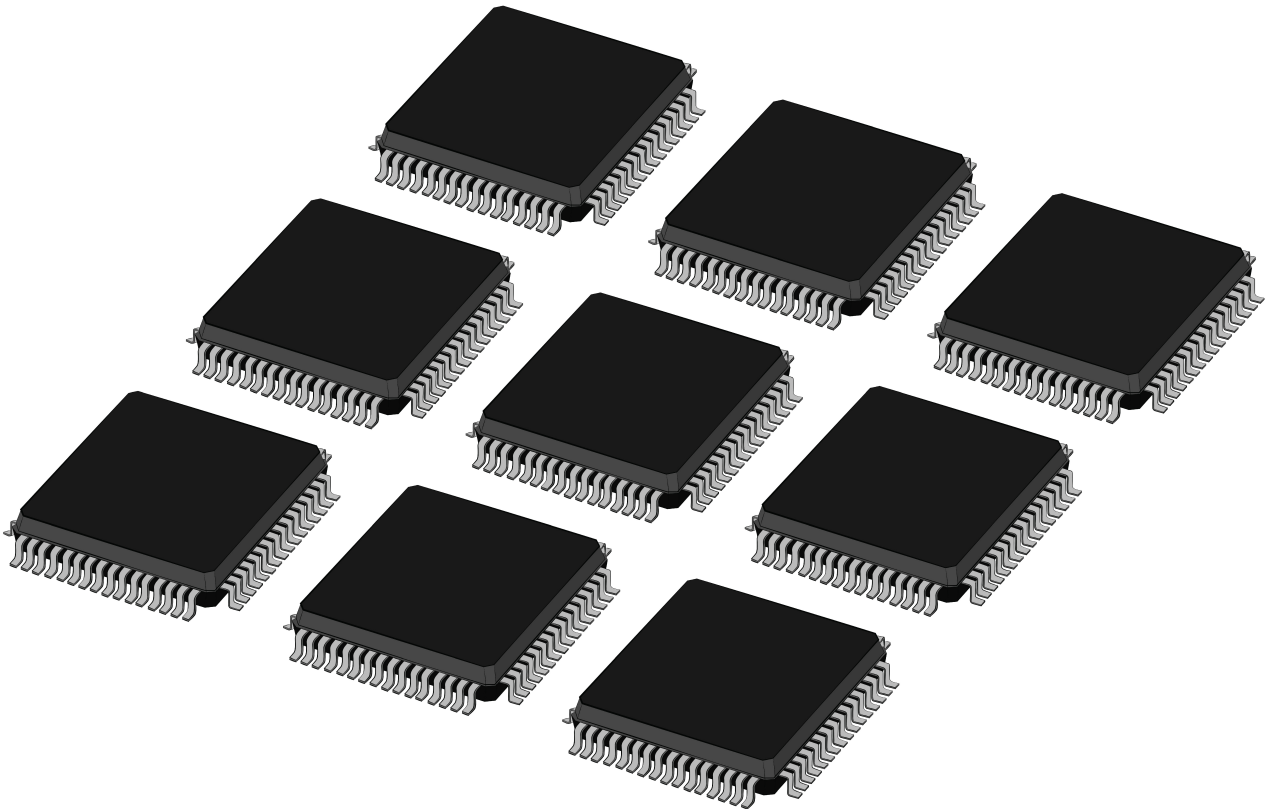


Systems

QueenField



Paco Reina Campo

Abstract

Systems.

Contents

1	OVERVIEW	5
1.1	DEPENDENCES	5
1.2	HARDWARE PROJECT	5
1.3	SOFTWARE PROJECT	6
2	PERIPHERAL	7
2.1	MPSoC-DBG	7
2.2	MPSoC-DMA	7
2.3	MPSoC-GPIO	7
2.4	MPSoC-MPI	7
2.5	MPSoC-MPRAM	7
2.6	MPSoC-MSI	8
2.7	MPSoC-NoC	8
2.8	MPSoC-SPRAM	8
2.9	MPSoC-UART	8
3	ARCHITECTURE	9
3.1	PROCESSIGN UNIT	9
3.1.1	PU-MSP430	9
3.1.2	PU-OR1K	9
3.1.3	PU-RISCV	9
3.2	SYSTEM ON CHIP	9
3.2.1	SoC-MSP430	9
3.2.2	SoC-OR1K	10
3.2.3	SoC-RISCV	10
3.3	MULTI-PROCESSOR SYSTEM ON CHIP	10
3.3.1	MPSoC-MSP430	10
3.3.2	MPSoC-OR1K	10
3.3.3	MPSoC-RISCV	10
4	ENVIRONMENT	11
4.1	DESIGN AND VERIFICATION	11
4.1.1	PU-DV	11
4.1.2	SoC-DV	11
4.1.3	MPSoC-DV	11
4.2	REAL TIME OPERATING SYSTEM	11
4.2.1	PU-RTOS	11
4.2.2	SoC-RTOS	12
4.2.3	MPSoC-RTOS	12
5	ACCELERATOR	13
5.1	NEURAL TURING MACHINE	13
5.1.1	PU-NTM	13
5.1.2	SoC-NTM	13
5.1.3	MPSoC-NTM	13
5.2	DIFFERENTIABLE NEURAL COMPUTER	13
5.2.1	PU-DNC	13
5.2.2	SoC-DNC	14

5.2.3	MPSoC-DNC	14
6	ADVANCED	15
6.1	FINANCIAL TECHNOLOGY	15
6.1.1	PU-FinTech	15
6.1.2	SoC-FinTech	16
6.1.3	MPSoC-FinTech	16
6.2	ENERGY TECHNOLOGY	17
6.2.1	PU-EnergyTech	17
6.2.2	SoC-EnergyTech	17
6.2.3	MPSoC-EnergyTech	18
6.3	LIFE TECHNOLOGY	18
6.3.1	PU-LifeTech	18
6.3.2	SoC-LifeTech	18
6.3.3	MPSoC-LifeTech	20

List of Tables

List of Figures

1.1	Dependences	5
1.2	Hardware Project	6
1.3	Software Project	6
6.1	PU FinTech	15
6.2	SoC FinTech	16
6.3	MPSoC FinTech	16
6.4	PU EnergyTech	17
6.5	SoC EnergyTech	17
6.6	MPSoC EnergyTech	18
6.7	PU LifeTech	19
6.8	SoC LifeTech	19
6.9	MPSoC LifeTech	20

Chapter 1

OVERVIEW

1.1 DEPENDENCES

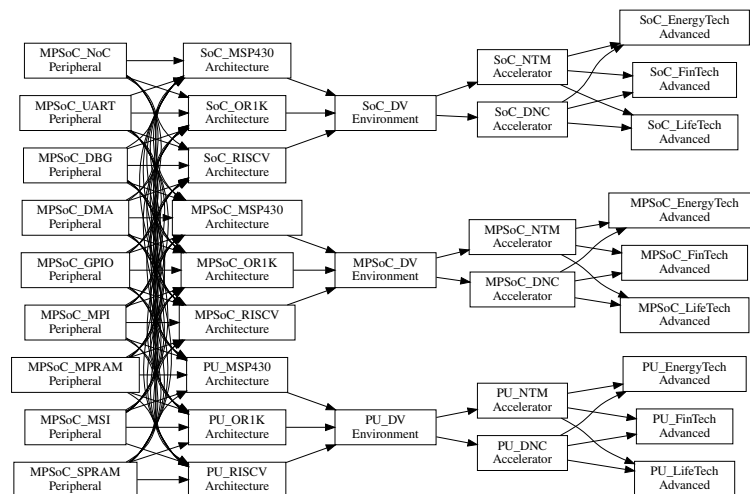


Figure 1.1: Dependences

1.2 HARDWARE PROJECT

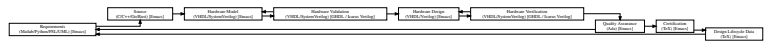


Figure 1.2: Hardware Project

1.3 SOFTWARE PROJECT



Figure 1.3: Software Project

Chapter 2

PERIPHERAL

..
.....
.....
.....

2.1 MPSoC-DBG

..
.....
.....
.....

2.2 MPSoC-DMA

..
.....
.....
.....

2.3 MPSoC-GPIO

..
.....
.....
.....

2.4 MPSoC-MPI

..
.....
.....
.....

2.5 MPSoC-MPRAM

..
.....
.....
.....

2.6 MPSoC-MSI

[illegible]

2.7 MPSoC-NoC

[illegible]

2.8 MPSoC-SPRAM

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100	101	102	103	104	105	106	107	108	109	110	111	112	113	114	115	116	117	118	119	120	121	122	123	124	125	126	127	128	129	130	131	132	133	134	135	136	137	138	139	140	141	142	143	144	145	146	147	148	149	150	151	152	153	154	155	156	157	158	159	160	161	162	163	164	165	166	167	168	169	170	171	172	173	174	175	176	177	178	179	180	181	182	183	184	185	186	187	188	189	190	191	192	193	194	195	196	197	198	199	200	201	202	203	204	205	206	207	208	209	210	211	212	213	214	215	216	217	218	219	220	221	222	223	224	225	226	227	228	229	230	231	232	233	234	235	236	237	238	239	240	241	242	243	244	245	246	247	248	249	250	251	252	253	254	255	256	257	258	259	260	261	262	263	264	265	266	267	268	269	270	271	272	273	274	275	276	277	278	279	280	281	282	283	284	285	286	287	288	289	290	291	292	293	294	295	296	297	298	299	300	301	302	303	304	305	306	307	308	309	310	311	312	313	314	315	316	317	318	319	320	321	322	323	324	325	326	327	328	329	330	331	332	333	334	335	336	337	338	339	340	341	342	343	344	345	346	347	348	349	350	351	352	353	354	355	356	357	358	359	360	361	362	363	364	365	366	367	368	369	370	371	372	373	374	375	376	377	378	379	380	381	382	383	384	385	386	387	388	389	390	391	392	393	394	395	396	397	398	399	400	401	402	403	404	405	406	407	408	409	410	411	412	413	414	415	416	417	418	419	420	421	422	423	424	425	426	427	428	429	430	431	432	433	434	435	436	437	438	439	440	441	442	443	444	445	446	447	448	449	450	451	452	453	454	455	456	457	458	459	460	461	462	463	464	465	466	467	468	469	470	471	472	473	474	475	476	477	478	479	480	481	482	483	484	485	486	487	488	489	490	491	492	493	494	495	496	497	498	499	500	501	502	503	504	505	506	507	508	509	510	511	512	513	514	515	516	517	518	519	520	521	522	523	524	5
---	---	---	---	---	---	---	---	---	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	---

2.9 MPSoC-UART

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100	101	102	103	104	105	106	107	108	109	110	111	112	113	114	115	116	117	118	119	120	121	122	123	124	125	126	127	128	129	130	131	132	133	134	135	136	137	138	139	140	141	142	143	144	145	146	147	148	149	150	151	152	153	154	155	156	157	158	159	160	161	162	163	164	165	166	167	168	169	170	171	172	173	174	175	176	177	178	179	180	181	182	183	184	185	186	187	188	189	190	191	192	193	194	195	196	197	198	199	200	201	202	203	204	205	206	207	208	209	210	211	212	213	214	215	216	217	218	219	220	221	222	223	224	225	226	227	228	229	230	231	232	233	234	235	236	237	238	239	240	241	242	243	244	245	246	247	248	249	250	251	252	253	254	255	256	257	258	259	260	261	262	263	264	265	266	267	268	269	270	271	272	273	274	275	276	277	278	279	280	281	282	283	284	285	286	287	288	289	290	291	292	293	294	295	296	297	298	299	300	301	302	303	304	305	306	307	308	309	310	311	312	313	314	315	316	317	318	319	320	321	322	323	324	325	326	327	328	329	330	331	332	333	334	335	336	337	338	339	340	341	342	343	344	345	346	347	348	349	350	351	352	353	354	355	356	357	358	359	360	361	362	363	364	365	366	367	368	369	370	371	372	373	374	375	376	377	378	379	380	381	382	383	384	385	386	387	388	389	390	391	392	393	394	395	396	397	398	399	400	401	402	403	404	405	406	407	408	409	410	411	412	413	414	415	416	417	418	419	420	421	422	423	424	425	426	427	428	429	430	431	432	433	434	435	436	437	438	439	440	441	442	443	444	445	446	447	448	449	450	451	452	453	454	455	456	457	458	459	460	461	462	463	464	465	466	467	468	469	470	471	472	473	474	475	476	477	478	479	480	481	482	483	484	485	486	487	488	489	490	491	492	493	494	495	496	497	498	499	500	501	502	503	504	505	506	507	508	509	510	511	512	513	514	515	516	517	518	519	520	521	522	523	524	5
---	---	---	---	---	---	---	---	---	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	---

Chapter 3

ARCHITECTURE

..
.....
.....
.....

3.1 PROCESSIGN UNIT

..
.....
.....
.....

3.1.1 PU-MSP430

..
.....
.....
.....

3.1.2 PU-OR1K

..
.....
.....
.....

3.1.3 PU-RISCV

..
.....
.....
.....

3.2 SYSTEM ON CHIP

..
.....
.....
.....

3.2.1 SoC-MSP430

..
.....

... ..
... ..

3.2.2 SoC-OR1K

.. ..
... ..
... ..
... ..

3.2.3 SoC-RISCV

.. ..
... ..
... ..
... ..

3.3 MULTI-PROCESSOR SYSTEM ON CHIP

.. ..
... ..
... ..
... ..

3.3.1 MPSoC-MSP430

.. ..
... ..
... ..
... ..

3.3.2 MPSoC-OR1K

.. ..
... ..
... ..
... ..

3.3.3 MPSoC-RISCV

.. ..
... ..
... ..
... ..

Chapter 4

ENVIRONMENT

..
.....
.....
.....

4.1 DESIGN AND VERIFICATION

..
.....
.....
.....

4.1.1 PU-DV

..
.....
.....
.....

4.1.2 SoC-DV

..
.....
.....
.....

4.1.3 MPSoC-DV

..
.....
.....
.....

4.2 REAL TIME OPERATING SYSTEM

..
.....
.....
.....

4.2.1 PU-RTOS

..
.....

... ..
... ..

4.2.2 SoC-RTOS

.. ..
... ..
... ..
... ..

4.2.3 MPSoC-RTOS

.. ..
... ..
... ..
... ..

Chapter 5

ACCELERATOR

..
.....
.....
.....

5.1 NEURAL TURING MACHINE

..
.....
.....
.....

5.1.1 PU-NTM

..
.....
.....
.....

5.1.2 SoC-NTM

..
.....
.....
.....

5.1.3 MPSoC-NTM

..
.....
.....
.....

5.2 DIFFERENTIABLE NEURAL COMPUTER

..
.....
.....
.....

5.2.1 PU-DNC

..
.....

... ..
... ..

5.2.2 SoC-DNC

.. ..
... ..
... ..
... ..

5.2.3 MPSoC-DNC

.. ..
... ..
... ..
... ..

Chapter 6

ADVANCED

6.1 FINANCIAL TECHNOLOGY

6.1.1 PU-FinTech

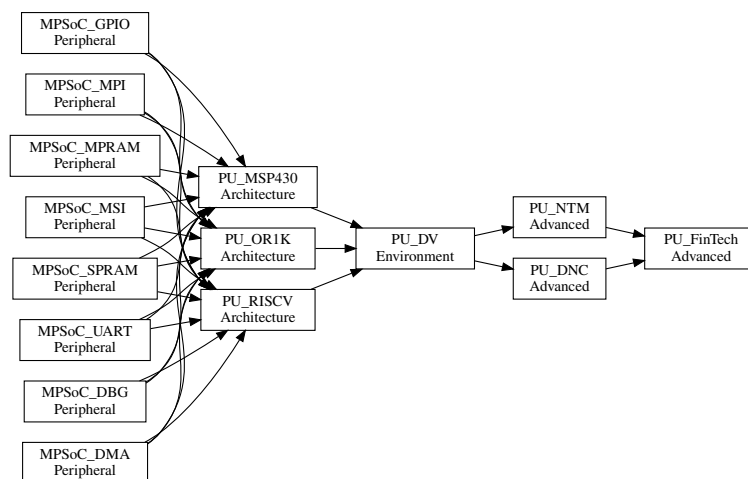


Figure 6.1: PU FinTech

6.1.2 SoC-FinTech

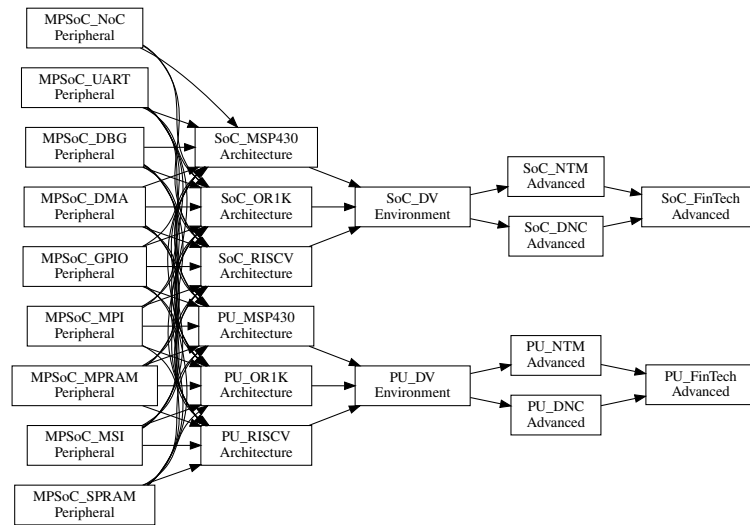


Figure 6.2: SoC FinTech

6.1.3 MPSoC-FinTech

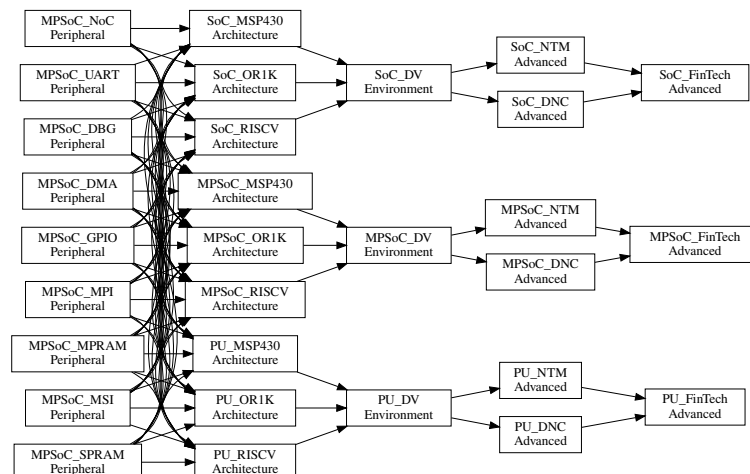


Figure 6.3: MPSoC FinTech

6.2 ENERGY TECHNOLOGY

6.2.1 PU-EnergyTech

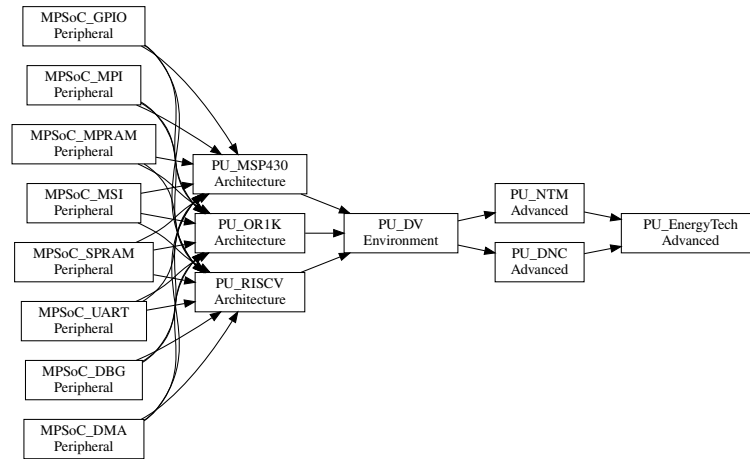


Figure 6.4: PU EnergyTech

6.2.2 SoC-EnergyTech

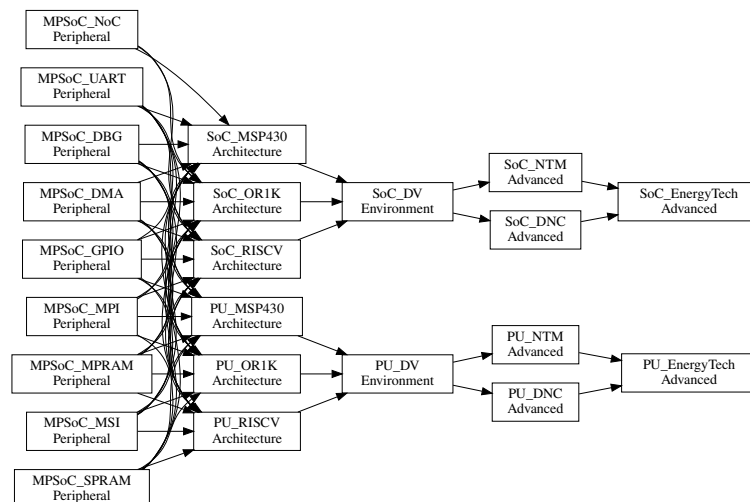


Figure 6.5: SoC EnergyTech

6.2.3 MPSoC-EnergyTech

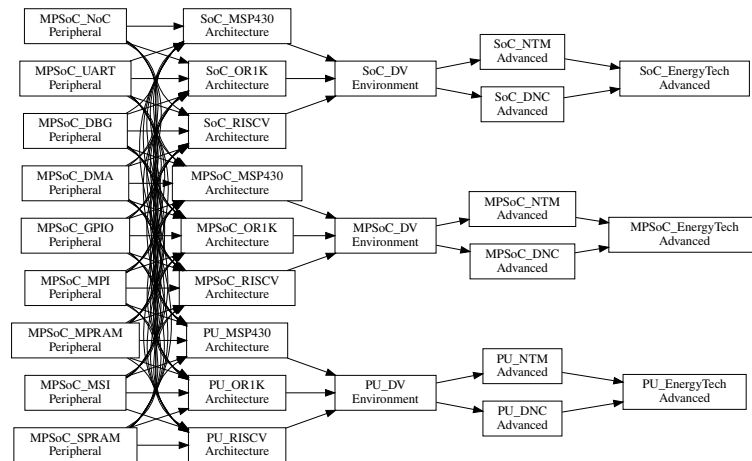


Figure 6.6: MPSoC EnergyTech

6.3 LIFE TECHNOLOGY

6.3.1 PU-LifeTech

6.3.2 SoC-LifeTech

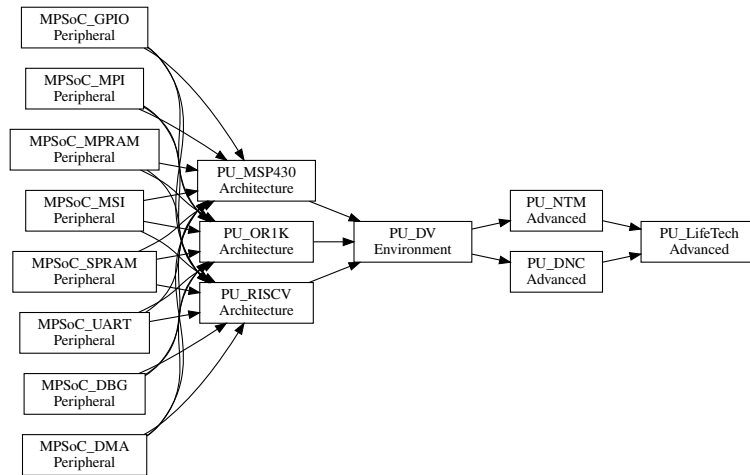


Figure 6.7: PU LifeTech

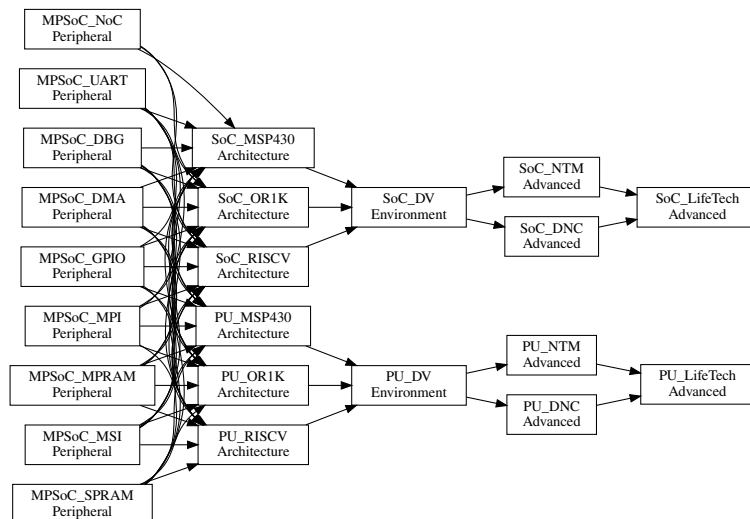


Figure 6.8: SoC LifeTech

6.3.3 MPSoC-LifeTech

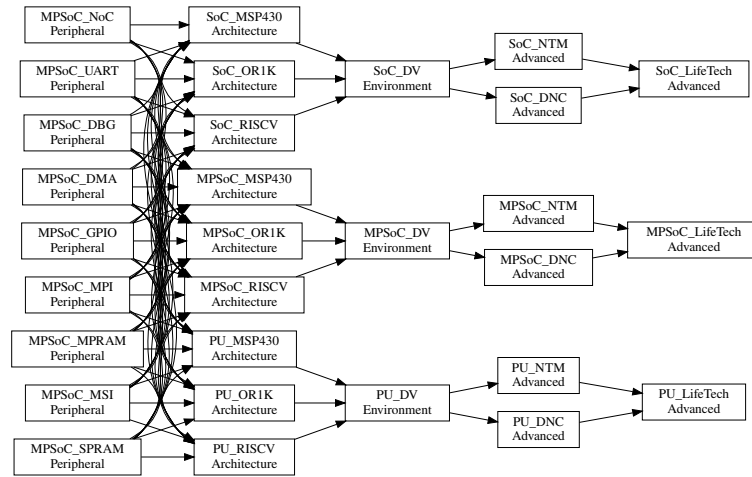


Figure 6.9: MPSoC LifeTech