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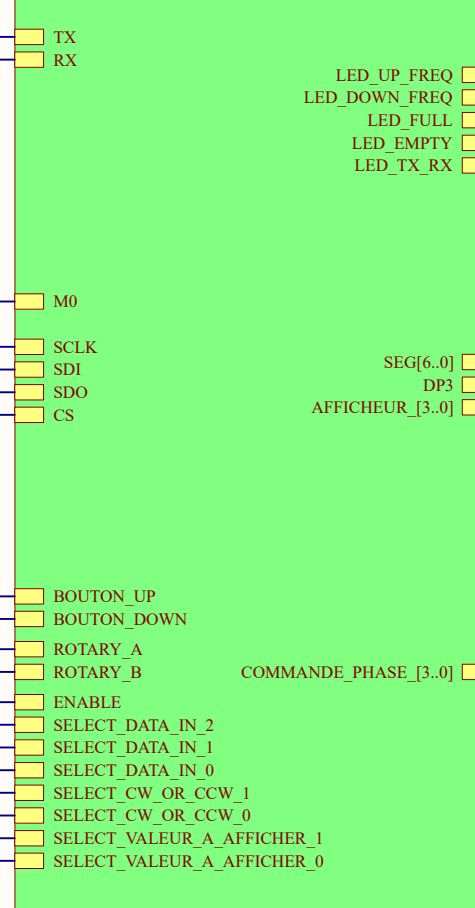
3

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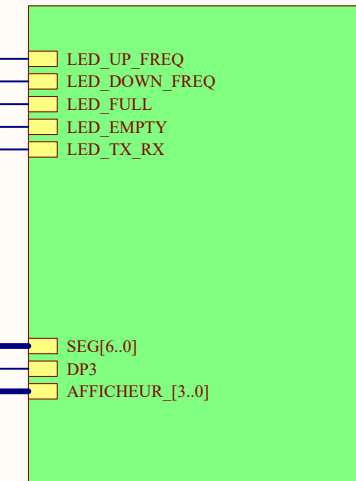
Designator  
CarteSpartan6\_Liaison\_USB-UART.SchDoc



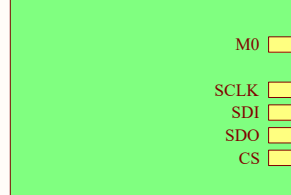
Designator  
CarteSpartan6\_FPGA\_IO.SchDoc



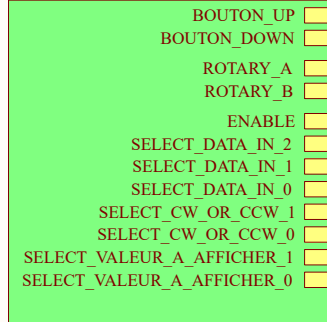
Designator  
CarteSpartan6\_Affichage\_Frequence\_And\_LEDs.SchDoc



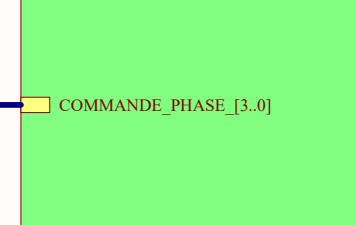
Designator  
CarteSpartan6\_FlashSPI\_16MB.SchDoc



Designator  
CarteSpartan6\_ControlVitesse.SchDoc





Designator  
CarteSpartan6\_PowerMOTOR.SchDoc



Designator  
CarteSpartan6\_PowerSupply.SchDoc

Designator  
CarteSpartan6\_JTAG\_Programming.SchDoc

Title: Intro Carte Spartan 6			 
Project: Carte FPGA commande moteur pas à pas Spartan 6			
Size: A4	Reference: Sources	Revision: A	
Date: 25/09/2021	Drawn by: Edward PAEZ	Sheet: 1 of 9	

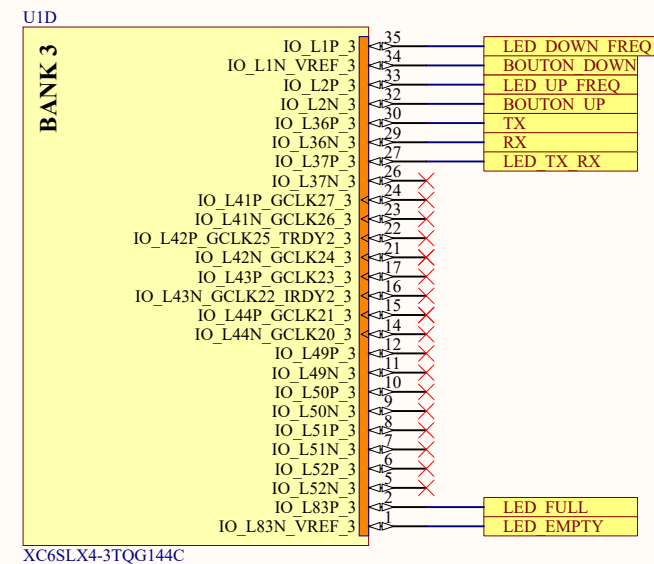
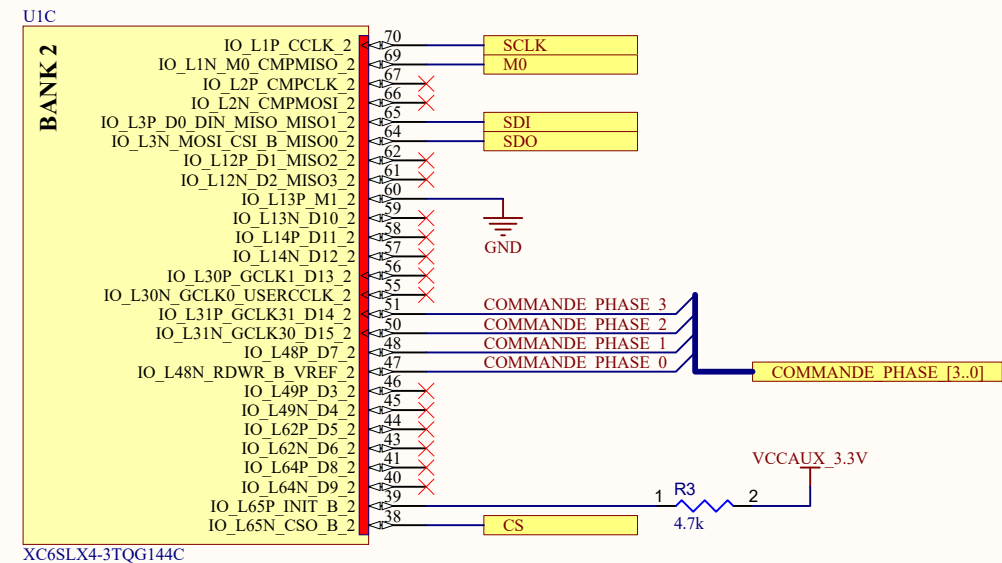
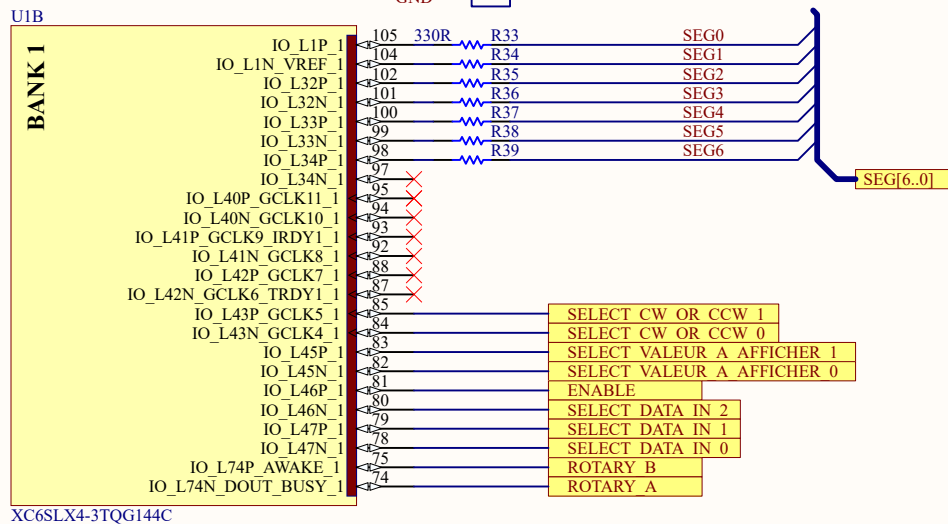
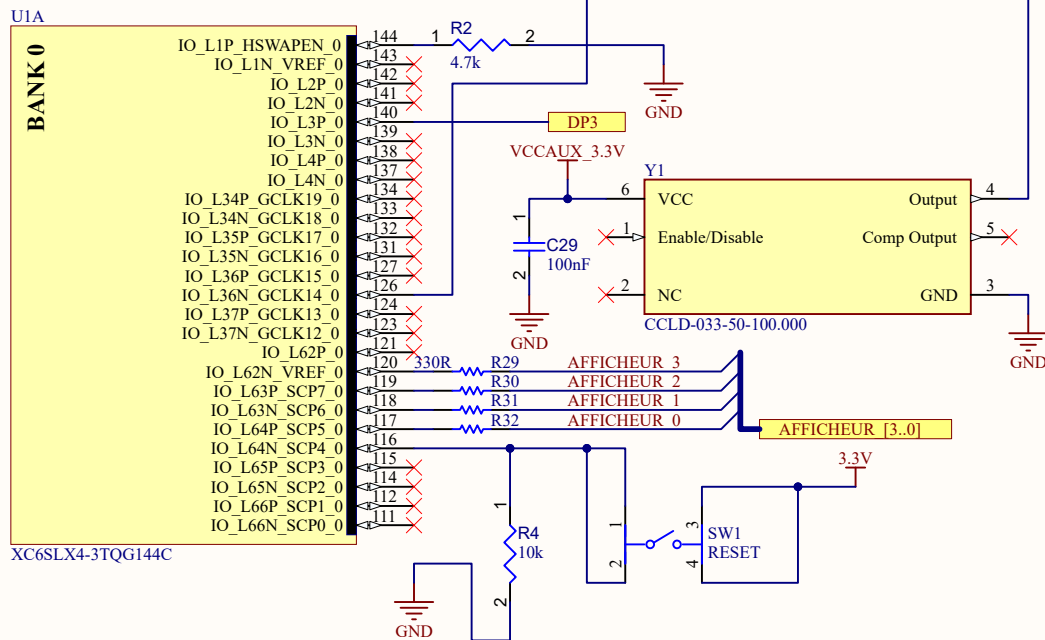
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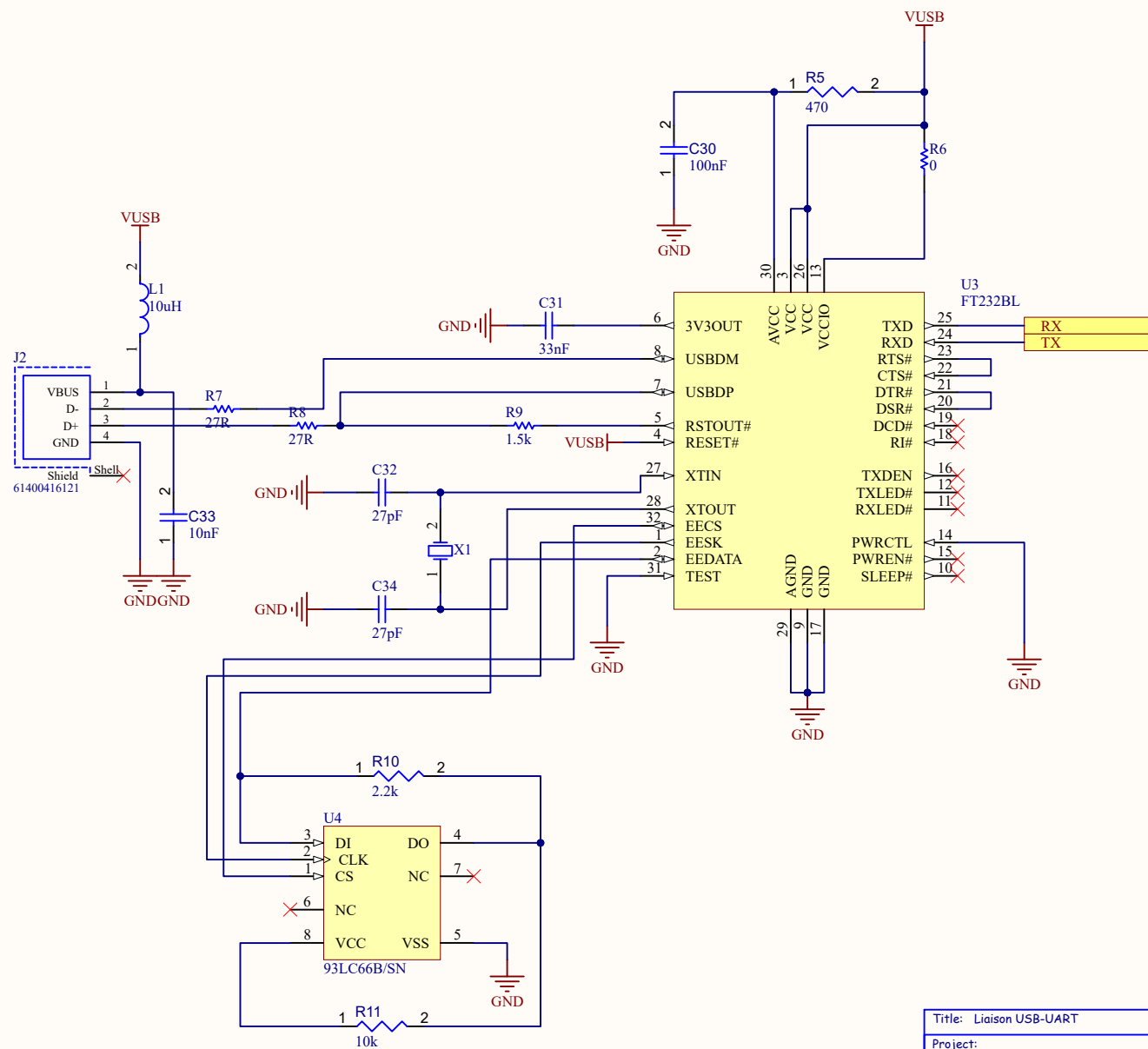
4





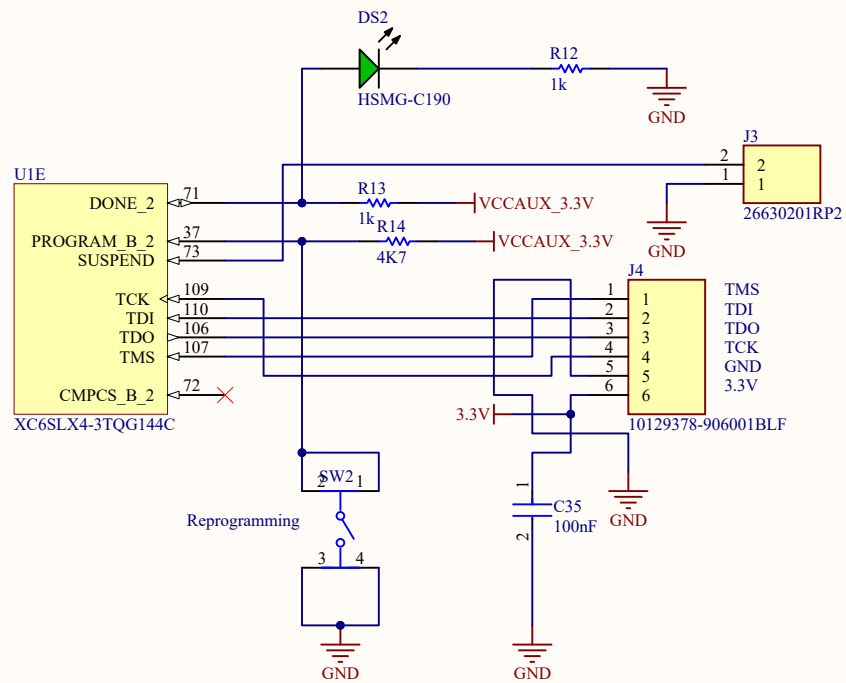
Title: FPGA IO		
Project:		
Size:	Reference: Sources	Revision: A
Date: 25/09/2021	Drawn by: Edward PAEZ	Sheet: 3 of 9





Title: Liaison USB-UART		
Project:		
Size:	Reference: Sources	Revision: A
Date: 24/09/2021	Drawn by: Edward PAEZ	Sheet: 4 of 9





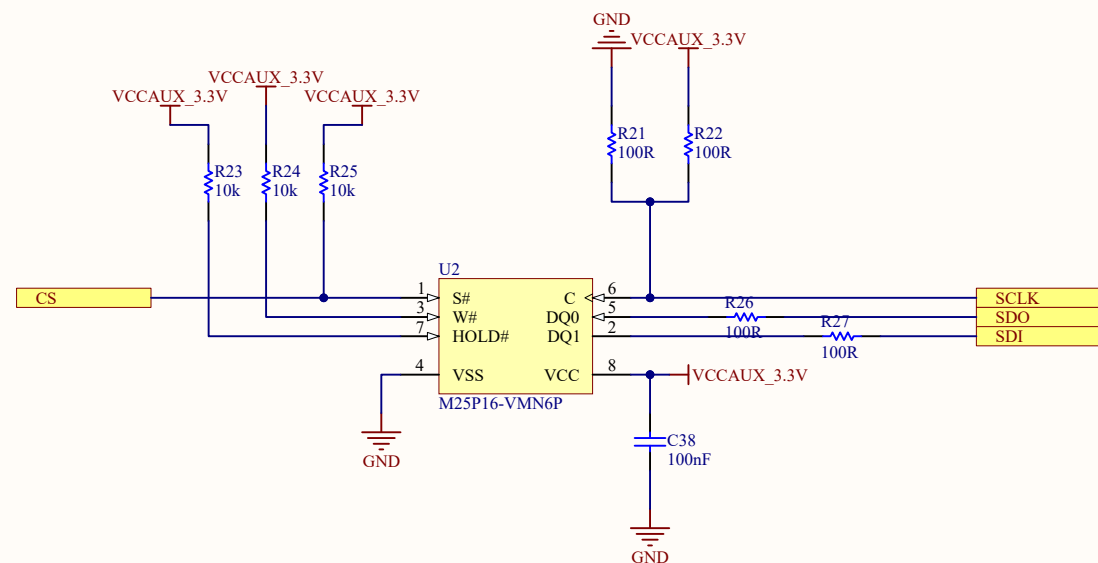
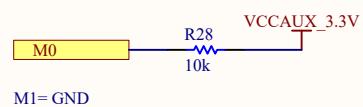
Title: JTAG Programming		
Project:		
Size:	Reference: Sources	Revision: A
Date: 24/09/2021	Drawn by: Edward PAEZ	Sheet: 5 of 9





Table 2-1: Spartan-6 FPGA Configuration Modes

Configuration Mode	M[1:0]	Bus Width	CCLK Direction
Master Serial/SPI	01	1, 2, 4 <sup>(1)</sup>	Output
Master SelectMAP/BPI <sup>(2)</sup>	00	8, 16	Output
JTAG <sup>(3)</sup>	xx	1	Input (TCK)
Slave SelectMAP <sup>(2)</sup>	10	8, 16	Input
Slave Serial <sup>(4)</sup>	11	1	Input



Title: Flash SPI 16Mbit		
Project:		
Size:	Reference: Sources	Revision: A
Date: 24/09/2021	Drawn by: Edward PAEZ	Sheet: 7 of 9



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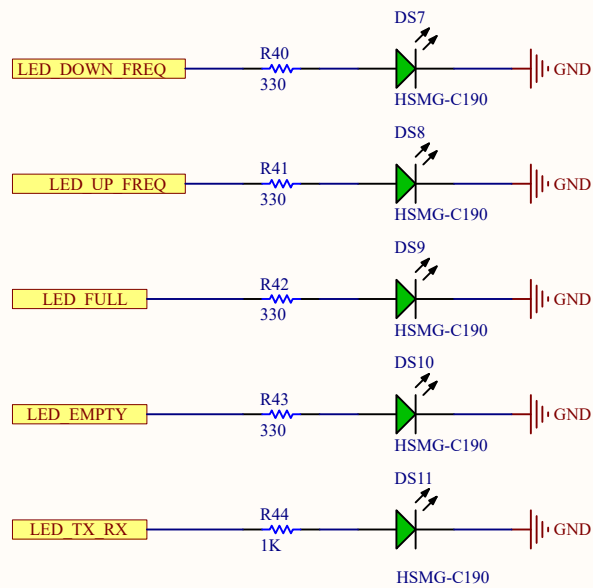
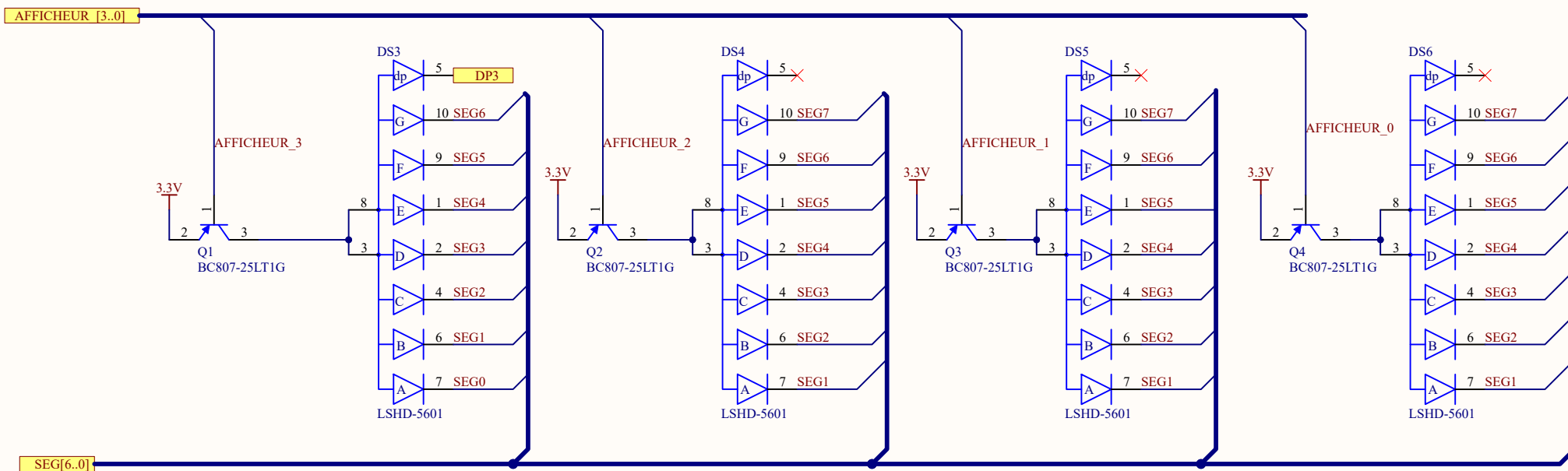
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Title: Affichage Fréquence et LED'S		
Project:		
Size:	Reference: Sources	Revision: A
Date: 25/09/2021	Drawn by: Edward PAEZ	Sheet: 8 of 9



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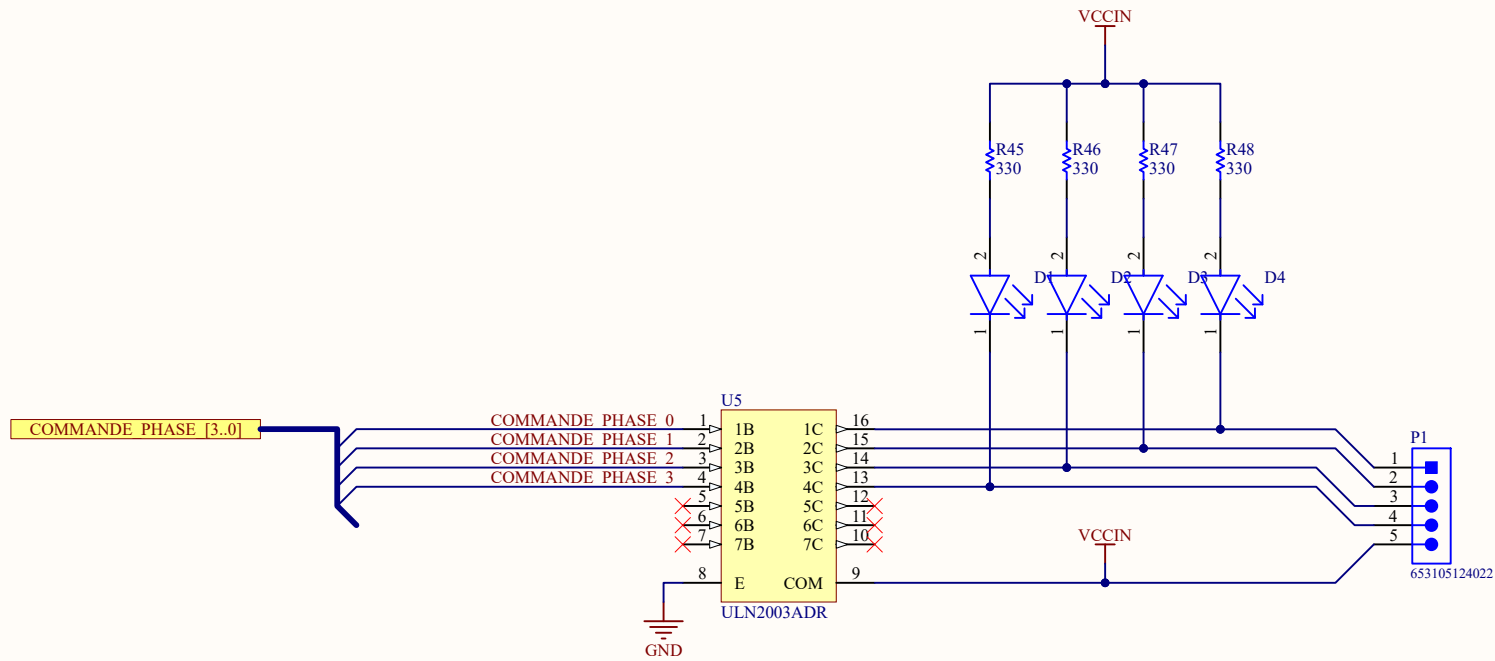
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Title: Power MOTOR			
Project:			
Size:	Reference: Sources	Revision: A	
Date: 24/09/2021	Drawn by: Edward PAEZ	Sheet: 9 of 9	

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