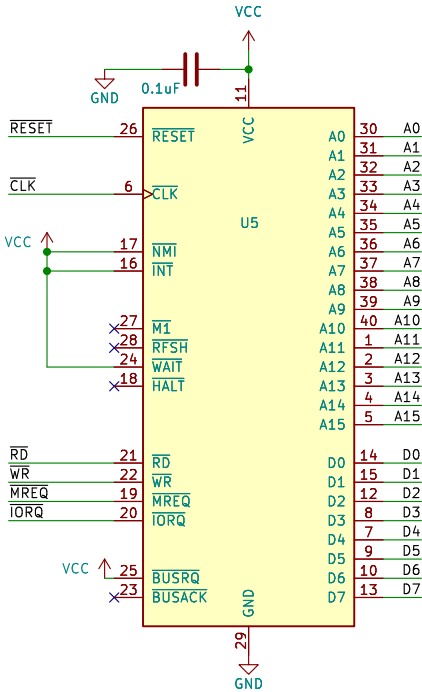
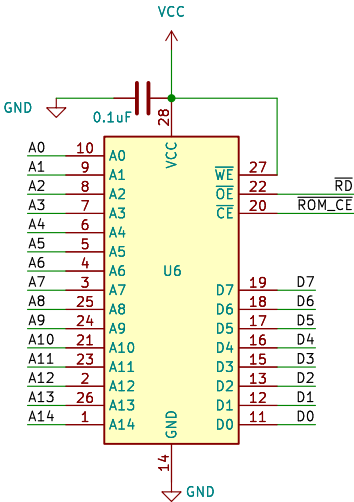


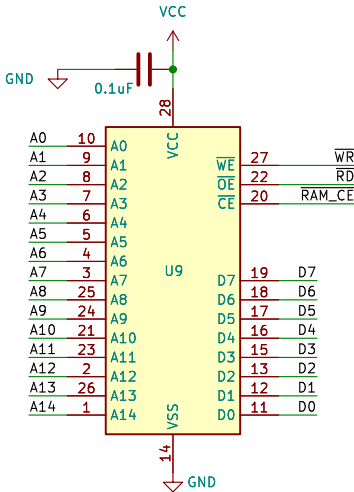
Z80CPU



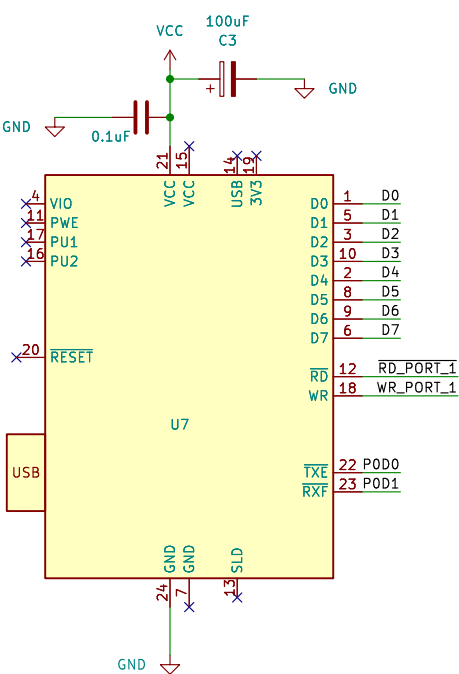
AT28C256 (32kB ROM)



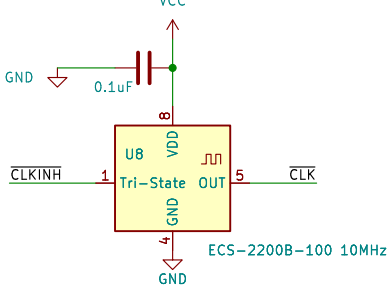
AS6C2256 (32kB SRAM)



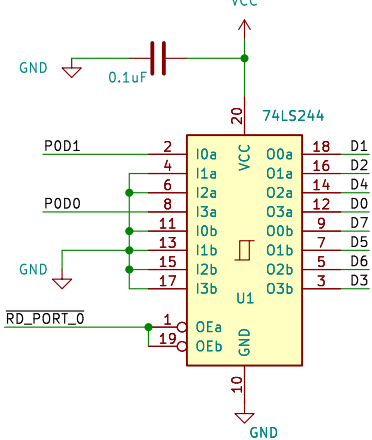
UM245R



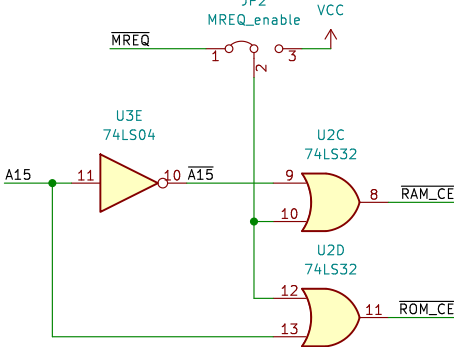
Clock



Port 0 - Read Buffer



Memory Glue Logic



D0 1
D1 2
D2 3
D3 4
D4 5
D5 6
D6 7
D7 8

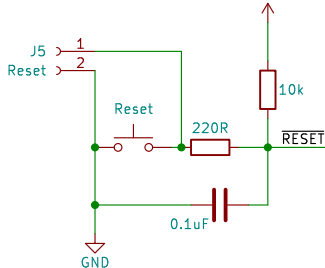
RD 1
WR 2
MREQ 3
IORQ 4
RESET 5
CLK 6
CLKINH 7

VCC 1
GND 2

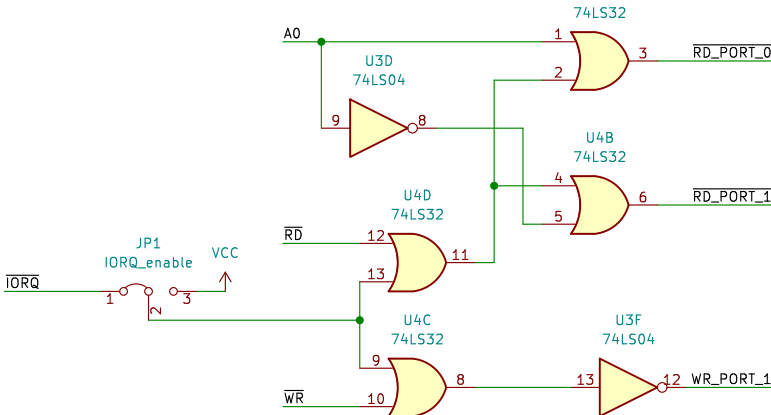
A0 1
A1 2
A2 3
A3 4
A4 5
A5 6
A6 7
A7 8
A8 9
A9 10
A10 11
A11 12
A12 13
A13 14
A14 15
A15 16

J1 Address

Reset



I/O Glue Logic



Unused

