

ARM Instructions Worksheet #5

Multiplication

Single/Double-Length, Signed/Unsigned

Prerequisite Reading: Chapter 5

Revised: March 26, 2020

Objectives: To use the web-based simulator ("CPULator") to better understand ..

What is left in R4 by the LDR pseudo-instruction at 00000014₁₆?

What product is left in R0 by the MUL instruction at 0000001816?

- 1. The MUL, SMULL, and UMULL instructions
- 2. Single versus double-length products.
- 3. Signed versus unsigned multiplication.

To do offline: Answer the questions that follow the listing below. (Numbers at fur left are memory addresses.)

,		.syntax .global	unified _start
00000000 0000004 00000008 000000010 00000014 00000018 0000001C	_start:	LDR LDR MUL SMULL LDR LDR MUL UMULL	R2,=+3 // *** EXECUTION STARTS HERE *** R3,=-5 R0,R2,R3 R0,R1,R2,R3 R2,=3 R3,=0x80000000 R0,R2,R3 R0,R1,R2,R3
00000020	done:	B .end	done

Note: Use this hex to decimal converter to convert 64-bit products to decimal.

	R2 (8 hex digits)	R2 (as signed decimal)
What is left in R2 by the LDR pseudo-instruction at 0000000016?	೦೨೨೪೨೨∫	3
	R3 (8 hex digits)	R3 (as signed decimal)
What is left in R3 by the LDR pseudo-instruction at 0000000416?	R0 (8 hex digits)	R0 (as signed decimal)
What product is left in R0 by the MUL instruction at 0000000816?	EEEEEE 1	-15
What is left in R1.R0 by the SMULL instruction at 0000000C ₁₆ ?	R0 (8 hex digits)	R1.R0 (as signed decimal)
Did the single-length signed product produced by the previous MUL	overflow?	Yes: No: 🔀
•	R2 (8 hex digits)	R2 (as unsigned decimal)
What is left in R2 by the LDR pseudo-instruction at 00000010 ₁₆ ?		

R3 (8 hex digits)) კიეიიძებ

R0 (8 hex digits)

1000000

R3 (as unsigned decimal)

R0 (as unsigned decimal)

7147 4 83648

What is left in R1.R0 by the UMULL instruction at $0000001C_{16}$?	R1 (8 hex digits)	R0 (8 hex digits)	R1.R0 (as unsigned decimal)
Did the single-length unsigned produ	ct produced by the previous N	IUL overflow?	Yes: No:
Getting ready: Now use the simulator to	collect the following informa	ntion and compare to your earlies	answers.
1. Click <u>here</u> to open a browser for	the ARM instruction simulate	or with pre-loaded code.	
Note: You can change the number for needed. For 64-bit products, use this		w between hex, unsigned decimal	and signed decimal as
Step 1: Press F2 exactly 2 times to execut	te the two LDR pseudo-instruc	tions (MOV, MVN) to provide the op	erands
What is left in R2 by the LDR pseudo	-instruction at 00000000 ₁₆ ?	R2 (8 hex digits)	R2 (as signed decimal)
What is left in R3 by the LDR pseudo	-instruction at 00000004 ₁₆ ?	R3 (8 hex digits)	R3 (as signed decimal)
Step 2: Press F2 exactly once to execute	the MUL RO, R2, R3 instructio	on	
What product is left in R0 by the MUL	instruction at 00000008 ₁₆ ?	R0 (8 hex digits)	R0 (as signed decimal) - 以
Step 3: Press F2 exactly once to execute	the SMULL RO,R1,R2,R3 in:	struction.	
What is left in R1.R0 by the SMULL instruction at 0000000C ₁₆ ?	R1 (8 hex digits)	R0 (8 hex digits)	R1.R0 (as signed decimal)
Did the single-length signed product p	produced by the previous MUL	overflow?	Yes: No: No:
Step 4: Press F2 exactly 2 times to execute	e the two LDR pseudo-instruc	tions (MOV, MOV) to provide the o	perands *
What is left in R2 by the LDR pseudo-	instruction at 00000010 ₁₆ ?	R2 (8 hex digits)	R2 (as unsigned decimal) R3 (as unsigned decimal)
What is left in R4 by the LDR pseudo-i	instruction at 00000014 ₁₆ ?	R3 (8 hex digits)) გემემმებ	214 7 4 1 36 4 h
Step 5: Press F2 exactly once to execute th	re MUL: RO, R2, R3 instruction	9N:«	BAND HITCHING TO TURBUNING NICES
What product is left in R0 by the MUL i		R0 (8 hex digits) {;; ১১১ ১১০ ১	R0 (as unsigned decimal) २।५२५ ४३६ ५४
Step 6: Press F2 exactly once to execute th	e UMULE: RO, R1, R2, R3 in	struction:«————————————————————————————————————	The second of th
What is left in R1.R0 by the UMULL instruction at 0000001C ₁₆ ?	R1 (8 hex digits)	R0 (8 hex digits) 炎ッジョックい	R1.R0 (as unsigned decimal)
Did the single-length unsigned product	produced by the previous M	MUL overflow?	Yes: No: