

# EGCI491 Computer Engineering Seminar

## L<sup>A</sup>T<sub>E</sub>X Assignment II

Firstname Lastname

February 1, 2025

### 1 Timeline

Table 1: Project Timeline

| Plan                         | Timeline |     |       |       |     |     |      |     |     |     |     |     |
|------------------------------|----------|-----|-------|-------|-----|-----|------|-----|-----|-----|-----|-----|
|                              | 2025     |     |       |       |     |     |      |     |     |     |     |     |
|                              | Jan      | Feb | March | April | May | Jun | July | Aug | Nov | Oct | Nov | Dec |
| 1: Study PQA                 |          |     |       |       |     |     |      |     |     |     |     |     |
| 2: Study FPGA                |          |     |       |       |     |     |      |     |     |     |     |     |
| 3: Chapter1                  |          |     |       |       |     |     |      |     |     |     |     |     |
| 4: Chapter2                  |          |     |       |       |     |     |      |     |     |     |     |     |
| 5: Chapter3                  |          |     |       |       |     |     |      |     |     |     |     |     |
| 6: Testing                   |          |     |       |       |     |     |      |     |     |     |     |     |
| 7: Chapter4                  |          |     |       |       |     |     |      |     |     |     |     |     |
| 8: Chapter5                  |          |     |       |       |     |     |      |     |     |     |     |     |
| 9: Project Pre-<br>sentation |          |     |       |       |     |     |      |     |     |     |     |     |