

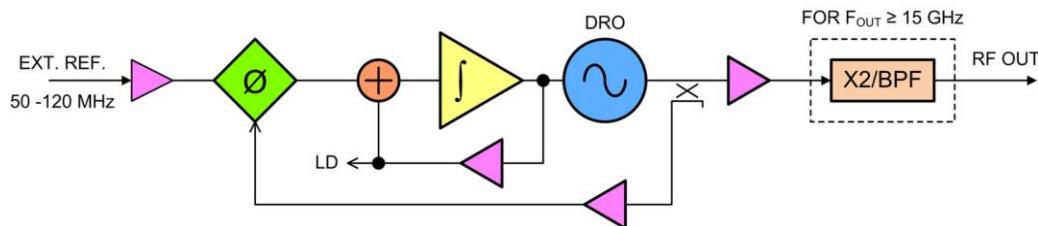
Model PLDRO: Phase-Locked Dielectric Resonator Oscillator

Features

- Output Frequencies from 8 to 34 GHz
- Input Frequency:
 - Internal TCXO/OCXO Reference
 - External 10 – 120 MHz Reference
- +5.0 VDC Operation/Low Power Design
- Load and Drive Insensitive
- Best Price/Performance Ratio
- Dual Loop 10 MHz Input Reference Available



Block Diagram



Description

Our PLDROs are designed to incorporate many highly desirable features into one low-cost unit. These PLDROs utilize a combination of MIC & Surface Mount Components to achieve the maximum lowest production cost to performance ratio. One unit can cover a frequency from 8 – 34 GHz and the low profile and rugged construction, as well as the RoHS-compliant design, provides excellent durability against harsh environments due to shock or vibration.

PLDRO Key Specifications

Output Frequency:	8 – 34 GHz
Spurious:	-70 dBc
Harmonics (typ.):	-30 dBc
Output Power (min.):	+13 dBm
Output Power Variation:	±3 dB
External Reference:	10 - 250 MHz
Internal Reference:	±0.5 PPM
Alarm:	Open Collector/TTL
Power Requirements:	+5.0 VDC @ 500 mA
Temp. Range:	-10°C to +70°C

PLDRO TYPICAL PHASE NOISE @ 8 GHz

