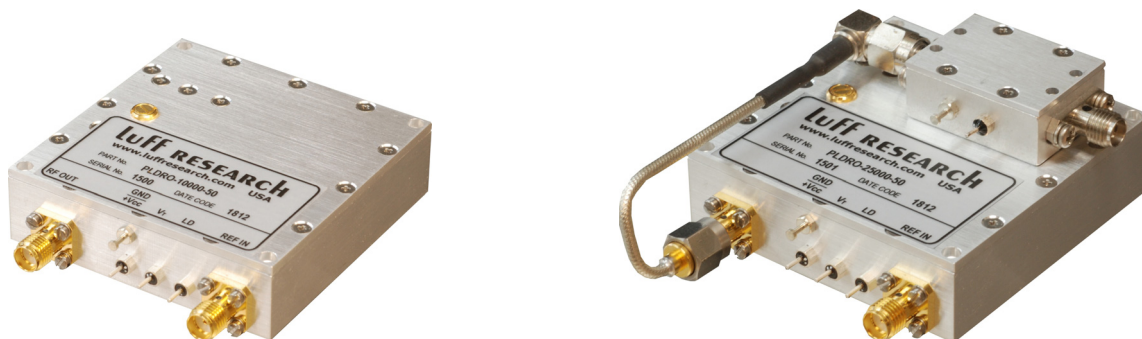


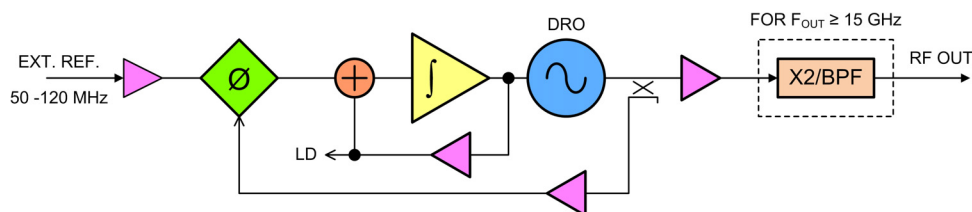
## Model PLDRO: Phase-Locked Dielectric Resonator Oscillator

### • Features

- Output frequencies from 8 to 25 GHz
- Reference Input - External 50-120 MHz, Internal TCXO or OCXO
- +5.0 Vdc operation, low power design
- Load and drive insensitive
- Best price/performance ratio
- Dual loop 10 MHz reference input option available



### • Block Diagram



### • Description

This new PLDRO design incorporates many highly desirable features in a rugged low cost unit. This unit can be configured to operate with a reference frequency from one of the following sources: (1) external 10 MHz; (2) external input frequency in the 50 - 120 MHz range or (3) internal VCXO, TCXO or OCXO. Another unique feature of this unit is its low power consumption and +5VDC operation. This highly reliable unit is thoroughly burned in and tested.

### • PLDRO Key Specifications

Output Frequency:	8 - 25 GHz
Spurious:	-70 dBc
Harmonics (typ):	-30 dBc
Output Power (min.):	+13 dBm
Output Power Variation:	3 dB
External Reference:	10 MHz or 50 - 120 MHz
Internal Reference:	±10 PPM (option A) ±1 PPM (option B) ±0.1 PPM (option C)
Alarm:	Open Collector
Power Requirements:	+5.0 Vdc @ 500 mA
Temperature Range:	-10° to +70°C

