

SPECIFICATIONS

Output Frequency

Frequency:	25.0 GHz
Frequency stability and accuracy	
<input checked="" type="checkbox"/> External reference unit	Same as input
<input type="checkbox"/> Internal reference unit	±0.5 PPM (over temp range)
Aging (After 2 months):	±1 PPM max per year @ 25°C
Adjustability (typ.):	10 years
Phase noise in dBc/Hz (typ.):	
L(100 Hz)	-75
L(1 kHz)	-100
L(10 kHz)	-100
L(100 kHz)	-110
L(1 MHz)	-125
L(10 MHz)	-135
Spurious (max.):	-70 dBc
Harmonics / Sub-Harmonics (typ.):	-20 / -40 dBc
Power out (min.) @ 25°C:	+13 dBm
Power variation (freq. & temp.) (max.):	2 dB
VSWR (typ.):	1.5:1
Phase-lock indicator (LD), High = lock:	<input checked="" type="checkbox"/> Open collector <input type="checkbox"/> TTL

Reference Frequency

Input reference frequency:	10 MHz
Input level:	0 dBm ±3 dB

DC Power

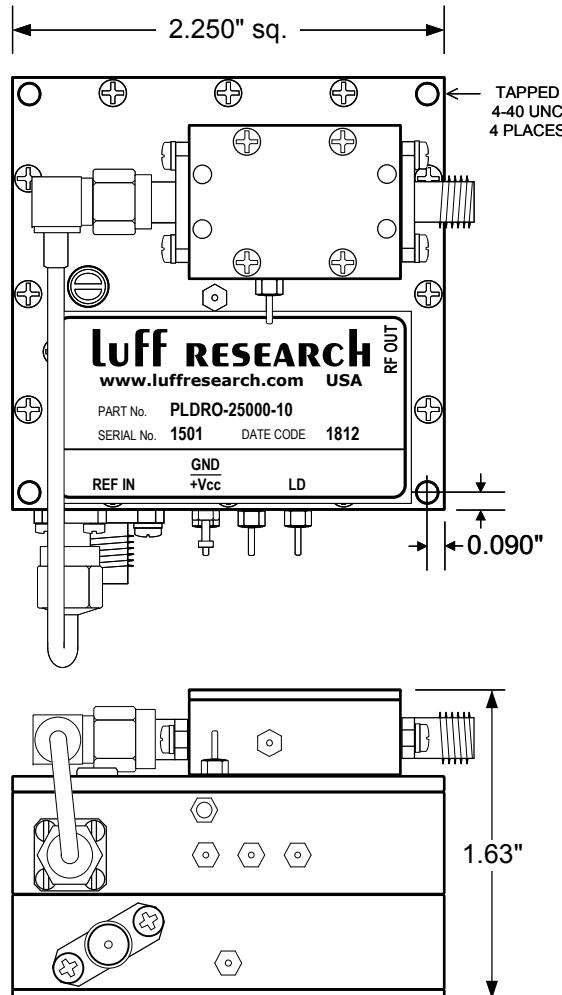
+5.0 Vdc (min.) +6 Vdc (max.)	750 mA
Internal regulator	

Mechanical

RF connectors:	SMA (F)
DC connections:	Filtered Feed Throughs

Environment

Operating temperature range (surface):	-10°C to 70°C
Storage temperature range:	-40°C to 85°C
Relative humidity (non-condensing):	90%RH @ 40°
Shock:	30 G / 10msec
Vibration:	4 G / 20 Hz - 20 kHz



Note:

1. This unit has the feature that when the input reference is removed, the internal reference is automatically switched in as the reference to the phase-locked loop.

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PRODUCT DATA SHEET
Phase-Locked Dielectric Resonator Oscillator
Model: PLDRO-25000-10
05/01/12