ASHOK KUMAR SUHAG
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Seeking opportunity to work with a reputed organisation, which maintains its employee's enthusiasm thereby making them work excellently towards the common objective.

SYNOPSIS

- Presently working as a Faculty/ Research Associate at **Gautam Buddha University,** Greater NOIDA**.**
- Worked as a Faculty at **Vivekananda Institute of Technology,** Jaipur**.**
- Worked as a Faculty at **Marudhar engg. College**, Bikaner
- Completed **M.TECH** thesis from **Indian Institute of Science**, Bangalore, INDIA.
- Completed **M.TECH in MICROELECTRONICS & VLSI DESIGN** from Kurukshetra University Kurukshetra, HARYANA, INDIA.
- Worked as a project assistant in **Central Electronics Engineering Research Institute (CEERI), Pilani** for **one year** in **MEMS & MICROSENSOR** Department
- Completed **B.E. In Electronics Instrumentation & Control Engineering** from IET, University of Rajasthan (JAIPUR), INDIA.

ORGANIZATIONAL EXPERIENCE – 2.0 years

Organization : **Gautam Buddha University, Greater NOIDA**
Duration : May 2010.

Organization : **Vivekananda Institute of Technology, Jaipur**
Duration : Jan. 2010 to April 2010

Organization : **Marudhar Engg. College, Bikaner**
Duration : August 2008 – Dec. 2009

Organization : **INDIAN INSTITUTE OF SCIENCE, BANGALORE**
Details : **Universal Pattern Set for Arithmetic Circuits & Delay Fault Testability of Sequential Circuits.**
Summary : In this project we are trying to develop a pattern set for the arithmetic circuits which has minimum number of patterns and that give maximum fault coverage during the testing ultimately which reduces the cost of testing and In Delay Fault Testability of Sequential Circuits we designed the Delay testable Enhanced Scan Flip-flop which implements the enhanced scan flip-flop with the slow hold signal.
Duration : 6 months, February 2008 to August 2008.

Organization : **Central Electronics Engineering Research Institute (CEERI), Pilani (CSIR LAB)**
Project : **Fabrication of Micro Hot Plate as a gas sensor**
Duration : Aug. 6th 2005 to Sept. 14th 2006 as a Research Assistant.
Result : Successfully fabricated
Paper Published: 2 (national)

My Role:

- Fabrication of Micro Hot Plate as a gas sensor
- Sputtering
- Familiar with all the other IC fabrication processes

TECHNICAL PRESENTATION

Study of the crucial process in the fabrication of MEMS Hotplate Structure.
V.K.Khanna, Mahanth Prasad, Ashok Suhag, M.K. Sharma, V.K.Diwedi and Chander Shekhar

Proceedings of ISSS-MEMS 2006, National Conference on Smart Structures and MEMS Systems for Aerospace Applications, RCI, DRDO, Hyderabad, India, pp.1-6
1-2 December 2006.

MEMS and Semiconductor Technology-Bases Generic Structures for the Fabrication of High-Performance Chemical Sensors.

V.K.Khanna, Mahanth Prasad, Ashok Suhag, Y.K.Jain M.K. Sharma, V.K.Diwedi and Chander Shekhar
National Conference on Sensors and Actuators: Emerging Technological Challenges, CGCRI, Kolkata, Dec21-21, 2006, Abstracts, p.11, Invited Lecture 1-06.

Micro-actuator Technologies for Future Spacecraft Mission.
Synopsis: Advance Technology to Reduce the Size of Spacecraft.

PROJECT

Project : **CHARACTERISATION OF ELECTRO LESS DEPOSITION OF COPPER AND NICKEL AS A BARRIER LAYER**
Duration : Aug. 2007 to Dec. 2007
Summary : In this project we are depositing the nickel film by electro less method as a diffusion barrier layer for the copper layer and characterising the film thickness and the sheet resistance against the deposition time and the deposition temperature.

Project : **AUTOMATED GUIDED VEHICLE**
Duration : Jan.2005 to June 2005

ACADEMIA

❖	M.TECH in Microelectronics & VLSI DESIGN	**KUK**	**70%**	2008
❖	B.E. in Electronics Instrumentation & Control Engineering	**IET,UOR**	**71%**	2005

SKILL MATRIX

Software-Tools : Design Compiler, Design Vision(TM),DFT Compiler (TM), TetraMAX (R) Version Y-2006.06- SP4,by Synopsys.
OS : MS DOS, WINDOWS NT/XP,LINUX.
Languages : VHDL.

INDUSTRIAL TRAINING

Organization : **Central Electronics Engineering Research Institute (CEERI), Pilani.**
Duration : 6 weeks, July 2003
Details : P.C. BASED INTERFACE FOR GAS FLOW CONTROL VALVE

Organization : **BIRLA CEMENT WORKS, CEMENT DIVISON, SATNA (M.P.).**
Duration : 6 weeks, July 2004
Details : Process Plant, PLC, and DCS.

PERSONAL DETAILS

Date of Birth : January 25, 1983
Current Address : H.No. – 7 (Type IV), Gautam Buddha university, Greater NOIDA.
Phone : +919610418757 (Mobile)

Ashok Kumar Suhag