

Dr. Vivek Shrivastava

Assistant Professor

Department of Electrical Engineering

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Education:

- Ph.D. in Power System Reliability (Indian Institute of Technology, Kharagpur, India, 2009)
- B. E. in Electrical Engineering (R.G.P.V. University, Bhopal, India, 2001)

Area of Research:

Power System Reliability, Load Flow Analysis, Load Forecasting, Bayesian Analysis, Accelerated Life Testing Analysis, Weibull Analysis, Reliability Design, Failure Mode Effect and Criticality Analysis (FMECA), Fault Tree Analysis(FTA), Event Tree Analysis(ETA), VLSI

Courses offered:

Undergraduate: Basic Electrical Engineering, Power System Analysis & Control, Computer Added Power System, VLSI Design, Electronics & Logic Design

Post Graduate Level: Reliability Analysis & Prediction

Work Experience:

1. **System Reliability Engineer:** SanDisk Semiconductor (Shanghai) Co. Ltd., Shanghai (Oct 2008-Dec 2009)
2. **Senior Research Fellow:** Sponsored Research & Industrial Consultancy (SRIC), IIT Kharagpur (Nov 2005- Nov 2006).
3. **Junior Research Fellow:** Sponsored Research & Industrial Consultancy (SRIC), IIT Kharagpur (Nov 2003- Nov 2005).
4. **Lecturer:** Department of Electrical Engineering - S. R. C. E. M., Banmore, Gwalior (Feb 2002- Aug 2003).
5. **Visiting Lecturer:** Department of Electrical Engineering – M.I.T.S. Gwalior (July 2002- June 2003).

Journal's Reviewer:

1. Electric Power System Research (Elsevier Publication).
2. IEEE Transaction on Energy Conversion.
3. Reliability Engineering & System Safety (Elsevier Publication).
4. International Journal of Computer Aided Technology and Engineering (Inderscience Publication)

Membership of Professional Institutions

1. Institute of Electronics & Electrical Engineers (IEEE), USA
2. International Association for Engineers (IAENG), Hong Kong.

Administrative and Organizational Activities

1. **Founder Secretary:** International Conference on Reliability & Safety Engineering (**INCRESE 2005**), organized by Reliability Engineering Centre, IIT Kharagpur
2. **Senate Research Representative:** Indian Institute of Technology Kharagpur (2006-07)

Publications:

International Journals

1. **Shrivastava Vivek**, Misra R.B., (2009), "Bayesian Belief Network As a Tool for Load Forecasting," International Journal of Communications in Dependability and Quality Management, , vol. 12, no.1, pp 91-100.
2. **Shrivastava Vivek**, Misra R.B., Bansal R.C. (2009), "An Assessment of Electrical Load Forecasting Using Artificial Neural Network," accepted for publication in International Journal of Computer Aided Engineering and Technology (IJCAET).
3. **Shrivastava Vivek**, Misra R.B. (2007), "Analysis of Various Conditional Configuration of Load Bus using Method of Moment," International Journal of Communications in Dependability and Quality Management Volume 10, Number 3, pp. 140-149.
4. **Shrivastava Vivek**, Misra R.B., (2010), "Development of Bayesian Belief Network Model for Electric Load Demand," International Journal of Communications in System Assurance Engineering and Management (will be published in June 2010)

Conference Papers

International Conference

1. **Shrivastava Vivek**, Tushir S. (2008), “An Assessment of Two-Dimensional Warranty Forecasting Model,” published in proceeding of International Conference on Reliability Safety & Quality Engineering (ICRSQE 2008), IIT Bombay pp. 507-510.
2. **Shrivastava Vivek**, Misra R.B. (2008), “A Novel Approach of Input variable selection for ANN Based Long Term Load Forecasting,” published in proceeding of IEEE International conference POWERCON 2008, New Delhi, October 2008.
3. **Shrivastava Vivek**, Tushir S. (2007), “Two-Dimensional Warranty Analysis And Forecasting For Automotive Components,” Published in proceeding of International Conference on Reliability & Safety Engineering (INCRESE 2007), IIT Kharagpur pp. 369-375.
4. **Shrivastava Vivek**, Misra R.B. (2007), “Probabilistic Load Flow Computation using Method of Moments & Cumulants,” published in Proceedings of International Conference on Electrical Engineering design & Technologies (ICEEDT 2007), at Hammanet, Tunisia, November 2007.
5. **Shrivastava Vivek**, Misra R.B. (2007), “Probabilistic Load Flow Calculation using Method of Moments,” Proceedings of International Conference on Modelling & Simulation (CITICOMS 2007), Coimbatore, pp. 777-784.
6. **Shrivastava Vivek** (2005), “Reliability Issues in a Deregulated Power System,” Proceedings of International Conference on Reliability and Safety Engineering (INCRESE’05), Bhubaneswar, pp. 807-812.

National Conference

1. **Shrivastava Vivek**, Misra R.B. (2007), “Load Flow Analysis using Method of Moments” Proceedings of Conference on Mathematical Modelling, Optimization and Their Applications (OPTIMA’07), New Delhi, 2007.
2. **Shrivastava Vivek**, Misra R.B (2006), “Probabilistic Power System Planning using Bayesian Belief Networks” Proceedings of Conference on Computational Intelligence to Emerging Electric Power Systems (CIEEPS’06), Pondicherry, pp. 33-40.
3. **Shrivastava Vivek** (2003), “Test and Testability for VLSI Circuits” Proceeding of Conference on Services through Information Technology Enabled Systems (SITES’03), Gwalior.

Foreign Countries Visited for Professional Activities

1. **People's Republic of China:** In Shanghai since Oct 2008 to Dec 2009.
2. **United State of America:** In Milpitas, Silicon Valley, California, 2009.
3. **Hong Kong, SAR:** In Hong Kong, 2009.

Knowledge of Foreign Language

1. **German:** Certified course work during PhD curriculum.
2. **Mandarin (Chinese):** Learned at work place in Shanghai.

Short term courses / Workshops / Seminars etc.:

1. Power System Reliability (Member Organising Body- IIT Kharagpur)
2. Early Reliability Software Prediction (Member Organising Body- IIT Kharagpur)