

SCHOOL OF INFORMATION AND COMMUNICATION TECHNOLOGY

COURSE STURCTURE AND DETAILED SYLLABUS

2 YEARS M. TECH. ICT FOR ENGINEERING GRADUATES

SPECIALIZATION:

WIRELESS COMMUNICATION & NETWORKS



**GAUTAM BUDDHA UNIVERSITY
GAUTAM BUDH NAGAR, GREATER NOIDA
2011-2012**

SEMESTER- I

Sr.No	Course Code	Courses	L-T-P	Credits
		<u>THEORY</u>		
1	EC531	Advanced Digital Communication System	3-1-0	4
2	EC533	Wireless Networks	3-1-0	4
3	EC539	Advanced Digital Signal Processing	3-1-0	4
4		Elective -2	3-0-0	3
5	SS101	Human Values & Buddhist Ethics	2-0-0	2
		<u>PRACTICALS</u>		
6	CS591	Programming Lab	0-0-3	2
7	EC581	Digital Communication Lab	0-0-3	2
8	GP531	General Proficiency	---	1
		Total	14-3-6	22
		Total Contact Hours	23	

Elective-2

Sr.No	Course Code	Courses
1.	EC541	Advanced Computer Networks
2.	EC565	Embedded System Design
3.	CS574	Principles of Artificial Intelligence
4.	CS536	Computer System Architecture
5.	EC535	Advanced Digital Circuit Design
6.	CS576	Data Mining

SEMESTER- II

Sr. No.	Course Code	Courses	L-T-P	Credits
		<u>THEORY</u>		
1	MA402	Simulation and Modeling	3-1-0	4
2	EC532	Advanced Communication Networks	3-0-0	3
3	EC534	Wireless System Design	3-0-0	3
4		Elective-3	3-0-0	3
5		Elective-4	3-0-0	3
		<u>PRACTICALS</u>		
6	EC584	Design Lab-I	0-0-3	2
7	EC592	Major Project	0-0-10	5
8	GP532	General Proficiency	---	1
		Total	15-1-13	24
		Total Contact Hours	29	

Electives (3 & 4)

Sr.No	Course Code	Courses
1.	EC542	Quality of Services in Networks
2.	EC566	VLSI for Wireless Communication
3.	EC536	VLSI Technology
4.	EC544	Advanced RF Engineering
5.	EC548	Probability and Stochastic Processes
6.	EC550	Advanced Microwave Communication
7.	EC552	Networking Protocols
8.	EC554	Mobile Computing
9.	EC556	Image Processing and Biometrics
10.	EC558	Network Programming
11.	EC576	CMOS RF Circuit Design
12.	EC570	Principles of MEMS Design
13.	CS447/CS547	Multimedia Techniques
14.	CS449/CS561	Soft Computing
15.	CS404/CS534	Open Source Software System
16.	EC562	Optical Networks

SEMESTER – III

Sr. No.	Course Code	Courses	L-T-P	Credits
		<u>THEORY</u>		
1	EC631	Security in Wireless Networks	3-0-0	3
2	CS633	Research Techniques in ICT	3-0-0	3
3		Elective-5	3-0-0	3
4		Elective-6	3-0-0	3
		<u>PRACTICALS</u>		
5	EC681	Computing Lab	0-0-3	2
7	EC691	Dissertation Part-I	0-0-14	7
8	GP631	General Proficiency	---	1
		Total	12-0-17	22
		Total Contact Hours	29	

Electives (5 & 6)

Sr.No	Course Code	Courses
1	EC641	Network Reliability
2	EC643	Smart Antenna Systems
3	EC645	Broadband Wireless Networks
4	EC647	Sensor Networks
5	EC649	Ad-hoc Wireless Networks
6	EC651	Multicast Communication

7	EC671	Modern Optimization Techniques
8	EC675	Mixed Signal VLSI Design
9	EC677	DSP Integrated Circuits

SEMESTER-IV

Sr. No	Course Code	Courses	L-T-P	Credits
1	EC690	Dissertation Part-II	---	21
2	GP632	General Proficiency	---	1
		Total	---	22

Grand Total Credits = 90

ADVANCED DIGITAL COMMUNICATION SYSTEM			
Course Code:	EC531	Credits:	4
No. of Lectures (Hrs/Week):	3+1T	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Pulse Modulation Analog Signals:- Sampling of Signal, Sampling Theorem for Low Pass and Band Pass Signals, Aliasing, Pulse Amplitude Modulation (PAM), Time Division Multiplexing (TDM), Channel Bandwidth for PAM-TDM Signal, Types of Sampling, Instantaneous, Natural and Flat Top (Mathematical and Spectral Analysis), Aperture Effect, Introduction to Pulse Position and Pulse Duration Modulation.

Unit II: Pulse Code Modulation Digital Signal:- Quantization, Quantization Error, Pulse Code Modulation (PCM), Signal-to-Noise Ratio in PCM, Companding, Data Rate and Bandwidth of Multiplexed PCM Signal, Inter-symbol Interference, Eye Diagram, Line Coding NRZ, RZ, Biphasic, Duo Binary Etc ,Differential PCM (DPCM), Delta Modulation (DM), and Adaptive Delta Modulation (ADM), Slope Overload Error ,Granular Noise ,Comparison of various system in terms of Bandwidth and Signal-to-Noise Ratio.

Unit III: Digital Modulation Techniques :- Analysis, Generation and Detection (Block Diagram), Spectrum and Bandwidth of Amplitude Shift Keying (ASK), Binary Phase Shift Keying (BPSK), Differential Phase Shift Keying (DPSK), Offset and Non-offset Quadrature Phase Shift Keying (QPSK), M-ary PSK, Binary Frequency Shift Keying (BFSK), M-ary FSK, Minimum Shift Keying, Quadrature Amplitude Modulation (QAM), Comparison of digital modulation techniques on the basis of probability of error, Matched Filter.

Unit IV: Concept of Probability, Relative Frequency and Probability Conditional Probability and Independent Events, Random Variables, Discrete Random Variables, Cumulative Distribution Function(CDF), Probability Density Function(PDF),Statistical Averages (Means),Chebyshev's Inequality, Central Limit Theorem.

Unit V: Spread Spectrum Modulation: Pseudo random noise sequences, notion of spread spectrum, direct sequence, frequency hopping, processing gain. Convolution codes and Golay codes.

Text Books:

- [1] B. Sklar, Digital Communication, Pearson Education.
- [2] Tomasi: Advanced Electronics Communication Systems, 6th Edition, PHI

References:

- [1] Taub & Schilling, Principles of Communication system, TMH.
- [2] Lathi B.P., Modern Analog and Digital Communication systems, Oxford Uni. Press.
- [3] Haykin Simon, Digital Communication, Wiley Publication.
- [4] Proakis, Digital communication, McGraw Hill
- [5] Schaum's Outline series, Analog and Digital Communication.
- [6] Singh and Sapre: Communication System, TMH
- [7] Couch: Digital and Analog Communication, Pearson Education
- [8] David Smith: Digital Transmission Systems, Springer- Macmillan India Ltd

WIRELESS NETWORKS			
Course Code:	EC533	Credits:	4
No. of Lectures (Hrs/Week):	3+1T	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Cellular fundamentals, SIR calculation, capacity enhancement techniques, channel allocation schemes, handover, path loss modeling, multipath, channel measurements, simulation of radio channels cellular wireless networks-2G TDMA, 2G-CDMA, 3G systems, cordless systems, , IMT 2000, UMTS, CDMA2000, All-IP based cellular networks, WLL, Mobile data networks-CDPD, GPRS

Unit II: Multiple access techniques-TDMA, FDMA, spread spectrum technology-DS-SS, FH-SS, code division multiple access, generation of spreading sequences, SDMA, packet radio- protocols, CSMA protocols, reservation protocols

Unit III: Wireless LAN: evolution of WLAN, IEEE802.11 Physical layer, MAC layer, IEEE 802.11(a,b,e,f,g,h,i) Wireless routing protocols, Mobile IP, IPv4, IPv6, wireless TCP, TCP performance over wireless links, protocols for 3G and 4G networks, Integration of voice and data, VoIP applications

Unit III: WPAN and Geo-location systems: IEEE 802.15 WPAN, wireless geo-location technologies, geo-location standards channel coding, delay, cross layer techniques, .mobility management, radio resources and power management, mobility models, Satellite communication-parameters and configurations, capacity allocation,

Unit-IV: Bluetooth-overview, radio specifications, baseband specifications, link manager specifications, logical link control and adaptation protocol, IEEE802.15, Design of wireless modem, power efficiency, out of band radiation, UWB pulse transmission

Unit V: Issues in wireless networks-security and privacy issues, authentication in mobile networks, power management, energy awareness computing

Text Books:

- [1] William Stallings, “ Wireless communications and Networks,” PHI
- [2] Kaveh Pahlavan, Prashant Krishnamurthy, “ Principles of Wireless Networks” Pearson education 2002
- [3] Theodore S. Rappaport, “ Wireless Communication, Principles and Practice, PHI

References:

ADVANCED DIGITAL SIGNAL PROCESSING			
Course Code:	EC539	Credits:	4
No. of Lectures (Hrs/Week):	3+1T	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

UNIT I: Digital Filter Design

Amplitude and phase responses of FIR filters - Linear phase filters - Windowing techniques for design of Linear phase FIR filters - Rectangular, Hamming, Kaiser windows - frequency sampling techniques - IIR Filters - Magnitude response - Phase response - group delay - Design of Low Pass Butterworth filters (low pass) - Bilinear transformation - prewarping, impulse invariant transformation.

UNIT II: Discrete Random Signal Processing

Discrete Random Processes- Ensemble averages, stationary processes, Autocorrelation and Auto covariance matrices. Parseval's Theorem, Wiener-Khintchine Relation- Power Spectral Density- Periodogram Spectral Factorization, Filtering random processes, Low Pass Filtering of White Noise, Parameter estimation: Bias and consistency.

UNIT III: Spectrum Estimation

Estimation of spectra from finite duration signals, Non-Parametric Methods-Correlation Method, Periodogram Estimator, Performance Analysis of Estimators -Unbiased, Consistent Estimators- Modified periodogram, Bartlett and Welch methods, Blackman -Tukey method. Parametric Methods - AR, MA, ARMA model based spectral estimation, Parameter Estimation -Yule-Walker equations, solutions using Durbin's algorithm

Unit IV: Adaptive Filters

FIR adaptive filters -adaptive filter based on steepest descent method-Widrow-Hoff LMS adaptive algorithm, Normalized LMS. Adaptive channel equalization-Adaptive echo cancellation-Adaptive noise cancellation- Adaptive recursive filters (IIR), RLS adaptive filters-Exponentially weighted RLS-sliding window RLS.

UNIT V: Multirate Digital Signal Processing

Mathematical description of change of sampling rate - Interpolation and Decimation, Decimation by an integer factor - Interpolation by an integer factor, Sampling rate conversion by a rational factor, Filter implementation for sampling rate conversion- direct form FIR structures, Polyphase filter structures, time-variant structures, Multistage implementation of multirate system. Application to sub band coding - Wavelet transform and filter bank implementation of wavelet expansion of signals.

Text Books:

- [1] Monson H.Hayes, Statistical Digital Signal Processing and Modeling, John Wiley and Sons, Inc.,Singapore, 2002.
- [2] John G.Proakis, Dimitris G.Manolakis, Digital Signal Processing Pearson Education, 2002.
- [3] Rafael C. Gonzalez, Richard E.Woods, Digital Image Processing, Pearson Education, Inc., 2nd Edition, 2004.

References:

- [1] John G.Proakis et.al., Algorithms for Statistical Signal Processing, Pearson Education, 2002.
- [2] Dimitris G.Manolakis et.al., Statistical and adaptive signal Processing, McGraw Hill, Newyork,2000.

PROGRAMMING LAB			
Course Code:	CS591	Credits:	2
No. of Lab (Hrs/Week):	3	End Sem Exam Hours:	3
Total No. of Lab Sessions:	10		

List of Experiments

1. To find the sum of individual digits of a given number
2. To print the Fibonacci series for 1 to n value
3. To print a prime numbers up to 1 to n
4. To find the roots of the quadratic equation
5. To calculate the sum. $\text{Sum} = 1 - x^2/2! + x^4/4! - x^6/6! + x^8/8! - x^{10}/10!$
6. Programs that use recursive function to find the factorial of a given integer.
7. Factorial of a number is nothing but the multiplication of numbers from a given number to 1
8. Program that use non recursive function to find the factorial of a given integer.
9. Factorial of a number is nothing but the multiplication of numbers from a given number to 1
10. To find the GCD of two given integers by using the recursive function
11. To find the GCD of two given integers by using the non recursive function
12. WAP to read a series of words from terminal using scanf function.
13. WAP to read a line of text containing a series of words from the terminal.
14. WAP for all type of sorting.
15. WAP. to illustrate the comparison of structure variables
16. WAP to print the address of variable along with its value.
17. WAP to illustrate the use of pointers in arithmetic operation.
18. WAP to read data from keyboard, write it to a file called INPUT, again read the same data from the INPUT file, and display it on the screen.

DIGITAL COMMUNICATION LAB			
Course Code:	EC581	Credits:	2
No. of Lab (Hrs/Week):	3	End Sem Exam Hours:	3
Total No. of Lab Sessions:	10		

List of Experiments

1. To verify the sampling theorem.
2. To study ASK (Amplitude Shift Keying) System.
 - Modulate a digital signal using amplitude shift keying.
 - Demodulate a amplitude shift keyed signal.
3. To study FSK (Frequency Shift Keying) System.
 - Modulate a digital signal using frequency shift keying.
 - Demodulate a frequency shift keyed signal.
4. To study BFSK (Binary Frequency Shift Keying) System.
 - Modulate a digital signal using Binary Frequency shift keying.
 - Demodulate a Binary Frequency Shift keyed signal.
5. To study PSK (Phase Shift Keying) System.
 - Modulate a digital signal using phase shift keying.
 - Demodulate a phase shift keyed signal.
6. To study BPSK (Binary Phase Shift Keying) System.
 - Modulate a digital signal using binary phase shift keying.
 - Demodulate a binary phase shift keyed signal.
7. To study QPSK (Quadrature Phase Shift Keying) System.
 - Modulate a digital signal using Quadrature phase shift keying.
 - Demodulate a Quadrature phase shift keyed signal.
8. To study DPSK (Differential Phase Shift Keying) System.
 - Modulate a digital signal using differential phase shift keying.
 - Demodulate a differential phase shift keyed signal.
9. To study Pulse Code Modulation System (PCM) System.
 - Generate, modulate and transmit a pulse coded signal.
 - Receive and demodulate a pulse coded signal.
10. To study TDM (Time Division Multiplexing) System.
 - Generate and transmit a TDM signal.
 - Receive and de-multiplex a TDM signal.
11. To study M-ARY FSK modulation and demodulation.
12. To study and implement the cyclic redundancy check.
13. To study the circuit of PAM modulator and demodulator.
14. To study the circuit of PWM modulator and demodulator.
15. To study the circuit of PPM modulator and demodulator.

COMPUTER SYSTEM ARCHITECTURE			
Course Code:	CS536	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

UNIT I REGISTER TRANSFER AND MICROOPERATION

Register transfer language, bus and memory transfers, bus architecture, bus arbitration, arithmetic logic, shift microoperation, arithmetic logic shift unit.

UNIT II PROCESSOR DESIGN

Processor organization: general register organization, stack organization, addressing mode, instruction format, data transfer & manipulations, program control, reduced instruction set computer.

UNIT III INPUT-OUTPUT ORGANIZATION

I/O interface, synchronous and asynchronous data transfer, strobe, handshaking schemes, modes of transfer, interrupts & interrupt handling, direct memory access, input-output processor.

UNIT IV MEMORY ORGANIZATION

Memory hierarchy, main memory (RAM and ROM Chips), organization of 2^d and $2^{1/2d}$, auxiliary memory, cache memory, virtual memory, memory management hardware.

UNIT V INTRODUCTION TO PARALLELISM

Pipelining, Arithmetic pipeline, Instruction Pipeline, Flynn's classification of computer, Feng classification, Parallel Architecture, RISC & CISC, Interconnection Network Architecture

Reference Books:

1. Patterson, Computer Organisation and Design, Elsevier Pub. 2009
2. William Stalling, "Computer Organization", PHI
3. Vravice, Hamacher & Zaky, "Computer Organization", TMH
4. Mano, "Computer System Architecture", PHI
5. John P Hays, "Computer Organization", McGraw Hill
6. Tannenbaum, "Structured Computer Organization", PHI
7. P Pal chaudhry, 'Computer Organization & Design', PHI
8. Kai Hawng, Computer Architecture, TMH

2 Years M.Tech. (ICT) for Engineering Graduate

Effective from 2011-2012

DATA MINING			
Course Code:	CS576	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

UNIT I INTRODUCTION

Data warehousing, data warehouse vs relational databases, various schemas, architecture, Data mining definition & task, KDD versus data mining, data mining architecture, data mining query languages.

UNIT II DATA MINING TECHNIQUES

Clustering techniques, classification and regression rules, characterization and discrimination, outlier analysis, evolution analysis, association rules.

UNIT III DATA MINING RULES

The Apriori Algorithm, generating association rules, mining multilevel association rules, mining multidimensional association rules from relational databases, metarule-guided mining of association rules, constraint pushing, mining guided by rule constraints, Incremental mining.

UNIT IV CLUSTERING TECHNIQUES

Cluster analysis, partitioning methods: k -Means and k -Medoids, hierarchical methods, agglomerative and divisive hierarchical clustering, density- based methods, constraint based clustering.

UNIT V WEB MINING

Mining complex data objects, spatial databases, multimedia databases, time series and sequence data; mining text databases, web content mining, web structure mining, web usage mining, web mining techniques, role of agents in web mining.

Text Books:

1. Data Warehousing In the Real World, Sam Anahory & Dennis Murray, 1997.
2. Data Mining- Concepts & Techniques, Jiawei Han & Micheline Kamber, 2001, Morgan Kaufmann.

References:

1. Data Mining Techniques, Arun Pujar, University Press, Hyderabad, 2001.
2. Data Mining, Pieter Adriaans & Dolf Zantinge, Pearson, 1997.
3. Data Warehousing, Data Mining and OLTP; Alex Berson, 1997, Mc Graw Hill.
4. Data warehousing System; Mallach, Mc Graw Hill, 2000.

PRINCIPLES OF ARTIFICIAL INTELLIGENCE			
Course Code:	CS574	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

UNIT I INTRODUCTION TO ARTIFICIAL INTELLIGENCE

Basic concept of artificial intelligence (AI, development of logic, components of AI, mind body problem, Chinese room experiment, artificial life, parallel and distributed AI.

Concept of state space search, problem formulation from AI view point, representing the problems from AI viewpoint, production system, developing production rules, characteristics of production system, algorithm for problem solving using AI technique.

UNIT II SEARCH TECHNIQUES

Finding the solution using a search methodology, Use of search in AI problem solution, blind search techniques, heuristic search techniques, concept of heuristic knowledge, designing of the heuristic function, types of heuristic search techniques: generate and test, best first search, problem reduction using AND – OR graph, local search technique, properties of heuristic search techniques, overestimation and underestimation of heuristic function, hill climbing search.

UNIT III INTRODUCTION TO KNOWLEDGE

Basic concept of knowledge, types of knowledge: declarative knowledge, procedural knowledge, inheritable knowledge, inferential knowledge, relational knowledge, heuristic knowledge, commonsense knowledge, explicit knowledge, tacit knowledge.

UNIT IV INTRODUCTION TO LOGIC

Introduction, propositional calculus, syntax of propositional calculus, semantics of propositional calculus, well formed formula, properties of statements, inferencing of propositional logic, predicate logic, syntax of predicate logic, semantics of predicate logic, representation of facts.

UNIT V : Developemnt of expert system:

Basic components of expert system, funvtionality of expert system, identification of task domain knowledge, designing methodologies of expert system.

Text Book:

1. Artificial Intelligence, Elanie Reich: Tata mcgraw Hill publishing house, 2008.
2. Artificial intelligence, Peterson, TataMcGraw Hill, 2008.

Reference books:

3. Artificial intelligence, Russel and Norvig, Pearson Printice Hall Publication, 2006.
4. Artificial Intelligence, Winston, PHI publication, 2006.

ADVANCED COMPUTER NETWORKS			
Course Code:	EC541	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Principles Of Computer Communications

Introduction to data communications and networks, protocol architecture, networking protocols, reference models, TCP/IP and OSI model, standards organizations, data transmission signal propagation, bandwidth of a signal and a medium; data transmission rate and bandwidth, modes of data transmission, guided media and wireless transmission, multiplexing; types of multiplexing; FDM vs. TDM, switching.

Unit II: Data Link and Network Layer Issues

Design issues in data link and network layers, framing, error control, flow control, sliding window protocols, Medium Access Control Sub layer (MACS), Multiple access Ethernet, Wireless LAN, factors affecting routing algorithms, routing algorithm.

Unit III: Advanced Internetworking and Mobility

Internetworking, addressing, routing and routing algorithms, congestion control algorithms, IPv4, ICMP, ARP, IPv6 and ICMPv6 extensions and functionality, mobile IP, service integration and quality of service (QoS) in IP networks.

Unit IV: Advanced Transport Issues and Signaling

Transport services, element of transport protocols, TCP and UDP, RTP, performance issues, congestion control and QoS, techniques to improve QoS, integrated services, signaling for multi-constrained services and applications.

Unit V: Self Organizing Networks

Introduction to Adhoc, sensor and mesh networks, routing in these networks, socket programming, SMTP, HTTP, Remote login, DNS, FTP.

Text Books:

- [1] S. Tanenbaum, Computer Networks, 4th edition, Prentice Hall, 2008.
- [2] W. Stallings, "Data and Computer Communications, 8th edition, Prentice Hall, 2007.
- [3] Forouzan, "Data Communications and Networking, 4th edition, McGraw Hill, 2007.

Reference:

- [1] B.A. Forouzan, "TCP/IP Protocol Suite", TMH, 3rd edition, 2006.
- [2] Jochen H. Schiller, "Mobile Communications", Pearson Education, 2003.
- [3] H. Soliman, "Mobile IPv6 - Mobility in a Wireless Internet, Addison-Wesley, 2004.
- [4] Douglas E. Comer, "TCP/IP Principles, Protocols and Architecture", Pearson Education.

EMBEDDED SYSTEM DESIGN			
Course Code:	EC565	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Introduction to an embedded systems design

Introduction to Embedded system, Embedded System Project Management, ESD and Co-design issues in System development Process, Design cycle in the development phase for an embedded system, Use of target system or its emulator and In-circuit emulator, Use of software tools for development of an ES.

Unit II: RTOS & its overview

Real Time Operating System: Task and Task States, tasks and data, semaphores and shared Data Operating system Services-Message queues-Timer Function-Events-Memory Management, Interrupt Routines in an RTOS environment, basic design Using RTOS.

Unit III: Microcontroller

Role of processor selection in Embedded System (Microprocessor V/s Micro-controller), 8051 Microcontroller: Architecture, basic assembly language programming concepts, Instruction set, Addressing Modes, Logical Operation, Arithmetic Operations, Subroutine, Interrupt handling, Timing subroutines, Serial data transmission, Serial data communication

Unit IV: Embedded system development

Embedded system evolution trends. Round - Robin, robin with Interrupts, function-One-Scheduling Architecture, Algorithms. Introduction to-assembler-compiler-cross compilers and Integrated Development Environment (IDE). Object Oriented Interfacing, Recursion, Debugging strategies, Simulators.

Unit V: Networks for Embedded Systems

The I²C Bus, The CAN bus, SHARC link Ports, Ethernet, Myrinet, Internet, Introduction to Bluetooth: Specification, Core Protocol, Cable replacement protocol. IEEE 1149.1 (JTAG) Testability: Boundary Scan Architecture

Text Books:

- [1] Raj Kamal, Embedded Systems, TMH.
- [2] K.J. Ayala, "The 8051 Microcontroller", Penram International.
- [3] J. B. Peatman, "Design with PIC Microcontrollers", Prentice Hall

References

- [1] David E. Simon, "An Embedded Software Primer," Pearson Education
- [2] John Catsoulis, "Designing Embedded Hardware," O'reilly
- [3] Frank Vahid, Tony Givargis, "Embedded System Design," John Wiley & Sons, Inc
- [4] Karim Yaghmour, "Building Embedded Linux Systems", O'reilly
- [5] Michael Barr, "Programming Embedded Systems," O'reilly

ADVANCED DIGITAL CIRCUIT DESIGN			
Course Code:	EC535	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Introduction

Digital number systems and information representation; arithmetic operations, decimal and alphanumeric codes, Binary logic, Boolean algebra (identities, functions and manipulation), standard forms, simplification, Logic gates, switch-level and logic CMOS implementation, integrated circuits.

Unit II: Combinational Logic Design

Components of Combinational Design, Multiplexer and Decoder, Multiplexer Based Design of Combinational Circuits, Implementation of Full Adder using Multiplexer and Decoder, Types of PLD, Combinational Logic Examples, PROM - Fixed AND Array and Programmable OR Array Implementation of Functions using PROM, PLA, PAL, Comparison of PROM, PLA and PAL Implementation of a Function using PAL, Types of PAL Outputs, Device Examples

Unit III: Sequential Logic Design

Introduction to Sequential Circuits, R-S Latch and Clocked R-S Latch, D Flip Flop, J-K Flip Flop, Master Slave Operation, Edge Triggered Operation, Clocking of Flip-flops, Setup and Hold Times, Moore Circuit, Mealy Circuit Clocking Rules, Sequential Circuits – Design Rules, Sequential Circuit Design Basics, Design of a 4-bit Full Adder using D Flip-flop, Pattern Identifier, State Graph, Transition Table, Implementation of Pattern Identifier, MUX Based Realization, ROM Based Realization, PAL Implementation

Unit IV: Synchronous Design Using Programmable Devices

PLD families, ROMs, Logic array (PLA), Programmable array logic, GAL, bipolar PLA, NMOS PLA, PAL 14L4, Xilinx logic cell array, I/O Block, Programmable interconnect, Xilinx – 3000 series and 4000 series FPGAs, Altera CPLDs, Altera FLEX 10K series PLDs, Designing a synchronous sequential circuit using PLA/PAL, Realization of finite state machine using PLD

Unit V: System Design using HDL

HDL operators, Arrays, concurrent and sequential statements, packages, Data flow, Behavioral – structural modeling, compilation and simulation of HDL code, Test bench, Realization of combinational and sequential circuits using HDL, Registers, counters, sequential machine, serial adder, Multiplier-Divider, System Design examples.

Text Books:

- [1] Charles H. Roth Jr “Fundamentals of Logic Design” Thomson Learning 2004
- [2] J. Bhaskar, A Verilog Primer, BSP, 2003.
- [3] J. Bhaskar, A Verilog HDL Synthesis BSP, 2003

References:

- [1] Nripendra N Biswas “Logic Design Theory” Prentice Hall of India, 2001
- [2] Parag K. Lala “Digital system Design using PLD” B S Publications, 2003
- [3] Charles H Roth Jr. “Digital System Design using VHDL” Thomson learning, 2004
- [4] Douglas L. Perry “VHDL programming by Example” Tata McGraw Hill – 2006

ADVANCED COMMUNICATION NETWORKS			
Course Code:	EC402/EC532	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Digital switching systems-analysis, hierarchy, evolution, SPC, call processing, communication and control- level 1,2,& 3 control, interface control, network control processor, central processor, control architecture, multiplexed highways, switching fabric-space division switching, time division switching, STS, TST, TTT, switching system software-architecture, OS, database management

Unit II: MPLS-label stack and label distribution, traffic engineering, design of switching systems, and routers, switching networks-crossbar switches, multistage switches, shared memory switches, optical networks, DWDM techniques, IP over optical core switches

Unit III: Congestion control: integrated services, differentiated services, congestion control, congestion control in packet switching, frame relay congestion control, flow control at link level, TCP congestion control

Unit IV: Voice over IP: basic IP telephone system, digital voice sampling, compression techniques, protocol for VoIP, session initiation protocol

Unit V: Internetworking-connection mode network service, x.75 internetworking, network through ISDN, internetworking SNA, and x.25, x.300 internetworking standards, personal computer networking, data transmission in PTN, Data network standards, voice-data integration, fast packet switches

Text books:

- [1] Syed R. Ali, “ Digital Switching Systems, System Reliability and analysis, Tata McGraw-Hill
- [2] William Stallings, “ High Speed Networks and Internet” 2nd ed. Perason edu, 2005
- [3] Bellamy John, “ Digital Telephony” Wiley 3rd Ed, 2000
- [4] Viswanathan, T, "Telecommunications Switching Systems and Networks, “ PH

References:

- [1] Andrew S. Tanenbaum: Computer networks, PHI.
- [2] W. Stallings: Data and computer communications, MC, Milan.
- [3] Alberto Leon-Gercia, India Widjaja: Communication networks, fundamental concepts and key architecture, TATA McGraw Hill.
- [4] Bertsekas D. and Gallager R.: Data Networks, PHI.
- [5] Keshav S: An Engineering Approach to computer Networking, Addison Wesley.

WIRELESS SYSTEM DESIGN			
Course Code:	EC404/EC534	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit-I: RF system design procedures, methodology, link budget, propagation models, tower-top amplifiers, RF design guidelines, traffic projections, cell site design.

Unit-II: Network design- traffic forecasts, node dimensioning, interface design, placement of network nodes, topology.

Unit-III: Antenna systems,-base station antenna, cross pole antenna, dual band antenna, intelligent antenna, diversity techniques-derivation of selection diversity and maximal ratio combining improvement, polarization diversity, frequency diversity, time diversity, RAKE receiver. Communication sites-types, installation, towers, stealth, in-building and tunnel systems, inter, modulation, collocation.

Unit-IV: UMTS system design-design principles, coverage analysis, capacity analysis, radio access networks, overlaid UMTS over GSM, CDMA system design-design methodology, deployment guidelines, traffic estimation, radio elements, fixed network design requirements, traffic models, link budget, case studies-CDMA2000 1xRTT, EVDO, CDMA2000 1xRTT with EVDO overlay

Unit-V: Simulation of Wireless network systems-discrete event simulation, simulation models, performance evaluation and analysis of IEEE802.11 WLAN. Study of various network simulators, GloMoSim, ns-2, Opnet, design and performance evaluation of transport and routing protocols of mobile and wireless networks.

Text Books:

[1] Clint Smith, P.E. Daniel Collins, “ 3G Wireless Networks” Tata McGraw-Hill, 2nd Edition

[2] Theodore S. Rappaport, “ Wireless Communication, Principles and Practice” Pearson

[3] P. Nicopolitidis, M.S. Obaidat, G.I. Papadimitriou, A.S. Pomportsis, “ Wireless Networks”, John Wiley & Sons,

References:

IEEE Journals and proceedings

DESIGN LAB-1			
Course Code:	EC584	Credits:	2
No. of Lab (Hrs/Week):	3	End Sem Exam Hours:	3
Total No. of Lab Sessions:	10		

List of Experiments

1. Introduction to transmission media(CAT5, OFC, COAXIAL CABLE Wireless)
2. Introduces network interfaces(Wired and Wireless)
3. Configure and installing a Ethernet(10/100)
4. Performance evaluation of Ethernet(10/100)
5. Topology design(Ring, Bus,Star)
6. Generation of data packet and measurement(CBR, VBR, Poison)
7. Router configuration
8. Switch configuration
9. Server configuration
10. Congestion control of network
11. QoS of network
12. Protocols and the configuration
13. Wireless systems
14. Security (WEP, WPA)
15. Study about Qualnet

QUALITY OF SERVICES IN NETWORKS			
Course Code:	EC406/EC542	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: IP Quality Of Service

Level of QoS, IP QoS history, performance measures, QoS functions, layer 2 QoS technologies, multiprotocol label switching, end-to-end QoS.

Unit II: QOS Architectures

Intserv architecture; RSVP, reservation style, service types, RSVP media support, RSVP scalability, along with their case studies. Diffserv architecture; network boundary traffic conditioners, PHB, resource allocation policy, packet classification.

Unit III: Network Boundary Traffic Conditioners and Resource Allocation

Packet classification, packet marking, need of traffic rate management, traffic policing, traffic shaping along with their case studies. Scheduling of QoS support, sequence number computation based WFQ, flow based WFQ, flow based distributed DWFQ, class based WFQ, priority queuing, schedule mechanisms for voice traffic, MWRR, MDRR along with their case studies.

Unit IV: Congestion Avoidance

TCP slow start and congestion avoidance, TCP traffic behavior in a trial drop scenario, REDproactive queue management for congestion avoidance, WRED, flow WRED, ECN, SPD along with their case studies.

Unit V: QOS in MPLS-Based Networks

MPLS, MPLS with ATM, MPLS QoS, MPLS VPN, MPLS VPN QoS along with their case studies. traffic engineering; MPLS traffic engineering, the layer 2 overlay model, RRR, TE trunk definition, TE tunnel attributes, link resource attributes, distribution of link resource information, path selection policy, TE tunnel setup, link admission control, TE path maintenance, TE RSVP, IGP routing protocols, TE approaches along with their case studies.

Text Books:

- [1] Srinivas Vegesna, "IP Quality of Service," CISCO PRESS, 2001.
- [2] Santiago alvarez, "Qos for IP/MPLS Networks," Cisco Press, Pearson Education, 2006.

References:

- 1. IETF website: www.ietf.org

VLSI FOR WIRELESS COMMUNICATION			
Course Code:	EC408/EC566	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Introduction

Standards, Communication Systems, Overview of Modulation Scheme, QPSK, BPSK, MSK, Classical Channel, Wireless Channel Description, Path Environment, Path Loss, Friss Equation, Time Varying Channel Models, Envelope Fading, Frequency Selective Fading, Fast Fading.

Unit II: Receiver Architectures

Introduction, Receiver Front: Motivations, Heterodyne and other Architecture, Filter Design: Band Selection Filter, Image Rejection Filter, Channel Filter, Nonidealities and Design Parameters: Nonlinearity, Noise, Derivation of NF, IIP₃ of receiver Front end.

Unit III: Low Noise Amplifier (LNA)

Introduction, Matching Networks, Wideband LNA Design, DC bias, Gain and Frequency Response, Noise Figure, Narrowband LNA, Impedance Matching, Interpretation of Power Matching, Quality Factor, Core Amplifier, Noise Figure, Power Dissipation, Noise Contribution from other sources.

Unit IV: Active Mixers

Unbalanced Mixers, Single Balanced Mixers, Qualitative Description of Gilbert Mixer, Conversion Gain, Distortion analysis of Gilbert Mixer, Comparison of Sample and Hold Circuit and Sampling Mixer.

Unit V: Passive Mixers

Switching Mixers, Distortion in Unbalanced Switching Mixer, Conversion Gain in Unbalanced Switching Mixer, Noise in Unbalanced Switching Mixer, Sampling Mixer, Conversion Gain in Single Ended Sampling Mixer, Distortion in single ended sampling mixer, Intrinsic and Extrinsic noise.

Text Books:

- [1] Bosco Leung, "VLSI for Wireless Communication", PHI.
- [2] David Tsee, Pramod Viswanath, "Fundamentals of Wireless Communication", Cambridge Univ Press.

References:

- [1] Emad N Farag, M.I Elmasry, "Mixed Signal VLSI Wireless Design Circuits and Systems", Kluwer Publication.

VLSI TECHNOLOGY			
Course Code:	EC410/EC536	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Crystal Growth, Wafer Preparation, Epitaxy and Oxidation

Electronic Grade Silicon, Czochralski crystal growing, Silicon Shaping, processing consideration, Vapor phase Epitaxy, Molecular Beam Epitaxy, Silicon on Insulators, Epitaxial Evaluation, Growth Mechanism and kinetics, Thin Oxides, Oxidation Techniques and Systems, Oxide properties, Redistribution of Dopants at interface, Oxidation of Poly Silicon, Oxidation induced Defects.

Unit II: Lithography and Relative Plasma Etching

Optical Lithography, Electron Lithography, X-Ray Lithography, Ion Lithography, Plasma properties, Feature Size control and Anisotropic Etch mechanism, relative Plasma Etching techniques and Equipments

Unit III: Deposition, Diffusion, Ion Implantation and Metallization

Deposition process, Polysilicon, plasma assisted Deposition, Models of Diffusion in Solids, Flick's one dimensional Diffusion Equation – Atomic Diffusion Mechanism –Measurement techniques - Range theory- Implant equipment. Annealing Shallow junction – High energy implantation – Physical vapour deposition – Patterning.

Unit IV: Process Simulation and VLSI Process Integration

Ion implantation – Diffusion and oxidation – Epitaxy – Lithography – Etching and Deposition- NMOS IC Technology – CMOS IC Technology – MOS Memory IC technology - Bipolar IC Technology – IC Fabrication.

Unit V: Assembly Techniques and packaging of VLSI Devices

Analytical Beams – Beams Specimen interactions - Chemical methods – Package types – banking design consideration – VLSI assembly technology – Package fabrication technology.

Text Books:

- [1] S.M.Sze, "VLSI Technology", Mc.Graw.Hill Second Edition. 2002.
- [2] Douglas A. Pucknell and Kamran Eshraghian, "Basic VLSI Design", Prentice Hall India, 2003.

References:

- [1] Amar Mukherjee, "Introduction to NMOS and CMOS VLSI System design, Prentice Hall India, 2000.
- [2] Wayne Wolf, "Modern VLSI Design", Prentice Hall India.1998.

ADVANCED RF ENGINEERING			
Course Code:	EC412/EC544	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: INTRODUCTION TO RF ELECTRONICS

Introduction to RF Design and Wireless Technology: Design and Applications,

Unit II: COMPLEXITY AND CHOICE OF TECHNOLOGY

Basic concepts in RF design: Nonlinearly and Time Variance, Inter symbol interference, random processes and noise. Sensitivity and dynamic range, conversion of gains and distortion

Unit III: RF MODULATION

Analog and digital modulation of RF circuits, Comparison of various techniques for power efficiency, Coherent and non-coherent detection, Mobile RF communication and basics of Multiple Access techniques. Receiver and Transmitter architectures, direct conversion and two-step transmitters. RF Testing: RF testing for heterodyne, Homodyne, Image reject, Direct IF and sub sampled receivers.

Unit IV: BJT AND MOSFET BEHAVIOR AT RF FREQUENCIES

BJT and MOSFET behavior at RF frequencies, modeling of the transistors and SPICE model, Noise performance and limitations of devices, integrated parasitic elements at high frequencies and their monolithic implementation Overview of RF Filter design, Active RF components & modeling,

Unit V: RF CIRCUITS DESIGN

Matching and Biasing Networks. Basic blocks in RF systems and their VLSI implementation, Low noise Amplifier design in various technologies, Design of Mixers at GHz frequency range, Various mixers- working and implementation. Oscillators- Basic topologies VCO and definition of phase noise, Noise power and trade off. Resonator VCO designs, Quadrature and single sideband generators. Radio frequency Synthesizers- PLLS, Various RF synthesizer architectures and frequency dividers, Power Amplifier design, Linearization techniques, Design issues in integrated RF filters.

Text Books :

- [1] B. Razavi, "RF Microelectronics" PHI 1998
- [2] R. Jacob Baker, H.W. Li, D.E. Boyce "CMOS Circuit Design, layout and Simulation", PHI 1998.

References:

- [1] Thomas H. Lee "Design of CMOS RF Integrated Circuits" Cambridge University press 1998.
- [2] Y.P. Tsividis, "Mixed Analog and Digital Devices and Technology", TMH 1996
- [3] David M. POZAR: Microwave Engineering. - John Wiley & Sons - 2nd Edition (2003).

PROBABILITY AND STOCHASTIC PROCESSES			
Course Code:	EC424/EC548	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I:

Probability models, Algebra of events, probability axioms, conditional probability, Baye's rules, Bernoulli traits. **Discrete Random Variables:** Discrete random variables, probability mass functions, discrete distribution functions-Bernoulli, Binomial, geometric, Poisson, hyper geometric & uniform distributions, probability generating function.

Unit II:

Continuous Random variable: Exponential distribution, memory less property, application to reliability, hypo exponential, Erlang, Gamma, hyper exponential & Normal distributions, order statistics, distribution of sums.

Unit III:

Stochastic Process, Classification, Discrete and continuous time markov chain, Poisson process, renewal process, little's formula, Erlang Loss Model, M/M/1 Queue, M/M/m Queue Multidimensional Queue.

Unit IV:

Solution Techniques: Steady-State Solutions of Markov Chains, Solution for a Birth Death Process, Matrix-Geometric Method: Quasi-Birth-Death Process, Heisenberg Matrix: Non-Markovian Queues, Transient analysis, stochastic Petri nets, Numerical Solution: Direct Methods, Numerical Solution: Iterative Methods, Comparison of Numerical Solution Methods, Performance Measures,

Unit V:

Queueing Networks. Definitions and Notation. Performance Measures. Product-Form Queueing Networks. Algorithms for Product-Form Networks, priority Networks.

Text Books:**Reference Books:**

- [1] Research Methodologies, R. Panneerselvam, Prentice Hall, 2007.
- [2] Research in Education, Best John V. and James V Kahn, Wiley eastern, 2005.
- [3] Elements of Educational Research, Sukhia, S.P., P.V. Mehrotra, and R.N. Mehrotra, PHI publication, 2003.
- [4] Methodology of Research Education, K. Setia, IEEE publication, 2004.
- [5] Research methodology, Methods and Techniques, Kothari, C.R., 2000.

ADVANCED MICROWAVE COMMUNICATION			
Course Code:	EC426/EC550	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Microwave And Millimeter Wave Devices

Overview of microwave and millimeter wave vacuum tube devices, limitations of microwave vacuum tubes, gyrotron vacuum tube devices. Advances in microwave and millimeter wave solid state devices, Gunn devices, oscillator using, Gunn diode, and injection locked oscillators, IMPATT devices, and microwave and mm wave performance of IMPATT. Other solid state devices like Tunnel diode, BARITT and TRAPAT. Microwave and mm wave circuits:

Unit II: Microwave and MM Wave Circuits

Review of scattering matrix concept in the light of vector network analyzer, impedance matching network, couplers, power dividers, resonators and filters. Detectors, mixers, attenuators, phase shifters, amplifier and oscillator, Ferrite based circuits.

Unit III: Antennas

Hertzian dipole, loop antenna, helical antenna, frequency independent antenna: Du0Hamel principle, log spiral and log periodic dipole antenna array. Babinet principle, waveguide slot antenna, microstrip antenna, horn antenna, parabolic reflector. Antenna arrays and phased array antenna.

Unit IV: Satellite Communication

Orbital parameters, satellite trajectory, period, geostationary satellites, non-geostationary constellations. Communication satellites – Space craft subsystems, payload – repeater, antenna, attitude and control systems, telemetry, tracking and command, power sub system and thermal control. Earth stations antenna and feed systems, satellite tracking system, amplifiers, fixed and mobile satellite service earth stations. Terrestrial: line of sight transmission, relay towers and distance considerations. Communication link design: Frequency bands used, antenna parameters, transmission equations, noise considerations, link design, propagation characteristics of fixed and mobile satellite links, channel modeling, very small aperture terminals (VSAT), VSAT design issues.

Unit V: Microwave and MM Wave Propagation.

Overview of basic radio wave propagation mechanisms, Friis transmission formula, plane earth propagation model, troposcatter systems, ionosphere propagation, duct propagation, microwave radio link and calculation of link budget. Effect on radio wave propagation due to rain, fog, snow, ice, atmospheric gases, Earth's magnetic field.

Text Books:

- [1] David M Pozar, Microwave Engineering, John Wiley & Sons
- [2] R E Collin, Antenna & Radio wave Propagation, McGraw Hill Book Co.

References:

- [1] M Richharia: 'Satellite Communication Systems'.(Second Ed.),Macmillan Press Ltd.
- [2] Ferdo Ivanek (Editor): 'Terrestrial Digital Microwave Communications', Artech House
- [3] E. Hund: 'Microwave Communications', IEEE Press
- [4] Jordan & Balman, Electromagnetic waves & Radiating System
- [5] R E Collin, Microwave Engineering, McGraw Hill CO.

NETWORKING PROTOCOLS			
Course Code:	EC414/EC552	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Protocols of Physical Layer

Overview of physical layer operations of wired and wireless networks, Protocol Architecture: IEEE 802.3 Local Area Network Protocols, VLAN- Virtual Local Area Network, WLAN-Wireless LAN by IEEE 802.11 Protocols, FDDI-Fiber Distributed Data Interface, Token Ring- IEEE 802.5 LAN Protocol, DQDB-Distributed Queue Dual Bus(IEEE 802.6), SMDS- Switched Multimegabit Data Service, WiMAX- Broadband Wireless MAN (IEEE802.16).

Unit II: Protocols of Data Link Layer

Overview of data link layer operations of wired and wireless networks, medium access control protocols including CSMA, collision free protocols, WDMA protocols, wireless LAN protocols, ARP, RARP, X.25.

Unit III: Protocols of Network Layer

Overview of network layer operations of wired and wireless networks, IP-Internet Protocol (IPv4), IPv6-Internet Protocol version 6, ICMP & ICMPv6-Internet Message Control Protocol and ICMP version 6, BGP (BGP-4)-Border Gateway Protocol, EGP- Exterior Gateway Protocol, IRDP- ICMP Router Discovery Protocol, Mobile IP-IP Mobility Support Protocol for IPv4 & IPv6, NHRP- Next Hop Resolution Protocol, OSPF- Open Shortest Path First Protocol, RIP- Routing Information Protocol (RIP2), RIPng- Routing Information Protocol next generation for IPv6, RSVP- Resource ReSerVation Protocol, VRRP- Virtual Router Redundancy Protocol, BGMP-Border Gateway Multicast Protocol, DVMRP- Distance Vector Multicast Routing Protocol, IGMP- Internet Group Management Protocol, MARS- Multicast Address Resolution Server, MBGP-Multiprotocol BGP, MOSPF- Multicast Extensions to OSPF MSDP-Multicast Source Discovery Protocol, PGM-Pragmatic General Multicast Protocol, PIM-DM- Protocol Independent Multicast-Dense Mode, PIM-SM-Protocol Independent Multicast - Sparse Mode, MPLS-Multiprotocol Label Switching, RSVP-TE-Resource Reservation Protocol.

Unit IV: Protocols of Transport Layer

Overview of transport layer operations, Protocols: TCP-Transmission Control Protocol, UDP -User Datagram Protocol, RUDP- Reliable User Datagram Protocol, SCTP- Stream Control Transmission Protocol, CUDP-Cyclic UDP, DCCP-Datagram Congestion Control Protocol, FCP-Fiber Channel Protocol, IL-IL Protocol, NBF- NetBIOS Frames protocol, µTP-Micro Transport Protocol, RSVP, ECN, H.323, H.261- Video CODEC for Low Quality Videoconferencing, H.263: Video CODEC for Medium Quality Videoconferencing, H.264 / MPEG-4: Video CODEC For High Quality Video Streaming wireless TCP and UDP.

Unit V: Protocols of Application Layer

Overview of application layer operations, Protocols: DNS, SMTP, HTTP, BGP, DHCP, FTP, LDAP, MGCP, NNTP, NTP, POP, RIP, RPC, RTP, SIP, SNMP, SSH, Telnet, XMPP, security protocols.

Text Books:**References:**

- [1] Internetworking with TCP/IP Volume 1: Principles Protocols, and Architecture, Douglas Comer and Prentice Hall, fifth edition, 2006.
- [2] TCP/IP Protocol Suite, B.A. Forouzan, TMH, 3rd edition, 2006.
- [3] Computer Networks, S. Tanenbaum, 4th edition, Prentice Hall, 2003.
- [4] Network Protocols: Signature Edition, Matthew G. Naugle. Mcgraw-Hill Signature Series.
- [5] Network Protocols Handbook, Javlin, 4th edition, Javvin Technologies, Inc.

MOBILE COMPUTING			
Course Code:	EC416/EC554	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Mobile Computing Architecture

Introduction to Mobile Communications and Computing : novel applications, limitations, and architecture. GSM: Mobile services, System architecture, Radio interface, Protocols, Localization and calling, Handover, Security, and New data services.

Unit II: Client Programming

Desktop vs. mobile, hardware overview, mobile phones, PDA, design constraints in applications for handheld devices.

Unit III: Programming For the Palm OS

History of palm OS, palm OS architecture, application development, communication in palm OS, multimedia, and enhancements in the current release. Wireless devices with symbian OS; Introduction to Symbian OS, Symbian OS architecture, application for Symbian, control and compounds controls, active objects, localization, security on the Symbian OS, different flavors of windows CE, windows CE architecture, windows CE development environment.

Unit IV: Wireless Application Protocol (WAP)

Overview of the WAP, component of the WAP standards, protocol architecture, and treatment of protocols of all layers, services supporting WAP client, WAP architecture design principle, Bluetooth, J2ME.

Unit V: Special Topics in Mobile Computing

Mobile agent & its application, mobile data management, security framework for mobile environment, m-commerce: emerging applications, different players in m-commerce, m-commerce life cycle, mobile financial services, mobile entertainment services, and proactive service management.

Text Books:

- [1] Mobile Computing, Asoke. K Talukder and Roopa R. Yavagal, TMH, 2005
- [2] Mobile Communication, Jachan Schiller, Adison-Wesley, 2nd edition, 2003.

References:

- [1] Wireless Communication: T. S. Rappapost, Peasson Education, New Delhi, 2001
- [2] Wireless Networks: Kareh Pallavan & P. Krishnamurthy, Peasson Education, New Delhi, 2nd edition, 2004.
- [3] Mobile Commerce and Applications, Upkar Varshney, A tutorial at IEEE International Conference on Wireless Communications (WCNC), 1999.

IMAGE PROCESSING AND BIOMETRICS			
Course Code:	EC418/EC556	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Image Processing and Image Enhancement

Introduction, An image model, sampling & quantization, Basic relationships between Pixels, imaging geometry, Properties of 2 – D Fourier transform, FFT algorithm and other separable image transforms, Components of an image processing System., Digital Image Fundamentals- elements of visual perception, image sensing and acquisition, Basic relationships between pixels Fundamentals of spatial filtering, smoothing spatial filters (linear and non-linear).

Unit II: Pattern Recognition

Fundamentals of Pattern Recognition , Recognition Measurement, Errors, and Statistics, Recognition measurement and testing, Identification System Errors and Performance Testing, Computer Security, Bayesian decision theory, Multilayer neural networks, Unsupervised Learning and Clustering.

Unit III: Biometrics

Introduction to Biometrics, Fingerprints: Ridges, Furrows, Types of Fingerprints, Image and Signal Processing, Biometric Signal Processing, Other Biometric Modalities, Comparing Biometrics, Passwords, and Tokens, Multimodal Biometrics, Biometric Resources and Standards, Large Scale Biometrics and Systems Case Studies.

Unit IV: Analysis in Biometrics

Large-Scale Biometric Identification: Challenges and Solutions, Issues Involving the Human Biometric Sensor Interface, Fundamentals of Biometric-Based Training System Design, Biometric Systems and Applications, Force Field Feature Extraction for Fingerprint Biometrics, Behavioral Biometrics for Online Computer User Monitoring.

Unit V: Synthesis in Biometrics:

Introduction to Synthesis in Biometrics, Local B-Spline Multiresolution with Example in Iris Synthesis and Volumetric Rendering, image smoothing filters (Butterworth and Guassian low pass filters), image sharpening filters (Butterworth and Guassian high pass filters), selective filtering, Computational Geometry and Image Processing in Biometrics: On the Path to Convergence,, A Statistical Model for Biometric Verification.

Text Books:

[1] Practical Algorithms for Image Analysis: Description, Examples, and Code, Seul, O’Gorman, Sammon, 2000.

[2] S. Annadurai and R. Shanmugalakshmi: Fundamentals of Digital Image Processing, Pearson Education.

[3] R. C. Gonzalez and R. E. Woods: Digital Image Processing, 3rd Edition, Pearson Education.

[4] A. K. Jain: Fundamentals of Digital Image Processing, PHI Learning.

References:

[1] M. Sonka, V. Hlavac and R. Boyle: Digital Image Processing and Computer Vision: Cengage Learning.

[2] B. Chanda and D. D. Majumder: Digital Image Processing and Analysis, PHI Learning.

[3] S. Jayaraman, S. Esakkirajan and T. Veerakumar: Digital Image Processing, TMH.

AD-HOC WIRELESS NETWORKS			
Course Code:	EC513/EC649	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I : Introduction to Wireless Communication

Electromagnetic spectrum, radio propagation mechanisms, characteristics of the wireless channel, modulation techniques, multiple access techniques, voice coding, error control, computer network software, computer network architecture, IEEE 802 networking standards, wireless networks, issues in ad hoc wireless networks, Ad Hoc, wireless Internet.

Unit II: MAC Protocols For Wireless Networks

Issues in designing a MAC protocol for ad hoc wireless networks, design goals of a MAC protocol for ad hoc wireless networks, classifications of MAC protocols, contention-based protocols, contention-based protocols with reservation mechanisms, contention-based MAC protocols with scheduling mechanisms.

Routing Protocols for Ad Hoc Wireless Network

Issues in designing a routing protocol for ad hoc wireless networks, classifications of routing protocols, table-driven routing protocols, on-demand routing protocols, hybrid routing protocols, routing protocols with efficient flooding mechanisms, hierarchical routing protocols, power-aware routing protocols.

Unit III: Multicast Routing In Ad Hoc Wireless Networks

Issues in designing a multicast routing protocol, operation of multicast routing protocol, an architecture reference model for multicast routing protocol, classifications of multicast routing protocol, tree based multicast routing protocol, mesh-based multicast routing protocol, energy-efficient multicasting, multicasting with QoS guarantees, application- dependent multicast routing.

Unit IV: Transport Layer And Security Protocols For Ad Hoc Wireless Networks

issues in designing a transport layer protocol for ad hoc wireless networks, design goals of a transport layer protocol, classification of transport layer solutions, TCP over ad hoc wireless networks, other transport layer protocol for ad hoc wireless networks, security in ad hoc wireless networks, network security requirements, issues and challenges in security provisioning, network security attacks, key management, secure routing in ad hoc wireless networks.

Unit V: Quality of Service In Ad Hoc Wireless Networks

Issues and challenges in providing QoS in ad hoc wireless networks, classifications of QoS solutions, MAC layer solutions, network layer solutions, QoS frameworks for ad hoc wireless networks. energy management in ad hoc wireless networks, need for energy management in ad hoc wireless networks, classification of energy management schemes, battery management schemes, transmission power management schemes, system power management schemes.

Text Books:

[1] C. Siva Ram Murthy, B.S. Anoj, "Ad Hoc Wireless Networks, Architectures and Protocols", Prentice Hall, 2004.

[2] C. K. Toh, "Ad Hoc Mobile Wireless Networks 1/e: Protocols and Systems, "PH PTR, 2001.

References:

[1] Hekmat Ramin, "Ad-hoc Networks: Fundamental Properties and Network Topologies", Springer Publication.

[2] Mohammad Ilyas, "The Handbook of Ad Hoc Wireless Networks", CRC Press

CMOS RF CIRCUIT DESIGN			
Course Code:	EC428/EC576	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Introduction to RF design and Wireless Technology

Design and Applications, Complexity and Choice of Technology. Basic concepts in RF design: Nonlinearly and Time Variance, Intersymbol interference, random processes and noise. Sensitivity and dynamic range, conversion of gains and distortion.

Unit II: RF Modulation

Analog and digital modulation of RF circuits, Comparison of various techniques for power efficiency, Coherent and non-coherent detection, Mobile RF communication and basics of Multiple Access techniques. Receiver and Transmitter architectures. Direct conversion and two-step transmitters.

Unit III: RF Testing

RF testing for heterodyne, Homodyne, Image reject, Direct IF and sub sampled receivers.

Unit IV: BJT and MOSFET Behavior at RF Frequencies

BJT and MOSFET behavior at RF frequencies, Modeling of the transistors and SPICE model, Noise performance and limitations of devices, integrated parasitic elements at high frequencies and their monolithic implementation

Unit V: RF Circuits Design

Overview of RF Filter design, Active RF components & modeling, Matching and Biasing Networks. Basic blocks in RF systems and their VLSI implementation, Low noise Amplifier design in various technologies, Design of Mixers at GHz frequency range, Mixers- working and implementation. Oscillators- Basic topologies VCO and definition of phase noise, Noise power and trade off. Resonator VCO designs, Quadrature and single sideband generators. Radio frequency Synthesizers- PLLS, Various RF synthesizer architectures and frequency dividers, Power Amplifier design, Liberalization techniques, Design issues in integrated RF filters.

Text Books:

- [1] Thomas H. Lee “Design of CMOS RF Integrated Circuits” Cambridge University press 1998.
- [2] Y.P. Tsividis “Mixed Analog and Digital Devices and Technology” TMH 1996

References:

- [1] B. Razavi “RF Microelectronics” PHI 1998
- [2] R. Jacob Baker, H.W. Li, D.E. Boyce “ CMOS Circiut Design, layout and Simulation” PHI 1998

PRINCIPLES OF MEMS DESIGN			
Course Code:	EC422/EC570	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Introduction to MEMS

MEMS and Microsystems, Miniaturization, Typical products, Micro sensors, Micro actuation, MEMS with micro actuators, Micro-accelerometers and Micro fluidics, MEMS materials, Micro fabrication

Unit II: Mechanics for MEMS Design

Elasticity, Stress, strain and material properties, Bending of thin plates, Spring configurations, torsional deflection, Mechanical vibration, Resonance, Thermo mechanics – actuators, force and response time, Fracture and thin film mechanics.

Unit III: Electrostatic Design

Electrostatics: basic theory, electro static instability. Surface tension, gap and finger pull up, Electro static actuators, Comb generators, gap closers, rotary motors, inch worms, Electromagnetic actuators. bistable actuators.

Unit IV: Circuit and System Issues

Electronic Interfaces, Feedback systems, Noise, Circuit and system issues, Capacitive Accelerometer, Piezoelectric pressure sensor, Modeling of MEMS systems, CAD for MEMS.

Unit V: Introduction to Optical and RF MEMS

Optical MEMS, - System design basics – Gaussian optics, matrix operations, resolution. Case studies, MEMS scanners and retinal scanning display, Digital Micro mirror devices. RF Memes – design basics, case study – Capacitive RF MEMS switch, performance issues.

Text Books:

- [1] Stephen Santuria, "Microsystems Design", Kluwer publishers, 2000.
- [2] Nadim Maluf, "An introduction to Micro electro mechanical system design", Artech House, 2000.

References:

- [1] Mohamed Gad-el-Hak, editor, "The MEMS Handbook", CRC press Baco Raton, 2000.
- [2] Tai Ran Hsu, "MEMS & Micro systems Design and Manufacture" Tata McGraw Hill, New Delhi, 2002.

MULTIMEDIA TECHNIQUES			
Course Code:	CS447/CS547	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Introduction

Introduction to Multimedia, Multimedia Information, Multimedia Objects, Multimedia in business and work. Convergence of Computer, Communication and Entertainment products

Stages of Multimedia Projects

Multimedia hardware, Memory & storage devices, Communication devices, Multimedia software's, presentation tools, tools for object generations, video, sound, image capturing, authoring tools, card and page based authoring tools.

Unit II: Multimedia Building Blocks

Text, Sound MIDI, Digital Audio, audio file formats, MIDI under windows environment Audio & Video Capture.

Unit III: Data Compression

Huffman Coding, Shannon Fano Algorithm, Huffman Algorithms, Adaptive Coding, Arithmetic Coding Higher Order Modeling. Finite Context Modeling, Dictionary based Compression, Sliding Window Compression, LZ77, LZW compression, Compression, Compression ratio loss less & lossy compression.

Unit IV: Speech Compression & Synthesis

Digital Audio concepts, Sampling Variables, Loss less compression of sound, loss compression & silence compression.

Unit V: Images

Multiple monitors, bitmaps, Vector drawing, lossy graphic compression, image file formatting animations Images standards, JPEG Compression, Zig Zag Coding, Multimedia Database .Content based retrieval for text and images, **Video:** Video representation, Colors, Video Compression, MPEG standards, MHEG Standard Video Streaming on net, Video Conferencing, Multimedia Broadcast Services, Indexing and retrieval of Video Database, recent development in Multimedia.

Text Books:**Reference:**

- [1] Tay Vaughan "Multimedia, Making IT Work" Osborne McGraw Hill.
- [2] Buford "Multimedia Systems" Addison Wesley.
- [3] Agrawal & Tiwari "Multimedia Systems" Excel.
- [4] Mark Nelson "Data Compression Book" BPB.
- [5] David Hillman "Multimedia technology and Applications" Galgotia Publications.
- [6] Rosch "Multimedia Bible" Sams Publishing.
- [7] Sleinreiz "Multimedia System" Addison Wesley.
- [8] James E Skuman "Multimedia in Action" Vikas.

SOFT COMPUTING			
Course Code:	CS449/CS561	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Fuzzy Logic

Introduction to fuzzy logic, classical and fuzzy sets, overview of fuzzy sets, membership function, fuzzy rule generation, operations on fuzzy sets: compliment, intersection, union, combinations on operations, aggregation operation.

Unit II: Fuzzy Arithmetic

Fuzzy numbers, linguistic variables, arithmetic operations on intervals & numbers, uncertainty based information, information and uncertainty, no specificity of fuzzy and crisp sets, fuzziness of fuzzy sets.

Unit III: Neural Network

Overview of biological neurons, computational neuron, mathematical model of neurons, ANN architecture, single layer and multilayer architectures, activation function, threshold value, self learning and forced learning algorithms, feed forward and feedback architectures.

Unit IV: Learning Fundamentals

Learning paradigms, supervised and unsupervised learning, reinforced learning, ANN training, algorithms perceptions, training rules, delta, back propagation algorithm, multilayer perception model, Hopfield networks, associative memories, applications of artificial neural networks,

Unit V: Genetic Algorithms

History of genetic algorithm, terminology of genetic algorithm, biological background, creation of offspring, working principles of genetic algorithms, fitness function, reproduction: Roulette wheel selection, Boltzmann selection, cross over mutation, inversion, deletion, and duplication, generation cycle. Concept of Uncertainty: Presence of uncertainty in real world problems, handling uncertain knowledge, degree of belief, degree of disbelief, uncertainty and rational decisions, decision theory, utility theory, concept of independent events, Bay's rule, using Bay's rule for combining events.

Text Books:**References:**

- [1] Artificial Neural Networks: An introduction to ANN Theory and Practice, Peteus J. Braspenning, PHI publication, 2005.
- [2] Fuzzy Logic: A spectrum of Theoretical and Practical issues, Paul P. Wang, pearson publication 2004.
- [3] Fuzzy Sets, Fuzzy logic, and Fuzzy Systems: Selected Papers- Lotfi Asker Zadeh, George J. Kilr, Bo yuan, 2005.
- [4] Foundations of Fuzzy logic and Soft Computing: 12th International Fuzzy conference proceeding, 2005.
- [5] Neural Networks Theory, Particia Melin, Oxford University press, 2003
- [6] Neural Networks Theory and Application, Oscar Castillo, Wiley Eastern publication 2003.

OPTICAL NETWORKS			
Course Code:	EC562	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

UNIT I: FIBER OPTIC GUIDES

Light wave generation systems, system components, optical fibers, SI, GI fibers, modes, Dispersion in fibers, limitations due to dispersion, Fiber loss, non linear effects, Dispersion shifted and Dispersion flattened fibers

UNIT II: OPTICAL SOURCES AND DETECTORS

Basic concepts, LED's structures spectral distribution, semiconductor lasers, gain coefficients, modes, SLM and STM operation, Transmitter design, Receiver PIN and APD diodes design, noise sensitivity and degradation, Receiver amplifier design. Basic concepts, Semiconductor laser amplifiers, Raman - and Brillouin - fiber amplifiers, Erbium doped – fiber amplifiers, pumping phenomenon, LAN and cascaded in-line amplifiers.

UNIT III: POWER LAUNCHING AND COUPLING

Output patterns, Power coupling, Power launching, Equilibrium Numerical Aperture, Laser diode to fiber coupling, LED coupling to single mode fibre, Fiber Splicing techniques, splicing single mode fibers. Multimode fiber joints and single mode fiber joints, Fibre alignment and joint loss.

UNIT IV: OPTICAL NETWORKS

Point-to- point links, System considerations, Link considerations, Overall fiber dispersion in multi mode and single mode fibers, Rise time considerations, Distance consideration in optical transmission system, Network topologies, SONET/SDH, Protection in SONET/SDH, WDM Principles, WDM networks, DWDM systems

UNIT V: ACCESS NETWORKS

Network Architecture overview, Enhanced HFC, Fiber to the curb, PON Evolution, Optical Code Division Multiplexing, Line coding in Optical links, Soliton communication system, photonics, High Speed Networks.

Text Books:

- [1] Rajiv Ramaswami , “Optical Networks” Third Edition , 2008.
- [2] Djafar K Mynbaev, Lowell L Scheiner , “Fiber- Optic Communications Technology” , 2000.
- [3] Keiser G, “Optical Fiber communications”, Second edition, McGrawhill. Inc., 1993.

REFERENCES:

- [1] Biswas Sambhu Nath, “Optoelectronic Engineering”, Dhanpat Rai Publication 1994.
- [2] Gowar J., Optical Communication Systems, PHI.
- [3] Senior, J M, “Optical Communication Principle and Practices”, II edition Pearson Education Ltd, 2006.

SECURITY IN WIRELESS NETWORKS			
Course Code:	EC501/EC631	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Security goals, attacks, services and mechanisms, cryptography and steganography

Unit II: Symmetric key ciphers-substitution ciphers, transposition ciphers, stream and block ciphers, algebraic structures and GF (2^n) fields in cryptography

Unit III: Modern block ciphers, modern stream ciphers, DES and AES, Elliptic curve cryptosystems

Unit IV: Message integrity, random oracle model, message authentication, Hash function, Integrity authentication, Digital signature- process, services, attacks, schemes, key management-symmetric key distribution, kerberos, symmetric key agreement, public key distribution

Unit V: Security in wireless LAN, IEEE802.11 security, eavesdropping, unauthorized access, interference and jamming, physical threats, counter majors, WEP, encryption, authentication, WPA, authorization, non repudiation, authentication and secure session, security architecture, VPN, wireless access to the Internet

Text book

[1] Behrouz A. Forouzan, “Cryptography & Network Security” Tata McGraw Hill

References:

RESEARCH TECHNIQUES IN ICT			
Course Code:	CS503/CS633	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I : Introduction to Research Techniques

Meaning of research, objectives of research, motivation in research, types of research-Introduction to experimental test bed, algorithmic research, simulation research, mathematical modeling approach, characteristics and prerequisites of research, significance of research, research process, Sources of research problem, criteria of identifying the problem, necessity of defining the problem, errors in selecting research problem, technique involved in defining the problem, Report and paper writing

Unit II: Data Analysis and Statistical Techniques

Data and their analyses, quantitative methods and techniques, Measure of central tendency, measures of variation, frequency distribution, analysis of variance methods, identifying the distribution with data, parameter estimation, Goodness-of-Fit tests-Chi-Square test, K-S Goodness-of-Fit test.

Correlation analysis, Regression analysis, time series and forecasting, Introduction to discriminant analysis, factor analysis, cluster analysis, conjoint analysis. Sampling methods, test of hypothesis.

Unit III: Random Numbers and Variates

Properties of random numbers, generation, tests for random numbers, random-variate generation-inverse Transform technique, direct transformation, convolution method, acceptance-rejection technique

Probability distributions functions, Moments, moment generating functions, joint distributions, marginal and conditional distributions, functions of two dimensional random variables

Poisson process-Markovian queues, single and multi server models, Little's formula, steady state analysis

Unit IV: Algorithmic Research

Algorithmic research problems, types of algorithmic research, types of solution procedure, steps of development of algorithm, steps of algorithmic research, design of experiments,

Unit V: Simulation and Soft Computing Techniques

Introduction to soft computing, Artificial neural network, Genetic algorithm, Fuzzy logic and their applications, Tools of soft computing, Need for simulation, types of simulation, simulation language, fitting the problem to simulation study, simulation models, verification of simulation models, calibration and validation of models, Output analysis, introduction to MATLAB, NS2, ANSYS, Cadence

Text Books:

[1] R. Panneerselvam, "Research Methodologies," PHI

[2] Jerry Banks, John S. Carson, Barry.L. Nelson David. M. Nicol, "Discrete-Event System Simulation", Prentice-Hall India

[3] Donald Gross, Carl M. Harris, "Fundamentals of Queueing Theory", 2nd Ed. John Wiley and Sons, New York,

References:

[1] Best John V. and James V Kahn: Research in Education, Wiley eastern, 2005.

[2] Sukhia, S.P., P.V. Mehrotra, and R.N. Mehrotra: Elements of Educational Research, PHI publication, 2003.

[3] K. Setia: Methodology of Research Education, IEEE publication, 2004.

[4] Kothari, C.R.: Research methodology, Methods and Techniques, 2000.

COMPUTING LAB			
Course Code:	EC681	Credits:	2
No. of Lab (Hrs/Week):	3	End Sem Exam Hours:	2
Total No. of Lab Sessions:	10		

List of Experiments

- Study and Analysis of Network Simulator-2
 - Location of different Protocols.
 - Simulate a Network.
 - Modify a C++ code in NS-2
 - Use a trace file
- Study and Analysis of QualNet.
 - Location of different Protocols.
 - Simulate a Network.
 - Modify a C++ code.
 - Create the results for analysis.
- Simulate a research Paper related to MANET using NS-2/Qualnet.
- Simulate a research Paper related to Sensor using NS-2/Qualnet.
- Simulate a research Paper related to Multicast Network using NS-2/Qualnet.
- Simulate a research Paper related to Security using NS-2/Qualnet.
- Simulate a research Paper related to Congestion Control in Wireless using NS-2/Qualnet.
- Simulate a research Paper related to V-Network using NS-2/Qualnet.
- Simulate a research Paper related to Routing Protocol of wired Network using NS-2/Qualnet.
- Simulate a research Paper related to Routing Protocol of Wireless Network using NS-2/Qualnet.
- Performance Evolution of firewall in Networking (If resource available).
- Performance Evolution of IP cameras in Networking. (If resource available)
- Performance Evolution of Phone in Networking (If resource available).
- Performance Evolution of Network Server(If resource available).

15. Mini Project (Network Driver, Network Performance Analysis Tool, etc.)

NETWORK RELIABILITY			
Course Code:	EC505/EC641	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Reliability Fundamentals

Causes of Failures, Useful Life of Components, Hazard Functions, Bathtub Curve, Characteristic Type of Failures: Early Failures, Constant Failure Rate, Wear-Out Failures, Failure Data Analysis, Mean Time to Failure (MTTF), Mean Residual Life (MRL), Reliability Measures

Unit II: Reliability Mathematics

Classical Set Theory, Boolean algebra, Sample Space, Probability Theory, Bayes Theorem, Probability Distributions, Cumulative Distributions, Variance, Moments, Estimation Theory, Laplace Transform, Markov Processes, Random Number Generation, Probability Plots

Unit III: System Reliability Analysis

Reliability Block Diagrams, Series Systems, Series Parallel Systems, Open and short Circuit Failures, Standby Systems, System Modeling, Assumptions for Modeling, Two State Modeling, Three State Models, Statistical Estimation of Failure Data, Interval Estimates, Hazard Models.

Unit IV: Reliability Modeling

Stress-Strength Model, System Modeling, Assumptions for Modeling, Two State Modeling, Three-state Modeling, Graphical Approach, Path Set/Cut Set Approach: Inclusion-Exclusion Method, Calculation of Bounds Method, Monte Carlo Simulation Method, Domination Theory, Sum of Disjoint Product Method, Non-path Set/cut Set approach: State Enumeration Method Transformation Method Topological Method BDD/OBDD/ROBDD Method, Reliability Measures: Connectivity Measures, Multicommodity Flow Measures, Maximum Flow (Capacity) Measures, Performability Measures.

Unit V: Maintability and Availability Analysis

Availability Prediction, Effects of Repair Strategies, Fault Trees, Event Tree Analysis, FMEA, FMECA, Theoretical and Analytical Overview of Availability and Maintainability in Communication System Design: Prediction in Assessment, Preliminary in Design, Evaluation in Detail Design. Application Modeling of Availability and Maintainability in Engineering Design

Text Books:

- [1] Aggarwal K. K., Reliability Engineering, Kluwer Academic Publishers, Boston Publication
- [2] Ramakumar R., Reliability Engineering: Fundamentals and Applications, Prentice Hall

Reference Books:

- [1] Misra K. B., Reliability Analysis and Prediction: A Methodology Oriented Treatment, Elsevier Publication

- [2] Kapur K. C. and L. R. Lamberson, Reliability in Engineering Design, John Wiley & Sons,
- [3] Martin L Shooman, Reliability of Computer Systems and Networks, Johan Wiley Sons
- [4] Naikan V.N.A., Reliability Engineering and life testing, PHI 2010.

SMART ANTENNA SYSTEMS			
Course Code:	EC507/EC643	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Basic Concepts of Radiation

Radiation mechanism: Basic sources of Radiation- Current distribution on antennas, Basic antenna parameters.

Unit II: Analysis And Synthesis of Antennas

Vector potential, Antenna theorems and definitions, dipole, loop, reflector, slot antennas. Types of linear arrays, current distribution in linear arrays, Antenna array synthesis techniques.

Unit III: Smart Antennas

Spatial processing for wireless systems: Introduction, Vector channel impulse response & the spatial signature. Spatial processing receivers, fixed beam forming Networks, switched beam systems, Adaptive antenna systems, Wide band smart antennas, Digital radio receiver & software radio for smart antennas.

Unit IV: Smart Antenna Techniques for CDMA

Non-coherent & coherent CDMA spatial processors, spatial processing rake receiver, Multi-user spatial processing, dynamic resectoring, down link beam forming for CDMA, MIMO.

Unit V: Micro Strip Antenna

Radiation Mechanism and Excitation techniques : Microstrip dipole; Patch ,Rectangular patch, Circular patch, and Ring antenna – radiation analysis from cavity model; input impedance of rectangular and circular patch antenna; Microstrip array and feed network; Application of microstrip array antenna.

Text Books:

- [1] Balanis A., “Antenna Theory Analysis and Design”, John Wiley and Sons, New York, 1982.
- [2] Joseph C. Liberti, Theodore S. Rappaport – “Smart Antennas for Wireless Communications: IS95 and third generation CDMA Applications”, Prentice Hall, Communications Engineering and Emerging Technologies Series.

References:

- [1] Kraus J.D., “Antennas”, II edition, John Wiley and Sons, New York, 1977.
- [2] Collin R.E. and Zucker F. – “Antenna theory” Part I, Tata Mc Graw Hill, New York, 1969.
- [3] I.J. Bahl and P. Bhartia,” Microstrip Antennas”,Artech House,Inc.,1980
- [4] W.L.Stutzman and G.A.Thiele,”Antenna Theory and Design”, 2nd edition,John Wiley& Sons Inc.,1998.

BRAODBAND WIRELESS NETWORKS			
Course Code:	EC509/EC645	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Introduction to broadband wireless, fixed broadband wireless, mobile broadband wireless, 3G cellular systems, spectrum options for BW, BW channels, fading, modeling of broadband fading channels, mitigation of fading

Unit II: OFDM-basics, timing and frequency synchronization, PAR computational complexity, simulation of OFDM systems, , MAC layer, performance enhancement, architecture, performance characterization, Multiple antenna techniques-spatial diversity, receiver diversity, transmit diversity, beam foaming, spatial multiplexing, channel estimation for MIMO-OFDM, advanced techniques for MIMO,OFDMA in WiMAX

Unit III: WiMAX, Physical layer- channel coding, hybrid ARQ, interleaving, symbol mapping, symbol structure, sub channel and subcarrier permutations, slot and frame structure, ranging power control, channel quality measurements, MAC layer-convergence SL, MAC PDU, bandwidth request, and allocation, QoS, network entry, an initialization, power saving, mobility management

Unit IV: WiMAX network architecture- design principle, reference model, protocol layering, network discovery and selection, IP address assignment, authentication and security, QoS architecture, mobility management, radio resource management, link level performance-methodology, AWGN channel performance, fading channel performance, advanced receiver architecture, system level architecture-channel modeling, methodology, system level simulation,

Unit V: Ultrawideband, Unlicensed wireless access, IEEE 802.20 MBWA, FOMA, iMODE, WiBRO, FWA, AWS, Multimedia-MedaiFLO, T-DBM, DVB-H, MVNO

Text Books:

- [1] Jeffrey G. Andrews, Arunabha Ghosh, Rias Muhamed, “ Fundamentals of WiMAX, Understanding Broadband Wireless Networking”, Pearson
- [2] Clint Smith, P.E. Daniel Collins, “ 3G Wireless Networks” Tata McGraw-Hill, 2nd Edition

References:

SENSOR NETWORKS			
Course Code:	EC511/EC647	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Sensor Network Operations

Overview of mission-oriented sensor networks, trends in sensor development, mission oriented sensor networks, dynamic systems perspective, Dense sensor networks, robust sensor positioning in wireless ad hoc sensor networks, trigonometric k clustering (TKC) for censored distance estimation, sensing coverage and breach paths in surveillance wireless sensor networks.

Unit II: Lower Layer Issues-Mac, Scheduling, And Transmission

Medium access control for sensor networks, comprehensive performance study of IEEE 802.15.4, providing energy efficiency for wireless sensor networks, link adaptation techniques.

Unit III: Network Routing

Load balanced query protocols for wireless sensor networks, energy efficient and MAC aware routing for data aggregation in sensor networks, ESS low energy security solution for large-scale sensor networks based on tree ripple zone routing scheme.

Unit IV: Sensor Network Applications

Evader centric program, Pursuer centric program, hybrid pursuer evader program, efficient version of hybrid program, Implementation and simulation results

Unit V: Embedded Soft Sensing For Anomaly Detection

Mobile robot simulation setup, software anomalies in mobile robotic networks, soft sensor, software anomaly detection architecture, anomaly detection mechanisms, test bed for software anomaly detection in mobile robot application, multisensor network-based framework; Basic model of distributed multi sensor surveillance system, super resolution imaging, optical flow computation, super resolution image reconstruction, experimental results.

Text Books:

- [1] Shashi Phoha, Thomas F. La Porta , Chrisher Griffin, “Sensor Network Operations”, Wiley-IEEE Press March 2006.
- [2] Jr. Edger H. Callaway, “Wireless sensor networks”, CRC Press.

References:

- [1] I. F. Akyildiz and M. C. Vuran, “Wireless Sensor Networks”, John Wiley and Sons Publ. Company
- [2] Feng Zho, Morgan Kaufmann ,”Wireless Sensor Networks: An Information Processing Approach”.

NETWORK PROGRAMMING			
Course Code:	EC420/EC558	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Introduction to Network Programming

OSI model, LINUX/UNIX standards, TCP and UDP & TCP connection establishment and format, buffer sizes and limitation, standard internet services, protocol usage by common internet application.

Unit II: Sockets

Address structures, value result arguments, byte ordering and manipulation function and related functions, elementary TCP sockets, connect, bind, listen, accept, fork and exec function, concurrent servers, close function and related function.

Unit III: TCP Client Server

TCP echo server functions, normal startup, terminate and signal handling server process termination, crashing and rebooting of server host shutdown of server host.

Unit IV: I/O Multiplexing and Socket Options

I/O models, select function, batch input, shutdown function, poll function, TCP echo server, getsockopt and stockpot functions. socket states, generic socket option IPV6 socket option ICMPV6 socket option IPV6 socket option and TCP socket options.

Elementary UDP Sockets

UDP echo server function, lost datagram, summary of UDP example, lack of flow control with UDP, determining outgoing interface with UDP, elementary name and address conversions, DNS, get host by name function, resolver option, function and IPV6 support, unnamed function, other networking information.

Unit V: IPC and Remote Login

File and record locking, Pipes, FIFOs streams and messages, Name spaces, system IPC, Message queues, Semaphores, Seminal line disciplines, Pseudo-terminals, terminal modes, Control-Terminals, login, RPC Transparency Issues.

Text Books:**References:**

- [1] Keir Davis, John Turner, Nathan Yocom, "The Definitive Guide to Linux Network Programming, ,Apress, 2004.
- [2] Warren Gay, Linux Socket by Example, Programming 2000.
- [3] Graham Glass, King abls, "UNIX for Programmers and Users", 3rd edition, Pearson Education, 1998.
- [4] M. J. Rochkind, "Advanced UNIX Programming", 2nd edition, Pearson Education, 2004.

MULTICAST COMMUNICATION			
Course Code:	EC515/EC651	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Multicast Communication

Types of Communication, Scalability, Applications of Group Communication, Characteristics of Groups - Special Aspects of Group Communication Quality of Service: What is QOS?, Describing a stream, Queuing techniques, Signalling QOS requirements, Integrated Services, RSVP, ST2, Differentiated Services, Differences and Integration Options, Multi Protocol Label Switching, Subnet Bandwidth Manager, Improving QOS in the best effort class - Issues in slow links

Unit II: Multicast Routing

Multicast Routing: Introduction, frame work, Controlling scope, delivery tree, Basic Routing Algorithms, Group Dynamics, Scoping and Multicast Address Allocation, Source-Based Routing, Steiner Trees, Trees with Rendezvous Points, Multicast Routing in the Internet, DVMRP, MOSPF, PIM, CBT, Multicast Routing between Domains

Unit III: Multicast in IP

The IP -Host Name Resolution - IP multicast, RTP, RTCP, RSVP, RTSP, Ipv6, IGMP (v1, v2 & v3), IP multicast applications - IP multicast security issues, Switching technology, multicast, Replication, Mirroring and Caching, IP TV.

Unit IV: Multicast in Transport Protocols

UDP, XTP, MTP, RMP, LBRM, SRM, RMTP, PGM, MFTP Inter-domain multicast routing, Multicast caveats, multicast on non-broadcast media, flooding, address allocation, Multicast in LANs

Unit V: Mbone and Multicast Security

the Multicast Backbone of the Internet, Mbone Architecture, Mbone Tools, Mbone Applications, Multicast routing and Mobile Systems, Multicast and DiffServ, Active Networks for Supporting Group Communication - Group Management for Large Dynamic Groups - Video conferencing, SDR : session directory, VIC & VAT, Reliable Multicast, Security issue of Source, Security issues of receivers.

Text Books:

- [1] Multicast Communication: Protocols, Programming, & Applications, Morgan Kaufmann in Networking, Ralph Wittmann, Martina Zitterbart, Edition 2001, Academic Press, USA.
- [2] Multicast Networking And Application, Kennet Miller AW Publication, 1999
- [3] IP Telephony, Oliver Hersent, David Gurle, Jean - Pierre Petit, A, Pearson Asia, LPE, 2001
- [4] Multicast Sockets: Practical Guide for Programmers by David Makofske, Kevin Almeroth, Edition 2003, Elsevier, USA.
- [5] Introduction to Data Multicasting, IP Multicast Streaming for Audio and Video Media Distribution Lawrence Harte, First Edition, 2008, Althos Publishing House.

References:

- [1] Multicast Communication: Protocols, Programming, & Applications, Morgan Kaufmann in Networking, Ralph Wittmann, Martina Zitterbart, Edition 2001, Academic Press, USA.
- [2] Multicast Networking And Application, Kennet Miller AW Publication, 1999
- [3] IP Telephony, Oliver Hersent, David Gurle, Jean - Pierre Petit, A, Pearson Asia, LPE, 2001
- [4] Multicast Sockets: Practical Guide for Programmers by David Makofske, Kevin Almeroth, Edition 2003, Elsevier, USA.

MODERN OPTIMIZATION TECHNIQUES			
Course Code:	EC517/EC671	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Artificial Neural Networks (ANN)

Objectives-History-Biological inspiration, Neuron model, Single input neuron, Multi-input neuron, Network architecture, Single layer of neurons, Multi-layers of Neurons.

Unit II: Perceptron and Learning

Perceptron architecture, Single-neuron perceptron, Multi-neuron perceptron- Perceptron Learning Rule, Constructing learning rules, Training multiple neuron perceptrons. Associative Learning: Simple associative network, Unsupervised Hebb rule- Hebb rule with decay, Instar rule, Kohonen rule. Widrow-Hoff Learning: Adaline Network, Single Adaline, Mean square Error, LMS algorithm, Analysis of Convergence.

Unit III: Neural Network Roles in VLSI Design

Applications of Artificial Neural Networks to Function Approximation, Regression, Time Series and Forecasting.

Unit IV: Genetic Algorithms and its Mathematical Foundations

Introduction, robustness of traditional optimization and search methods, goals of optimization, difference between genetic algorithms and traditional methods, a simple genetic algorithm, hand simulation, Grist for the search mill, similarity templates, learning the lingo. Foundation theorem, schema processing, the two armed and k-armed bandit problem, schemata processing, building block hypothesis, minimal deceptive problem (MDP), extended schema analysis, MDP results, similarity templates as hyper planes.

Unit V: Advance Topics

Data structures, reproduction, crossover and mutation, a time to reproduce and a time to cross, main program and results, mapping objective functions to fitness form, fitness scaling, codings, a multiparameter mapped fixed point coding, discretization, constraints.

Text Books:

- [1] Neural Network Design, PWS publishing company, 1995.
- [2] Introduction to Artificial Neural Systems, Jaico Pub.House, Bombay, 1994.
- [3] Van Nastrand Reinhold, "Neural Computing : Theory and practice", 1989.

References

- [1] Haykin S., "Neural Networks-A Comprehensive Foundations", Prentice-Hall International, New Jersey, 1999.
- [2] Freeman J.A., D.M. Skapura, "Neural Networks: Algorithms, Applications and Programming Techniques", Addison-Wesley, Reading, Mass, (1992).
- [3] Golden R.M., "Mathematical Methods for Neural Network Analysis and Design", MIT Press, Cambridge, MA, 1996.

MIXED SIGNAL VLSI DESIGN			
Course Code:	EC519/EC675	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: Signals, Filters, and Tools

Sinusoidal Signals, Pendulum Analogy, Amplitude in the x-y Plane, In-Phase and Quadrature Signals, Complex (z-) Plane, Comb Filters, Digital Comb Filter, Digital Differentiator, Intuitive Discussion of the z-Plane, Comb Filters with Multiple Delay Elements, Digital Integrator, Delaying Integrator, Exponential Fourier Series, Fourier Transform, Dirac Delta Function .

Unit II: Sampling and Aliasing

Sampling, Impulse Sampling, Time Domain Description of Reconstruction, Decimation, Sample-and-Hold, S/H Spectral Response, Reconstruction Filter, Circuit Concerns for Implementing the S/H, Track-and-Hold (T/H), Interpolation, Zero Padding, Hold Register, Linear Interpolation, K-Path Sampling, Switched-Capacitor Circuits, Non-Overlapping Clock Generation, Circuits Implementing the S/H, Finite Op-Amp Gain-Bandwidth, Auto zeroing,

Unit III: Analog Filters

Integrator Building Blocks, Lowpass Filters, Active-RC Integrators, Effects of Finite Op-Amp Gain Bandwidth Product, Active-RC SNR, MOSFET-C Integrators, gm-C Integrators, Common-Mode Feedback Considerations, High-Frequency Transconductor, Discrete-Time Integrators, Frequency Response of an Ideal Discrete-Time Filter, Filtering Topologies, Bilinear Transfer Function, Active-RC Implementation, Transconductor-C Implementation.

Unit IV: Digital Filters

Models for DACs and ADCs, Ideal DAC, Modeling of Ideal DAC, Ideal ADC, Number Representation, Increasing Word Size, Adding Numbers and Overflow, Two's Complement Sinc-Shaped Digital Filters, Counter, Aliasing, Accumulate-and-Dump, Lowpass Sinc Filters, Averaging without Decimation, Cascading Sinc Filters, Finite and Infinite Impulse Response Filters, Bandpass and Highpass Sinc Filters, Frequency Sampling Filters.

Unit V: Data Converter SNR

Quantization Noise, Quantization Noise Spectrum, Bennett's Criteria, RMS Quantization Noise Voltage, Quantization Noise as a Random Variable, Quantization Noise Voltage Spectral Density, Power Spectral Density, SNR, Effective Number of Bits, Coherent Sampling, SNDR, Spurious Free Dynamic Range, Dynamic Range, Specifying SNR and SNDR, Clock Jitter.

Text Books:

- [1] Jacob Baker, CMOS Mixed Signal Circuit Design.
- [2] Yannis Tsividis, "Mixed Analog-Digital VLSI Device and Technology",.

References:

- [1] Roubik Gregorian, "Introduction to CMOS Opamps and Comparators".

DSP INTEGRATED CIRCUITS			
Course Code:	EC521/EC677	Credits:	3
No. of Lectures (Hrs/Week):	3	Mid Sem Exam Hours:	2
Total No. of Lectures:	45	End Sem Exam Hours:	3

Unit I: DSP Integrated Circuits and VLSI Circuit Technologies

Standard digital signal processors, Application specific IC's for DSP, DSP systems, DSP system design, Integrated circuit design. MOS transistors, MOS logic, VLSI process technologies, Trends in CMOS technologies.

Unit II: Digital Signal Processing

Digital signal processing, Sampling of analog signals, Selection of sample frequency, Signal- processing systems, Frequency response, Transfer functions, Signal flow graphs, Filter structures, Adaptive DSP algorithms, DFT-The Discrete Fourier Transform, FFT-The Fast Fourier Transform Algorithm, Image coding, Discrete cosine transforms.

Unit III: Digital Filters and Finite Word length Effects

FIR filters, FIR filter structures, FIR chips, IIR filters, Specifications of IIR filters, Mapping of analog transfer functions, Mapping of analog filter structures, Multirate systems, Interpolation with an integer factor L, Sampling rate change with a ratio L/M, Multirate filters. Finite word length effects -Parasitic oscillations, Scaling of signal levels, Round-off noise, Measuring round-off noise, Coefficient sensitivity, Sensitivity and noise.

Unit IV: DSP Architectures and Synthesis of DSP Architectures

DSP system architectures, Standard DSP architecture, Ideal DSP architectures, Multiprocessors and multicomputers, Systolic and Wave front arrays, Shared memory architectures. Mapping of DSP algorithms onto hardware, Implementation based on complex PEs, Shared memory architecture with Bit – serial PEs.

Unit V: Arithmetic Units and Integrated Circuit Design

Conventional number system, Redundant Number system, Residue Number System, Bit-parallel and Bit-Serial arithmetic, Basic shift accumulator, Reducing the memory size, Complex multipliers, Improved shift-accumulator. Layout of VLSI circuits, FFT processor, DCT processor and Interpolator as case studies. Cordic algorithm.

Text Books:

- [1] Lars Wanhammar, "DSP Integrated Circuits", 1999 Academic press, New York
- [2] A.V.Oppenheim et.al, "Discrete-time Signal Processing", Pearson Education, 2000.

References:

- [1] Emmanuel C. Ifeachor, Barrie W. Jervis, " Digital signal processing – A practical approach", Second Edition, Pearson Education, Asia.
- [2] Keshab K.Parhi, "VLSI Digital Signal Processing Systems design and Implementation", John Wiley & Sons, 1999.