2- Year M.Tech. in ICT (Specialization in VLSI Design) (2015-2017) SEMESTER I

| N. N. | Subject | Courses | FAR | | Credits. | |
|---------------|------------------|---------------------------------------|--------|------------|----------|--|
| STAINU. | Su ojece Code | | 3-1-0 | C1 | 4 | |
| 1 | EC535 | Digital IC Design | 3-1-0 | C2 | 4. | |
| 2 | CS523 | Advanced Computer Architecture | 3-1-0 | C3 | 4 | |
| 3 | EC531 | Advanced Digital Communication System | 3-1-0 | SEC1 | 4 | |
| | CS527 | Research Techniques in ICT | 3-0-0 | OEI | 3 | |
| 5 | | Open Elective-1 | 0-0-3 | C4 | 2 | |
| 6 | EC585 | Design IC Design Lab | 0-0-3 | C5 | 2 | |
| 7 | EC581 | Advanced Digital Communication Lab | | | | |
| | | | | Non | | |
| 8 | GP | General Proficiency | | Credit | | |
| | | Total Credits | | | 23 | |
| | | Total Contact Hours | 15-4-6 | 7, 11 144. | 25 | |
| | | | | | | |

1. Open Electives-I Electives from other school

SEMESTER II

| | | | | | Credi |
|------|--------------|---------------------|---------|--------|-------|
| 4/O# | Subject Code | | 3-1-0 | GE1 | 4 |
| 1 | | Gelienc Bicchyo | 3-0-0 | C6 | 3 |
| 2 | EC536 | IC- Technology | 3-0-0 | C7 | 3 |
| 3 | EC538 | Analog IC Design | 3-0-0 | C8 | 3 |
| 4 | EC578 | CMOS VLSI Design | 3-0-0 | DSE1 | 3 |
| 5 | | Elective-1 | 0-0-3 | C9 | 2 |
| 6 | EC580 | Design Lab I | 0-0-10 | , DPI | 5 |
| 7 | EC592 | Major Project | | | |
| | | | | Non | |
| 8 | GP | General Proficiency | , | Credit | |
| | | Total Credits | | | 23 |
| | · | Total Contact Hours | 15-1-13 | | 29 |

Elective - 1

| [C N | Subject Code | Courses |
|-------|--------------|------------------------------------|
| S. No | Subject Code | Integrated Circuit Physical Design |
| 1 1 | EC574 | Integrated Circuit Physical Besign |
| | FC5(0 | Design of Semiconductor Memories |
| 2 | EC568 | |
| 3 | EC576 | CMOS RF Circuit Design |

Generic Elective (GE1)

1. MA 402 Modeling and Simulation

2. MA416 Probability and Stochastic Process

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SEMESTER - III

| ST. | | Courses | LTP | | Credits. |
|---------|-------|--------------------------|---------|--------|----------|
| 1 | EC633 | Low Power VLSI Design | 3-0-0 | C10 | 3 |
| 2 | EC675 | Mixed Signal VLSI Design | 3-0-0 | C11 | 3 |
| 3 | | Elective- 2 | 3-0-0 | DSE2 | 3 |
| 4 | | Elective- 3 | 3-0-0 | DSE3 | 3 |
| 5 | EC683 | Design Lab II | 0-0-3 | C12 | 2 |
| 6 | EC691 | Dissertation Phase-I | 0-0-14 | DP2 | 7 . |
| A May 1 | | | | | |
| 7 | GP | General Proficiency | | Non | |
| | | | | Credit | |
| | | | | | |
| | | Total Credits | | 21 | |
| | | Total Contact Hours | 12-0-17 | 29 | |

Elective - 2 & 3

| Sr. No | Subject Code | Courses |
|--------|--------------|--|
| 1 | EC665 | VLSI ASIC Design |
| 2 | EC667 🗸 | RF MEMS Design and Technology |
| 3 | EC673 | VLSI-Reliability Engineering |
| 4 | EC669 | Algorithm for VLSI Design Automation |
| 5 | EC677 | DSP Integrated Circuits |
| 6 | EC679 / | Test and Verification of VLSI Circuits |

SEMESTER - IV

| STANT | Subject Code | Courses | AINTER | | - Cr edits |
|-------|--------------|-----------------------|--------|--------|-------------------|
| 1 | EC690 | Dissertation Phase-II | | DP3 | 23 |
| | | | | | |
| 2 | GP | General Proficiency | | Non | 0 |
| | O1 | | | Credit | |
| | | Total | | | 23 |

Grand Total Credits = 90