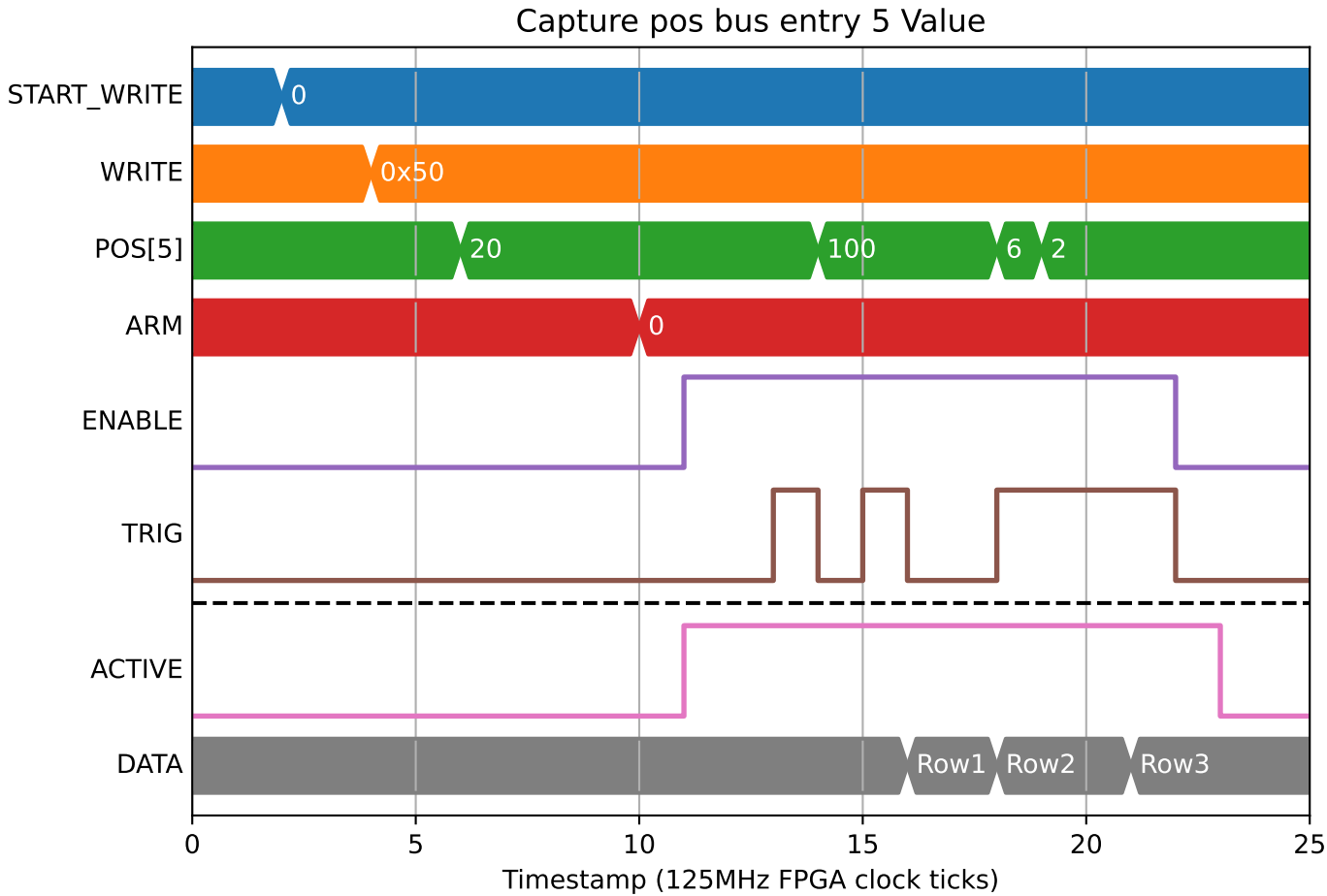
Capture pos bus entry 5 Value
START_WRITE
WRITE
POS[5]
ARM
ENABLE
TRIG
ACTIVE
DATA
0
0x50
20
100
6
2
0
Row1
Row2
Row3
0
5
10
15
20
25
Timestamp (125MHz FPGA clock ticks)