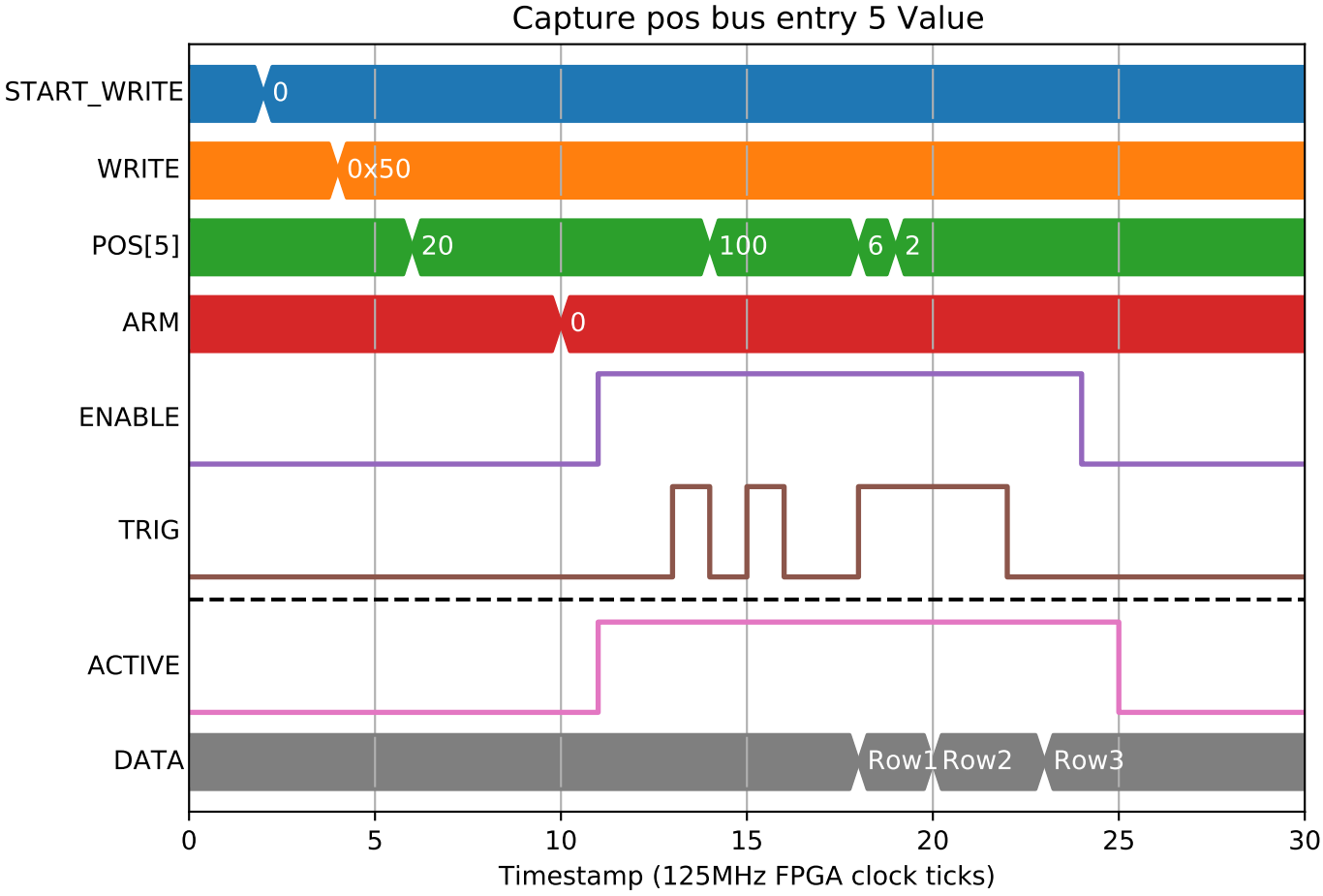
Capture pos bus entry 5 Value
START_WRITE
0
WRITE
0x50
POS[5]
20
100
6
2
ARM
0
ENABLE
TRIG
ACTIVE
DATA
Row1
Row2
Row3
0
5
10
15
20
25
30
Timestamp (125MHz FPGA clock ticks)