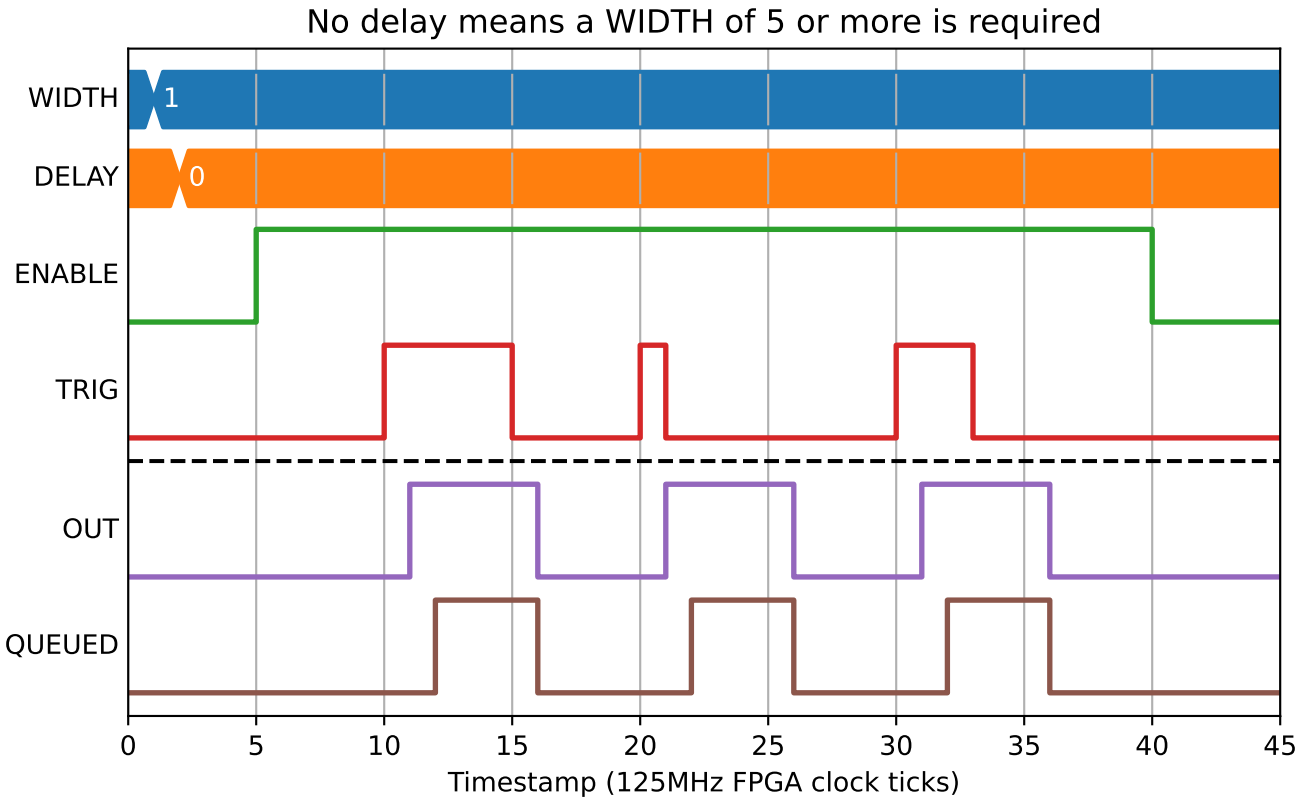
No delay means a WIDTH of 5 or more is required
WIDTH
1
DELAY
0
ENABLE
TRIG
OUT
QUEUED
0
5
10
15
20
25
30
35
40
45
Timestamp (125MHz FPGA clock ticks)