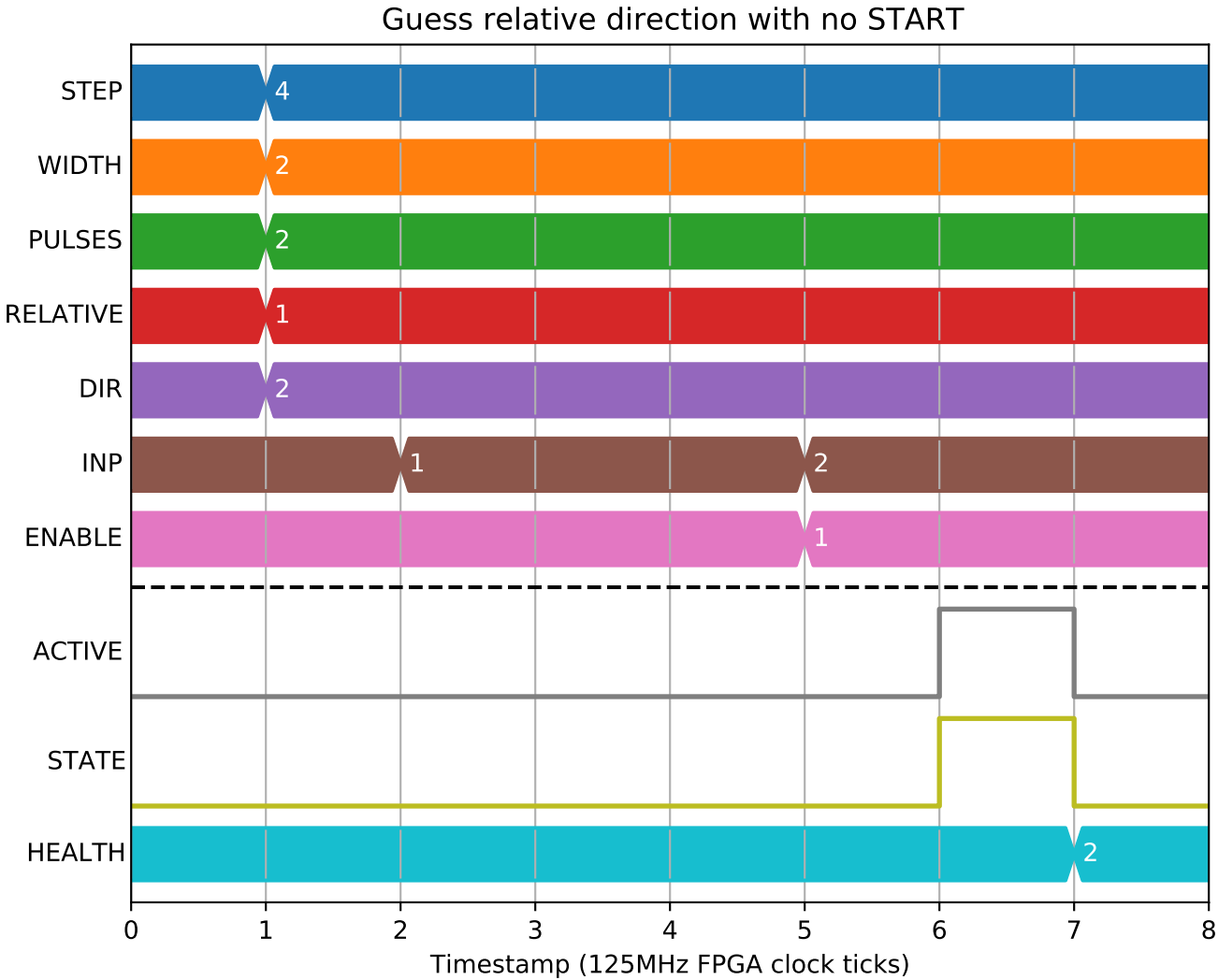
Guess relative direction with no START
STEP
WIDTH
PULSES
RELATIVE
DIR
INP
ENABLE
ACTIVE
STATE
HEALTH
4
2
2
1
2
1
2
1
2
0
1
2
3
4
5
6
7
8
Timestamp (125MHz FPGA clock ticks)