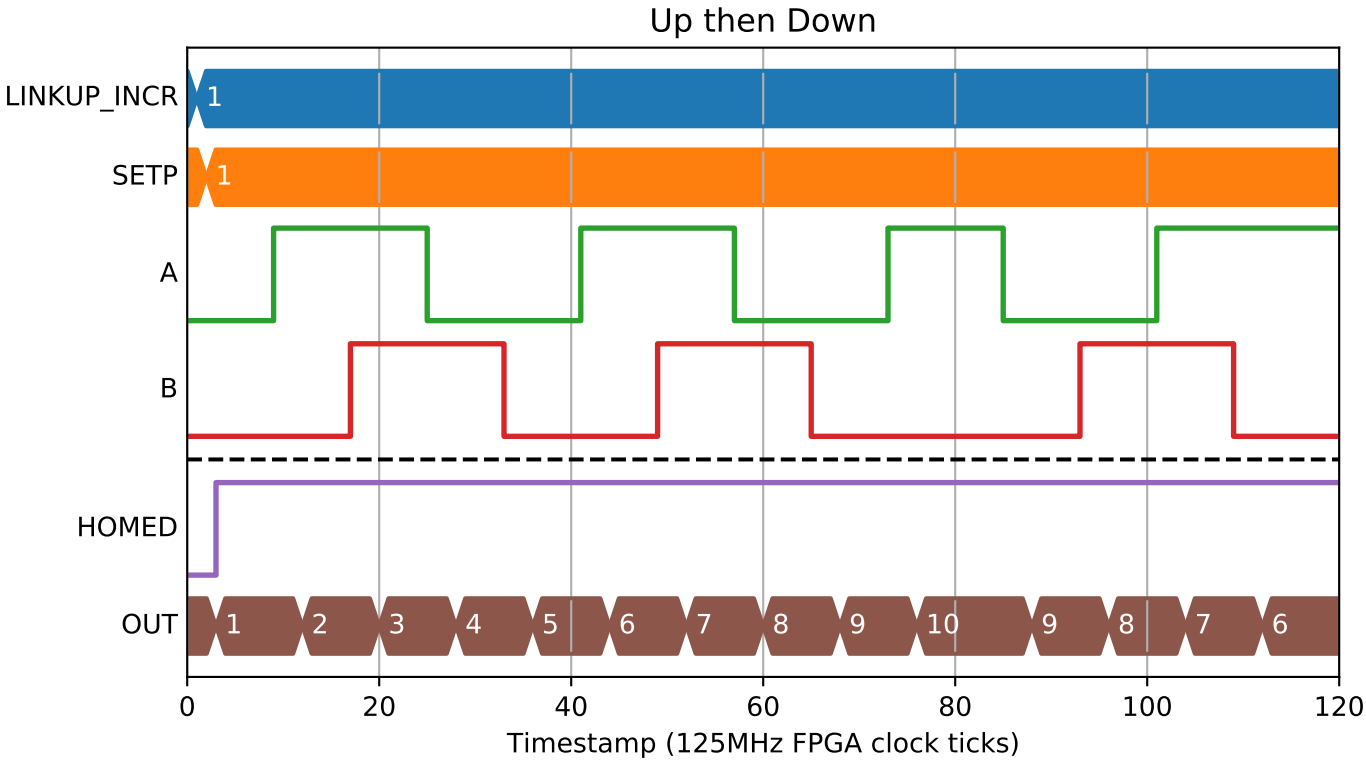
Up then Down
LINKUP_INCR
1
SETP
1
A
B
HOMED
OUT
1
2
3
4
5
6
7
8
9
10
9
8
7
6
0
20
40
60
80
100
120
Timestamp (125MHz FPGA clock ticks)