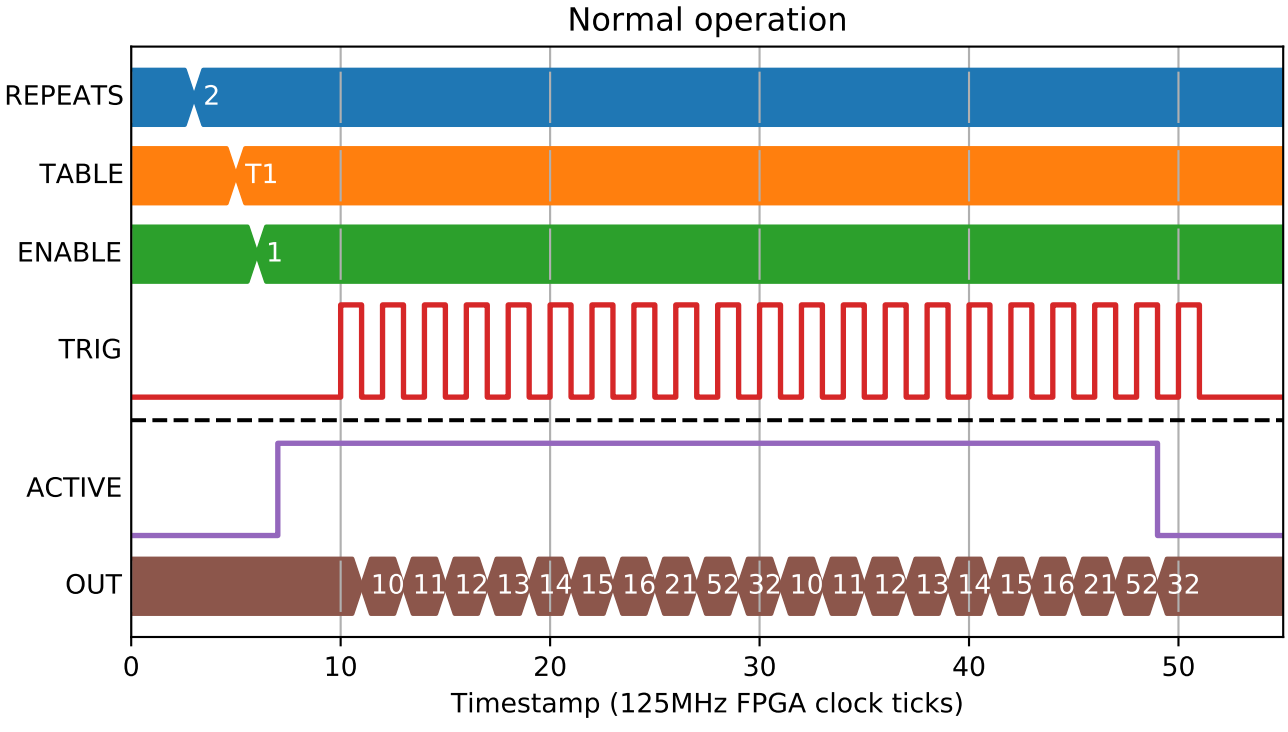
Normal operation
REPEATS
2
TABLE
T1
ENABLE
1
TRIG
ACTIVE
OUT
10 11 12 13 14 15 16 21 52 32 10 11 12 13 14 15 16 21 52 32
0
10
20
30
40
50
Timestamp (125MHz FPGA clock ticks)