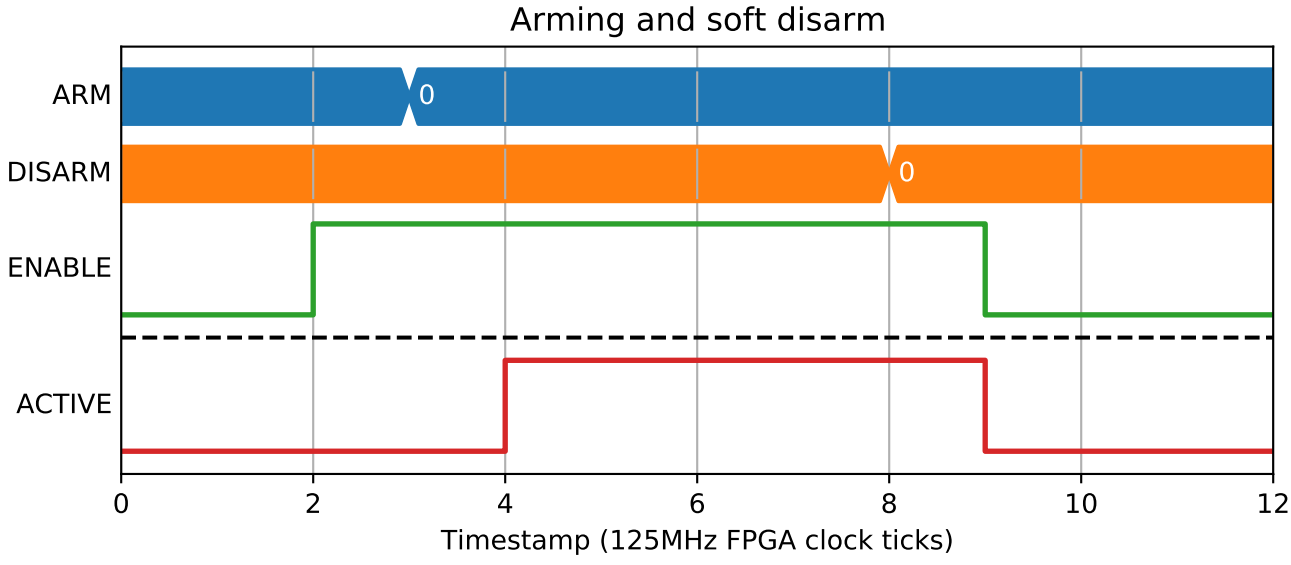
Arming and soft disarm
ARM
DISARM
ENABLE
ACTIVE
0
0
0
2
4
6
8
10
12
Timestamp (125MHz FPGA clock ticks)