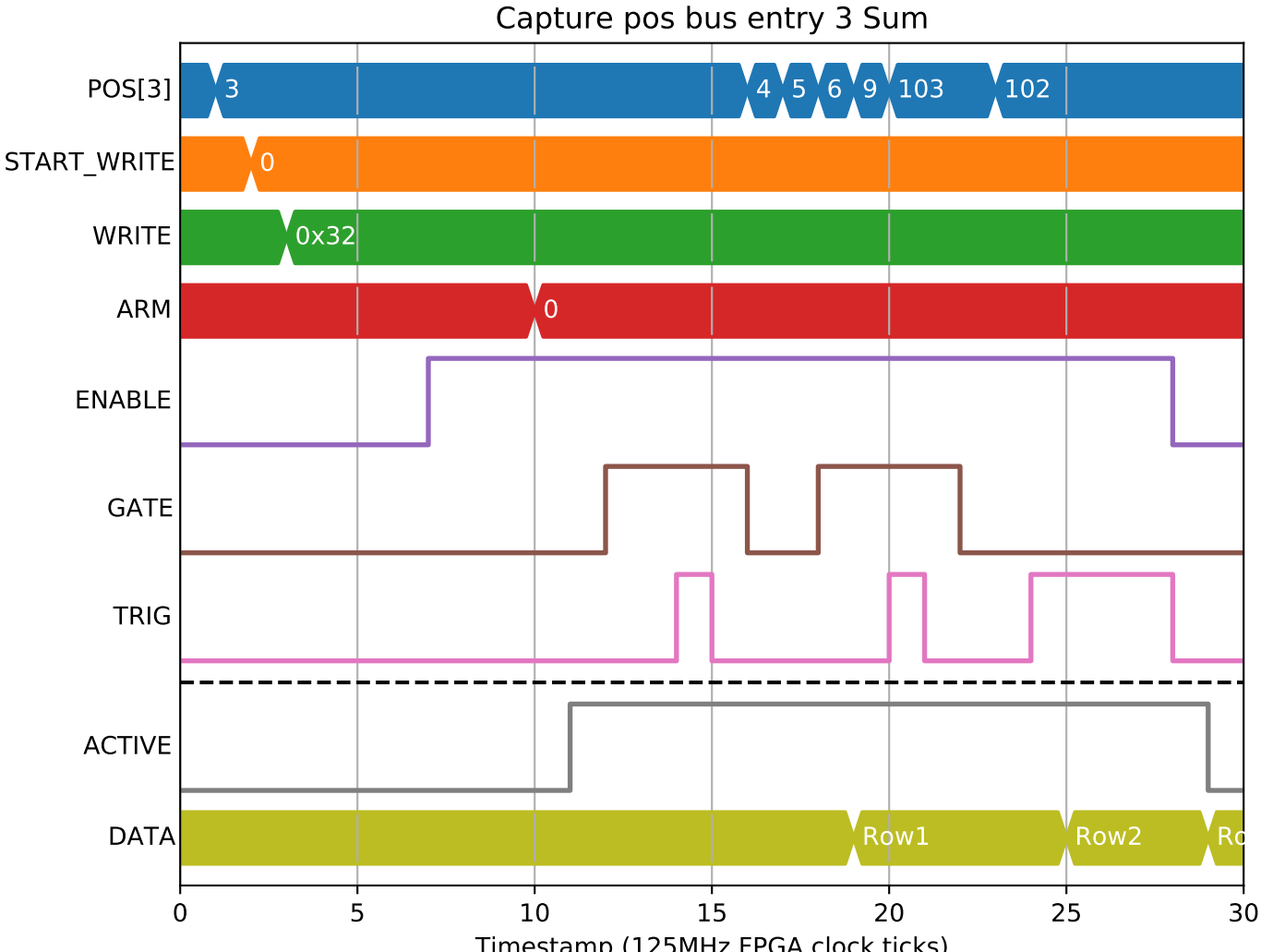
Capture pos bus entry 3 Sum
POS[3]
START_WRITE
WRITE
ARM
ENABLE
GATE
TRIG
ACTIVE
DATA
3
4
5
6
9
103
102
0
0x32
0
Row1
Row2
0
5
10
15
20
25
30
Timestamp (125MHz FPGA clock ticks)