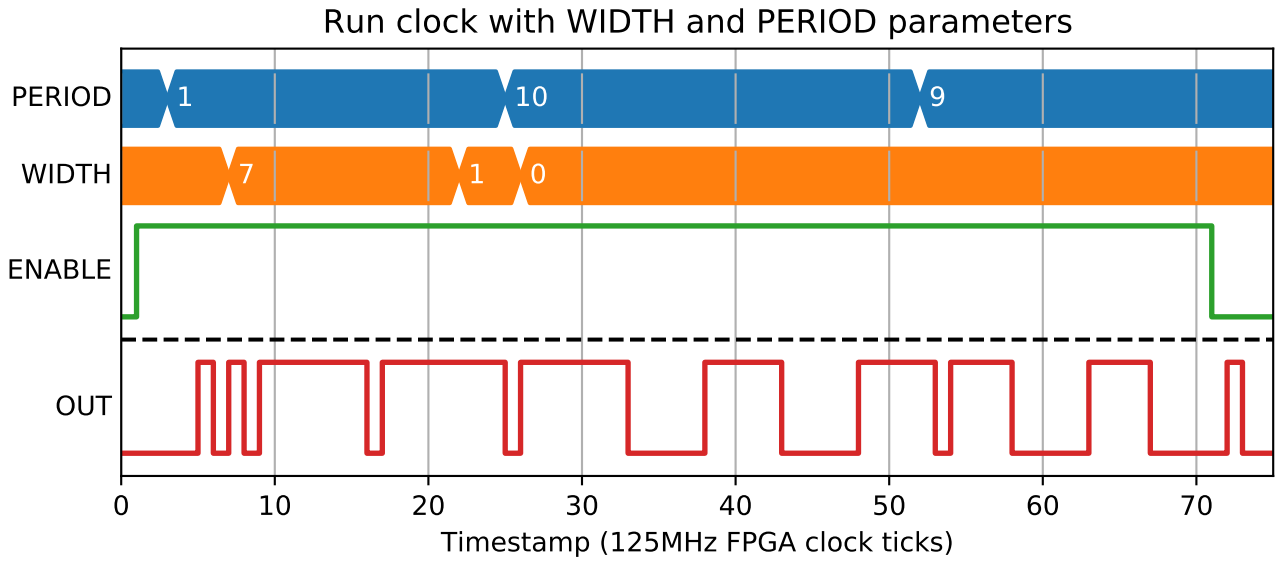
Run clock with WIDTH and PERIOD parameters
PERIOD
WIDTH
ENABLE
OUT
1
10
9
7
1
0
0
10
20
30
40
50
60
70
Timestamp (125MHz FPGA clock ticks)