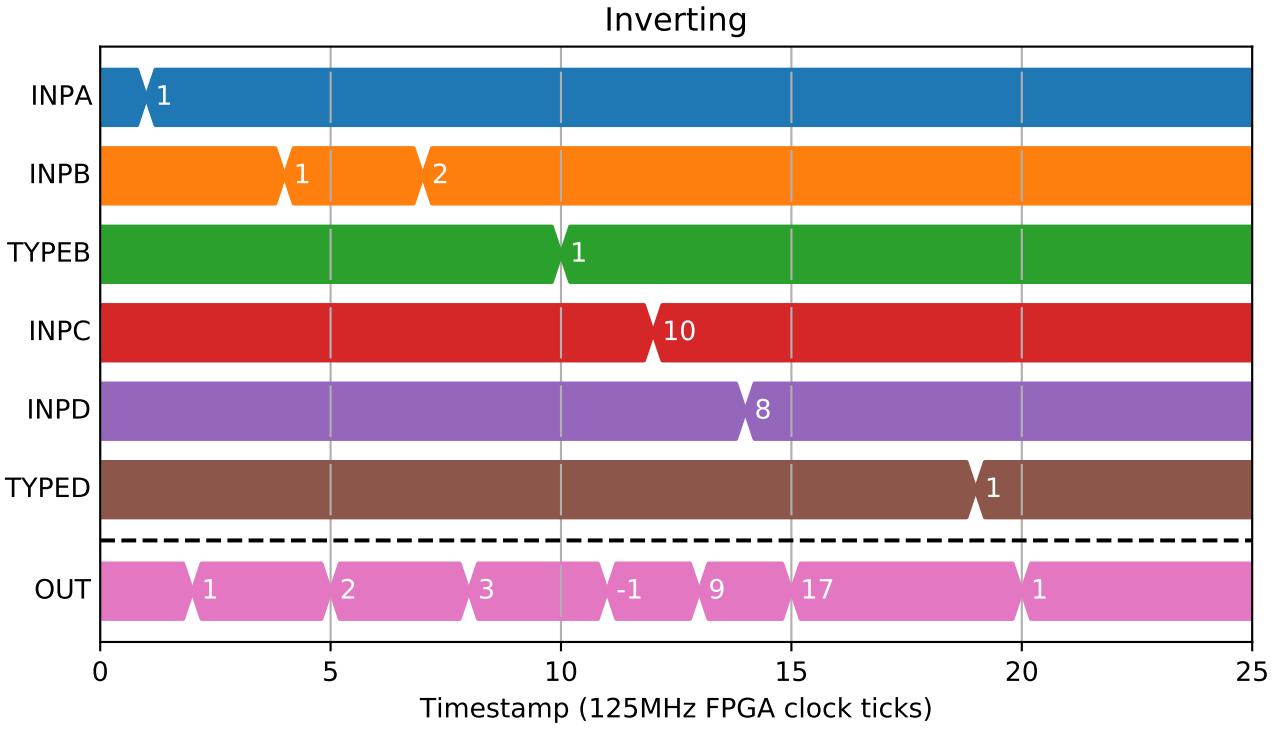
Inverting
INPA
INPB
TYPEB
INPC
INPD
TYPED
OUT
1
1
2
1
10
8
1
1
2
3
-1
9
17
1
0
5
10
15
20
25
Timestamp (125MHz FPGA clock ticks)