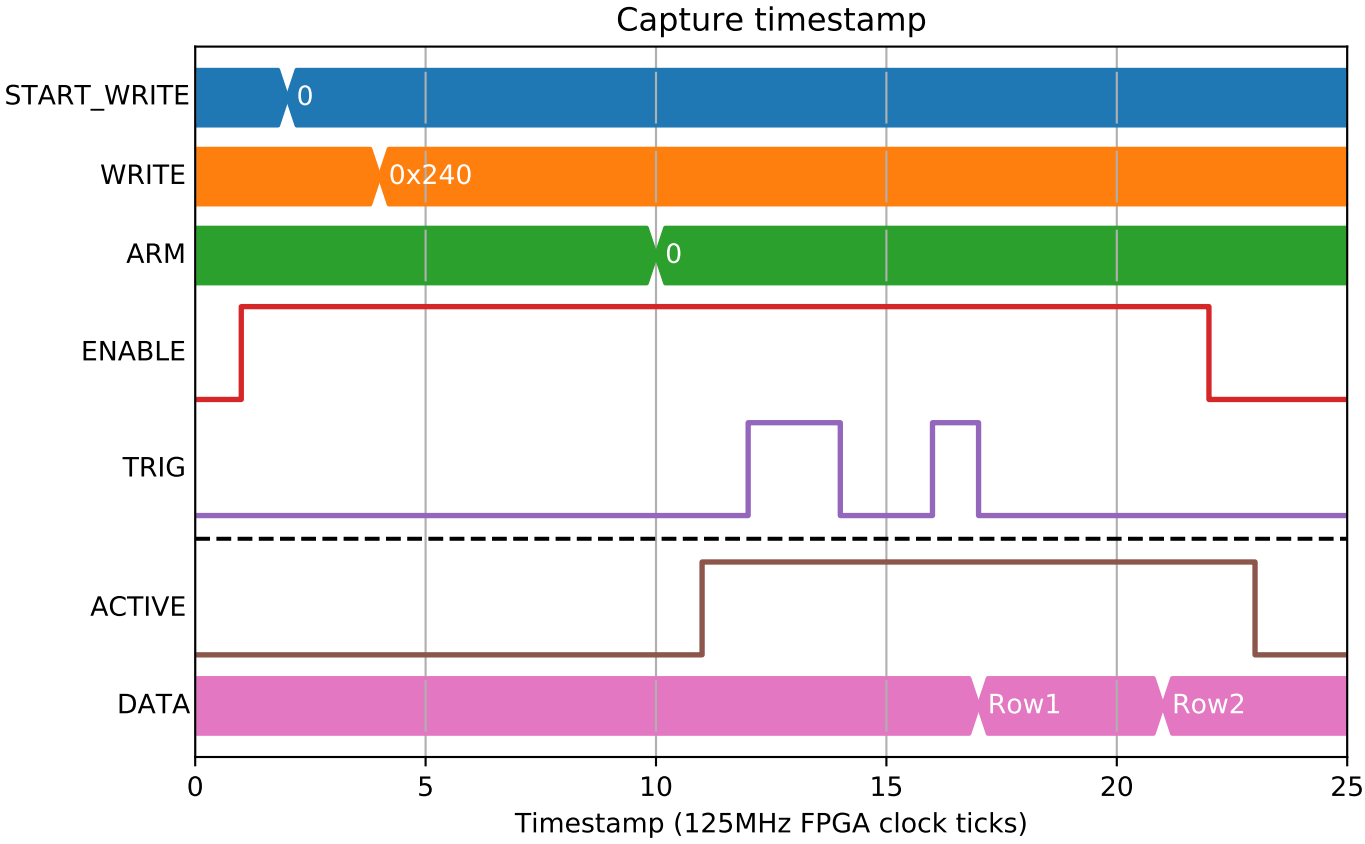
Capture timestamp
START_WRITE
0
WRITE
0x240
ARM
0
ENABLE
TRIG
ACTIVE
DATA
Row1
Row2
0
5
10
15
20
25
Timestamp (125MHz FPGA clock ticks)