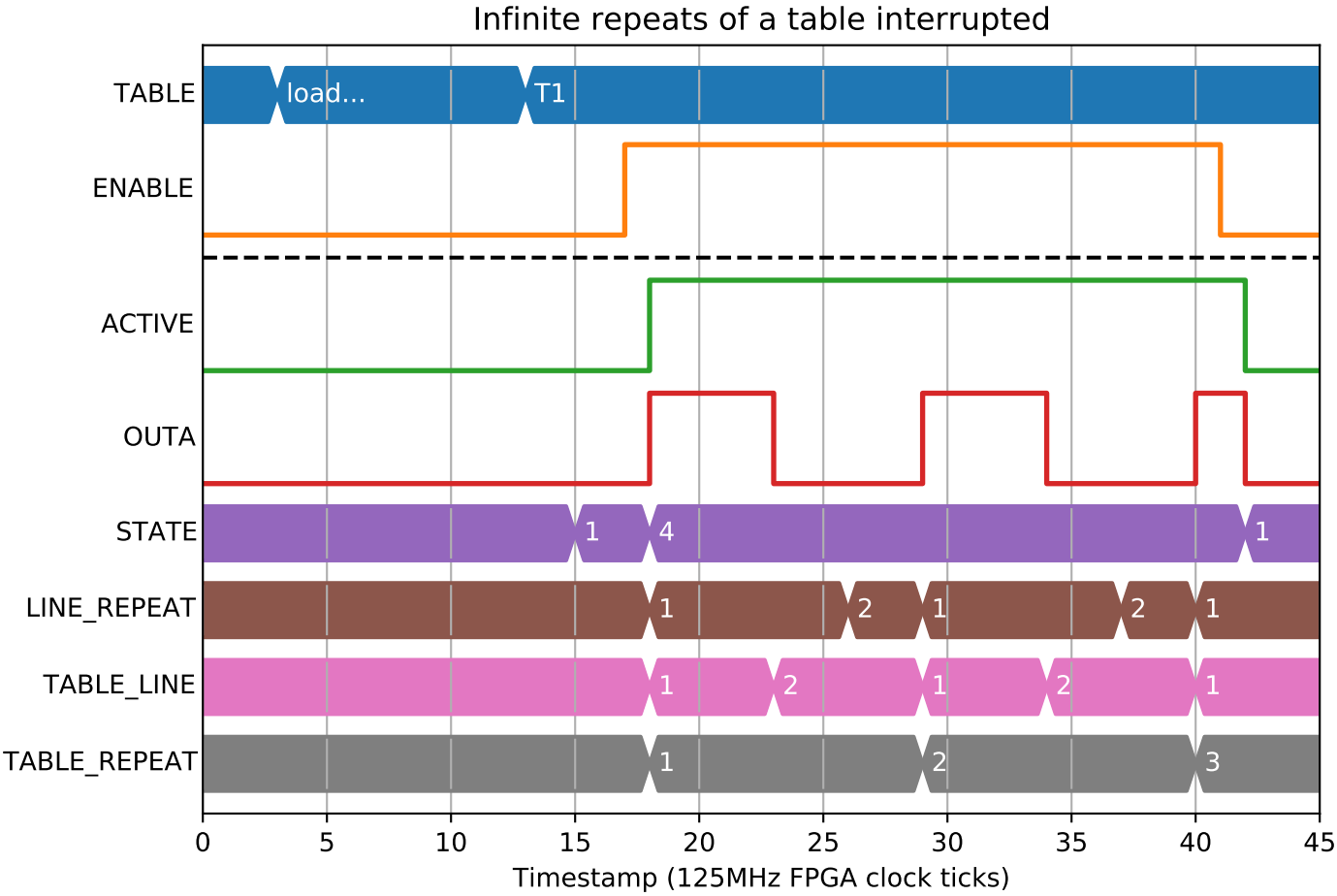
Infinite repeats of a table interrupted
TABLE
load...
T1
ENABLE
ACTIVE
OUTA
STATE
1
4
1
LINE_REPEAT
1
2
1
2
1
TABLE_LINE
1
2
1
2
1
TABLE_REPEAT
1
2
3
0
5
10
15
20
25
30
35
40
45
Timestamp (125MHz FPGA clock ticks)