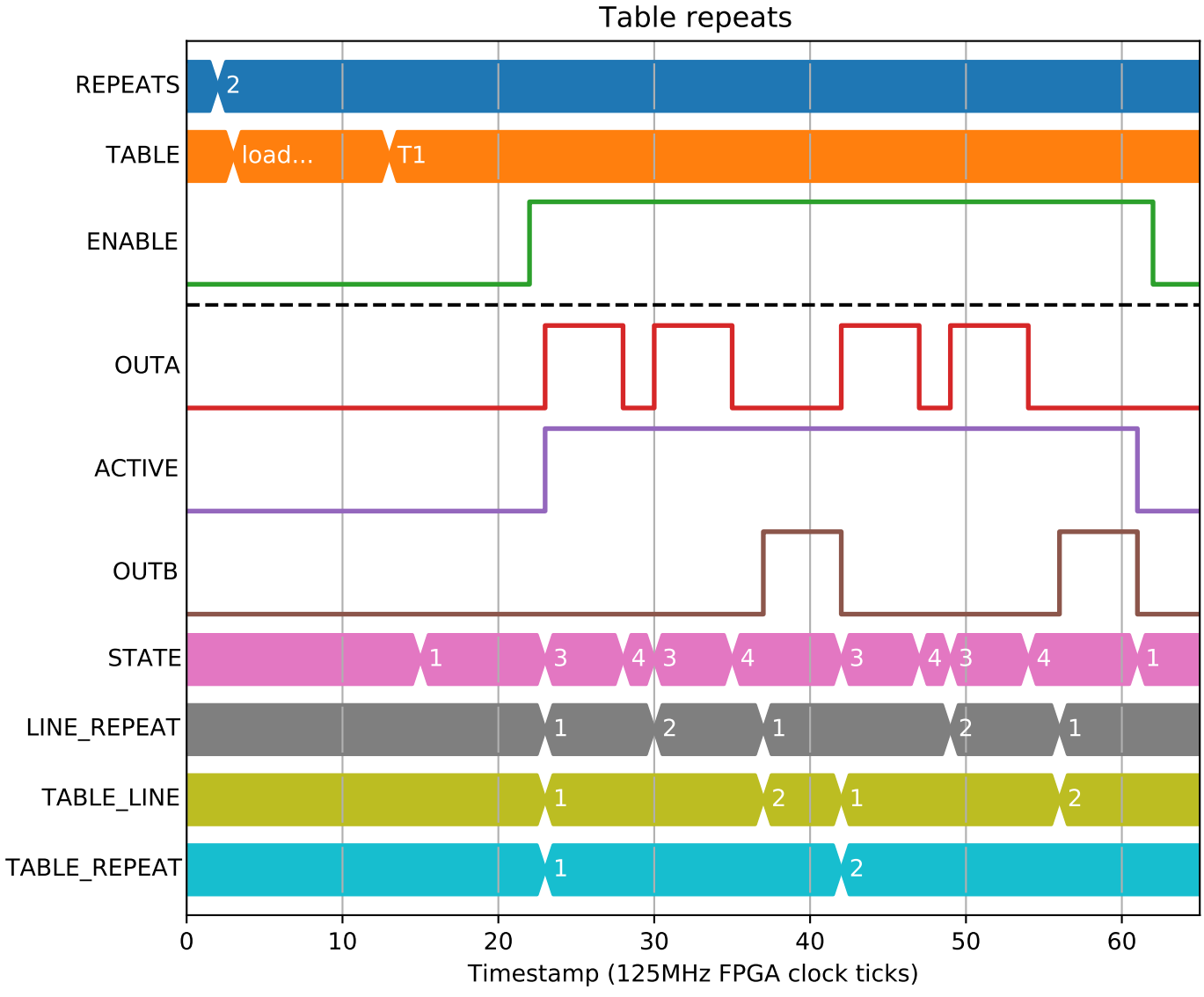
Table repeats
REPEATS
TABLE
ENABLE
OUTA
ACTIVE
OUTB
STATE
LINE_REPEAT
TABLE_LINE
TABLE_REPEAT
2
load...
T1
1
3
4
3
4
3
4
3
4
1
1
2
1
2
1
1
2
1
2
1
2
0
10
20
30
40
50
60
Timestamp (125MHz FPGA clock ticks)