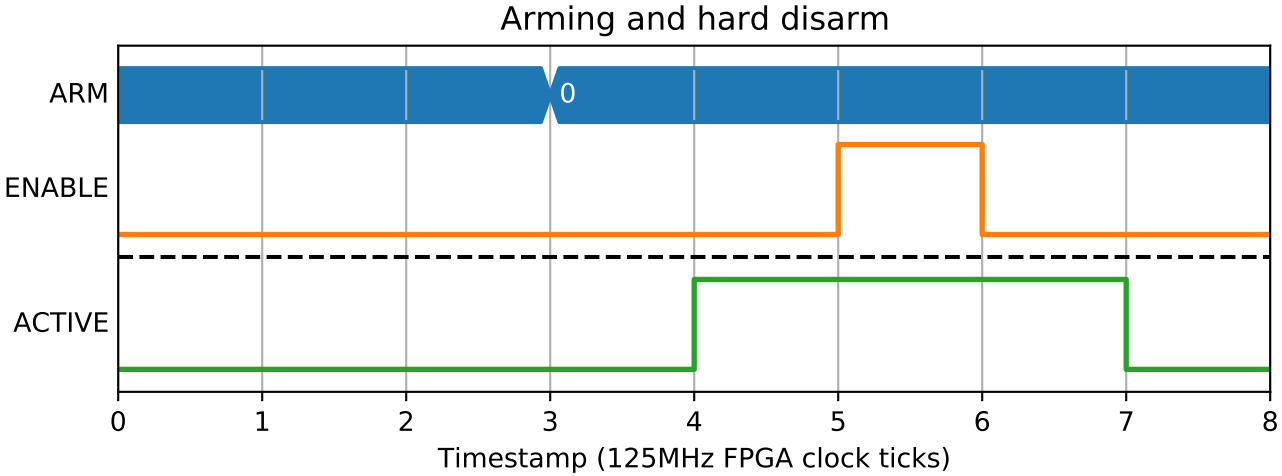
Arming and hard disarm
ARM
0
ENABLE
ACTIVE
0
1
2
3
4
5
6
7
8
Timestamp (125MHz FPGA clock ticks)