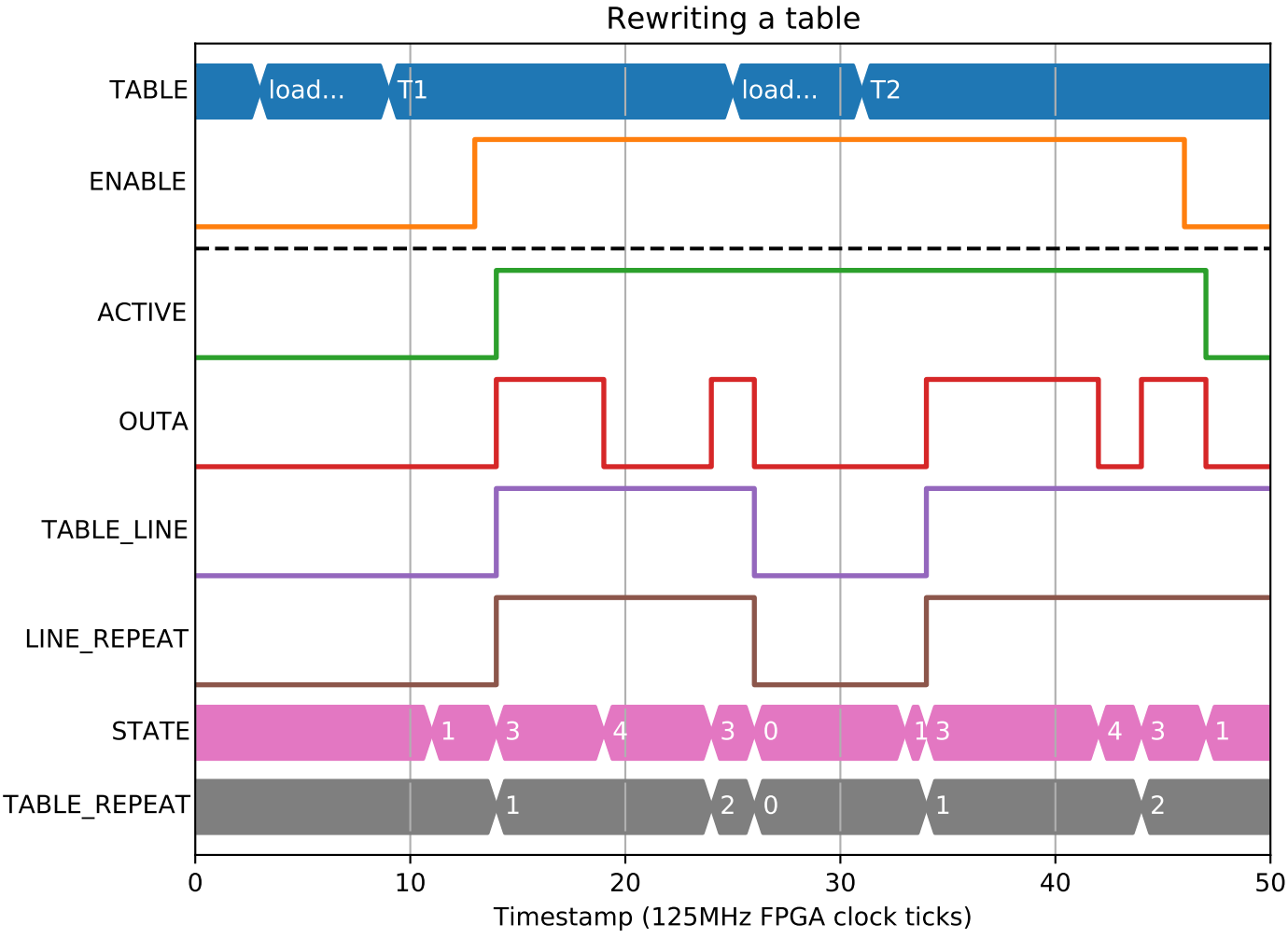
Rewriting a table
TABLE
load...
T1
load...
T2
ENABLE
ACTIVE
OUTA
TABLE_LINE
LINE_REPEAT
STATE
1
3
4
3
0
1
3
4
3
1
TABLE_REPEAT
1
2
0
1
2
0
10
20
30
40
50
Timestamp (125MHz FPGA clock ticks)