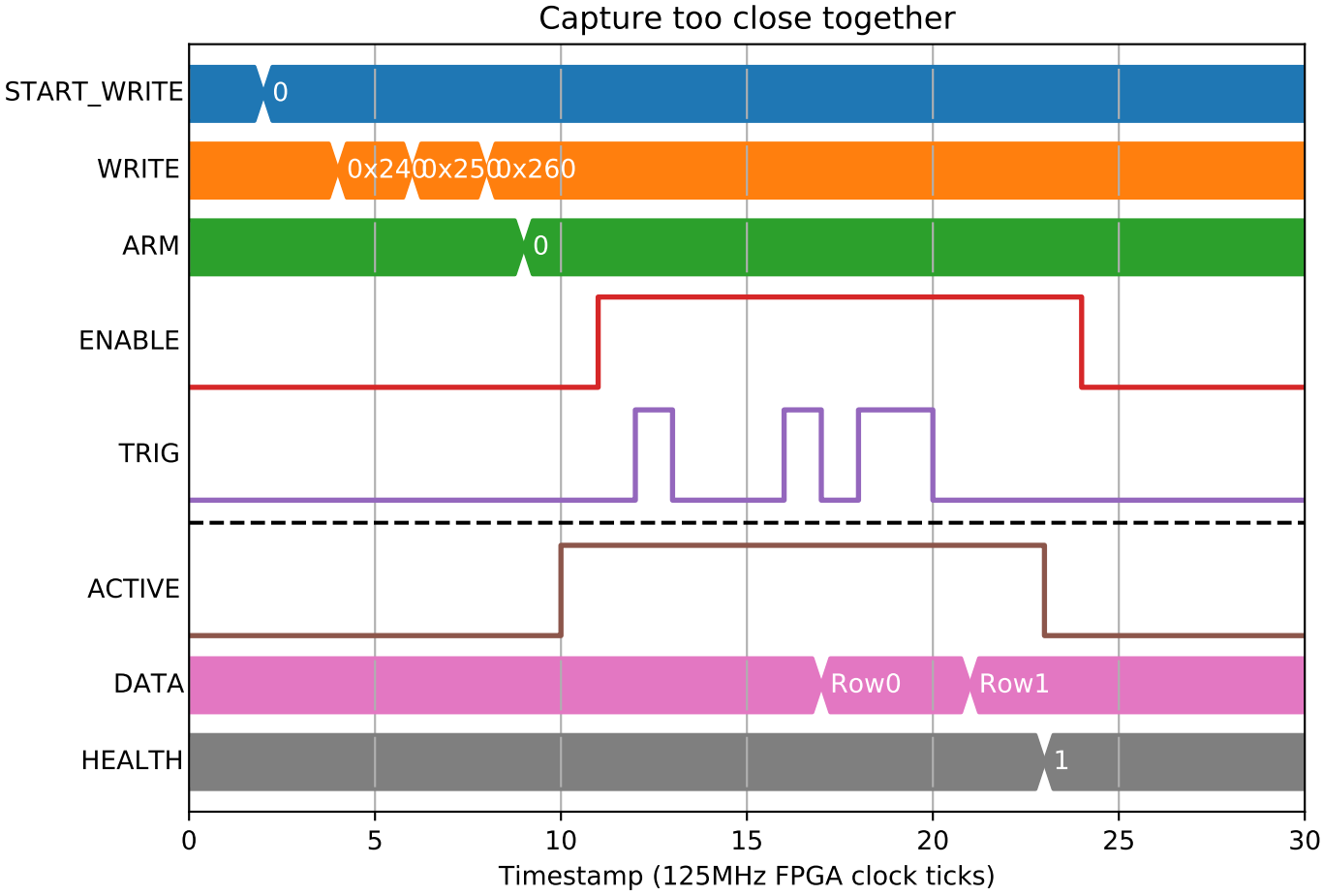
Capture too close together
START_WRITE
0
WRITE
0x240
0x250
0x260
ARM
0
ENABLE
TRIG
ACTIVE
DATA
Row0
Row1
HEALTH
1
0
5
10
15
20
25
30
Timestamp (125MHz FPGA clock ticks)