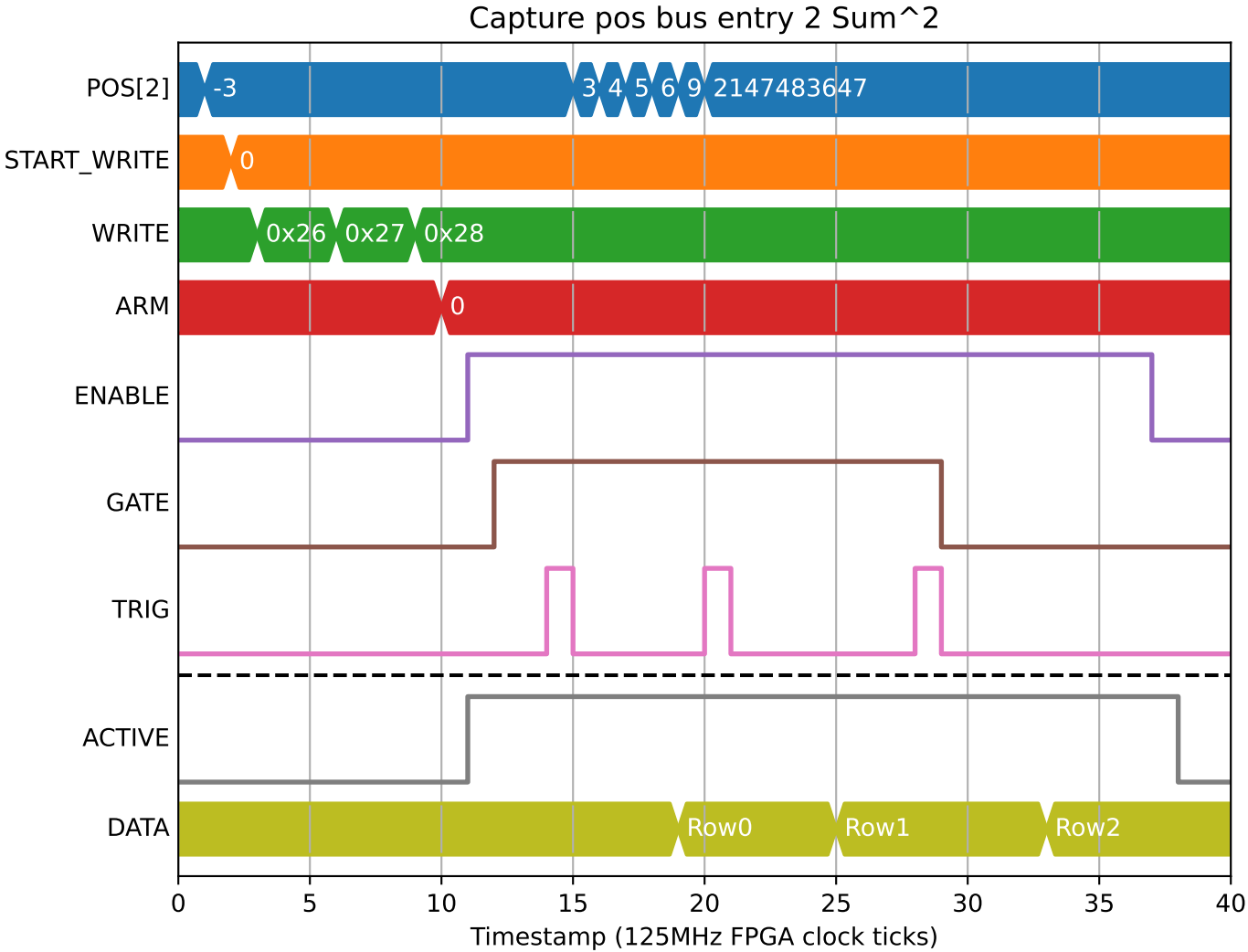
Capture pos bus entry 2 Sum^2
POS[2]
START_WRITE
WRITE
ARM
ENABLE
GATE
TRIG
ACTIVE
DATA
-3
3
4
5
6
9
2147483647
0
0x26
0x27
0x28
0
Row0
Row1
Row2
0
5
10
15
20
25
30
35
40
Timestamp (125MHz FPGA clock ticks)