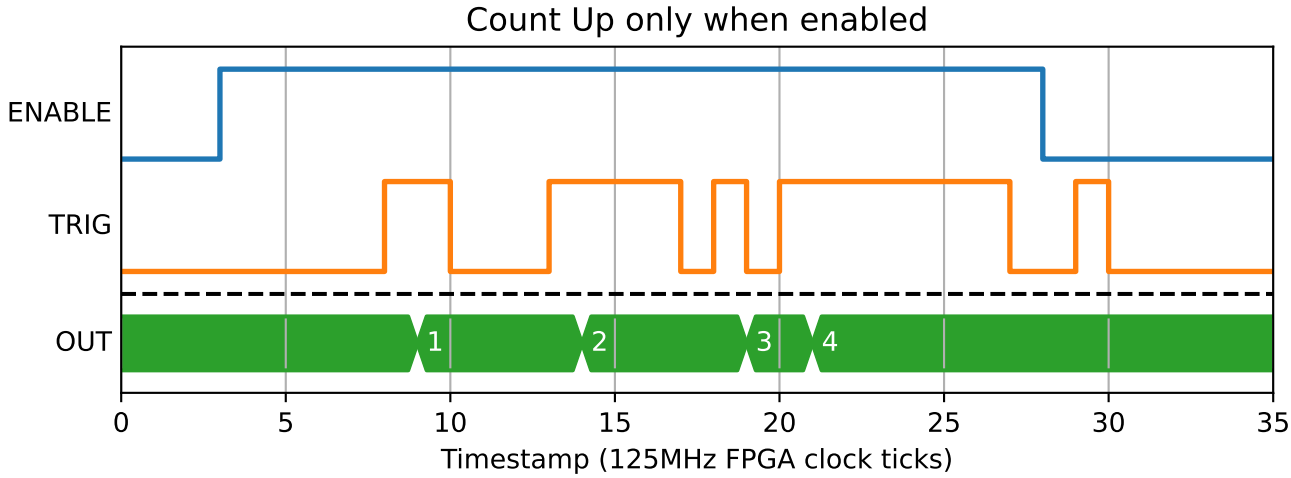
Count Up only when enabled
ENABLE
TRIG
OUT
1
2
3
4
0
5
10
15
20
25
30
35
Timestamp (125MHz FPGA clock ticks)