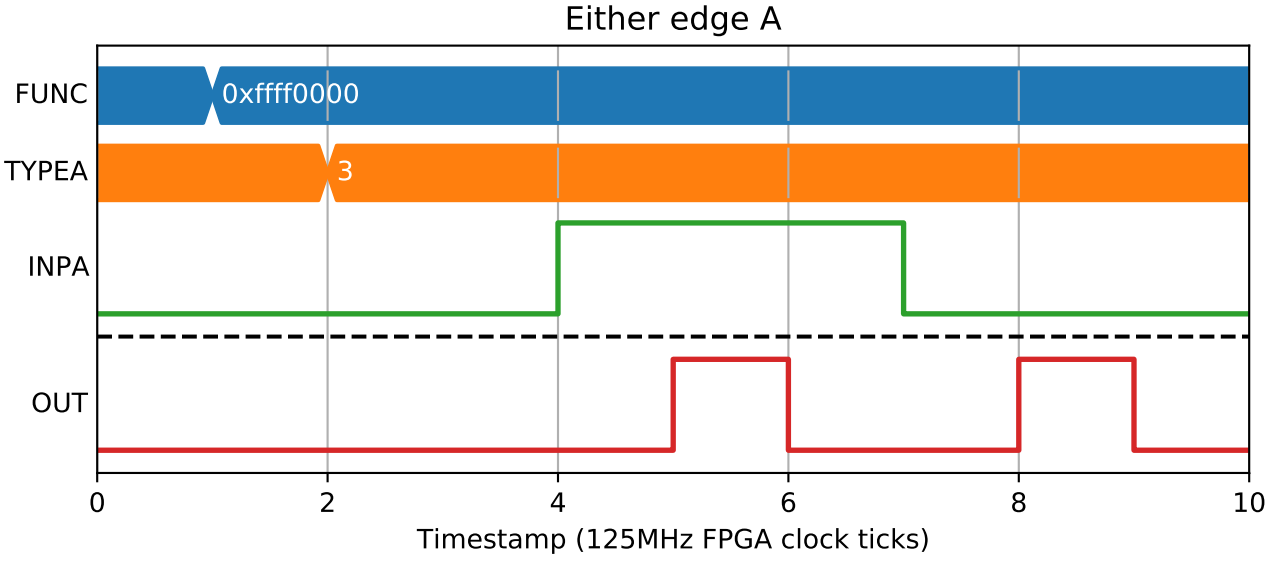
Either edge A
FUNC
0xffff0000
TYPEA
3
INPA
OUT
0
2
4
6
8
10
Timestamp (125MHz FPGA clock ticks)