

The diagram illustrates a sequence of 10 tasks, each represented by a colored bar with a duration label. The tasks are arranged in a sequence, with Task 10 starting at time 0 and Task 3 ending at time 10. The tasks are:

- Task 10 (blue): duration 10
- Task 9 (orange): duration 5
- Task 8 (green): duration 10
- Task 7 (red): duration 7
- Task 6 (purple): duration 1
- Task 5 (brown): duration 2
- Task 4 (pink): duration 3
- Task 3 (grey): duration 0

20.0

Timestamp (125MHz FPGA clock ticks)