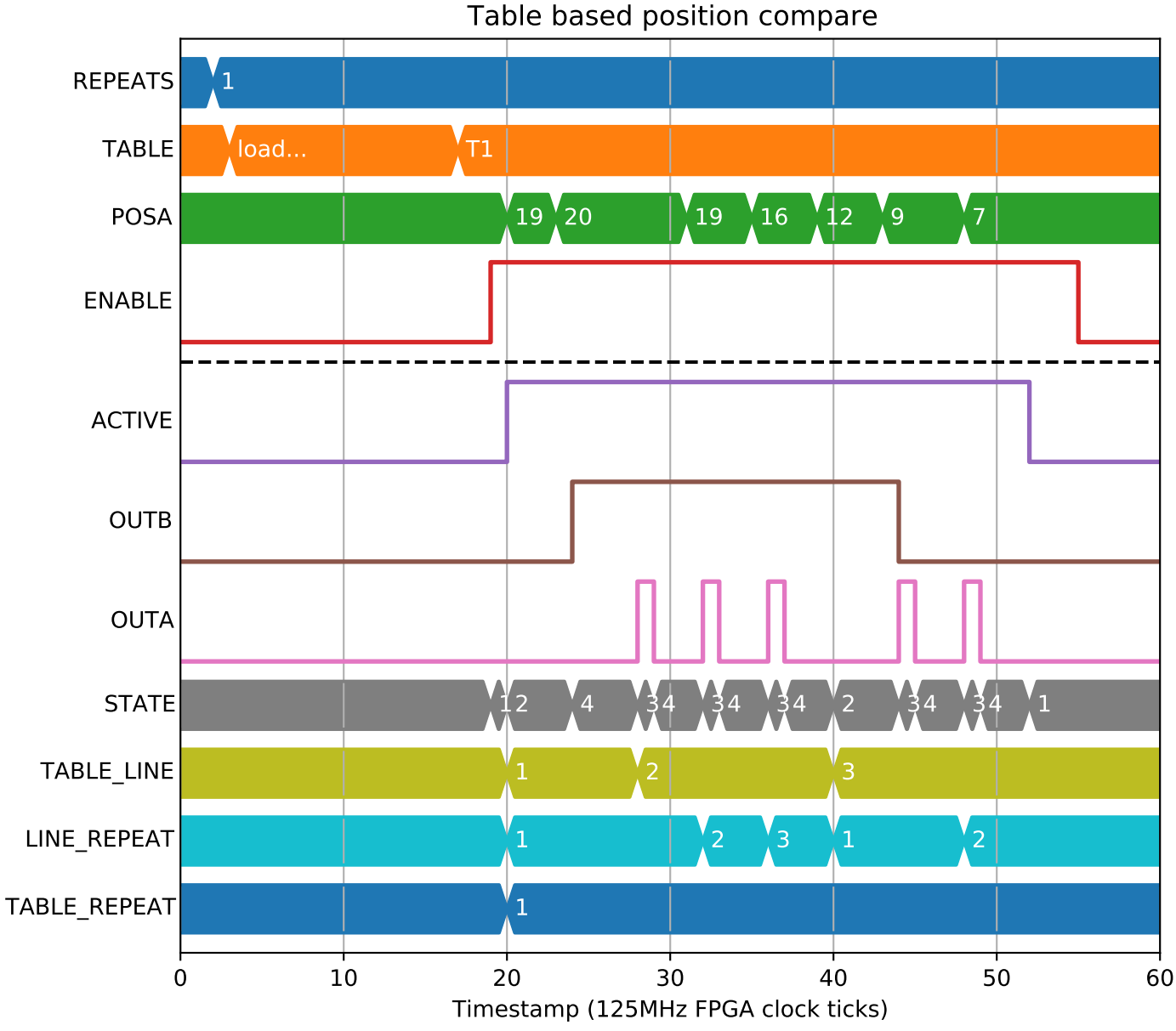
Table based position compare
REPEATS
TABLE
POSA
ENABLE
ACTIVE
OUTB
OUTA
STATE
TABLE_LINE
LINE_REPEAT
TABLE_REPEAT
1
load...
T1
19
20
19
16
12
9
7
1 2
4
3 4
3 4
3 4
2
3 4
3 4
1
1
2
3
1
2
3
1
2
1
0
10
20
30
40
50
60
Timestamp (125MHz FPGA clock ticks)