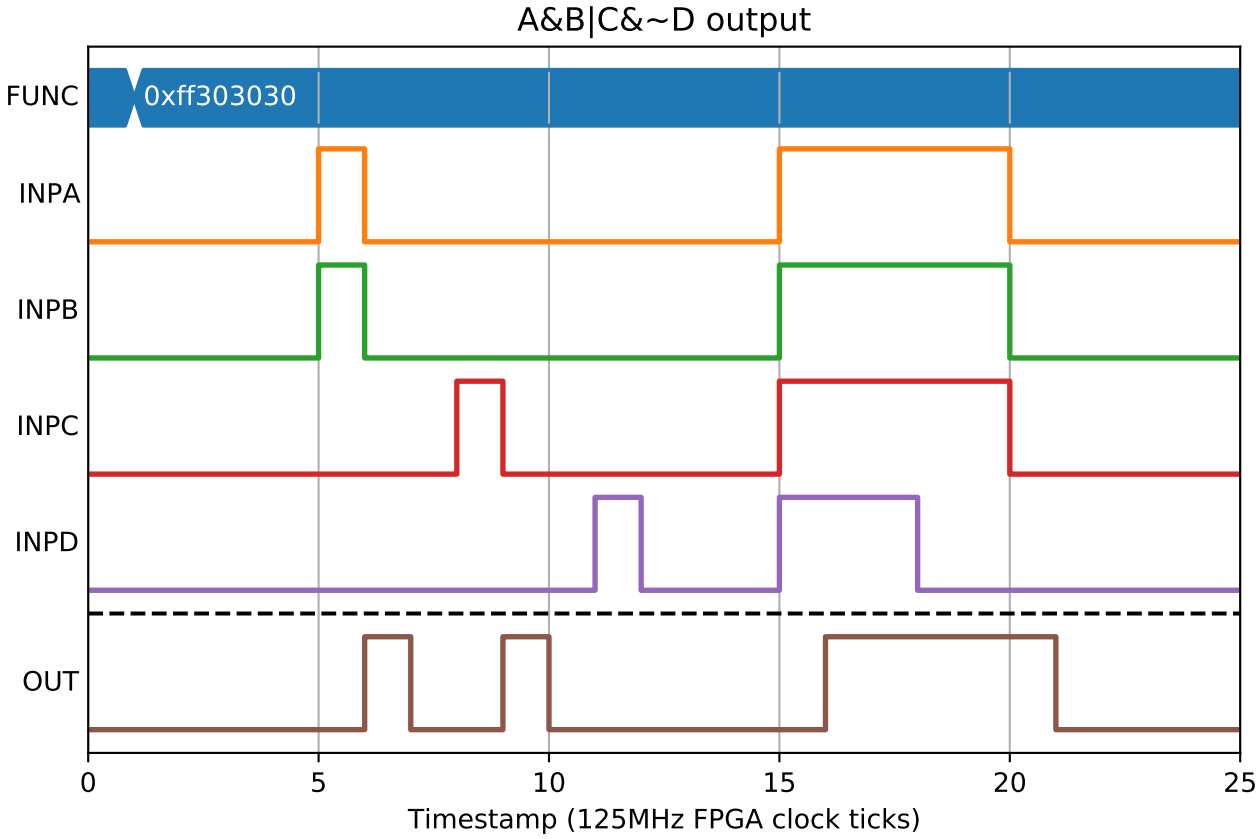
A&B|C&~D output
FUNC
0xff303030
INPA
INPB
INPC
INPD
OUT
0
5
10
15
20
25
Timestamp (125MHz FPGA clock ticks)