ARJUN PANDEY

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Education

Vellore Institute of Technology   **Oct 2021 – July 2025**
Bachelor of Technology in Electronics and Communication   *CGPA 9.38/10.00*

Indore Public School Eastern Campus   **July 2018-Aug 2020**
Higher Secondary

Research Interests

VLSI system Design and Verification, Verilog, Microcontroller Coding (Keil), Design Automation Tools (Cadence)

Conferences & Competitions

IITTNiF Agrithon 2023| PPT | **11ᵗʰ-12ᵗʰFeb 2023**
Agri based 24 Hackathon organized by the IIT Tirupati Navavishkar I-HUB Foundation   ***Tirupati, India***

* Participated in a collaborative effort to design and implement a sophisticated farmland automation system with the primary objective of effectively minimizing the expenses associated with land acquisition. Pitched a compelling project to industry experts, gaining their enthusiastic endorsement. The project is advancing with competition feedback.

* Conducted iterative experimentation using a scaled-down prototype of the operational system, facilitating real-time demonstrations and seamless data capture through dedicated web and mobile applications.

 * The collaborative team endeavor and innovative project ideation were acknowledged and featured in the local newspaper, serving a dual purpose of commendation, and fostering awareness about the prototype's significance.

 Pitchers Ideathon | PDF | PPT |
 Ideation competition based on multiple tracks

* Attained the distinguished title of "Best Freshers Team" among a competitive pool of over 250 participating teams, reflecting our exceptional performance and dedication.

* Through meticulous analysis and strategic planning, our efforts aimed to alleviate the challenges posed by high foot traffic. By implementing innovative methodologies and leveraging advanced technologies, we aspired to enhance the overall crowd flow, ensuring a smoother and more efficient experience for both locals and visitors, including adherence to COVID-19 protocols.

* Engineered a complete project framework, spanning prototype gate design, revenue model, sustainable development strategy, app model, and captivating UI design. This harmonized approach is primed to amplify project impact significantly.

Position of Responsibility

The Electronics Club, VIT Vellore   **Jan 2023 – Present**
Senior Core Member   *Vellore, India*

∗ Orchestrated a Sensored, immersive two-day electronics training workshop, a cornerstone of the club's prestigious technical week "Gravitas," drawing an impressive attendance of 350+ participants. This event left an indelible impact, fostering hands-on learning and technical exploration.

∗ Played a pivotal role as the backbone of the event, serving as the technical support for freshers during their training. This instrumental role not only facilitated seamless learning but also left a lasting impact by nurturing the technical acumen of budding enthusiasts.

Relevant Coursework

Tessolve Semiconductors Bangalore   **3rd June 2023**
VLSI internship cum Course   *Vellore, India*
    ∗ Hands on online course on building design of any given logical system using Systems Verilog.
    ∗ Basics of DSD and how to start systems Verilog design for any system provided after initial designing.

Training on System Verilog for Design and Verification (Intel)   **June 2023**
Design and Verification Training   *Vellore, India*
    ∗ Participated in an impactful industry-level training session dedicated to cultivating expertise in the design and verification of complex systems.

Web Development Course – Colt Steele (In-Progress)   **July 2021**
Full Stack Web Development course   *Indore, India*
    ∗ Worked with web technologies like HTML, CSS, JS, Tailwind CSS, etc.
    ∗ Worked with several Backend Technologies

Technical Implementation (Projects)

Personal Portfolio Website | [GitHub](#) | [Live](#) |
∗ Technologies used HTML, CSS, JS.
∗ JS based color saver that saves your color choice of the website and make the website the color you left   for your next visit.
∗ JS based affective contact us service. Also includes link to some of the major projects created and hosted online.

NASA Space Travel Website | [GitHub](#) | [Live](#) |
∗ Technologies used HTML, CSS, JS.
∗ Space travel website inspired and made based on a space missions carried out by NASA and ISRO.
∗ A interactive informative website about space and other celestial planets and their information based on various space missions.
∗ A web development project made using front end we stack.

Expense Tracker | [GitHub](#) | [Live](#) |
∗ Technologies used HTML, CSS, JS.
∗ Monthly expense tracker helping people with no much internet exposure to track their daily expenses with a very easy UI based expense tracker.
∗ JS based data saver that saves all your added transaction for further access so you can keep on add and go the incoming and outgoing cash flow easily.

Technical Skills

Languages & Frameworks (CS): HTML, CSS, JS.
Languages & frameworks (ECE): Verilog, Systems Verilog, Keil micro vision, Matlab, Cadence, R Studio, Ltspice, MultiSim.
Developer Tools: VS Code, Jupyter Notebook, MS Office.

Academic Achievements

* Qualified for **RMO** in 2018 among 480 students from MP conducted by **Mathematics Teachers' Association (MTA)** and **Homi Bhabha Centre for Science Education (HBCSE).**

* Selected among top **200** students from all CS and ECE related branches and specialization for **NSDC (National Skill Development Center)** training under **Pradhan Mantri Kaushal Vikas Yojana (PMKVY) 4.0.**

* Secured an overall rank of **105** under all branches under all boys' category batch of 2025.

* Overall academic excellence with a CGPA of **9.38**. (Up till Completion of second academic year).