Lab 01

5. Output = Pin 1 AND Pin 2

Pin 1	Pin 2	Output
1	1	1
0	1	0
1	0	0
0	0	0
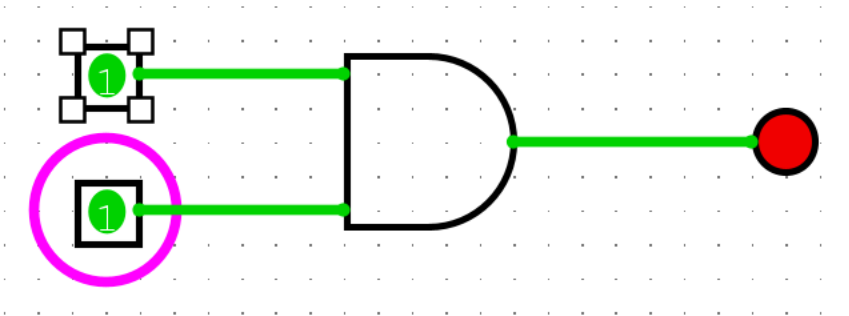
8. Output = Not Pin 1

Pin	Output
0	1
1	0
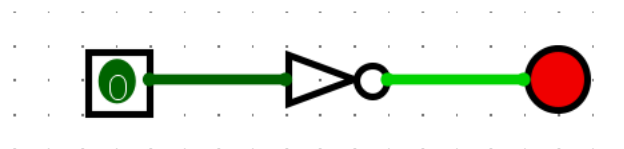
10. Output = Pin 1 NAND Pin 2

Pin 1	Pin 2	Output
0	0	1
0	1	1
1	0	1
1	1	0
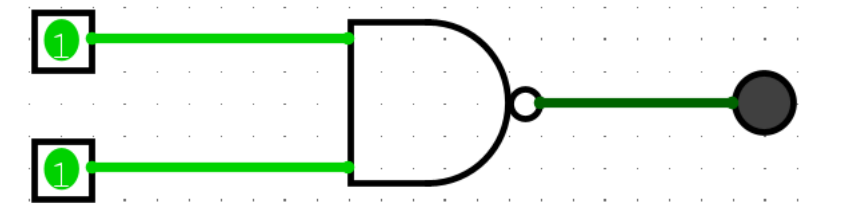
12.

Input 1	Input 2	Sum Output	Carry Output
1	0	1	0
1	1	0	1
0	0	0	0
0	1	1	0
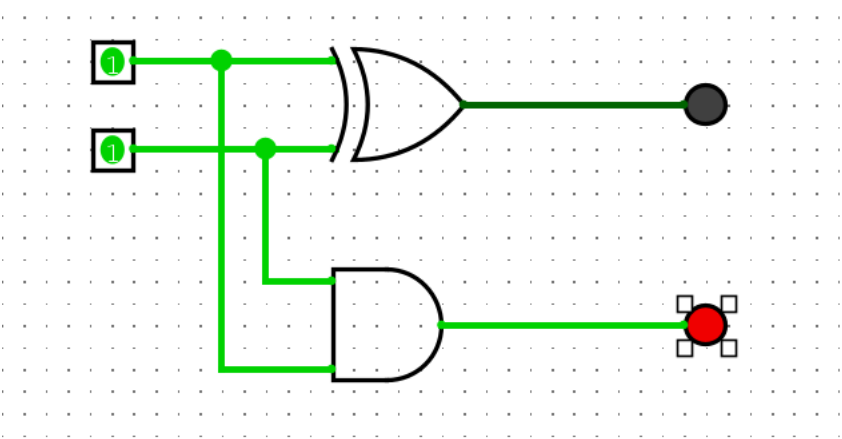
14.

Input 1	Input 2	Carry In	Sum Output	Carry Output
0	0	0	0	0
0	1	0	1	0
1	0	0	1	0

1	1	0	0	1
0	0	1	1	0
0	1	1	0	1
1	0	1	0	1
1	1	1	1	1