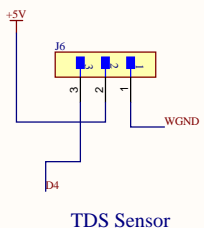
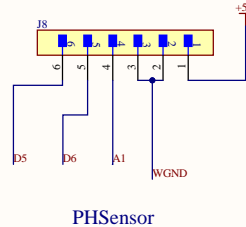
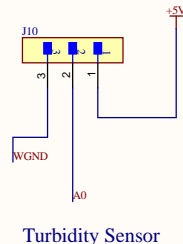
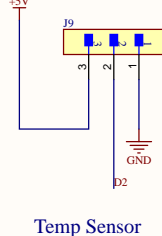
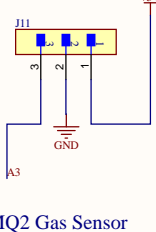
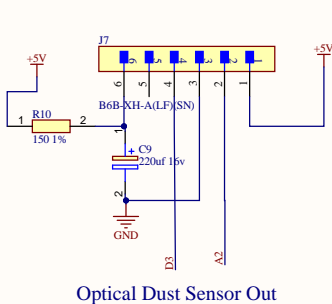
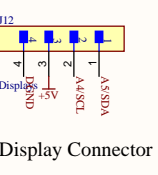
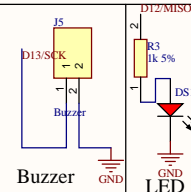
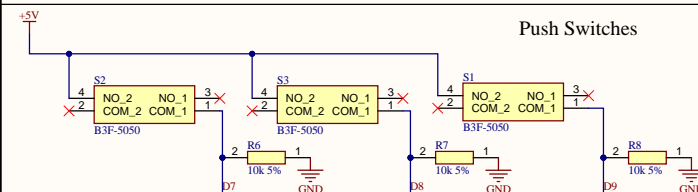
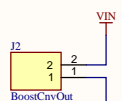
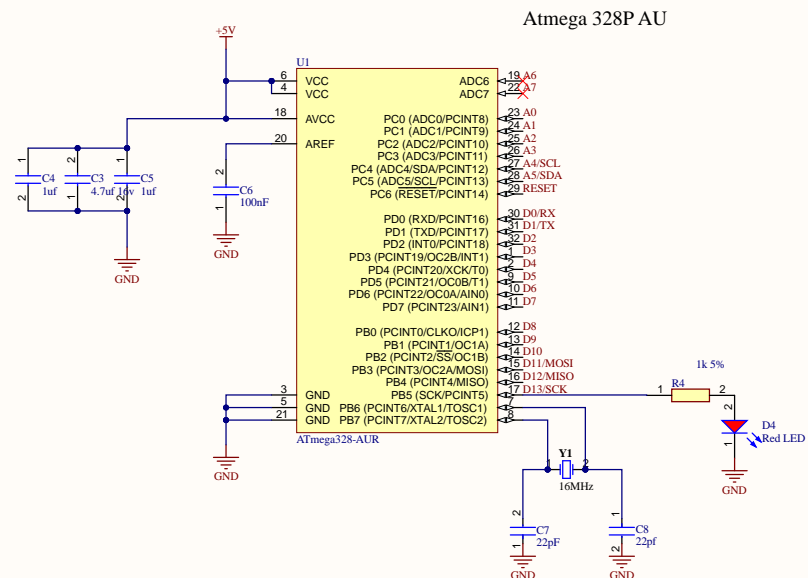
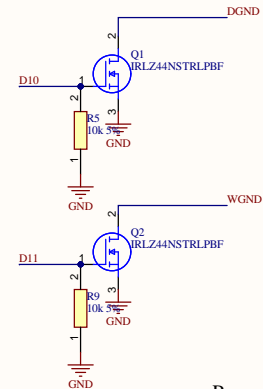
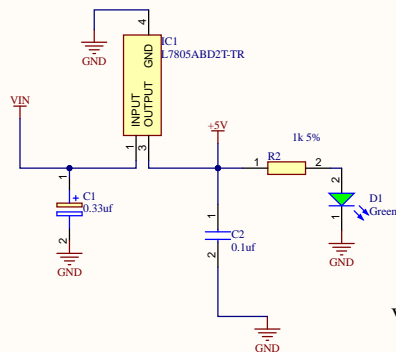
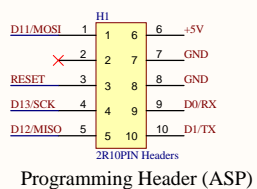
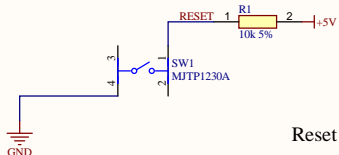
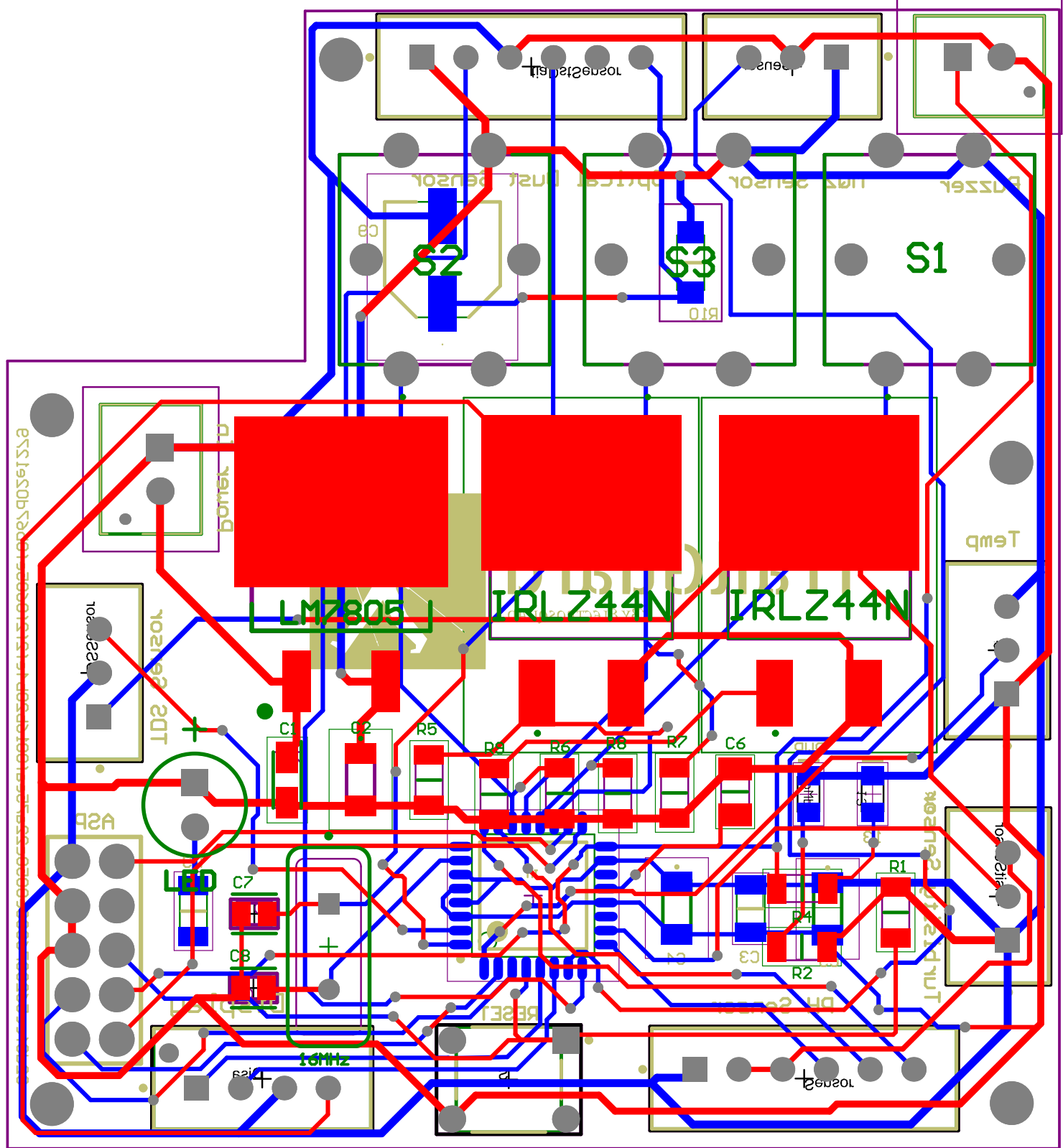


Microcontroller



APPROVALS		DATE	PROJECT				D	
ENG: *			PROJECT REVISION		DOCUMENT REVISION		DESIGN ITEM	
DSN: *			6ac8ce034142c5b39c7c62e6a12a12455d42b408cfd6		73019			
CHK: *			Pankaja Balasooriya		Pankaja Balasooriya			
REFERENCE DOCUMENTS			TITLE					
BOM:			TrailGuard					
ASSY DWG:			SIZE	CAGE CODE	DWG NO.		REV	
FAB DWG:			A3					
PCB DWG:			SCALE:	FILE NAME	trailGuard_SchDoc		SHEET	1 OF 1



Board Stack Report