Panteleimon Gavalas

Software Engineer

Profile

I am a 25-year-old aspiring software engineer with a solid foundation in Computer Engineering and a passion for creating innovative and efficient solutions.

Eager to transition into a software engineering role, leveraging strong analytical skills, hands-on experience with programming languages, and a deep understanding of complex systems.

Actively enhancing software development expertise through continuous learning and project-based applications.

Projects

View selected Projects below.

Supermarket Management Web Application

Team Project for University coursework

Tools & Technologies: Python (Flask), JavaScript (React), MySQL, HTML5/CSS

Developed a full-stack web application for supermarket administration. The application allowed for comprehensive management of supermarket data including stores, employees, customers and products.

Electric Vehicle Charging Management System

Team Project for University coursework

Tools & Technologies: Python (Flask), JavaScript (React), MySQL, HTML5/CSS

Created a full-stack web application to manage and monitor electric vehicle charging. The system served dual functionality, both for energy distributors as well as electric vehicle owners.

Work Experience

My complete work history is available on LinkedIn.

Senior Test Engineer

Infineon Technologies, Villach, Austria

04/2023 - Present

Design and implementation of automated test systems to verify the functionality, performance and reliability of Power Integrated Circuits, ensuring specified requirements through data analysis and troubleshooting.

Key contributions:

- Developed and maintained test automation scripts to streamline testing processes.
- Developed scripts to collect, log and analyze test data, ensuring accuracy and reliability of test results.
- Resolved issues in test systems by debugging software and hardware interactions.
- Created reusable libraries and frameworks to improve test efficiency and scalability across different projects.
- Used Git to manage test scripts, ensuring quality and collaborative development.

Radio Frequency Design Engineer (Master's Thesis)

Infineon Technologies, Villach, Austria

11/2021 - 03/2023

Design, development and testing of Radio Frequency Integrated Circuits that transmit and receive radio waves, ensuring optimal performance in areas like wireless communication and satellite systems.

Key contributions:

- Designed, simulated and optimized RF circuits and systems.
- Utilized software-controlled instruments for spectrum analysis, network analysis and data logging to validate system performance.
- Used software tools to design and simulate matching networks and impedance-matching circuits.
- Developed scripts using test automation platforms to streamline RF testing and measurement processes.

Education

3rd Lyceum of Nea Ionia - Athens, Greece

09/2014 - 06/2017

Apolytirion: 18.1 / 20

Panhellenic Exams: 19008 / 20000

National Technical University of Athens (NTUA) - Athens, Greece

Integrated Master's Degree in Electrical and Computer Engineering

09/2017 - 09/2022

GPA: 9.05 / 10

Relevant Coursework:

- Programming Languages
- Algorithms and Complexity
- Software Engineering
- Databases
- Computer Programming
- Foundations of Computer Science

Honors and Awards:

- Iro Georgitsi's Scholarship for Academic Excellence