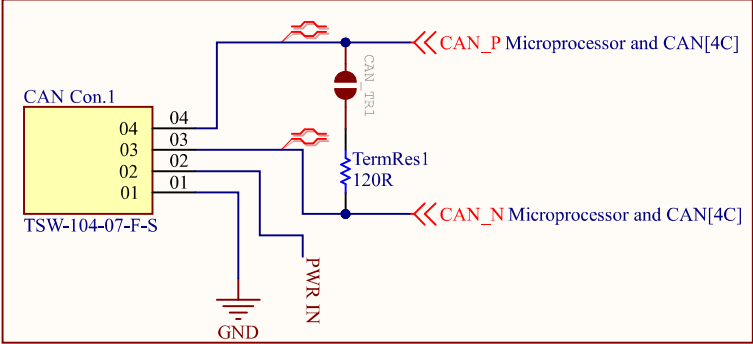
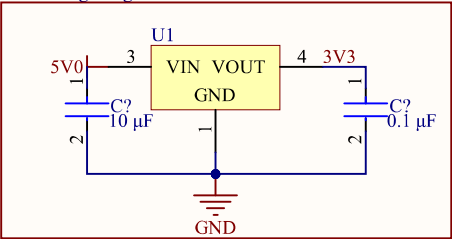


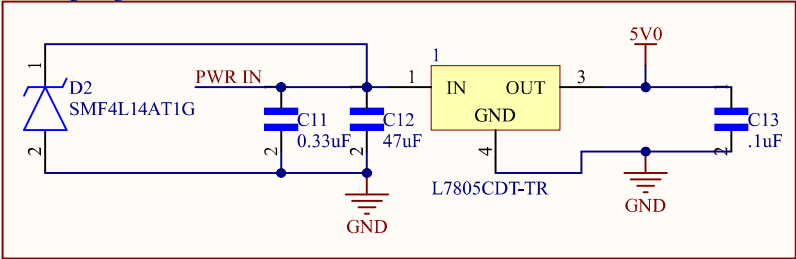
Power IN, CAN Connection



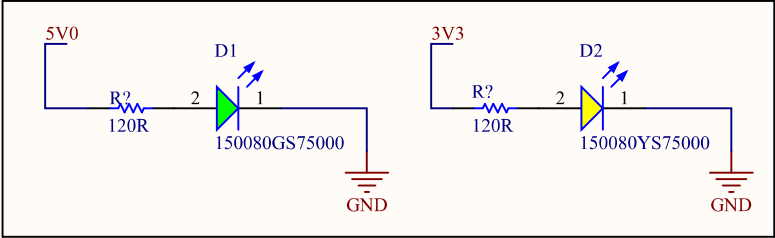
3V3 Voltage Regulation



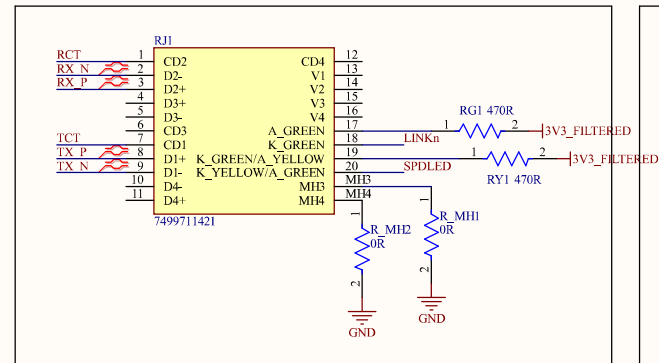
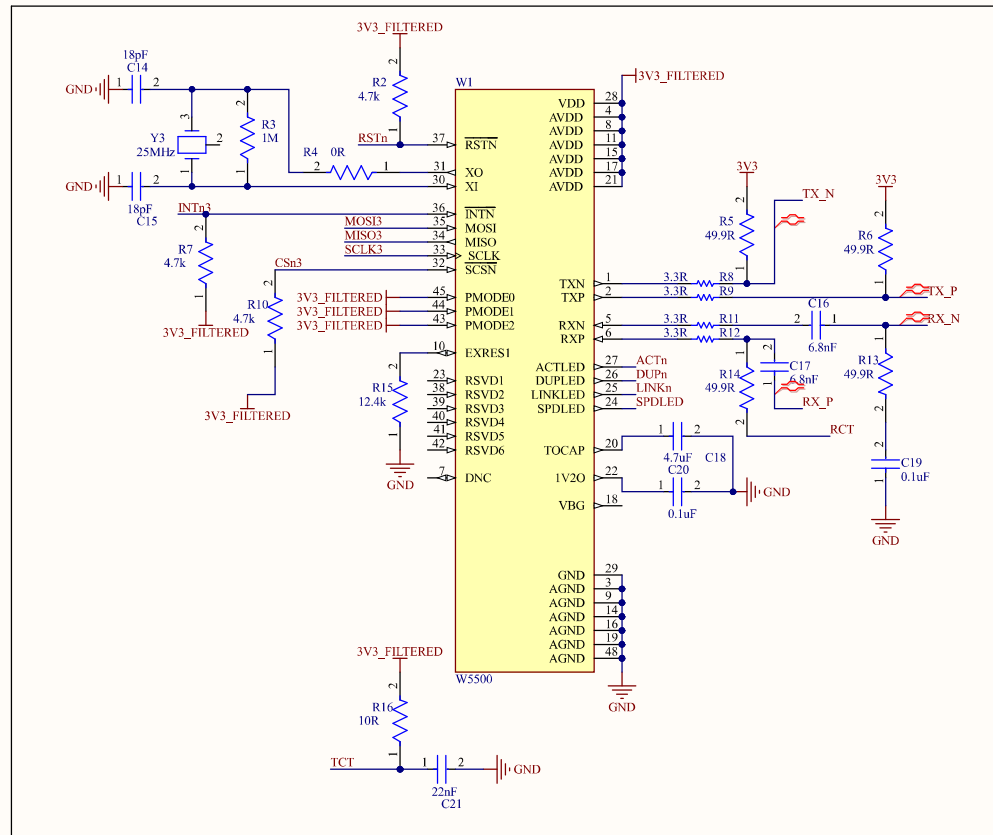
5V Voltage Regulation



5V and 3V LEDs



Title		
Size	Number	Revision
A		
Date:	10/10/2024	Sheet of
File:	C:\Users\...\Power Schematic.SchDoc	Drawn By:



**MISO**      << MISO\_OS Microprocessor and CAN[2C]

**MOSI**      << MOSI\_OS Microprocessor and CAN[2C]

**SCLK**      << SCLK\_OS Microprocessor and CAN[2C]

**CS**          << CS\_OS Microprocessor and CAN[2C]

The schematic diagram illustrates the power supply section of the BLM18BD121BH1D module. It features a 3V3 input connected to a series of capacitors (C22 to C31) and a BLM18BD121BH1D component. The output is labeled 3V3\_FILTERED and connected to GND.

ED  
TERED

3V3\_FILTERED +5

RLL1 10k RLH1 10k

INTn3 INTn

Q1 BSS138

3V3\_FILTERED +5

RLL2 10k RLH2 10k

MOSI3 MOSI

Q2 BSS138

3V3\_FILTERED +5

RLL3 10k RLH3 10k

MISO3 MISO

Q3 BSS138

3V3\_FILTERED +5

RLL4 10k RLH4 10k

SCLK3 SCLK

Q4 BSS138

3V3\_FILTERED +5

RLL5 10k RLH5 10k

CSn3 CS

Q5 BSS138

VCC is 3v3

# Board Stack Report