

A

A

B

B

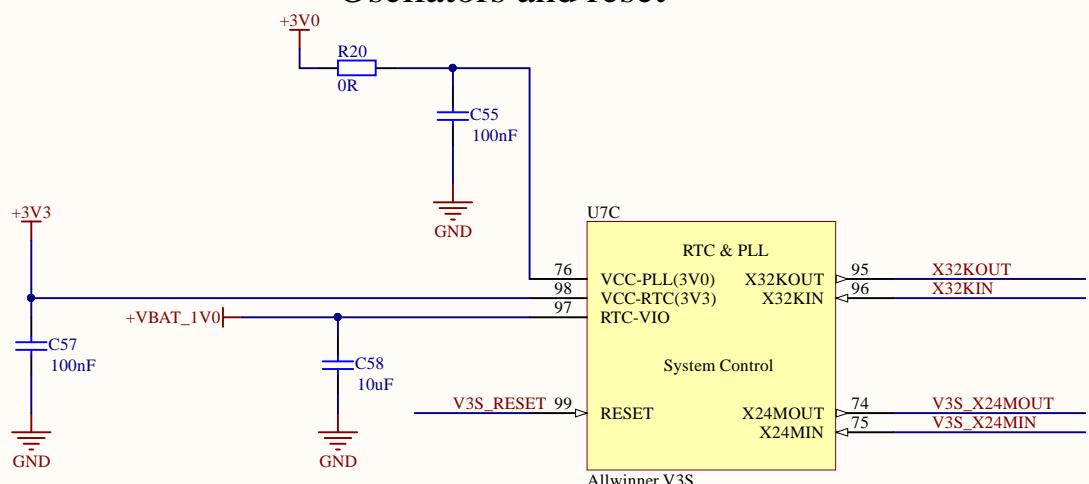
C

C

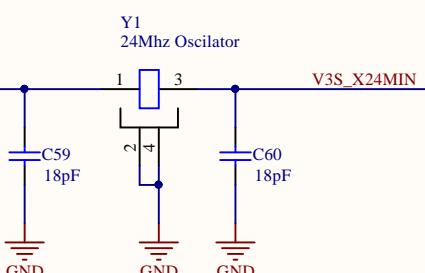
D

D

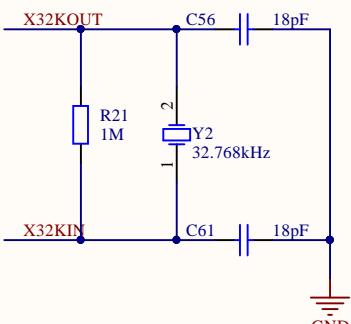
Oscillators and reset



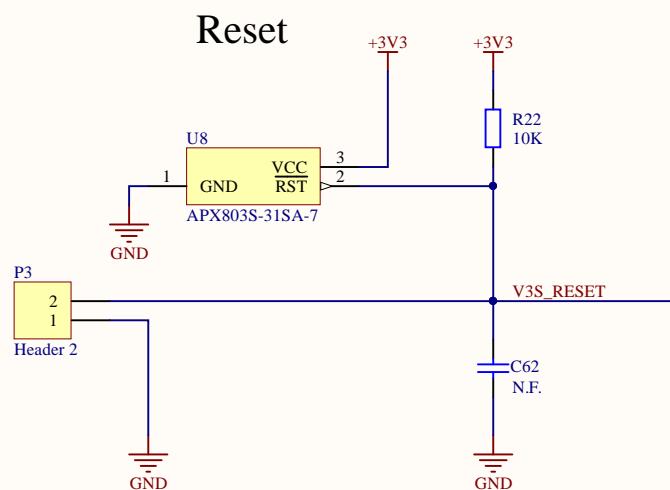
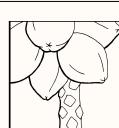
24Mhz Oscillator



32.768Hz Oscillator



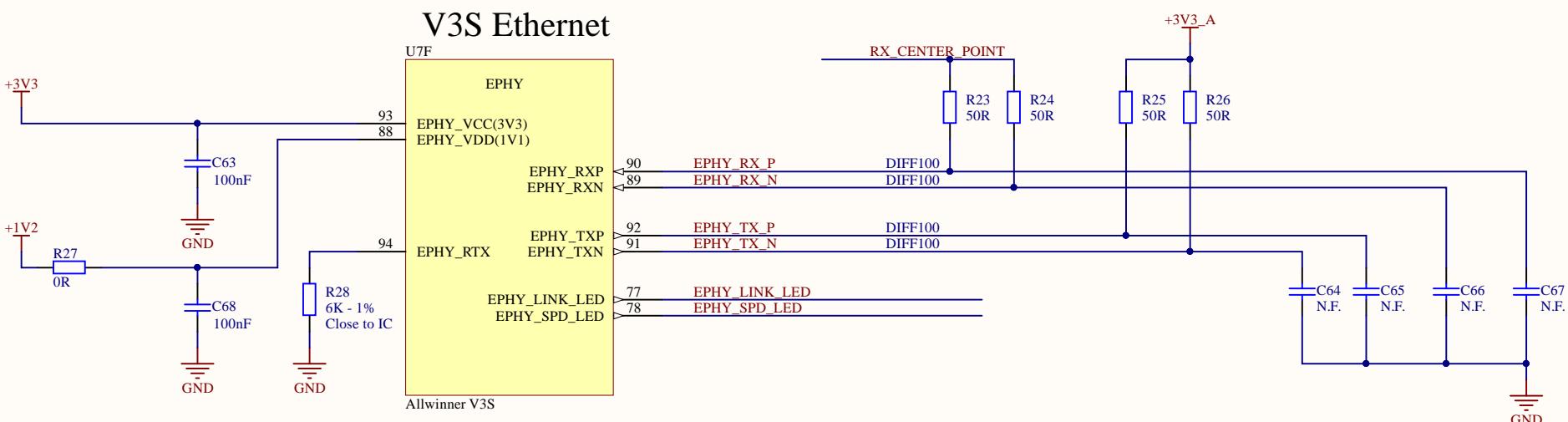
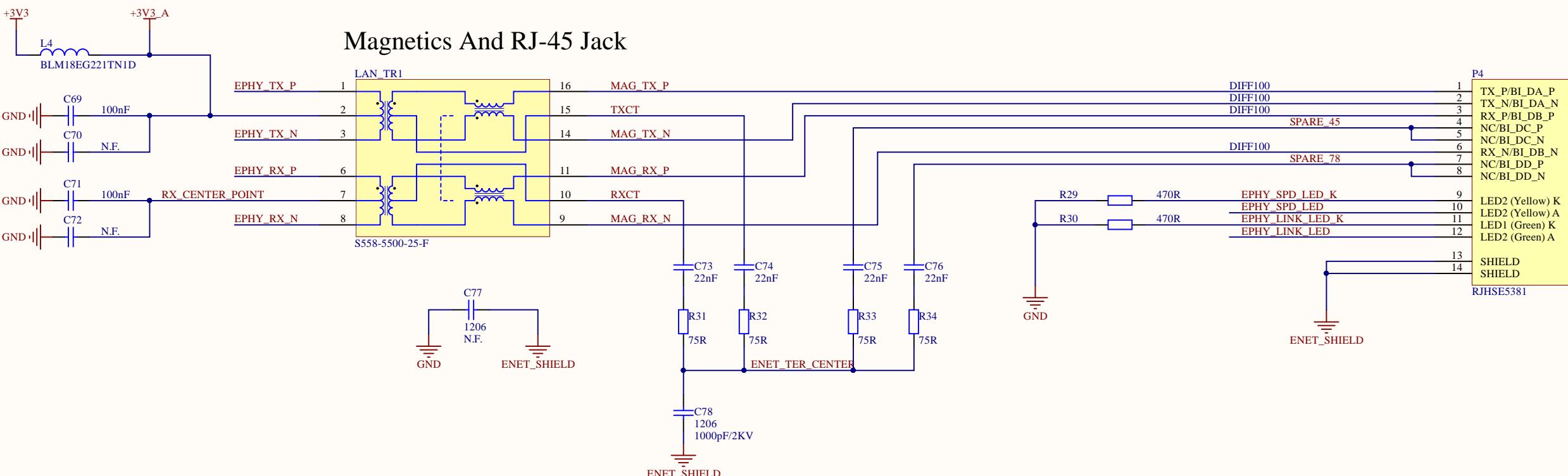
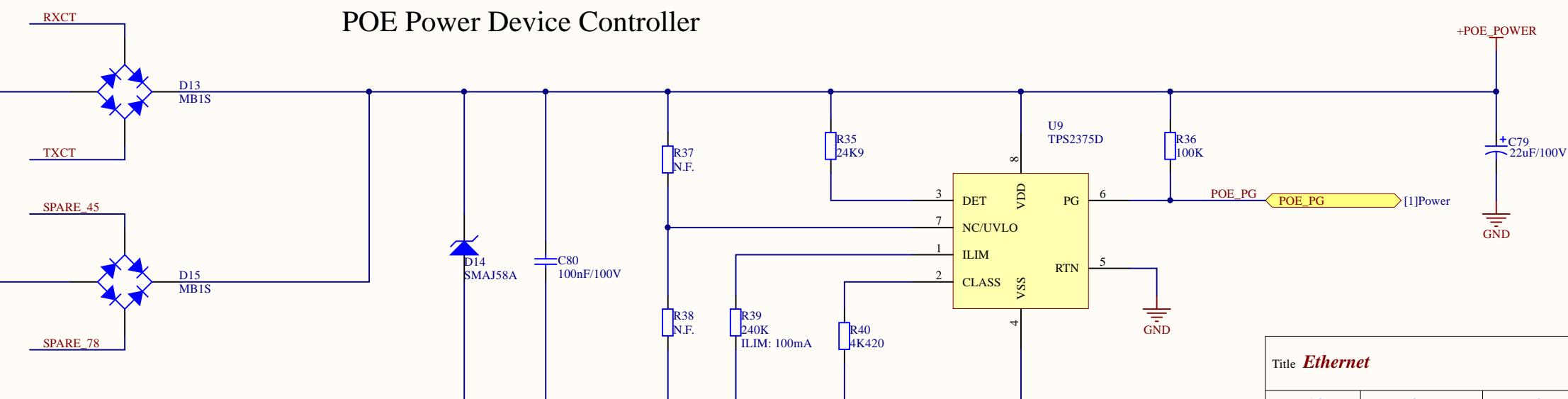
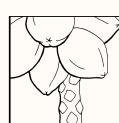
Reset

Title **V3S Power System**
 Gemini 2K Ltd
 7, Vitrage Technical Park
 27 Witney Rd
 Poole BH17 0GL
 UK


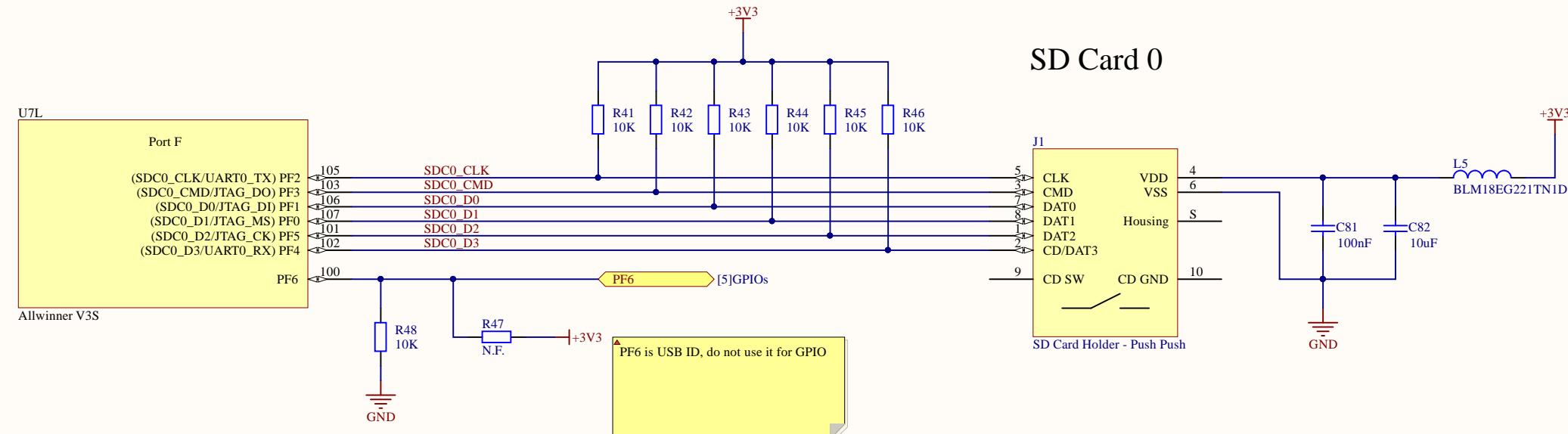
Size: A3 Number: 2 Revision: 2

Date: 7.5.2019 Time: 10:28:10 Sheet 2 of 9

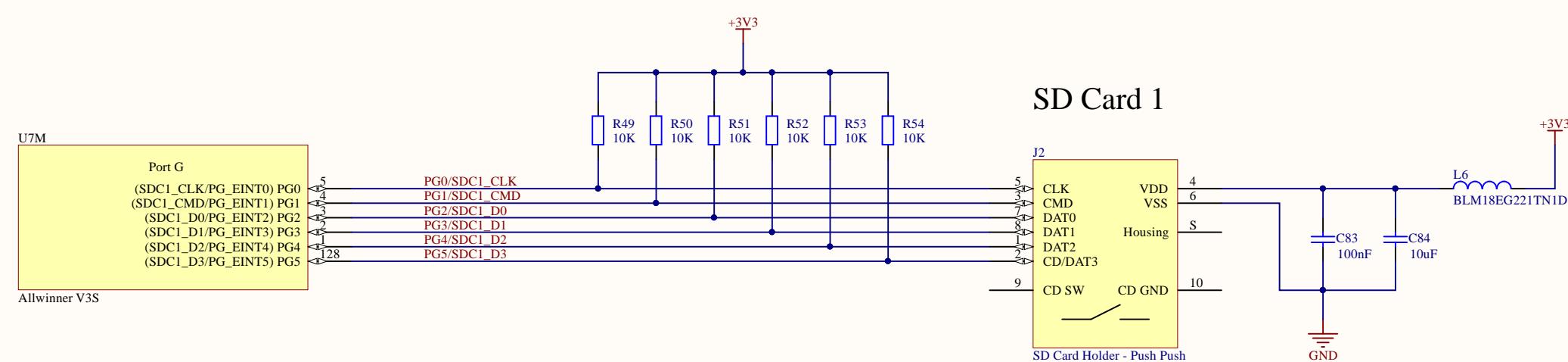
File: [2]V3S_Power_System.SchDoc

V3S Ethernet**Magnetics And RJ-45 Jack****POE Power Device Controller**Title **Ethernet**
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 Poole BH17 0GL
 UK

 Size: A3 Number: 3 Revision: 2
 Date: 7.5.2019 Time: 10:28:10 Sheet 3 of 9
 File: [3]Ethernet.SchDoc

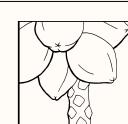
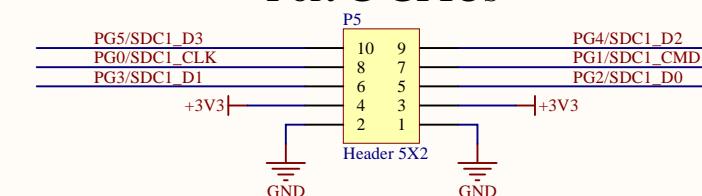
SD Card 0



SD Card 1



Port G GPIOs



U7I

Port B

39	PB0/UART2_TX
40	PB1/UART2_RX
41	PB2/UART2_RST
42	PB3/UART2_CTS
43	PB4/PWM0
44	PB5/PWM1
45	PB6/TWI0_SCK
46	PB7/TWI0_SDA
47	PB8/TWI1_SCK/UART0_TX
48	PB9/TWI1_SDA/UART0_RX
49	(TWI1_SCK/UART0_RX/PB_EINT8) PB8
	(TWI1_SDA/UART0_RX/PB_EINT9) PB9

Allwinner V3S

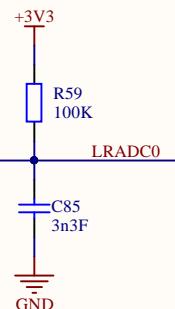
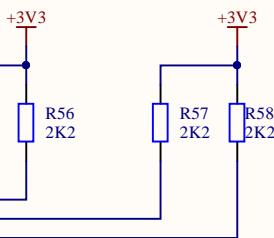
U7J

Port C

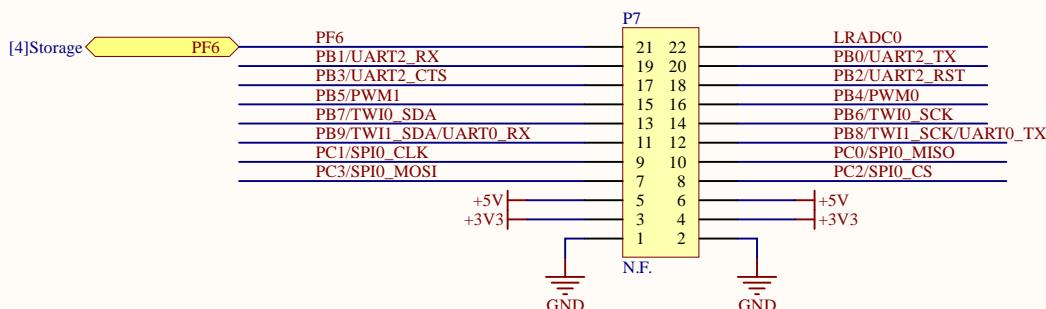
52	PC0/SPI0_MISO
53	PC1/SPI0_CLK
54	PC2/SPI0_CS
55	PC3/SPI0_MOSI

Allwinner V3S

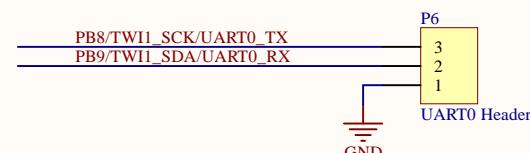
[1]Power
 PB2/UART2_RST



GPIO Header



UART0 Header (By default its the debug console)

Title **GPIOs**

Size: A3

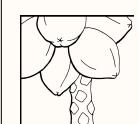
Number: 5

Revision: 2

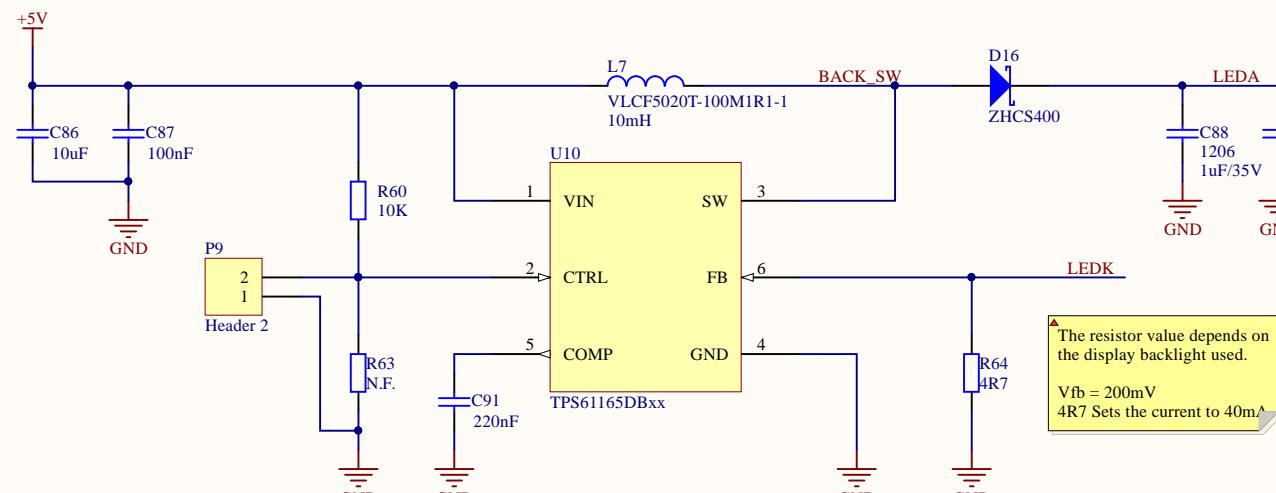
Date: 7.5.2019 r. Time: 10:28:10 Sheet 5 of 9

File: [5]GPIOs.SchDoc

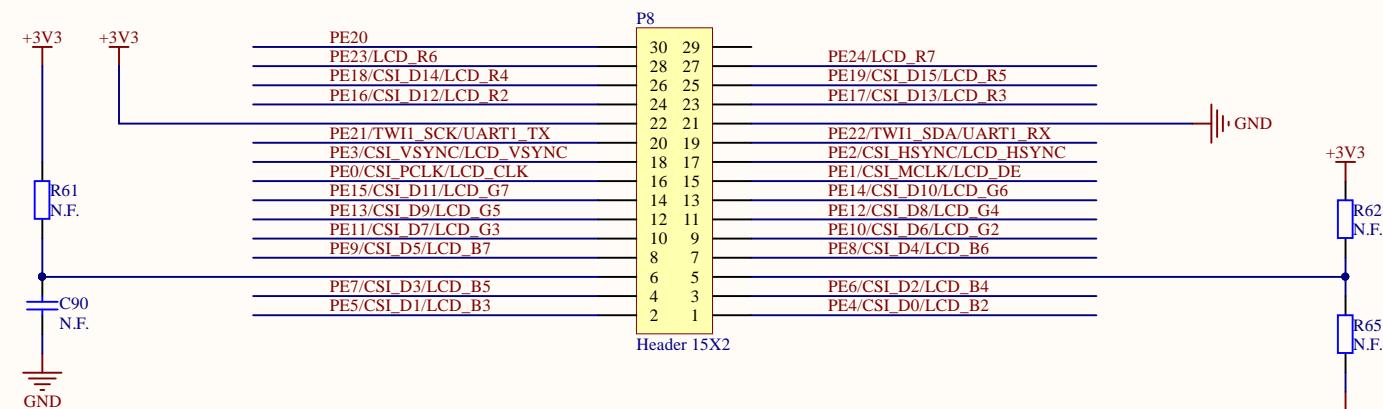
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 UK



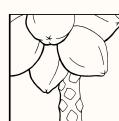
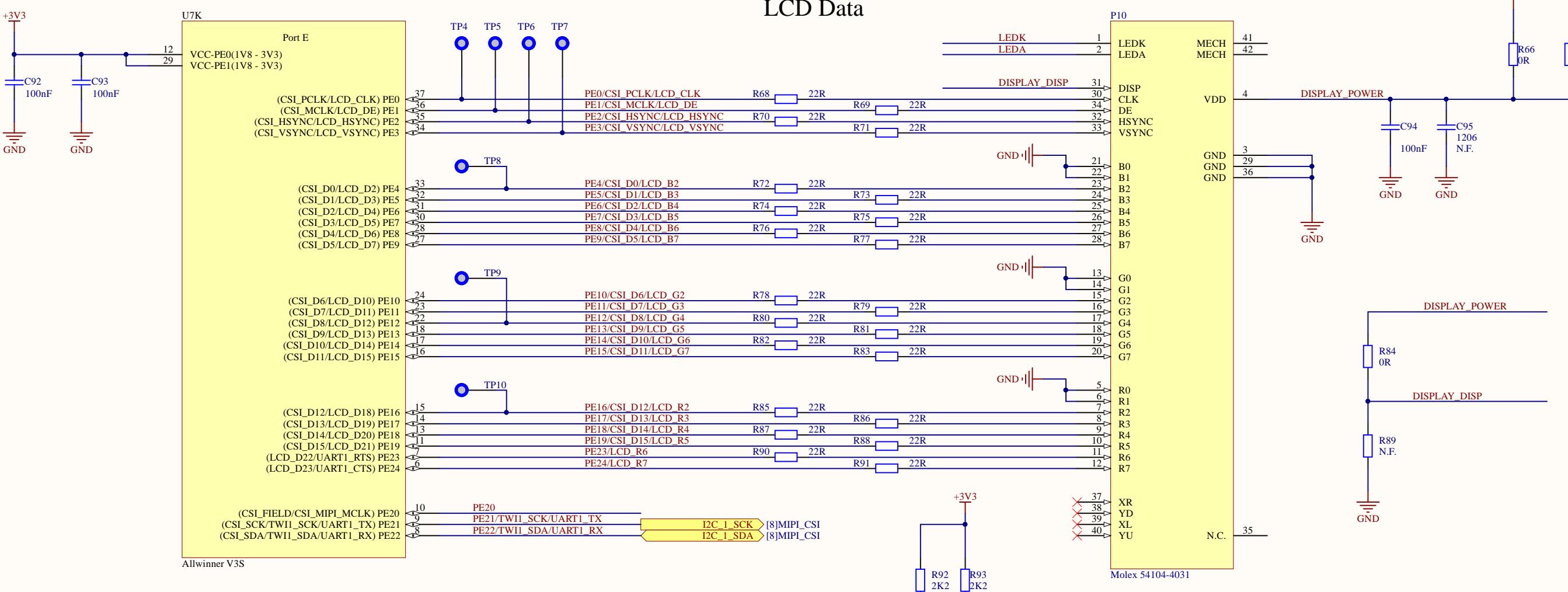
Backlight Step-Up



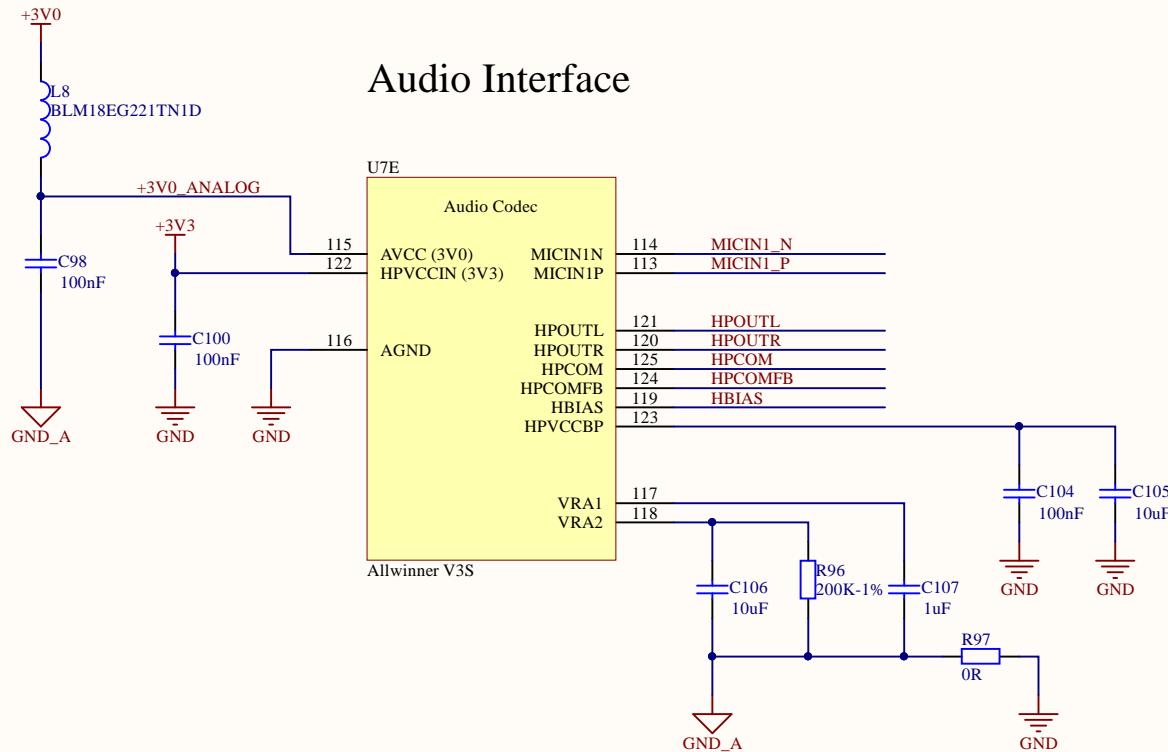
Port E GPIOs



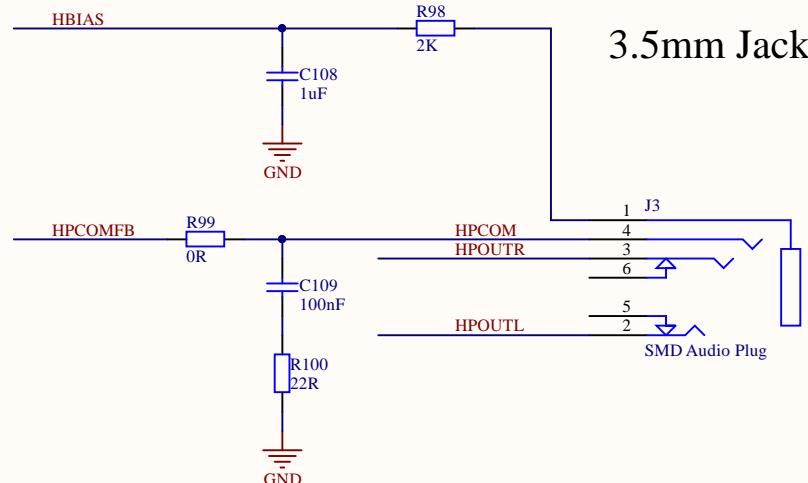
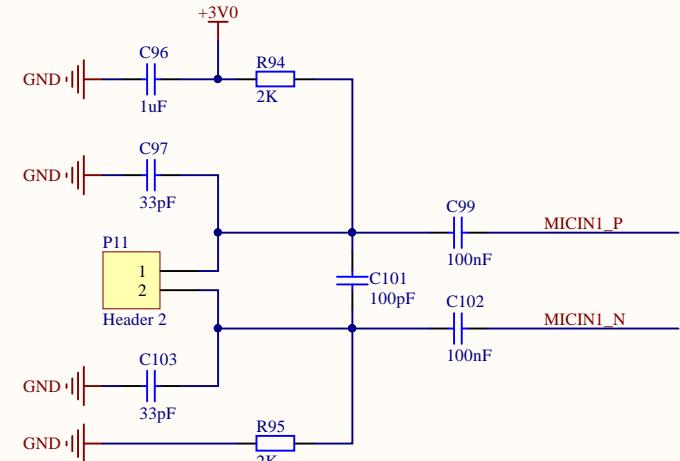
LCD Data



Audio Interface



Differential Microphone Header



Title **Audio**

Size: **A3**

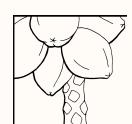
Number: **7**

Revision: **2**

Date: **7.5.2019 r.** Time: **10:28:11** Sheet **7** of **9**

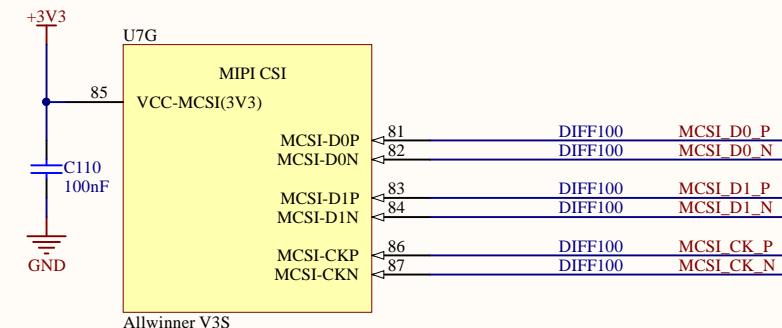
File: **[7]Audio.SchDoc**

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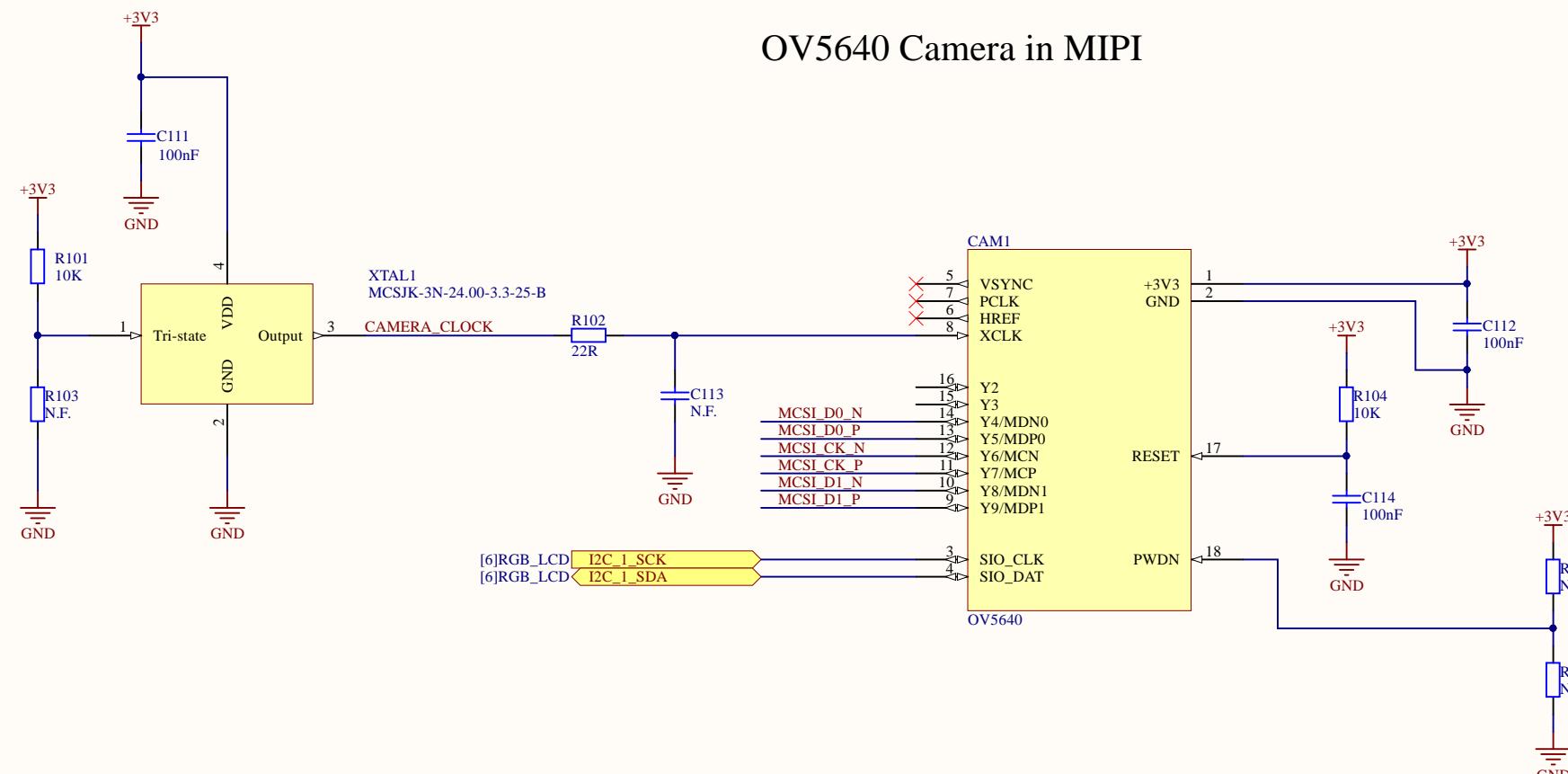
A

MIPI Interface



B

OV5640 Camera in MIPI



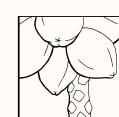
C

D

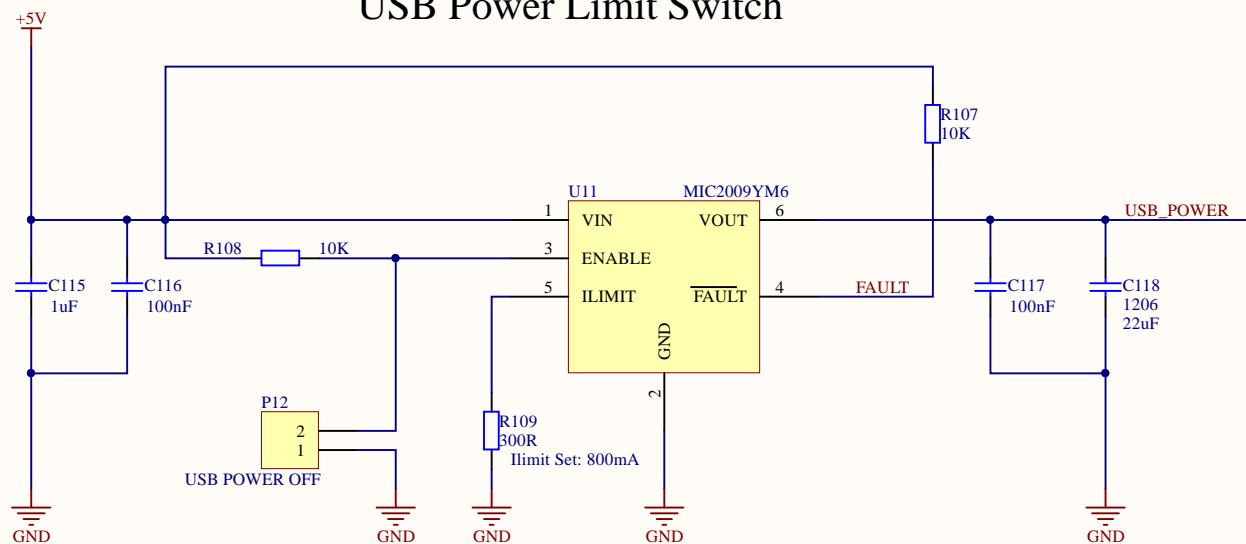
Title **MIPI CSI**

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UK

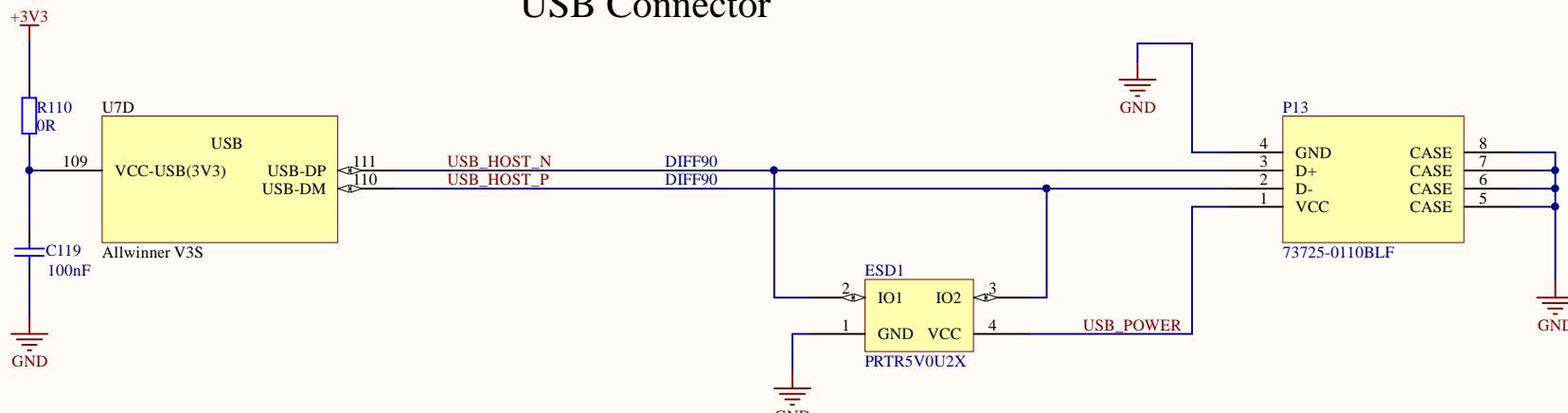
Size: A3	Number: 8	Revision 2
Date: 7.5.2019 r.	Time: 10:28:11	Sheet 8 of 9
File: [8]MIPI_CSI.SchDoc		



USB Power Limit Switch



USB Connector



Title **USB**

Size: **A3**

Number: **9**

Revision: **2**

Date: **7.5.2019 r.** Time: **10:28:11** Sheet **9** of **9**

File: **[9]USB.SchDoc**

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