Paras Shelar

✉ parasshelar2006nov@gmail.com | 📞 +91 9130583008
📍 Mumbai, Maharashtra

Career Objective

Motivated and curious Electronics Engineering diploma student with a strong interest in automation, VHDL design and embedded systems. Seeking a 6th semester internship opportunity to apply foundational knowledge in circuit design, digital electronics, and VHDL, while gaining hands-on experience in the semiconductor industry.

Education

Diploma in Electronics Engineering
Government Polytechnic, Mumbai
2023 – 2026 (Currently in 3rd Year, 6th Semester)

Projects

Function Generator *(Aug 2024)*

- Designed a basic function generator capable of producing sine, square, and triangular waveforms.
- Frequency range: up to 10 kHz.
- Gained experience in analog circuit design and waveform generation.

Dual Axis Solar Tracking System with IoT *(Oct 2025 – Present)*

- Developing a solar tracking system integrated with IoT features.
- Built a mobile app to monitor environmental parameters and control solar panel orientation remotely.
- Focused on sensor interfacing, microcontroller programming, and real-time data communication.

Skills

- Basics of VHDL
- Digital Electronics
- Internet of Things (IoT)
- PCB Soldering
- Embedded Systems
- Circuit Design