



Low-Latency EEG Marker Integration in Serious Games for Neurocognitive Assessment

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"The concern for man and his destiny must always be the primary interest of any technical effort. Never forget this between your diagrams and equations"

Albert Einstein

"Believe you can, and you are halfway there."

Theodore Roosevelt

Declaración

Me permito afirmar que he realizado esta tesis de manera autónoma y con la única ayuda de los medios permitidos. Todos los pasajes que se han tomado de manera textual o figurativa de textos publicados y no publicados, los he reconocido en el presente trabajo. Ninguna parte del presente trabajo se ha empleado en ningún otro tipo de tesis.

Manizales, 2025

Julian Andres Salazar Parias

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2025

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1 Preliminaries

1.1 Motivation

Brain–Computer Interfaces (BCIs) have emerged as a powerful class of technologies that enable direct communication between the brain and external devices. These systems are increasingly being applied in neurorehabilitation, education, and clinical diagnosis due to their ability to monitor and interpret neural activity in real time. BCIs have the potential to revolutionize the way cognitive states are assessed and modulated by offering closed-loop interaction mechanisms that adapt to the user’s brain dynamics [?, ?]. Central to this capability is the choice of neuroimaging modality, which must meet strict criteria in temporal resolution, portability, and cost-effectiveness—especially in applications involving children or naturalistic settings.

Several neuroimaging techniques have been explored for use in Brain–Computer Interface (BCI) systems, each with distinct advantages and limitations. Functional Magnetic Resonance Imaging (fMRI) offers high spatial resolution and whole-brain coverage, but its cost, immobility, and dependence on specialized facilities make it impractical for real-time interaction or integration with everyday environments [?]. Magnetoencephalography (MEG) provides excellent spatiotemporal resolution but is similarly constrained by high operational costs and the need for magnetically shielded rooms [?]. Functional Near-Infrared Spectroscopy (fNIRS), a more portable option, measures cortical hemodynamic responses with moderate spatial resolution and tolerance to movement [?]. However, its low temporal resolution limits its ability to capture fast-changing neural dynamics, such as those required for attentional monitoring or neurofeedback.

Electroencephalography (EEG), by contrast, emerges as the most suitable modality for BCI applications that demand real-time responsiveness, portability, and affordability. EEG records the brain’s electrical activity through non-invasive scalp electrodes, offering millisecond-level temporal resolution ideal for tracking rapid cognitive events like attention shifts or inhibitory control. While EEG’s spatial resolution is lower compared to fMRI or MEG, advances in signal processing—such as QEEG, functional connectivity analysis, and source localization—have greatly enhanced its ability to extract meaningful neurophysiological markers [?, ?, ?].

Moreover, EEG's lightweight hardware, low infrastructure requirements, and compatibility with embedded systems make it an ideal foundation for interactive, portable, and scalable BCI solutions.

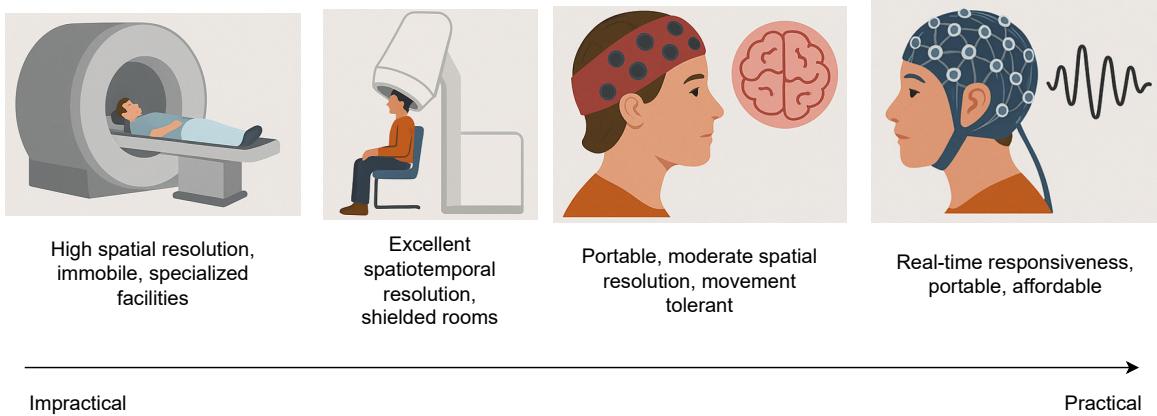


Figure 1-1: Comparison of neuroimaging modalities by spatial resolution, temporal resolution, and cost. EEG stands out for its affordability, portability, and millisecond-level responsiveness.

One of the most compelling clinical applications of EEG-based BCIs is in the assessment and intervention of neurodevelopmental disorders such as Attention Deficit Hyperactivity Disorder (ADHD). ADHD affects approximately 10 % of children in Colombia [?, ?] and is characterized by persistent symptoms of inattention, hyperactivity, and impulsivity that interfere with academic performance, social relationships, and emotional regulation. Conventional diagnostic practices rely heavily on behavioral questionnaires and clinical observation, which, while informative, are inherently subjective and susceptible to bias [?]. In this context, EEG offers a valuable alternative by enabling the objective measurement of neural correlates linked to attention and impulse control. Well-established EEG biomarkers such as elevated theta/beta ratios and altered event-related potentials (e.g., P300) have been extensively validated in the ADHD literature, making EEG a scientifically robust and clinically relevant tool for real-time cognitive monitoring and neurofeedback interventions.

Serious games are digital environments designed not solely for entertainment, but to fulfill educational, therapeutic, or cognitive objectives. In the context of neurodevelopmental disorders such as ADHD, they have become increasingly relevant as tools for both cognitive assessment and intervention [?]. Their engaging and adaptive nature allows them to target specific executive functions—like attention, inhibition, and working memory—while maintaining high user motivation, particularly among children [?]. These features make serious games particularly compatible with EEG-based BCIs for interactive cognitive modulation.

Serious games designed for ADHD not only provide engaging environments for cognitive stimulation, but also serve as structured frameworks for assessing and training specific executive functions. Two principal paradigms guide the design of these games. The first is the task-based paradigm, which integrates classical neuropsychological tasks—such as the

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Go/No-Go, n-back, or Stroop test—into interactive game mechanics. This allows for the precise measurement of behavioral responses tied to well-established cognitive models [?]. The second is the neurofeedback paradigm, in which the game dynamically responds to real-time EEG signals, offering auditory or visual feedback based on the user’s brain state. This paradigm supports operant conditioning mechanisms, encouraging users to self-regulate neural activity linked to attentional control and inhibition [?].

These paradigms are often aligned with four core cognitive models critical to ADHD pathology: attention, working memory, inhibition, and planning. Games targeting the attentional model aim to improve sustained and selective attention, often requiring players to maintain focus amid distractions or shifting stimuli [?]. Working memory is typically trained through tasks that require the temporary storage and manipulation of information, such as remembering sequences or updating mental representations. The inhibition model involves suppressing prepotent responses or resisting distractions—commonly implemented through fast-paced decision-making challenges or impulse control mechanics [?, ?]. Finally, the planning model emphasizes goal-directed behavior, encouraging users to sequence actions, solve multi-step problems, or anticipate future outcomes [?]. By aligning game mechanics with these cognitive models, serious games become powerful tools not only for engagement but for targeted neurocognitive intervention, particularly when combined with EEG-based BCIs that provide objective feedback on brain performance in real time.

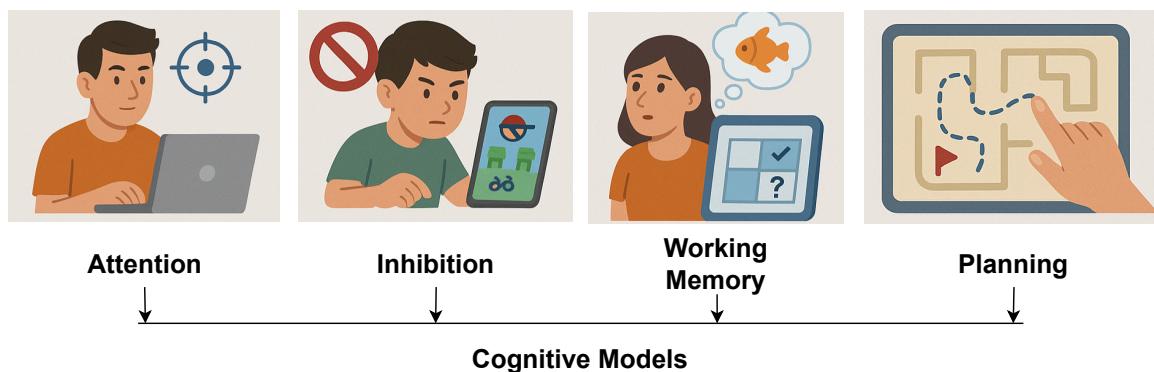


Figure 1-2: Core cognitive models targeted by serious games in ADHD interventions: attention, working memory, inhibition, and planning. Each model maps to a specific set of game dynamics and EEG markers.

Serious games integrated with BCI technology have demonstrated therapeutic benefits by reinforcing executive function, improving behavioral outcomes, and reducing symptom severity through active attention training and neurofeedback mechanisms [?]. Active BCIs, in which users intentionally modulate their focus to influence the outcome of the game, have been shown to strengthen cognitive control and promote long-term neuroplastic changes relevant to ADHD pathology [?]. These platforms also enable adaptive feedback, allowing interventions to dynamically adjust to each child’s neurocognitive profile.

However, the effectiveness of such systems depends on precise temporal synchronization

between game-generated stimuli and EEG signals. Detecting event-related potentials (such as the P300 wave) or dynamic oscillations in the theta and beta frequency bands during attentional tasks requires sub-millisecond timing accuracy [?, ?]. Without rigorous synchronization—typically achieved via TTL triggers or low-latency USB/Wi-Fi communication—EEG signal interpretation is susceptible to noise, jitter, and event misclassification [?]. This challenge is particularly critical in real-time therapeutic environments where accurate feedback is essential.

Recent developments in portable EEG hardware have expanded the applicability of BCIs for ADHD beyond clinical settings, enabling real-time monitoring and feedback in homes, classrooms, and therapeutic environments. Low-cost, wireless EEG headsets—equipped with dry electrodes and embedded microcontrollers—have been successfully integrated into neurofeedback systems and serious games designed for children [?]. These platforms allow for real-time signal acquisition and onboard processing, supporting closed-loop interventions without reliance on external computers. Thanks to ARM-based processors and system-on-chip (SoC) designs, it is now possible to run lightweight machine learning models directly on the device for real-time EEG classification [?]. Moreover, custom head-mounted EEG systems have shown reliable tracking of the theta/beta ratio, a key biomarker for ADHD, during interactive tasks [?].

Altogether, these technological advances offer a promising foundation for rethinking ADHD diagnosis and intervention—especially in child populations. Nevertheless, critical technical challenges remain, particularly the synchronization of cognitive stimuli with neurophysiological responses in embedded systems. This challenge motivates the present research, which aims to design and implement a portable EEG acquisition and analysis system with precise synchronization to game events, enabling objective, real-time support for cognitive stimulation and diagnostic processes in children with ADHD.

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1.2 Problem statement

The design and implementation of serious games synchronized with neurophysiological signals such as electroencephalography (EEG) presents a critical challenge, especially when targeting cognitive stimulation and diagnostic support in pediatric populations with Attention Deficit Hyperactivity Disorder (ADHD). The scientific validity of such applications is fundamentally dependent on the precise temporal synchronization of at least two disparate data streams: the high-temporal-resolution physiological data from the EEG system and the context-dependent event data generated by the serious game. A core technical obstacle lies in achieving this precise synchronization, a requirement that is essential for both the accuracy of event-related potential (ERP) measurements and the effectiveness of real-time interventions.¹ This thesis addresses two primary facets of this challenge: the temporal inaccuracies introduced by system-level operations and the physical limitations of the hardware itself.

1.2.1 Unpredictable Latency and Jitter Between Game Events and EEG Recordings

The primary technical issue in synchronizing EEG data with serious game events is the presence of unpredictable latency and jitter.² Latency is the delay between an event's physical occurrence (e.g., a stimulus appearing on screen) and its corresponding timestamp being recorded in the data stream. A more pernicious issue is jitter, defined as the statistical variability in that latency over time.⁴ While a constant latency might be correctable in post-processing, jitter introduces random, unpredictable timing errors that cannot be easily removed after data acquisition. This issue is caused by several interrelated factors: buffering delays in data pipelines, the non-deterministic scheduling of non-real-time operating systems, variability in communication protocols (such as USB, Bluetooth, or the Lab Streaming Layer), and asynchronous execution within game engines like Unity.⁵ These conditions lead to a lack of temporal precision, where the timestamp of an in-game event does not accurately align with the corresponding entry in the EEG data stream. This misalignment significantly compromises the quality of neurophysiological analysis. ERP components such as the P300 and N200, which are commonly used to evaluate attentional processes in ADHD, depend on millisecond-level synchronization between stimulus onset and neural response.⁷ When event markers are not precisely aligned due to jitter, the resulting ERP waveform becomes temporally "smeared," causing a reduction in both amplitude and interpretability, which degrades the signal-to-noise ratio and threatens diagnostic reliability.² This is particularly critical in pediatric populations where subtle attentional deficits are being assessed. Furthermore, in real-time systems like neurofeedback applications, where immediate feedback is essential for operant conditioning, even minor delays can disrupt the feedback loop. If the user receives auditory or visual feedback that no longer corresponds precisely to their brain state, the therapeutic effectiveness is reduced, potentially leading to user disengagement or ineffective

training outcomes.² The temporal precision required is demanding; some brain-computer interface (BCI) paradigms require accuracy within ± 2 milliseconds, yet jitter introduced by a game's graphical rendering at 50 frames per second can be as high as 20 milliseconds. Recent studies have quantified these challenges. For example, Larsen et al. (2024) found that even in systems optimized with Unity and LSL, event marker delays averaged 36 milliseconds with a jitter of 5 to 6 milliseconds—well above the acceptable margin for many ERP analyses.⁹ Additionally, Brain Products (2024) reports that embedded platforms lacking efficient buffering and timestamping can exhibit latencies up to 100 milliseconds, particularly under high computational load.⁵ These delays, caused by a lack of dedicated real-time scheduling and protocol optimization, result in a substantial loss of synchronization fidelity, ultimately undermining both research validity and clinical utility.

1.2.2 Resource and power constraints in embedded EEG platforms

The second major issue stems from the computational and energy limitations of embedded and wearable EEG systems. Designed to be mobile and unobtrusive, these systems often operate on limited battery power, constrained CPU cycles, and reduced memory.¹¹ These constraints are exacerbated when the system must simultaneously support real-time data acquisition, multichannel EEG streaming, and high-frequency event marker registration. Conventional EEG setups that rely on centralized data processing can also lead to high energy consumption and increased data transmission latency.¹² These limitations make it difficult to implement low-latency communication and high-resolution timestamping. Wireless data transmission, in particular, is very power-intensive.¹³ Protocols such as Bluetooth and Wi-Fi—commonly used in portable EEG systems—can introduce packet retransmissions, buffering delays, and inconsistent delivery times that worsen synchronization accuracy.⁵ The consequences are significant. System designers are forced to lower EEG sampling rates, simplify marker handling, or accept increased delays—all of which reduce the reliability of the collected data and the interactivity of the game.¹⁴ For example, a review of wearable EEG systems found that wireless devices consistently showed worse timing stability and synchronization performance compared to wired configurations, especially when embedded resources were under heavy computational load. Brain Products (2024) corroborates these findings, warning that system performance degrades as channel count and sampling rate increase—conditions commonly required in clinical-grade EEG systems.⁵ This creates a fundamental trade-off: increasing signal fidelity and temporal resolution compromises system portability, while optimizing for mobility sacrifices diagnostic precision. Therefore, a critical gap exists in establishing a robust methodology to reliably synchronize multimodal data streams from EEG systems and dynamic serious games with quantifiable, millisecond-level precision, while also operating within the power and resource constraints of embedded platforms. This thesis addresses the problem of ensuring the temporal integrity of these data streams to enable scientifically valid analysis of neuro-cognitive processes during gameplay.

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Challenges in EEG Synchronization with Games

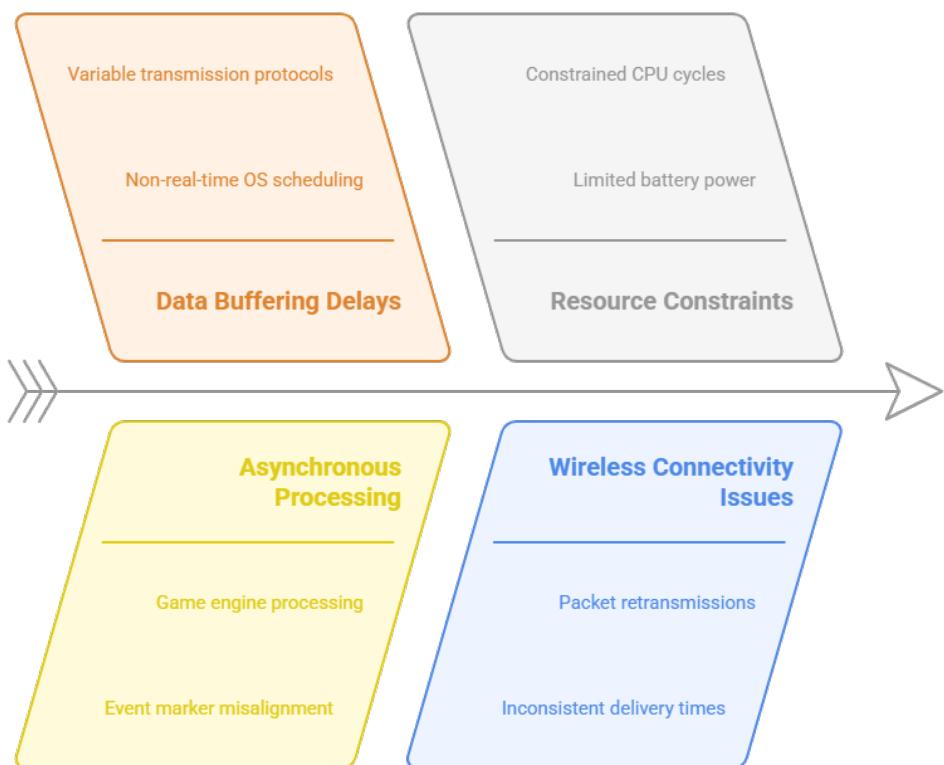


Figure 1-3

1.3 Pregunta de investigación

How can a low-latency and low-jitter data synchronization framework be developed and validated to ensure the temporal integrity of multimodal data from embedded EEG systems and dynamic serious game events, while respecting the inherent resource and power constraints of such platforms?

1.4 Aims

1.4.1 Objetivo General

Diseñar e implementar una arquitectura de adquisición de señales EEG optimizada para aplicaciones en entornos educativos y clínicos, enfocada en la reducción de latencias y la sincronización precisa de eventos, para mejorar la evaluación objetiva de patrones cognitivos y emocionales en niños con TDAH.

1.4.2 Objetivos Específicos

1. Analizar las limitaciones técnicas de los sistemas actuales de adquisición de EEG, incluyendo las latencias de transmisión y la baja densidad de canales.
2. Desarrollar algoritmos de baja latencia y estrategias de sincronización temporal para garantizar la alineación precisa entre estímulos de juegos serios y respuestas EEG.
3. Evaluar la arquitectura propuesta en entornos clínicos y educativos, verificando su eficacia en el diagnóstico y tratamiento del TDAH.

1.5 Estado del arte

In recent years, numerous wireless systems for EEG data acquisition have been developed, with two main approaches standing out: conventional remote monitoring systems and portable smart systems. The former simply digitize the EEG signals and transmit them to a remote unit for processing, usually in a deferred manner [?]. On the other hand, portable systems preprocess the signals on a local device, such as a microcontroller (MCU), and wirelessly transmit the data using low-power consumption protocols. **1-1** This latter approach is crucial for real-time applications, where low latency is essential.

Marker synchronization in portable EEG acquisition systems [?], particularly in applications combined with serious games [?], faces several technical and operational challenges. One of the main issues lies in latency in data transmission protocols. In portable EEG systems, precise synchronization between brain events and interactions in the game is crucial [?], but inherent limitations of portable acquisition systems, such as latencies in data transfer protocols, can cause temporal mismatches. These latencies primarily stem from bandwidth constraints in wireless transmission and the need to process large volumes of data in real-time [?].

The type of electrode [?] and the number of channels [?] are determining factors in the quality of the data acquired in portable systems. Although dry electrodes offer greater portability, they tend to generate lower-quality signals due to reduced conductivity, which can complicate precise synchronization with other devices, such as serious games. On the other hand, the use of systems with **low channel density (e.g., 8-16 channels)** [?] is a common strategy in these portable systems to minimize size and improve portability. However, low channel density can affect the spatial resolution of EEG data, limiting the ability to perform accurate analysis of brain patterns. This challenge is reflected in the need to optimize sampling [?] and data transfer protocols [?] to ensure that captured signals are transmitted efficiently without significant information loss

The sampling rate is another critical factor, as it directly affects the temporal resolution of EEG signals. The combination of low channel density and insufficient sampling rate can make it difficult to capture fast brain events, such as attention shifts, which are essential in applications like serious games. Furthermore, Signal Front-End Amplifiers (AFE) [?] play a key role in signal quality. While low-cost AFEs may be suitable for portable systems, they tend to have limitations in processing capacity, which impacts data synchronization by generating noise and distortions in the EEG signals, especially when connected to mobile devices with lower processing power.

Battery life [?] is a significant constraint for portable systems that require long monitoring sessions. EEG systems that operate for several hours often need to optimize their energy consumption, which may involve reducing the sampling rate or channel density, once again impacting data quality and real-time synchronization.

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Table 1-1: Dispositivos de adquisición utilizados para BCI. La tabla ofrece una descripción general de los diferentes dispositivos de hardware, sus especificaciones y protocolos de comunicación.

Hardware BCI	Empresa	Tipo de Electrodo	Canales	Frecuencia de Muestreo	AFE	Protocolo y Transferencia de Datos	Duración de la Batería
Cyton + Daisy [?]	OpenBCI	Flexible / Húmedo / Seco	16	250 Hz - 16 kHz	ADS1299	RF / BLE / Wi-Fi	8 h
actiCAP [?]	Brain Products GmbH	Flexible / Húmedo / Seco	16	256 Hz - 16 kHz	-	USB	16 h
EPOC X [?]	Emotiv	Rígido / Húmedo	14	128 Hz	-	BLE / Bluetooth	6–12 h
Diadem [?]	Bitbrain	Rígido / Seco	12	256 Hz	-	Bluetooth	8 h
g.Nautilus [?]	g.tec	Flexible	8 / 16 / 32	250 Hz	ADS1299	Propietario	10 h
Plataforma para EEG ambulatorio [?]	-	Activo / Seco	32	250 Hz - 1 kHz	ADS1299	Wi-Fi 802.11 b/g/n	26 h
Sistema para neurofeedback [?]	-	Pasivo / Seco	40	250 Hz	ADS1298	RF	-
BEATS [?]	-	Flexible / Húmedo	32	4 kHz	ADS1299	Wi-Fi	24 h (cableado)

In the field of brain-computer interfaces (BCIs), several devices have been developed, each with unique features tailored to specific use cases such as clinical research, neurofeedback, or consumer applications. The Cyton + Daisy system by OpenBCI [?] supports up to 16 channels and offers a wide sampling rate range of 250 Hz to 16 kHz, making it suitable for high-resolution EEG acquisition. The device uses flexible, wet, or dry electrodes and incorporates the ADS1299 AFE for high-quality signal conversion. It supports data transfer via RF, Bluetooth Low Energy (BLE), and Wi-Fi, allowing for versatile connectivity. With a battery life of 8 hours, this system is highly adaptable, suitable for both research and practical applications in various environments. Another system, actiCAP [?] by Brain Products GmbH, features flexible, wet, or dry electrodes and is capable of recording up to 16 channels with a

sampling rate range from 256 Hz to 16 kHz. The actiCAP does not use a dedicated AFE and instead relies on a USB protocol for data transfer. The device provides a robust 16-hour battery life, making it an ideal choice for long-duration experiments and clinical settings that require stable signal acquisition over extended periods. The EPOC X [?] by Emotiv is a more compact and consumer-oriented BCI device that uses rigid, wet electrodes and supports 14 channels with a sampling rate of 128 Hz. This device employs Bluetooth Low Energy (BLE) for wireless data transfer, and its battery life ranges from 6 to 12 hours, depending on usage. While its lower sampling rate may limit its use for high-resolution research, the EPOC X remains a popular choice for applications in neurofeedback, cognitive training, and general user interaction. The Diadem [?] system by Bitbrain uses rigid, dry electrodes and supports 12 channels with a sampling rate of 256 Hz. It operates via Bluetooth for data transmission and has a battery life of 8 hours, providing a balance between portability and signal quality. The g.Nautilus [?] system by g.tec offers great flexibility, supporting configurations with 8, 16, or 32 channels. It operates at a sampling rate of 250 Hz and uses the ADS1299 AFE for high-performance signal acquisition. The system is known for its proprietary data transmission protocol, ensuring reliable connectivity, and its battery lasts up to 10 hours, making it suitable for long-term monitoring and research studies. The BCI system used by [?] employs active, dry electrodes and supports up to 32 channels with a sampling rate range of 250 Hz to 1 kHz. It also incorporates the ADS1299 AFE for analog-to-digital conversion, ensuring high fidelity in signal capture. Data is transferred via Wi-Fi 802.11 b/g/n, enabling flexible and high-speed communication with external devices. The system boasts an impressive 26-hour battery life, making it an excellent option for extended usage in field studies or clinical applications. The BCI system described by [?] uses passive, dry electrodes and supports up to 40 channels with a sampling rate of 250 Hz. It incorporates the ADS1298 AFE for high-quality data acquisition and utilizes RF (Radio Frequency) for data transfer. While battery life details are not specified, this device is likely designed for portable, research-focused applications where wireless data transfer is essential for real-time monitoring. Finally, the [?] system features 32 flexible, wet electrodes and uses the ADS1299 AFE for high-precision EEG signal acquisition at a sampling rate of 4 kHz. Data is transmitted wirelessly via Wi-Fi, allowing for real-time data monitoring and analysis. The system's battery life is 24 hours when wired, providing extended operation for intensive studies or clinical assessments that require continuous monitoring.

Each of these devices represents a different approach to EEG signal acquisition, offering varying numbers of channels, electrode types, sampling rates, and battery life. While some are optimized for research and clinical use with high sampling rates and extended battery life, others are more suited to consumer applications with lower sampling rates and shorter operational times. The choice of device depends largely on the specific needs of the user, whether for research, clinical monitoring, or personal use in neurofeedback and cognitive training applications.

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1.5.1 Evolution of Portable High-Fidelity EEG Architectures

1.5.2 Synchronization Paradigms: The "Jitter" Problem in Neurogaming

2 Theoretical Framework

This chapter establishes the scientific and engineering foundations required to develop the MONEEE system. It addresses the physiological nature of the signals being measured, the electronic principles of high-precision acquisition, and the computational challenges of synchronizing disparate digital systems.

2.1 Neurophysiology & Event-Related Potentials (ERPs)

Electroencephalography (EEG) measures the electrical activity of the brain via electrodes placed on the scalp. While continuous EEG provides information about the state of the brain (e.g., sleep stages, seizure activity), cognitive neuroscience often focuses on the brain's response to specific sensory, cognitive, or motor events. These time-locked responses are known as Event-Related Potentials (ERPs).

Figure 2-1: Typical waveform of an Event-Related Potential showing P300 and N200 components.

2.1.1 The P300 and N200 Components

ERPs are small voltage fluctuations (typically $1\mu V$ to $20\mu V$) embedded within the larger background EEG noise ($50\mu V$ to $100\mu V$). Two components are particularly relevant for neurocognitive assessment and serious games:

- **The N200 (N2):** A negative deflection peaking approximately 200–350 ms after stimulus onset. It is primarily associated with executive control functions, specifically mismatch detection (when a stimulus deviates from expectation) and response inhibition.

2. Theoretical Framework

- **The P300 (P3b):** A large positive deflection peaking 300–600 ms post-stimulus. The P300 reflects processes related to context updating and the allocation of attentional resources. Its amplitude is often proportional to the improbability of the target stimulus (the .^oddball.^effect), making it a robust metric for assessing attention and cognitive workload in ADHD patients.

2.1.2 The Necessity of Millisecond-Level Precision

Because ERPs are much smaller than background brain activity, they cannot be reliably identified in a single trial. Instead, researchers use *Signal Averaging*. By averaging N time-locked trials, the background noise (assumed to be random with a mean of zero) decreases by a factor of \sqrt{N} , while the time-locked ERP signal remains constant.

However, this technique relies on the assumption of temporal stability. If the synchronization marker (the "trigger" indicating when the game event occurred) has variable latency—known as *jitter*—the averaging process will "smear" the ERP peaks. Mathematically, if the latency follows a Gaussian distribution with standard deviation σ_t , the averaged signal is effectively low-pass filtered. A jitter of just $\sigma_t > 10$ ms can significantly attenuate high-frequency components of the N200, rendering the diagnostic data invalid. Therefore, the MONEEE system requires hard-real-time synchronization capabilities.

2.2 Signal Acquisition Hardware

The fidelity of the EEG data depends heavily on the analog-front-end architecture. The MONEEE system utilizes the Texas Instruments ADS1299, a specialized Analog-to-Digital Converter (ADC) for biopotential measurements.

2.2.1 Delta-Sigma ($\Delta\Sigma$) Analog-to-Digital Converters

Unlike Successive Approximation Register (SAR) ADCs often found in general-purpose microcontrollers, the ADS1299 is a Delta-Sigma modulator. This architecture is chosen for its superior noise performance and dynamic range.

1. **Oversampling:** The ADC samples the input at a frequency (f_{mod}) much higher than the Nyquist rate. This spreads the quantization noise over a wider bandwidth.

2. **Noise Shaping:** The modulator pushes the quantization noise into higher frequencies, away from the biological signal band (0–100 Hz).
3. **Digital Filtering:** A subsequent digital decimation filter removes the high-frequency noise and reduces the data rate to the user-selected output (e.g., 250 SPS or 500 SPS).

Figure 2-2: Simplified block diagram of a Delta-Sigma ADC architecture.

Simultaneous Sampling

A critical feature of the ADS1299 is its ability to sample all 8 channels simultaneously. In multiplexed architectures, a single ADC switches between channels sequentially, introducing a phase delay between electrodes (t_{skew}). For EEG connectivity analysis (coherence), t_{skew} must be zero. The ADS1299 achieves this by dedicating a separate $\Delta\Sigma$ modulator to every channel.

2.2.2 Heterogeneous Embedded Systems: MCU vs. MPU

To balance signal integrity with data throughput, modern acquisition systems often employ a heterogeneous architecture, combining a Microcontroller Unit (MCU) and a Microprocessor Unit (MPU).

- **The MCU (e.g., TM4C1294):** Acts as the hard real-time controller. It operates "bare metal" or with a Real-Time Operating System (RTOS). Its primary role is **determinism**. When the ADC indicates data is ready (via a DRDY hardware interrupt), the MCU must capture it within microseconds to prevent FIFO overflows. It is responsible for timestamping the incoming data precisely when it arrives.
- **The MPU (e.g., Raspberry Pi Compute Module):** Runs a rich Operating System like Linux. It handles high-throughput tasks such as network stacks (TCP/IP), file storage, and complex drivers. However, standard Linux is non-deterministic; the kernel scheduler may preempt a task for 10–20 ms to handle background processes.
- **The Synergy:** In the MONEEE architecture, the MCU guarantees the timing of the acquisition, effectively isolating the sensitive bio-signals from the non-deterministic jitter of the Linux-based MPU.

2. Theoretical Framework

2.3 Digital Synchronization Protocols

Synchronizing the EEG data (recorded by hardware) with the game events (generated by software) is the principal challenge of this thesis. Several methods exist, each with trade-offs regarding latency and intrusiveness.

Table 2-1: Comparison of Synchronization Methods

Method	Mechanism	Precision	Implementation
Optical (Photodiode)	Hardware sensor detects pixel changes on screen.	High (< 1 ms)	High (External hardware required).
Network (LSL)	Time-sync via NTP-like protocols over LAN.	Medium (< 5 ms)	Low (Software only).
Hardware Trigger (TTL)	Parallel port or USB-to-TTL directly to ADC.	Very High (< 1 ms)	Medium (Legacy ports or dongles).

2.3.1 Optical vs. Network Synchronization

Optical (Ground Truth): Placing a photodiode on the monitor to detect the exact moment a visual stimulus appears. This bypasses all operating system and GPU rendering delays. It is often used as the "gold standard" to validate other methods but is impractical for widespread clinical deployment due to the hardware setup required.

Lab Streaming Layer (LSL): A network-based middleware that handles time-series data. It synchronizes streams by mapping local machine timestamps to a common clock, performing jitter correction. While effective, it relies on the quality of the local network and the accuracy of the software timestamps generated by the game engine.

2.3.2 USB Latency Characteristics

The MONEEE system utilizes USB for communication between the MCU and the MPU/Host. Understanding USB latency is vital for "soft-triggers" (commands sent from the PC to the amplifier).

- **Polling Intervals:** USB is a host-controlled bus. The host polls the device for data.
 - *Full Speed (USB 2.0):* Frame time is 1 ms. Data is transferred only once per frame.

2. Theoretical Framework

- *High Speed:* Microframe time is $125\mu s$.
- **CDC Class Overhead:** The Communication Device Class (CDC) emulates a serial port. While convenient for development, the OS driver stack buffers data to improve throughput, often at the cost of latency. A command sent by the game engine may sit in the OS output buffer for several milliseconds before being placed on the USB bus (Start-of-Frame), creating variable delays inconsistent with ERP analysis requirements.

3 Hardware Architecture (The MONEEE System)

3.1 System Overview & Block Diagram

High-level data flow: Electrodes → ADS1299 → TM4C1294 → RPi CM4.

3.2 Analog Front-End (AFE)

Configuration of the ADS1299. Bias drive implementation (Driven Right Leg) for noise rejection. Power supply isolation (Battery management integration).

3.3 The Digital Core (The Split-Architecture)

3.3.1 Real-Time Unit (TI TM4C1294)

Chosen for its hardware floating-point unit and deterministic interrupt handling. It acts as the SPI master for the ADS1299.

3.3.2 Compute Unit (Raspberry Pi CM4)

Handling high-bandwidth data storage and potential Wi-Fi transmission.

3.3.3 Inter-Processor Communication

How the TM4C sends data to the RPi (e.g., via high-speed SPI or UART).

3.4 The Event Interface (USB-C)

Hardware implementation of the USB Type-C port. Signal conditioning: Ensuring the tablet's USB power/data lines do not introduce noise into the EEG ground.

4 Firmware and Synchronization Strategy

4.1 TM4C1294 Firmware Design

4.1.1 Interrupt Service Routines (ISR)

Using the ADS1299 DRDY (Data Ready) pin to trigger data retrieval.

4.1.2 The "Timestamping.^{Eng}ine

This is the core contribution. The Tablet sends a Hex Code (e.g., 0x0A for "Target Appears"). The TM4C receives this via USB/Serial. The firmware must instantly inject this code into the current EEG sample packet before sending it to the RPi.

4.2 Serious Game Integration

Description of the library used on the Tablet side to send Hex codes. Protocol definition (Start byte, Event Code, End byte).

4.3 Raspberry Pi CM4 Software

Linux kernel configuration for real-time performance (optional: PREEMPT_RT). Data formatting (e.g., converting raw bytes to EDF+ or XDF formats).

5 Validation and Results

5.1 Signal Quality Verification

Noise floor analysis (Input-shorted test). Frequency response analysis.

5.2 Synchronization Latency Analysis (Crucial)

5.2.1 The Round-Trip"Test

A test setup where the tablet flashes the screen AND sends the Hex code. A photodiode measures the screen, and the MONEEE records the Hex code. The time difference is the System Latency.

5.2.2 Jitter Measurement

Quantifying the variability of the USB transmission.

5.3 Proof of Concept

A pilot recording of a P300 response using the Serious Game on a healthy subject.

6 Conclusion and Future Work

