Parima Shah

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EDUCATION

Vellore Institute of Technology|Bachelor of Technology 2022 - 2026

Electrical and Communication Engineering

Navrachana School Sama

CBSE Class 12 CGPA:91 /100

Navrachana School Sama

CBSE Class 10 CGPA: 95 /100

PROFESSIONAL EXPERIENCE

Atvantik.Pvt.Ltd | Web Design Intern

Jun 2023 - July 2023

CGPA: 8.75/10.0

- Designed To-Do list using **HTML CSS and JS** to make it responsive and user friendly.
- Created an interactive prototype in **Figma** for Nike webpage, which facilitated a seamless design review process for three key projects, resulting in shortening project timelines by 20%.
- Worked with cross-functional teams to deliver high quality web solutions.

Maven Silicon | VLSI Design Intern

Jun 2023 - Aug 2023

- Engaged in over **120 hours** of in-depth technical sessions on **Digital Electronics**, significantly enhancing understanding and applying practical skills.
- Acquired hands-on experience in **Verilog operations**, designing and implementing 10+ combinational and sequential circuits.
- Successfully developed an **AHB2APB Bridge** project, completing it within a 4-week timeframe, and demonstrating proficiency in digital design and system integration.

LEADERSHIP AND EXTRACURRICULAR

IEEE-CAS | Management Head

Jan 2024 - Jan 2025

 Managed a 10-member cross-functional (events, technical, editorial) team and coordinated with them to conduct various technical events like VLSI Silicon summit, PCB designing, Embed systems-based hackathons etc.

Riviera College Fest | Fest Volunteer

Feb 2024 - Mar 2024

- Managed logistics for 15+ celebrities, coordinating transportation and accommodation to ensure seamless operations.
- Ensured accurate execution and compliance with over 20 celebrity contracts, maintaining high professional standards.

IEEE Global Conference Finance and Sponsorship volunteer

July 2023 - Jan 2024

- Contacted **250+ companies** for sponsorship. Managed several **leads** and 1-1 calls with marketing and sales heads of **MNCs.**
- Increased high connectivity on LinkedIn while reaching out the companies. Handled several emails and efficiently contributed in **brochure** making.

PROJECTS

AHB2APB Bridge | Verilog

- Understanding of **AHB** and **APB** communication protocols.
- Proficient in coding and simulating bridge logic.
- Executed comprehensive synthesis and timing analysis using **advanced EDA tools**, resulting in a streamlined data processing workflow that improved analysis efficiency by 40% and reduced project turnaround time by 3 days.
- Applied advanced debugging and verification techniques to validate design functionality.

Amazon Clone HTML CSS

- Created structured webpage using semantic **HTML** for product listing, user accounts and an efficient **Grid** layout technique for page design.
- Applied CSS style and layout the e-commerce website, ensuring a visually appealing design
- Used CSS media queries to create a responsive design to adapt to various screen sizes and devices.
- Employed CSS Flexbox, Navigation Menu, Animation and Transitions to make my webpage appealing.

SKILLS

Software: HTML, CSS, Verilog, Python, Java, DSA, Embedded C, MATLAB Simulink, NI Labview, Node.js, ModelSim, PyCharm, KneiluVision, Analytical Skills.

Certifications: Tabla Niketan (complete Madhyama Purna with distinction) and Elementary and Intermediate drawing exams (First class).