

Introduction to Open CL on FPGAs (Intel Coursera)

Week 1 - Introduction and Parallel Computing

Question 1:

Which of the following is a need for parallel computing:

- ☐ Power Wall
- ☐ Instruction level parallelism wall
- ☐ Memory wall
- ☐ a and b
- ☒ a, b, and c

(All of the above are a need for parallel computing)

Question 2:

Resource and Data sharing problems are solved by the following mechanisms

- ☒ Barriers
- ☐ Entries
- ☒ Locks
- ☐ Docking

(Barriers block other functions until they reach the barriers requirement. Locks lock up the resources a function is using until it is done)