## **Introduction to Open CL on FPGAs (Intel Coursera)**

## Week 1 - Introduction and Parallel Computing

Question 1:	
Which of the following is a need for parallel computing?	
O	Power Wall
0	Instruction level parallelism wall
0	Memory wall
0	a and b
•	a, b, and c
Question 2:	
Res	source and Data sharing problems are solved by the following mechanisms:
<b>V</b>	Barriers
	Entries
V	Locks
	Docking