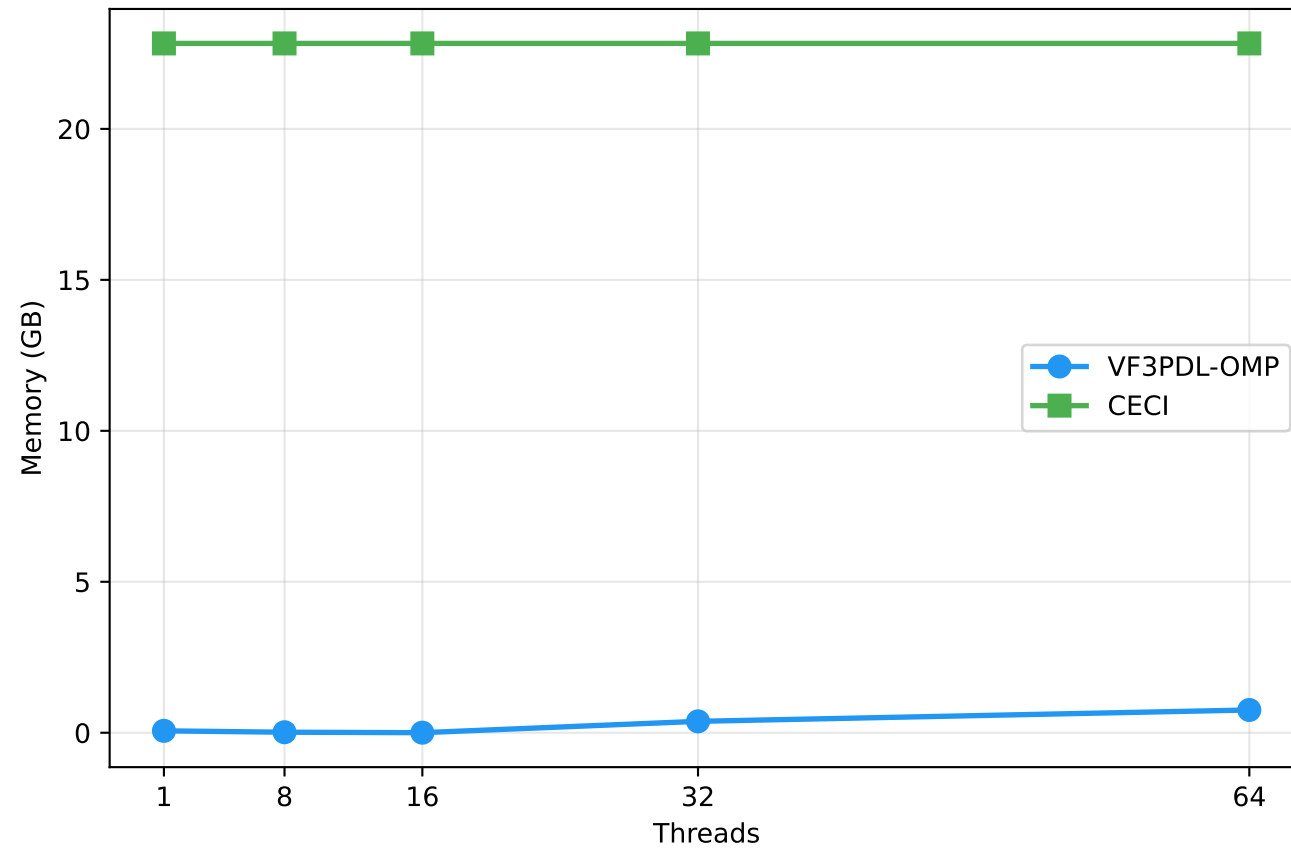
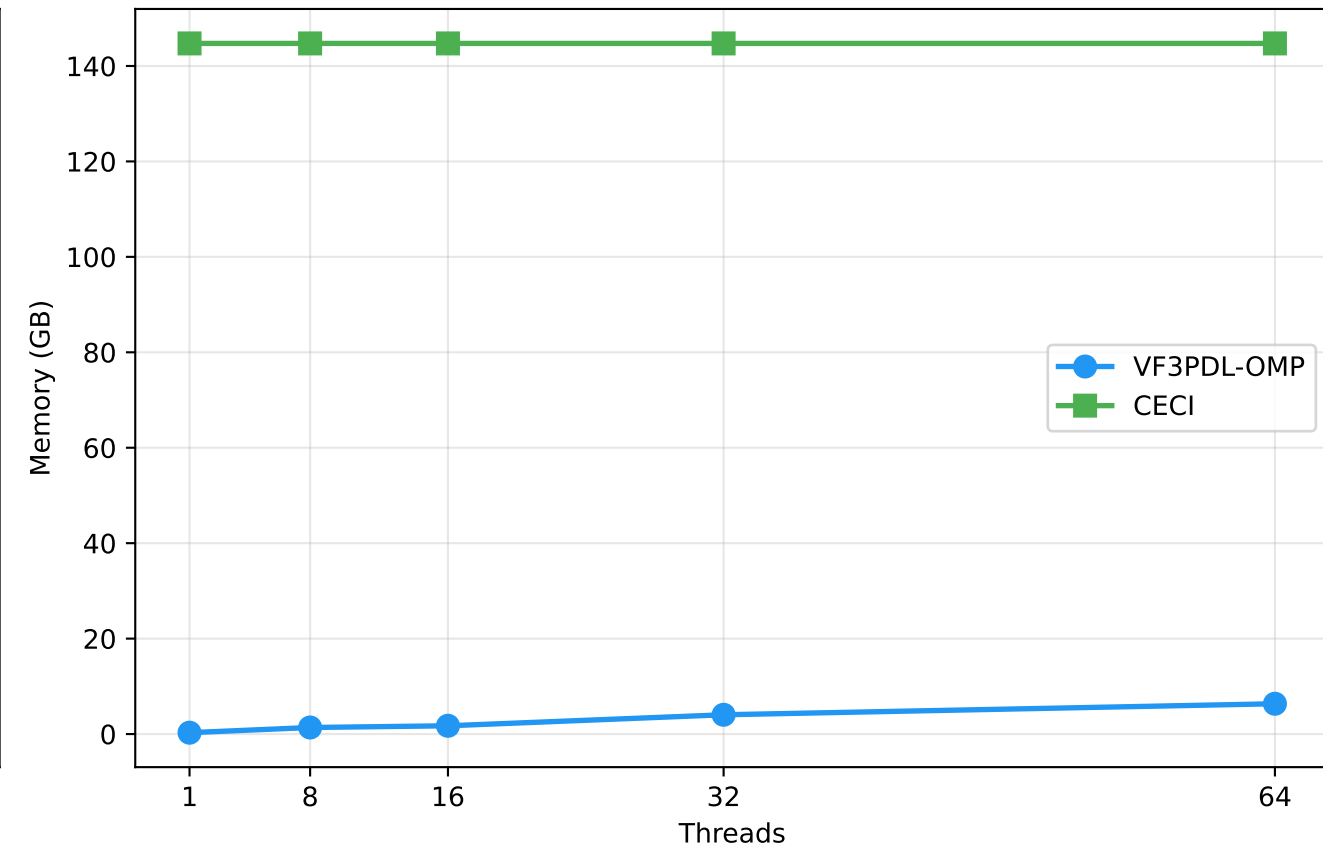


# Sparse 16v - Memory vs Threads

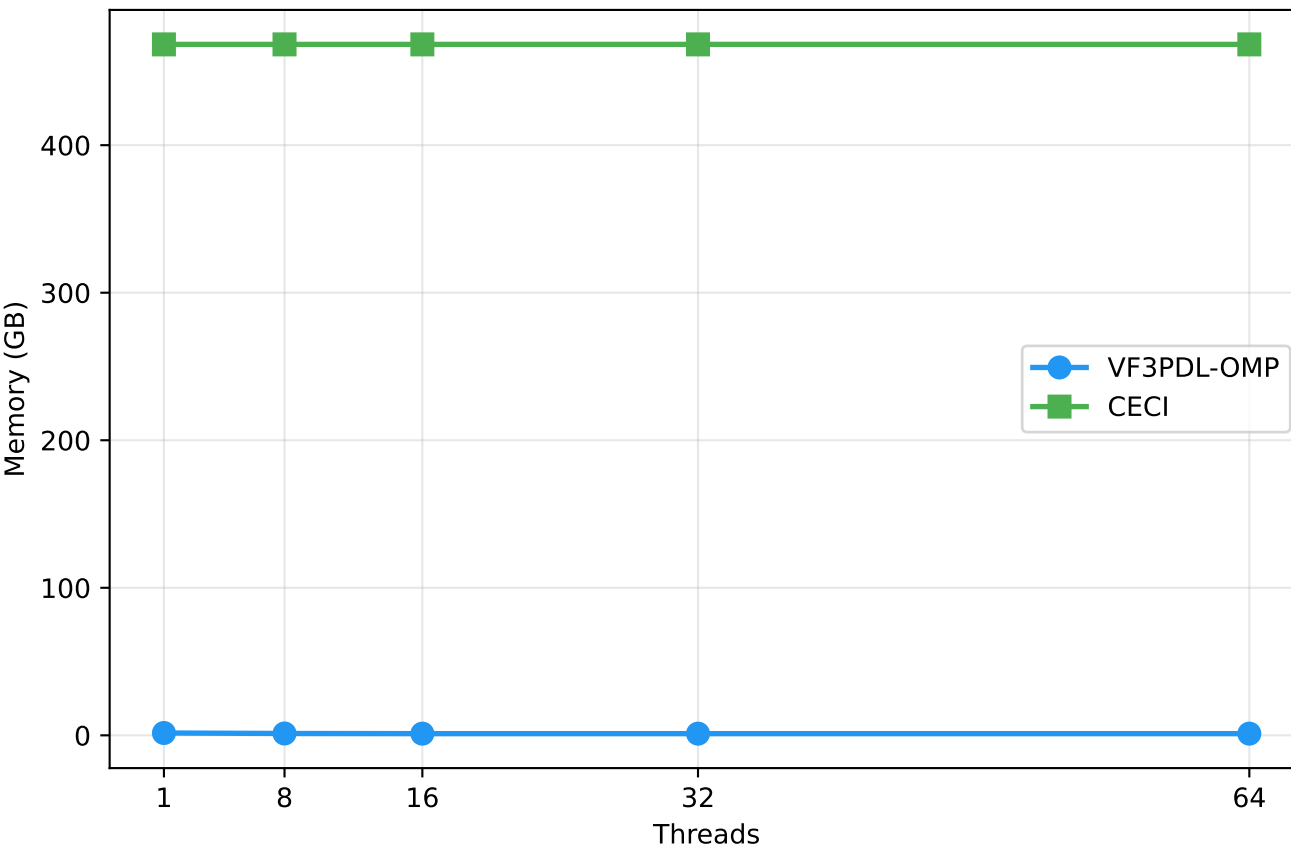
ENRON



DBLP



RoadNet



LiveJournal

