U1

Pin			Pin
3	A	QA	14
4	B	QB	13
5	C	QC	12
6	D	QD	11
		RCO	15
2	CLK		
10	ENT		
7	ENP		
9	LOAD		

74163

HI
LO

Q1
Q2
Q3
Q4

OFFTIME = 0.1us
ONTIME = 0.1us
DELAY =
STARTVAL = 1
OPPVAL = 0

CLK

U2A
1
2
3
7400

U3A
3
1
2
7408

X
S1

(A) Sequencial_Logic_Circuit-Modulo-6_Counter.dat (active)

CLK:1
X:pin1
Q1
Q2
Q3
Q4

0s   0.5us   1.0us   1.5us   2.0us   2.5us   3.0us
Time