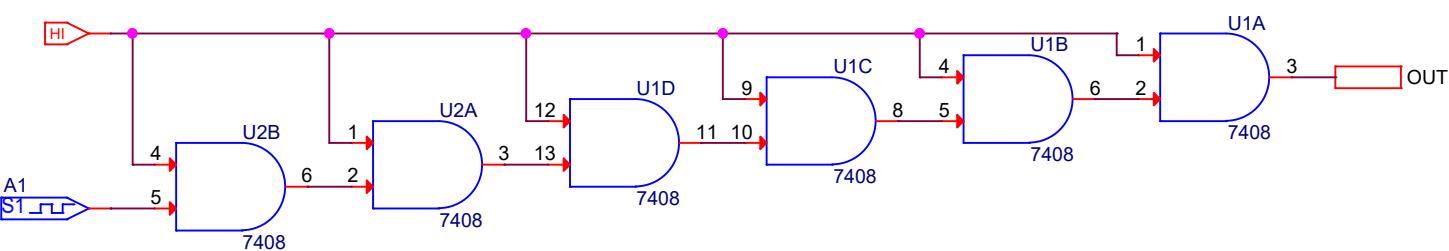


Title		
Combinational Logic Circuit - Delay Time (1)		
Size A	Document Number 2-0-18-001_1	Rev -
Date: Monday, November 17, 2025	Sheet 1 of 1	

\*\* Profile: "SCHEMATIC1-Combinational\_Logic\_Circuit-Delay\_Time\_1" [ C:\Users\user\Desktop\2-0 Electronics\Chapt...  
Date/Time run: 11/17/25 00:43:49 Temperature: 27.0





Title		
Combinational Logic Circuit - Delay Time (2)		
Size A	Document Number 2-0-18-001_2	Rev -
Date: Tuesday, November 18, 2025	Sheet 1 of 1	

\*\* Profile: "SCHEMATIC2-Combinational\_Logic\_Circuit-Delay\_Time\_2" [ c:\users\user\desktop\2-0 electronics\chapt...  
Date/Time run: 11/18/25 00:08:43 Temperature: 27.0

