

LAB 05 Tasks

Task 01

Declare a 32-bit signed integer `val1` and initialize it with the eight thousand. If `val1` is incremented by 1 using the `ADD` instruction, what will be the values of the Carry and Sign flags?

Task 02

Write down the values of the Carry, Sign, Zero, and Overflow flags after each instruction has executed:

mov ax, 7FF0h	
add al, 10h	; a CF =, SF =, ZF =, OF =
add ah, 1	; b CF =, SF =, ZF =, OF =
add ax, 2	; b CF =, SF =, ZF =, OF =

Task 03

Declare an array variable, `array1` with type `BYTE` and initialize it with: 61, 43, 11, 52, 25. Declare another array, `array2` with the same data type as before. This array should hold the sorted elements in ascending order from the first array. The elements are to be sorted manually. Output the sorted array using loop.

Task 04

- Define three arrays in the `.data` section as follows:
 - `arrayB`: `BYTE` array with elements 10, 20, and 30.
 - `arrayW`: `WORD` array with elements 150, 250, and 350.
 - `arrayD`: `DWORD` array with elements 600, 1200, and 1800.
- Declare three `DWORD` variables to store the sum of elements from each array: `SUM1`, `SUM2`, and `SUM3`.
- In the `.code` section, write a main procedure that follows these steps:
 - Load the addresses of the arrays into registers.
 - Calculate `SUM1` as the sum of the first elements of each array (`arrayB[0] + arrayW[0] + arrayD[0]`).
 - Calculate `SUM2` as the sum of the second elements of each array (`arrayB[1] + arrayW[1] + arrayD[1]`).
 - Calculate `SUM3` as the sum of the third elements of each array (`arrayB[2] + arrayW[2] + arrayD[2]`).
- Display the results using `WriteDec` and `Crlf` procedures from the `Irvine32` library.

Example Output:

760
1470
2180

Hints:

- Use **LEA** for Addressing: Utilize **LEA** to load effective addresses of arrays into registers efficiently.
- Essential Registers: Employ **esi**, **edi**, and **ebx** to hold addresses of arrays and perform arithmetic operations.