

# PARVEEN KUMAR

## Personal Details

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## TECHNICAL SKILLS

- C, C++
- JavaScript
- HTML
- CSS
- Verilog
- VHDL
- Embedded C
- Arduino IDE
- Tanner
- Microwind
- Xilinx Vivado
- YOCTO
- React
- Git
- 802.11 MAC
- Renesas S2G7 Board

## Work Experience

### SpanIdea Systems Pvt. Ltd

Working as a Software Developer June 2022 - Present

- Projects :
- Worked on Web Development and developed a UI for maintaining the testcases of a Wifi router system.
  - Worked on building Yocto ( Custom Linux ) for an Intel board to configure the board in a separate UI.
  - Worked on RDK-B and build custom linux distribution system for Renesas board and configure it's UI.
  - Worked on GPON transmission network. Made custom network and configured them.

### Educational Projects

- Made Power Efficient Circuit using Adiabatic Techniques.  
Platform used Microwind  
Recognized and published paper in ICICS LPU 2022.
- Made Alarm Clock using Verilog. 2021-2022  
Platform used Xilinx Vivado
- Worked in product based project with Texas Instruments.  
Medication Adherence 2019-2021  
Tools used : MSP430FF29 controller, CC3100 Boost, HX711 amplifier and miniature Load cell.  
Recognized as a finalist in IICDC 2019.

### Education

- B.Tech ( Electronics and Communication Engineering )  
Lovely Professional University 2018-2022
- XII  
S.E.D. Senior Secondary School 2017-2018
- X  
S.E.D. Senior Secondary School 2015-2016