SCHEMATIC	CONTENT	PAGE
Power	5V, 3.3V, 2.5V, 1.5V, 1.2V, 1.0V, 0.75V	3
Bank Power	Bank 0_1 : ADJ (3.3V, 2.5V)  Bank 2_3 : 3.3V  Bank 4_5_6 : 1.5V  Bank 7 : ADJ (3.3V, 2.5V, 1.2V)	4
Bank0_1	LVDS, GPIO, CLK, LED	5
Bank2	PHY1, JTAG, SMA	6
Bank3	PHY2, Flash, SD Card, MODE, DONE, RECONFIG, READY, RST	7
Bank4_5_6	DDR3, KEY, SW	8
Bank7	GPIO_3.3V, Backup Power	9

Copyright (c) 2018 by Gowin Semiconductor Corporation, Ltd

Title

DK-START-GW2A18

Size A3 COVER PAGE

Date: Friday, September 07, 2018 Sheet 1 of 9

\_\_\_

5 4 3 2 1

5V电源 POWER 插座 电源开关 DDR3 2Gbit RJ45-2 20-pin 双排插 PHY2 B4 B5 30-pin 双排插 针 GPIO 1 GO₩IN В3 针 GPIO 2 USB to RJ45-1 PHY1 JTAG GW2A18 - PG256 电压 B2 选择 B0 MODE osc **Config Flash** 50MHz **LED \* 4** SD Card 2.0mm 20-pin双 排插针 LVDS 1 2.0mm 20-pin双 排插针 LVDS 2 复位按键 MINI-调试开关 外部 USB-B 时钟 调试按键\*4

Copyright (c) 2018 by Gowin Semiconductor Corporation, Ltd

Title

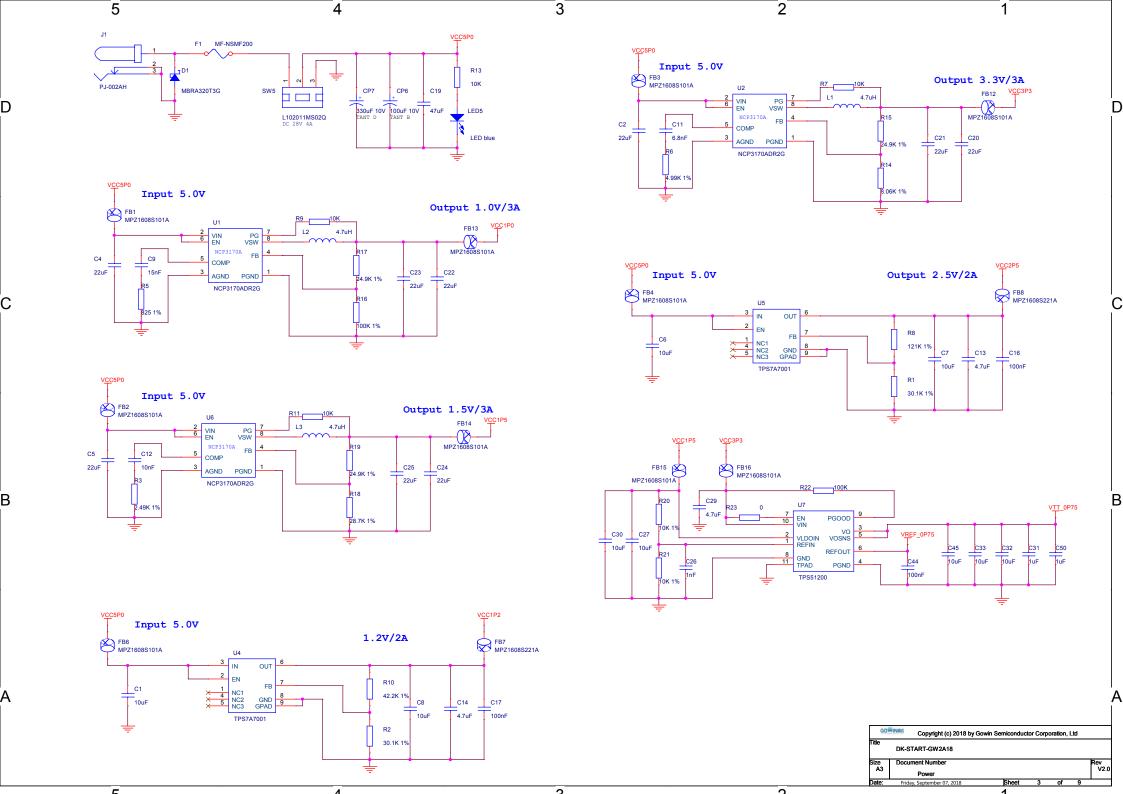
DK-START-GW2A18

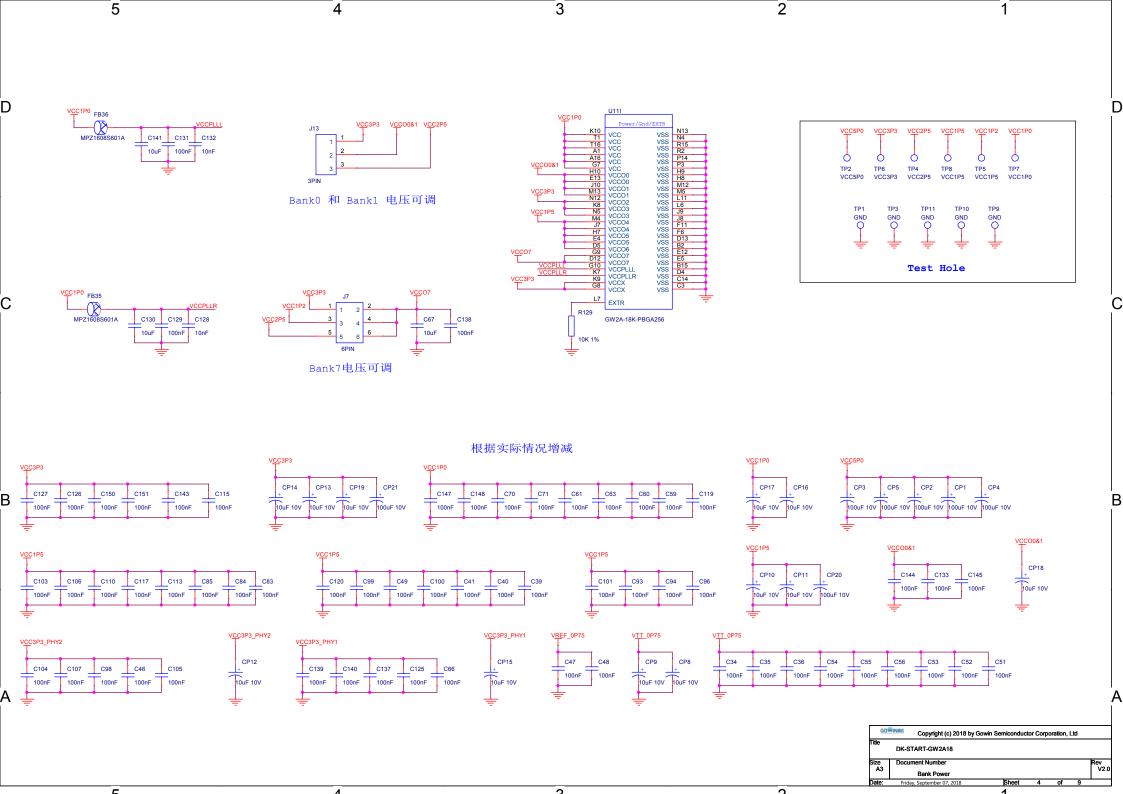
Size Document Number
A3 Document Number
Block Diagram
Date: Friday, September 07, 2018 Sheet 2 of 9

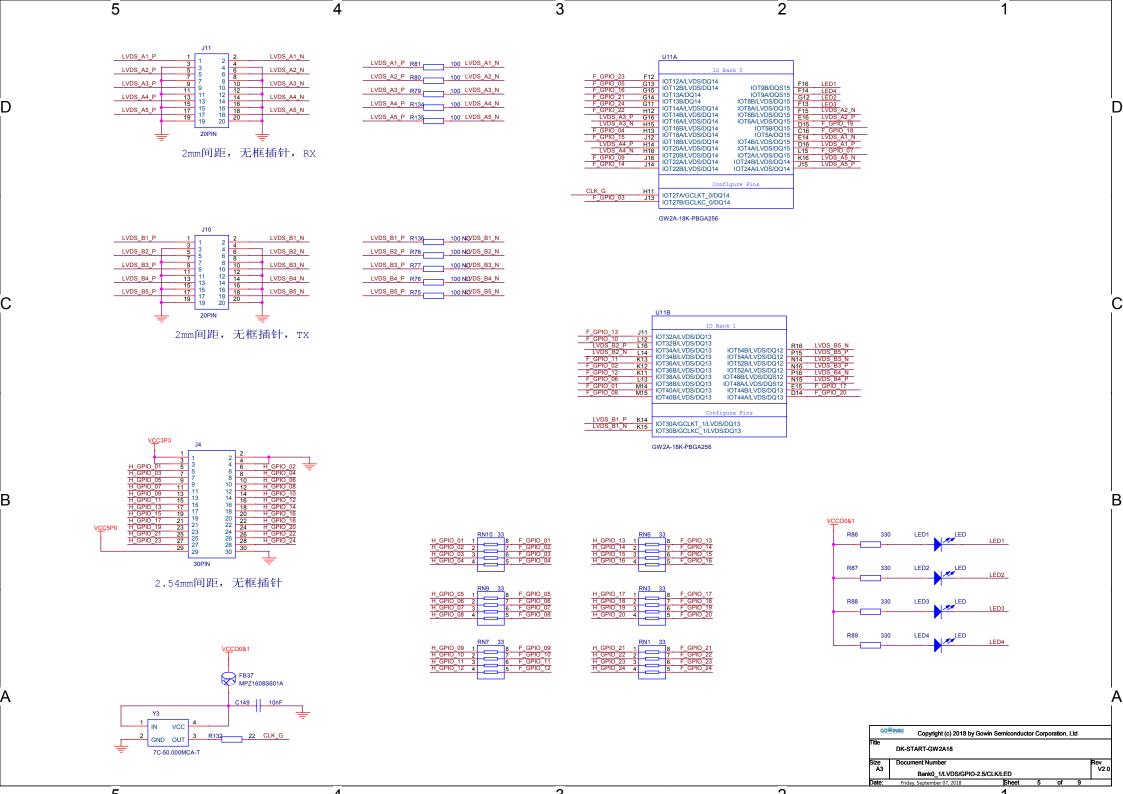
\_\_\_\_

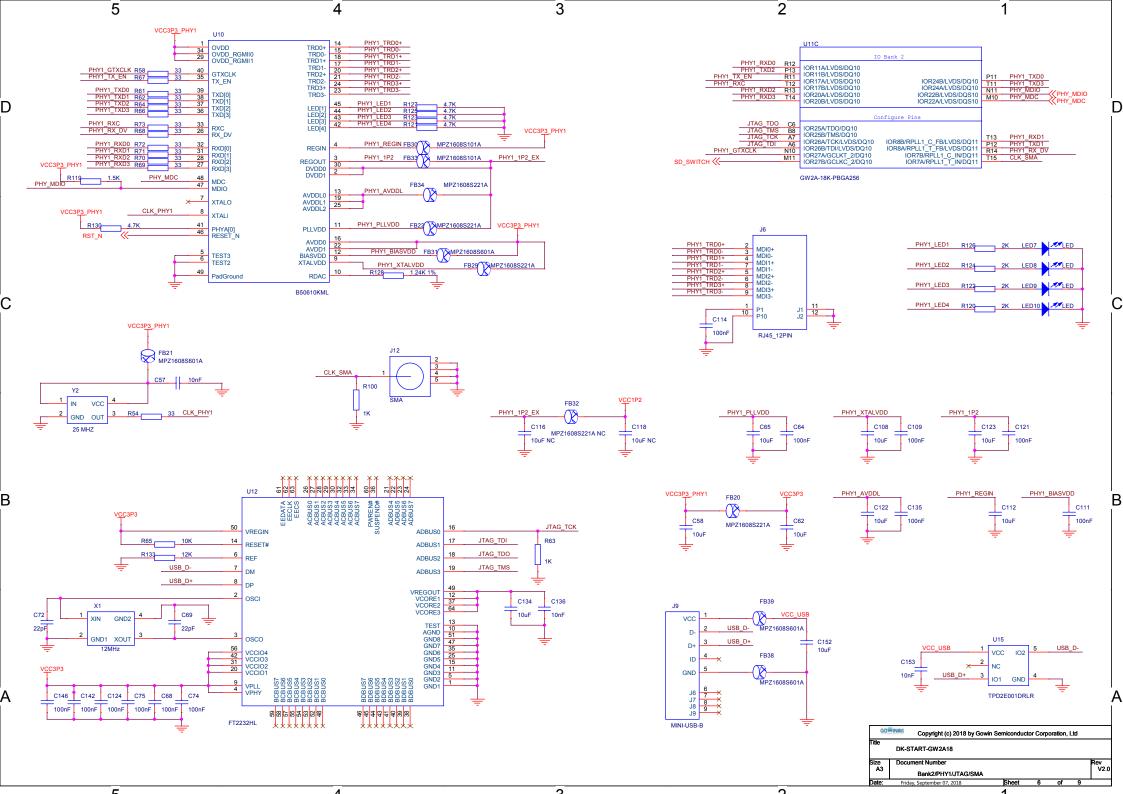
В

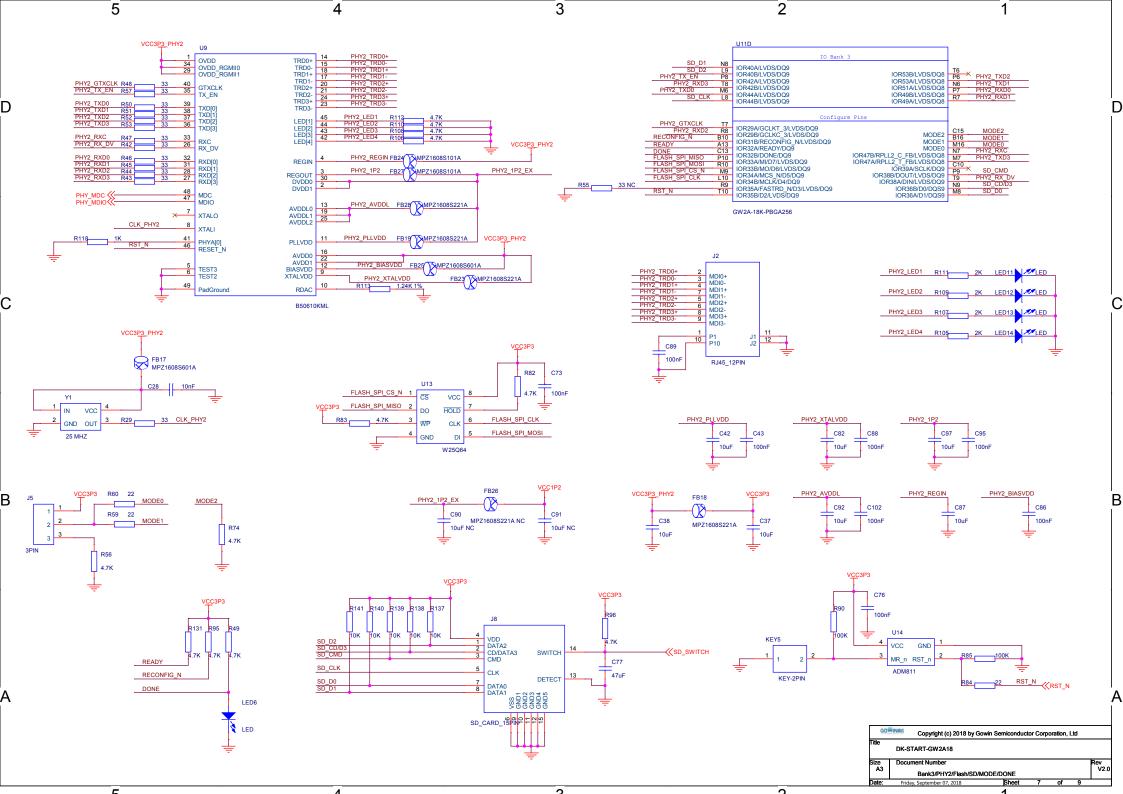
Α

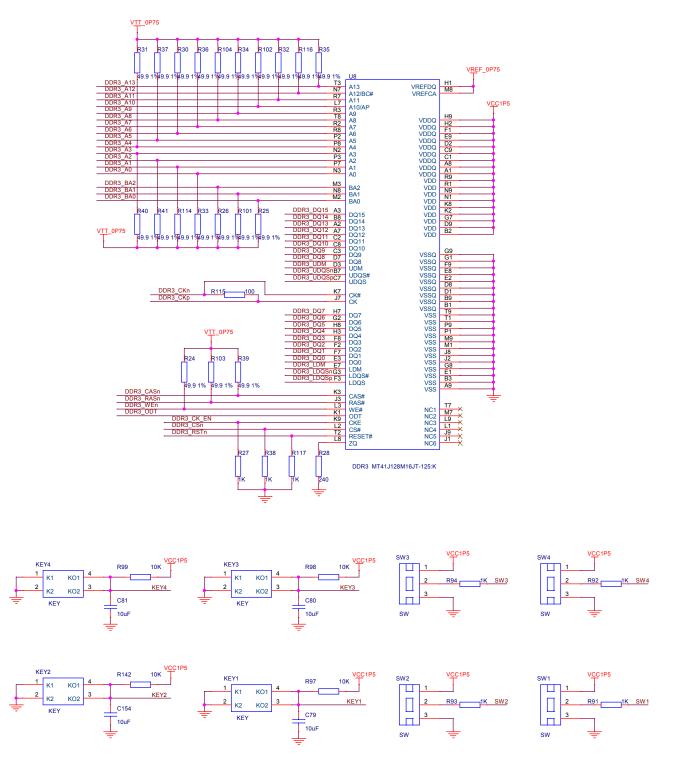


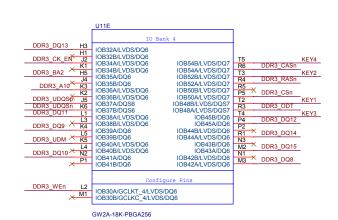


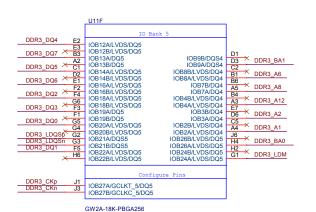


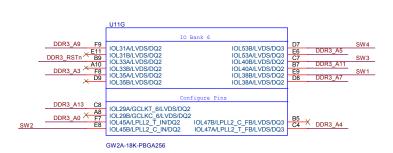


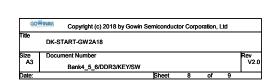


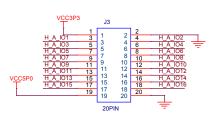




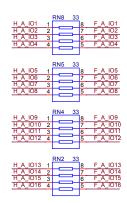


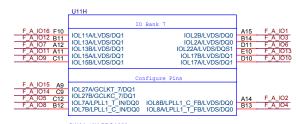






2.54mm间距, 无框插针





GW2A-18K-PBGA256

