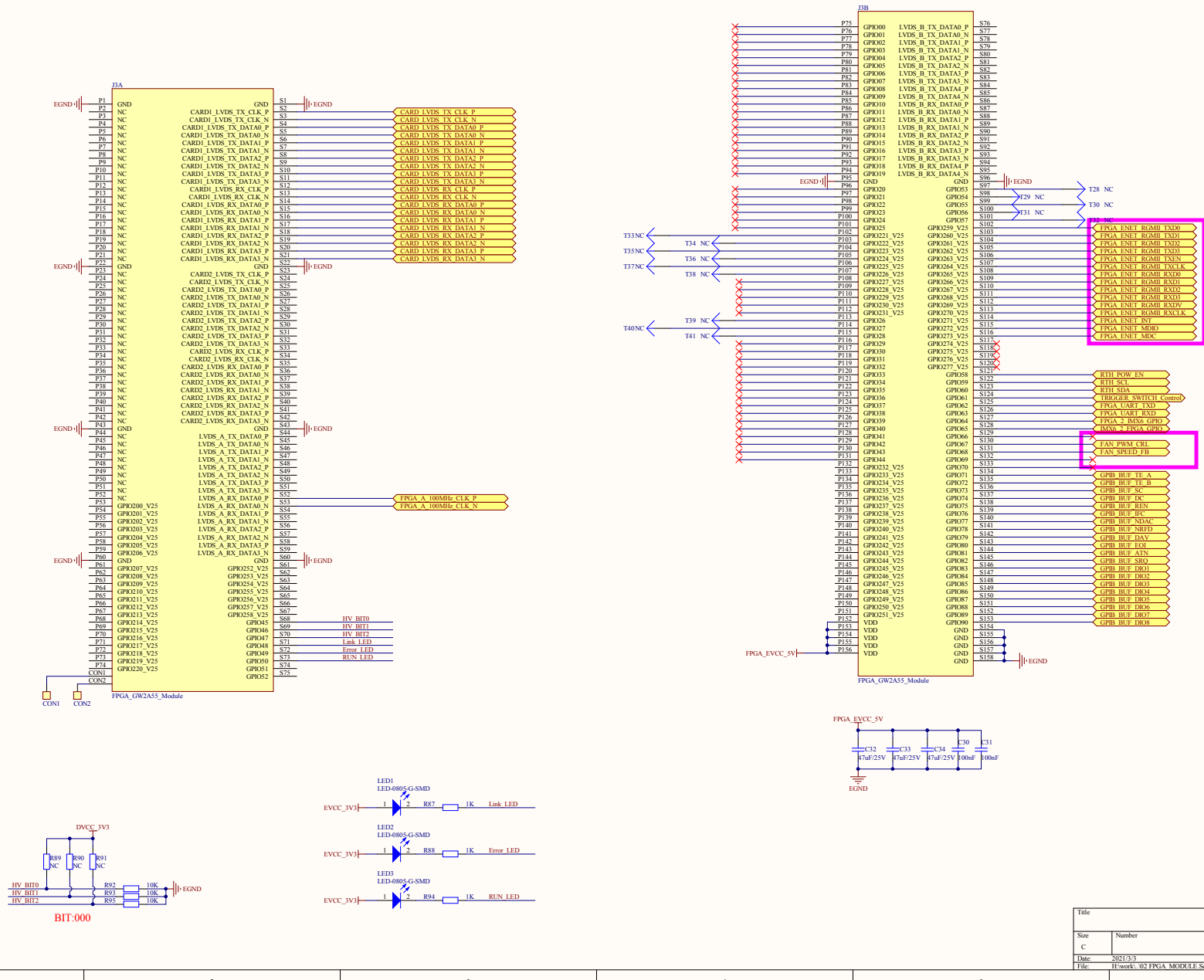
J3A
FPGA_GW2A55_Module
CON1
CON2
J3B
FPGA_GW2A55_Module
FPGA_EVCC_5V
C32 47uF/25V
C33 47uF/25V
C34 47uF/25V
C30 100nF
C31 100nF
LED1 LED-0805-G-SMD
LED2 LED-0805-G-SMD
LED3 LED-0805-G-SMD
EVCC_3V3
R87 1K Link_LED
R88 1K Error_LED
R94 1K RUN_LED
DVCC_3V3
R89 NC
R90 NC
R91 NC
R92 10K
R93 10K
R95 10K
HV_BIT0
HV_BIT1
HV_BIT2
BIT:000
EGND
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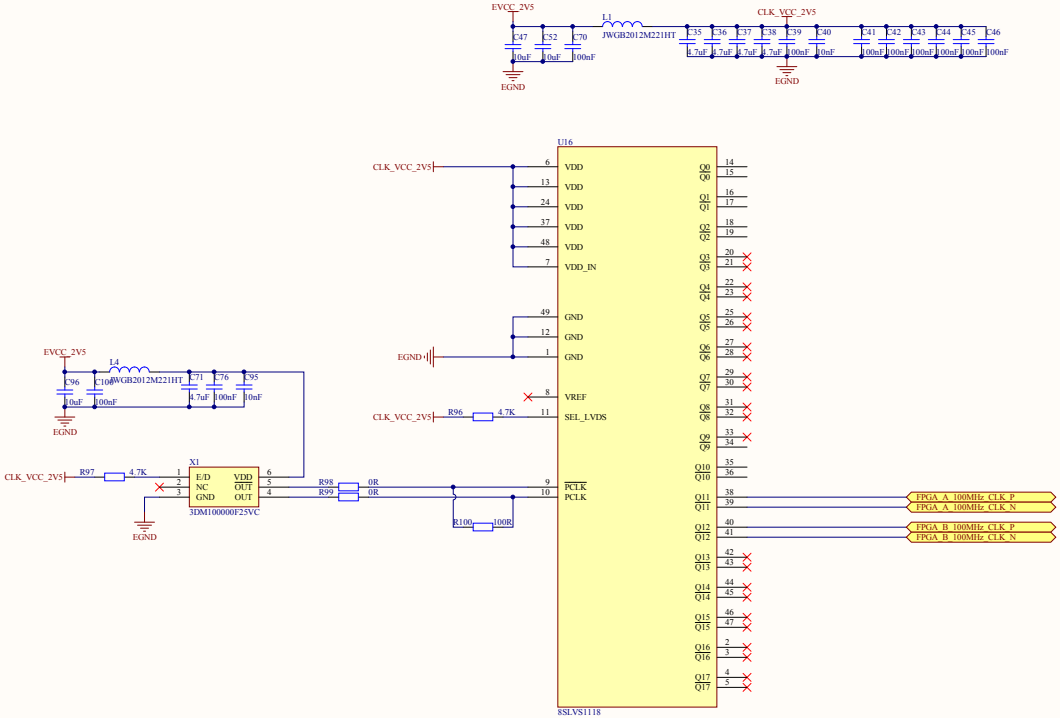

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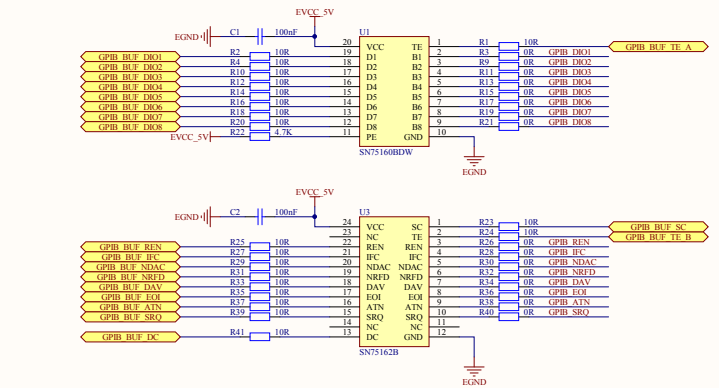

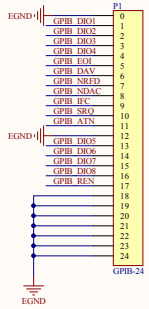

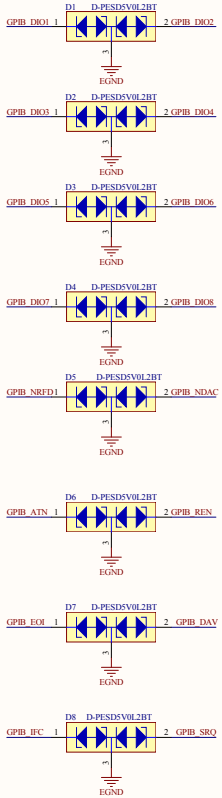

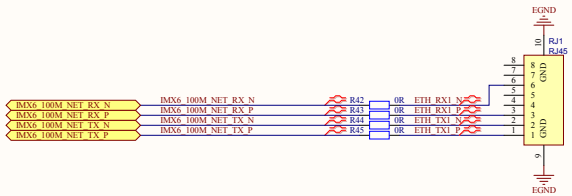

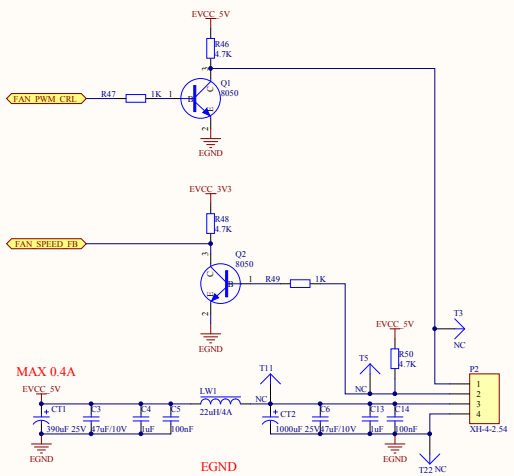

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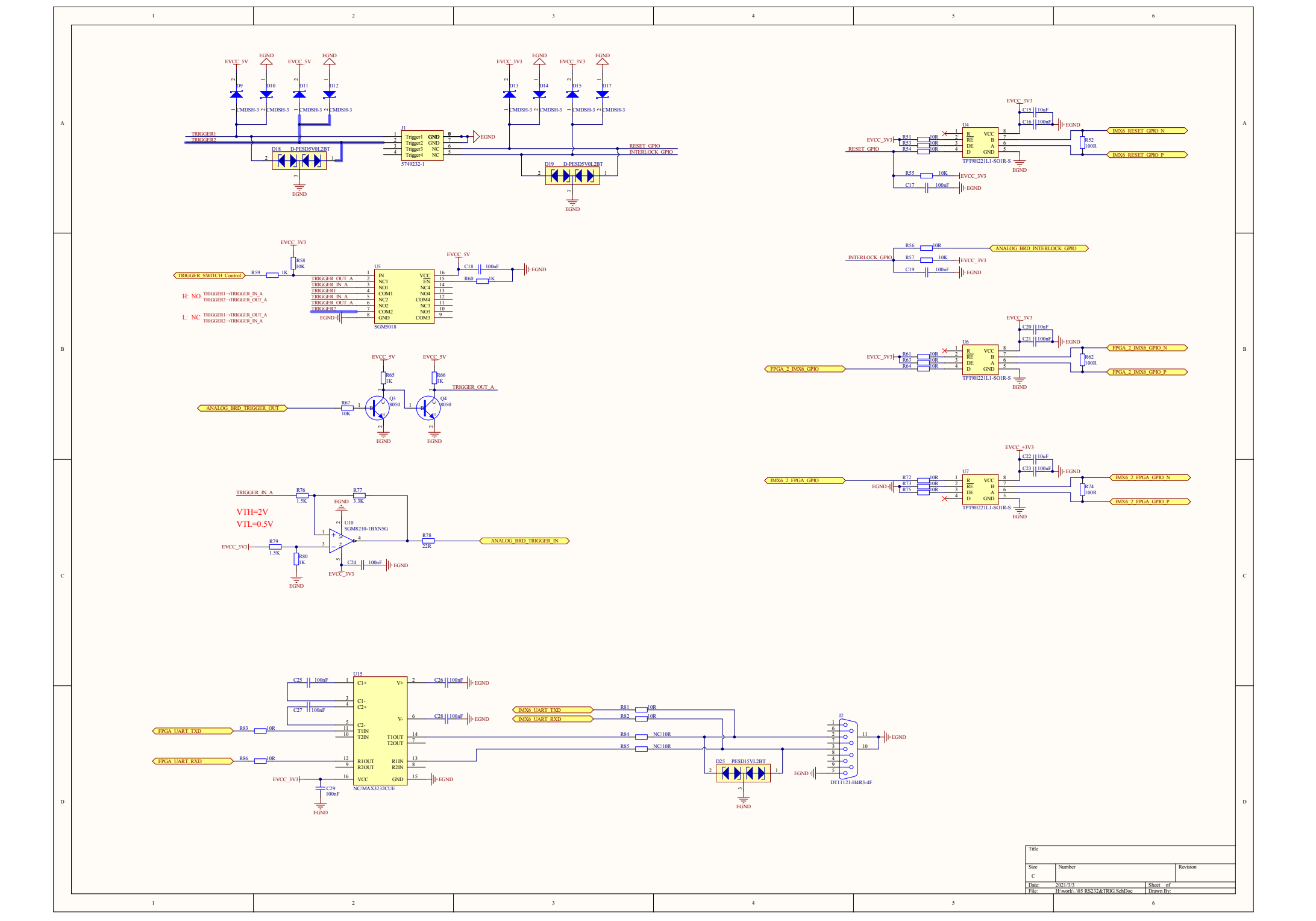
EVCC_5V
EGND
D9
D10
D11
D12
CMDSH-3
TRIGGER1
TRIGGER2
D18
D-PESD5V0L2BT
J1
Trigger1
Trigger2
Trigger3
Trigger4
GND
NC
5749232-1
EVCC_3V3
D13
D14
D15
D17
RESET_GPIO
INTERLOCK_GPIO
D19
U4
TPT9H221L1-SO1R-S
R51
R53
R54
R55
10R
10K
C15
C16
10uF
100nF
IMX6_RESET_GPIO_N
IMX6_RESET_GPIO_P
R52
100R
C17
TRIGGER_SWITCH_Control
R59
1K
R58
U5
SGM5018
TRIGGER_OUT_A
TRIGGER_IN_A
TRIGGER1
TRIGGER2
H: NO
TRIGGER1→TRIGGER_IN_A
TRIGGER2→TRIGGER_OUT_A
L: NC
TRIGGER1→TRIGGER_OUT_A
TRIGGER2→TRIGGER_IN_A
R60
C18
R56
R57
C19
INTERLOCK_GPIO
ANALOG_BRD_INTERLOCK_GPIO
ANALOG_BRD_TRIGGER_OUT
R65
R66
R67
Q3
Q4
8050
FPGA_2_IMX6_GPIO
U6
R61
R63
R64
C20
C21
R62
FPGA_2_IMX6_GPIO_N
FPGA_2_IMX6_GPIO_P
IMX6_2_FPGA_GPIO
U7
R72
R73
R75
C22
C23
R74
IMX6_2_FPGA_GPIO_N
IMX6_2_FPGA_GPIO_P
TRIGGER_IN_A
R76
1.5K
R77
3.3K
VTH=2V
VTL=0.5V
U10
SGM8210-1BXN5G
R78
22R
ANALOG_BRD_TRIGGER_IN
R79
R80
C24
U15
NC/MAX3232CUE
C25
C26
C27
C28
C29
FPGA_UART_TXD
FPGA_UART_RXD
R83
R86
IMX6_UART_TXD
IMX6_UART_RXD
R81
R82
R84
R85
NC/10R
D25
PESD15VL2BT
J2
DT11121-H4R3-4F
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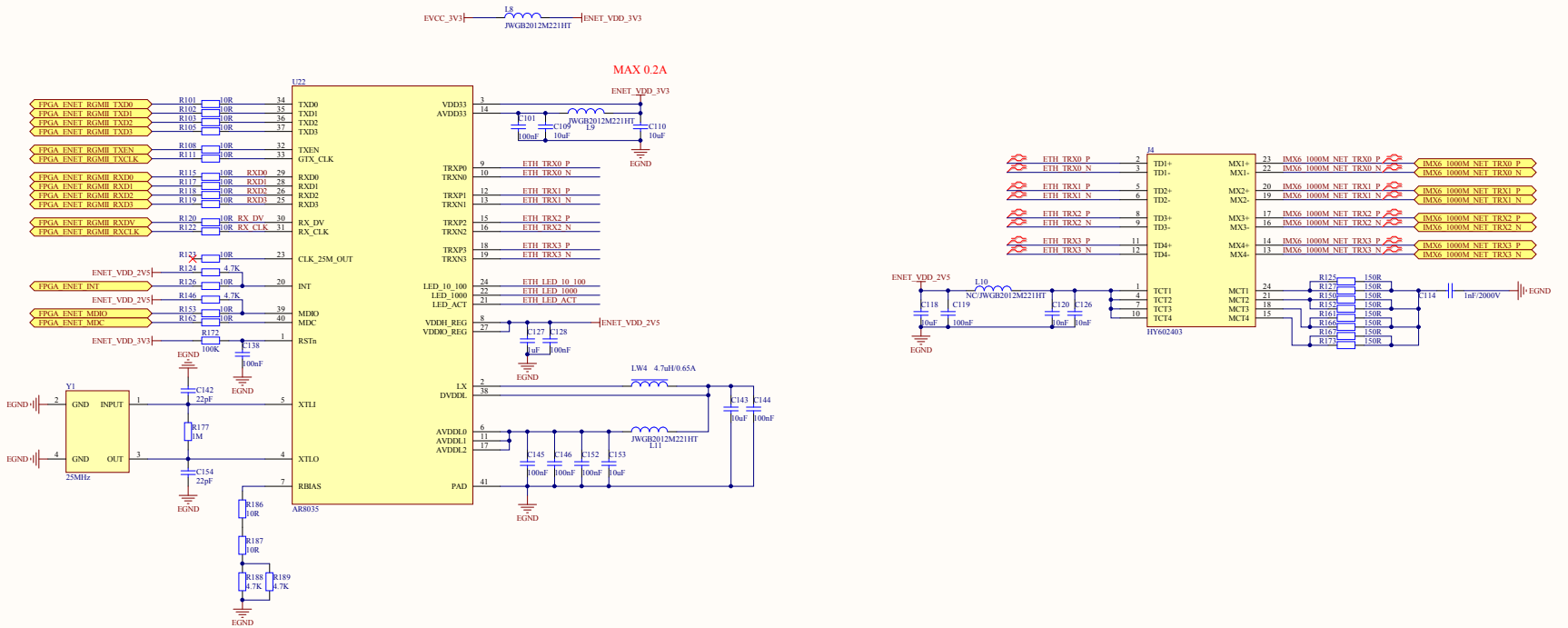

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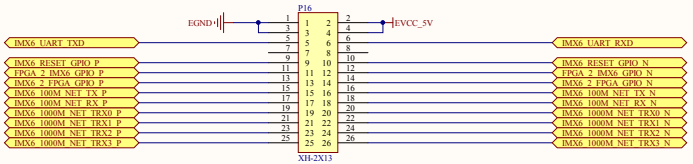

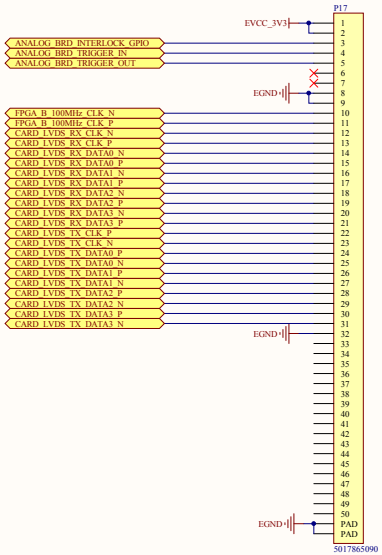

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