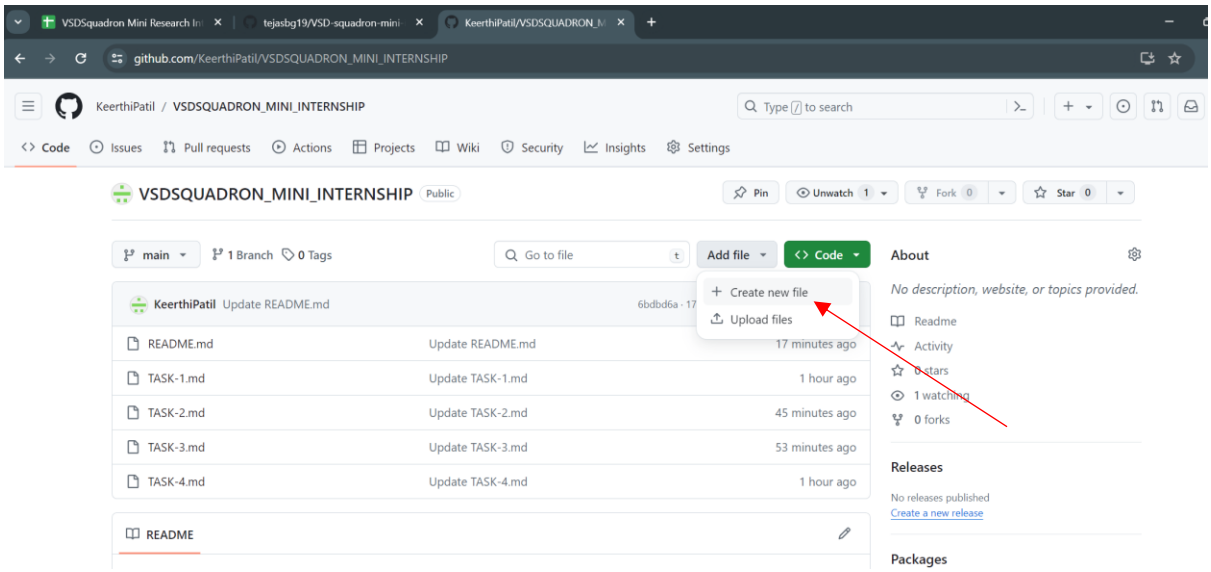
1. To insert the image:

Copy on the image and paste it on the place where you want to place.

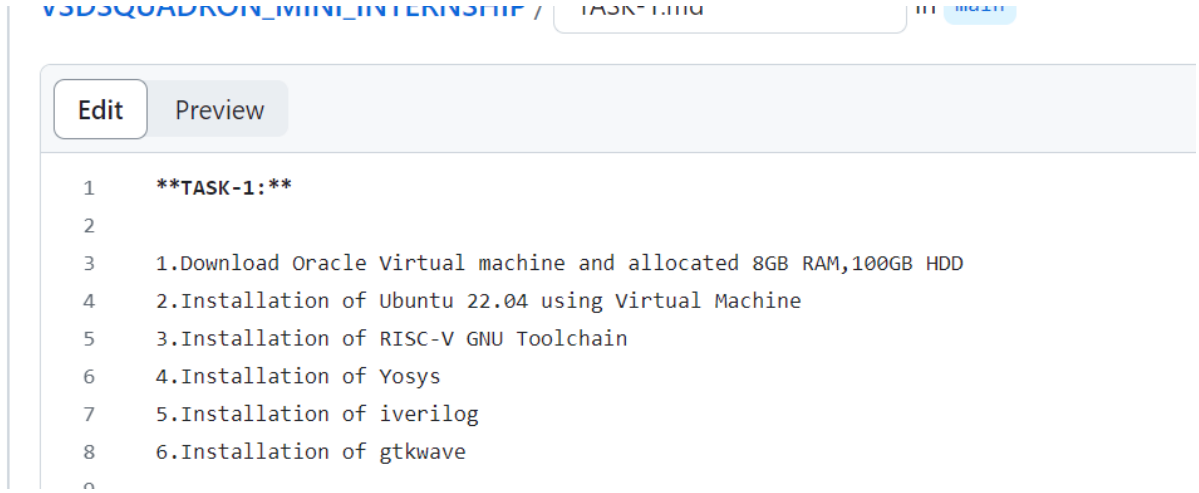
2. To create an file :

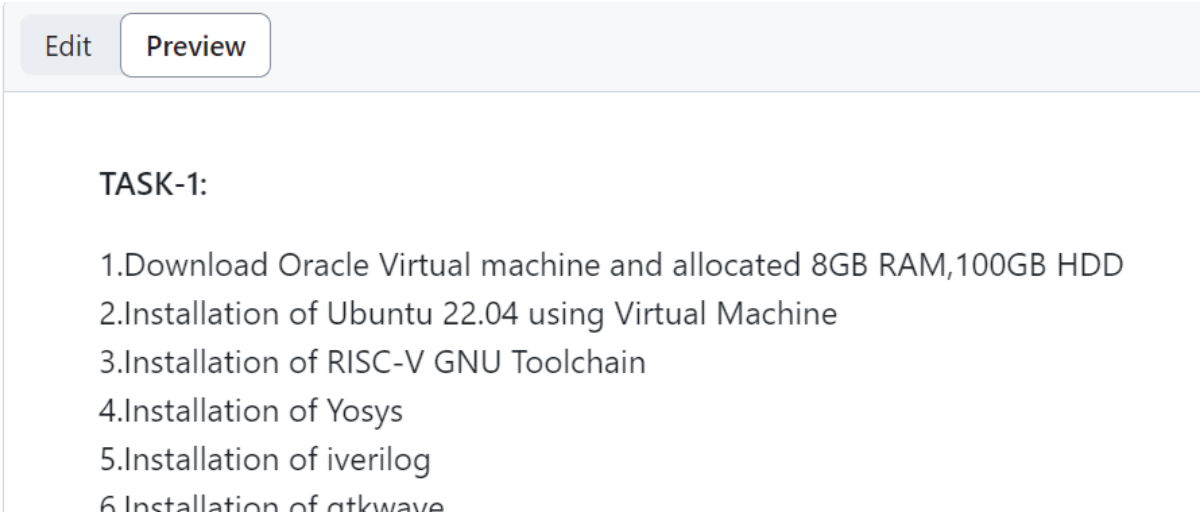


Name as **filename.md**

3. To make bold

select the sentence and press ctrl + B / add ** before and after sentence





4. To make Italian font

select the sentence and press ctrl + I / add _ before and after the sentence

Edit Preview

```
_TASK-1:_

1.Download Oracle Virtual machine and allocated 8GB RAM,100GB HDD
2.Installation of Ubuntu 22.04 using Virtual Machine
3.Installation of RISC-V GNU Toolchain
4.Installation of Yosys
5.Installation of iverilog
6.Installation of gtkwave
```

Edit Preview

TASK-1:

1.Download Oracle Virtual machine and allocated 8GB RAM,100GB HDD
2.Installation of Ubuntu 22.04 using Virtual Machine
3.Installation of RISC-V GNU Toolchain
4.Installation of Yosys
5.Installation of iverilog
6.Installation of gtkwave

5. To add heading

Add # and space in front of the heading

Preview | Code | Blame | 63 lines (44 loc) · 2.26 KB

```
# TASK-1:

    1.Download Oracle Virtual machine and allocated 8GB RAM,100GB HDD
    2.Installation of Ubuntu 22.04 using Virtual Machine
    3.Installation of RISC-V GNU Toolchain
    4.Installation of Yosys
    5.Installation of iverilog
    6.Installation of gtkwave
```

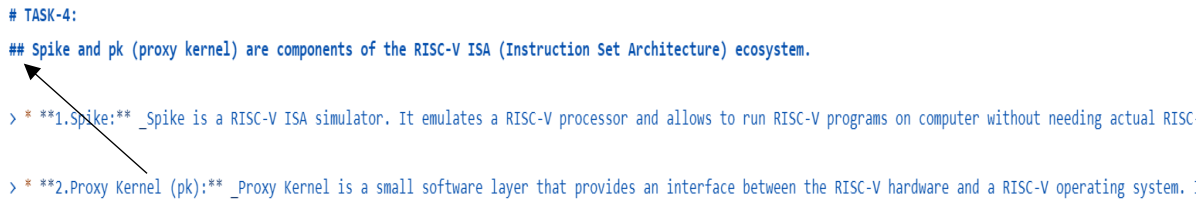
Preview | Code | Blame | 63 lines (44 loc) · 2.26 KB

TASK-1:

1.Download Oracle Virtual machine and allocated 8GB RAM,100GB HDD
2.Installation of Ubuntu 22.04 using Virtual Machine
3.Installation of RISC-V GNU Toolchain
4.Installation of Yosys
5.Installation of iverilog
6.Installation of gtkwave

6. To add as subheading

Add ## and space in front of the heading

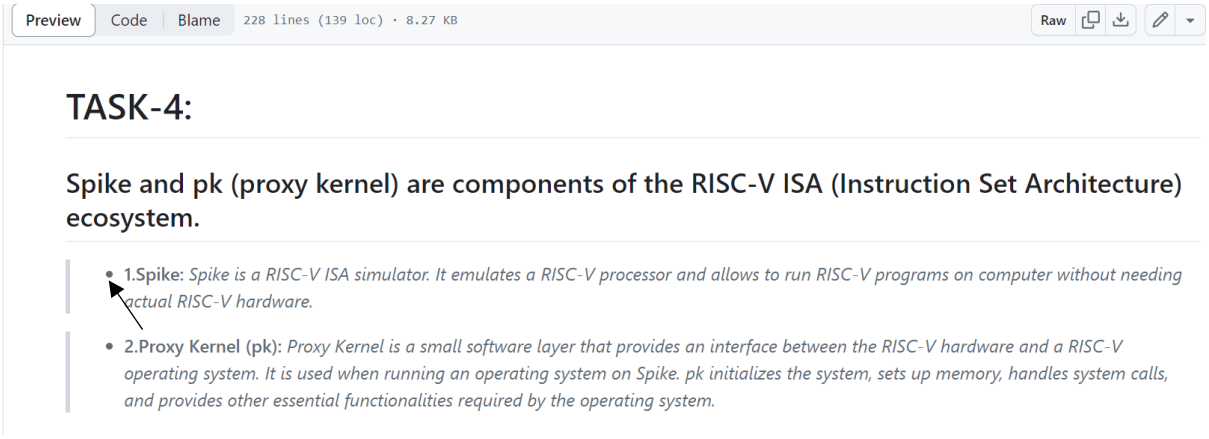


7. To add dot

Put >space*space before statement => > * example

Preview Code Blame 228 lines (139 loc) · 8.27 KB Raw

```
# TASK-4:
## Spike and pk (proxy kernel) are components of the RISC-V ISA (Instruction Set Architecture) ecosystem.

> * **1.Spike:** _Spike is a RISC-V ISA simulator. It emulates a RISC-V processor and allows to run RISC-V programs on computer without needing actual RISC-

> * **2.Proxy Kernel (pk):** _Proxy Kernel is a small software layer that provides an interface between the RISC-V hardware and a RISC-V operating system. I
```



8. To add box to some statements

Add ``` symbol at starting and end of statement.

```

Xyz

Xyz

Trm

```

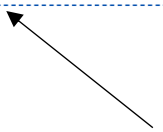
```
## Installation of spike:
```
$ riscv64-unknown-elf-gcc -Ofast -mabi=lp64 -march=rv64i -o sum1ton.o sum1ton.c
$gcc sum1ton.c
$./a.out
$ riscv64-unknown-elf-gcc -Ofast -mabi=lp64 -march=rv64i -o sum1ton.o sum1ton.c
```

Installation of spike:

```
$ riscv64-unknown-elf-gcc -Ofast -mabi=lp64 -march=rv64i -o sum1ton.o sum1ton.c
$gcc sum1ton.c
$./a.out
$ riscv64-unknown-elf-gcc -Ofast -mabi=lp64 -march=rv64i -o sum1ton.o sum1ton.c
```
```

9. To add line after statement

Add ------- after the statement

Xyzzzzzzzzzzzzz

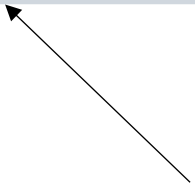
```
I have downloaded to VDI file which has spike,pk and riscv installaton and followed the Task-3.

> * Method 1 explains Manual Installation.
> * Method 2 is Pre-installed VDI.
> * Method 1 discusses installing Spike and pk, while Method 2 provides a VirtualBox disk image (VDI) with Spike and pk already installed.
---
# Compiling the .C using GCC Compiler:
```



METHOD-2:

I have downloaded to VDI file which has spike,pk and riscv installaton and followed the Task-3.

> - Method 1 explains Manual Installation.
> - Method 2 is Pre-installed VDI.
> - Method 1 discusses installing Spike and pk, while Method 2 provides a VirtualBox disk image (VDI) with Spike and pk already installed.

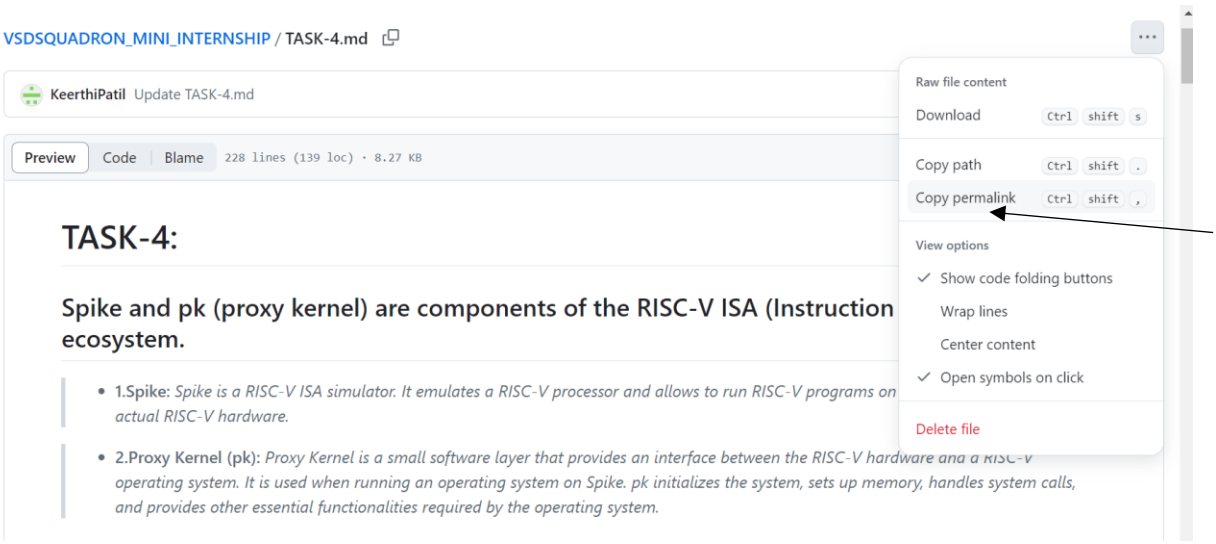


10. To add hyperlinks

Add this link text

Add the url here

Add the name which u want to display



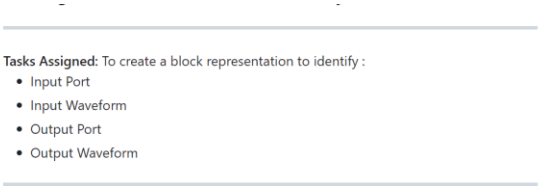
Select copy permalink add in url

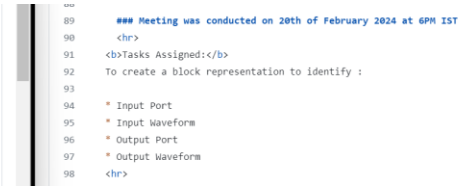
```
12
13    <a href="https://github.com/KeerthiPatil/VSDSQUADRON_MINI_INTERNSHIP/blob/013d3afb9cf6bb129c51351145294a6a20d56b6d/TASK-4.md">Task-4</a>
```

The above thing displays

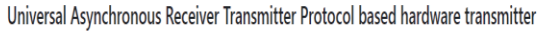
Task-4

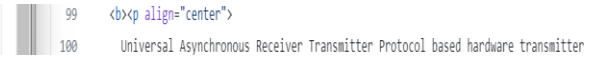
10.<hr> to give line



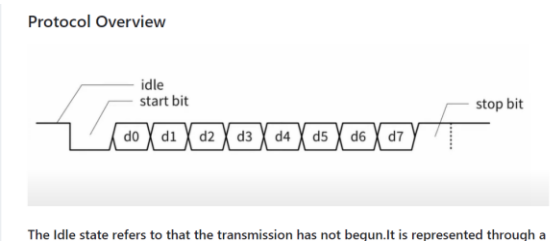


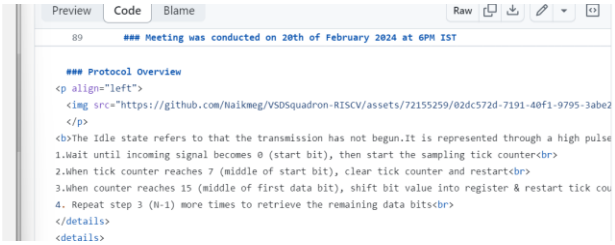
11.To align at centre



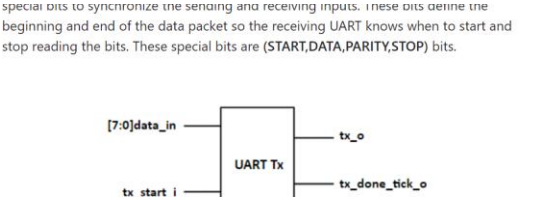


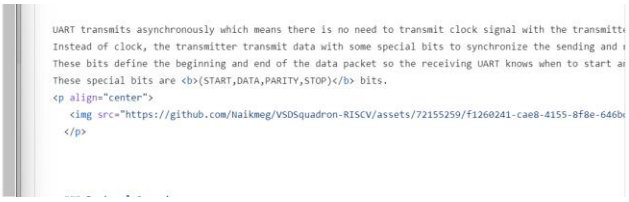
12.To align at left side image



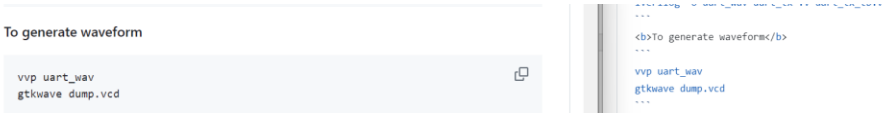


13.To align at centre image





14.To make bold



15.To make point

https://github.com/kmkalpana2001/DIGITAL-VLSI-SOC-DESIGN-AND-PLANNING

https://github.com/Pradyumna1312/FPGA_Workshop_VSD

https://github.com/deepsita/vsdBasicPD

https://github.com/britovski/vsdBasicPD

https://github.com/manjunathrv/VSD_Advanced_Physical_Design_with_sky130nmPDK

https://github.com/eugene-tarassov/vivado-risc-v/blob/master/.gitmodules