Slovenská technická univerzita

Fakulta elektrotechniky a informatiky

VNORENÉ SYSTÉMY

Práca s GPIO registrami

Cvičenie 2

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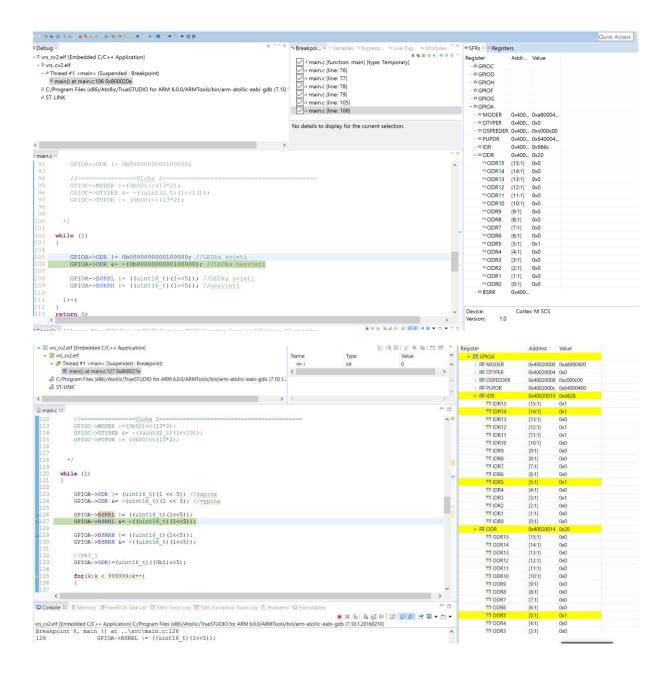
Úloha 1

```
// nastavenie periferie
   RCC AHBPeriphClockCmd(RCC AHBPeriph GPIOA, ENABLE);
     GPIOA->MODER \mid = (uint32_t)((0b01) << (5*2));
     GPIOA->OTYPER &= \sim ((0b1) <<5);
     GPIOA -> PUPDR \mid = (uint32 t) ((0b01) << (5*2));
     GPIOA->OSPEEDR |= (uint \overline{32} t) ((0b11) << (5*2));
                                                                              X<sub>16</sub> X<sub>10</sub> X<sub>2</sub>
 Modules □□
                ■SFRs 

Registers
% 2 √ | ⊞ ⊟ $ ▽
                                Addr... Value
                 Register
                  > ## GPIOF
                  > ## GPIOG

✓ IIII GPIOA

                    > IIII MODER
                                0x400... 0xa80004...
                    > IIII OTYPER
                                0x400... 0x0
                   > $88 OSPEEDER 0x400... 0xc000c00
                    > IIII PUPDR
                                0x400... 0x640004...
                   > 1010 IDR
                                0x400... 0x980c
                    ~ ⁵⁵⁵° ODR
                                0x400... 0x0
                      *** ODR15
                               [15:1]
                                       0x0
                      ODR14
                               [14:1]
                                       0x0
                      ₩ ODR13
                                [13:1]
                                       0x0
                      ODR12
                               [12:1]
                                       0x0
                      ODR11
                                [11:1]
                                       0x0
                      ODR10
                               [10:1]
                                       0x0
                      ODR9
                                [9:1]
                                       0x0
                      ODR8
                                [8:1]
                                       0x0
                      ODR7
                                       0x0
                                [7:1]
                      ODR6
                                [6:1]
                                       0x0
                      ODR5
                                       0x0
                                [5:1]
                      ODR4
                                [4:1]
                                       0x0
                      ODR3
                                [3:1]
                                       0x0
                      ODR2
                                [2:1]
                                       0x0
                      ODR1
                                [1:1]
                                       0x0
                      ODR0
                                [0:1]
                                       0x0
                                0x400...
                    > IIII BSRR
                                0x400... 0x0
                    > IIII LCKR
                                0x400... 0x0
                    > IIII AFRL
                    > IIII AFRH
                                0x400... 0x0
                          Register:
                                        ODR
                 Address:
                                        0x40020014
                 Value:
                                  0
Size:
                                  32
                 Reset value:
                                        0x0000000
                                        0xFFFFFFF
             ^ Reset mask:
```



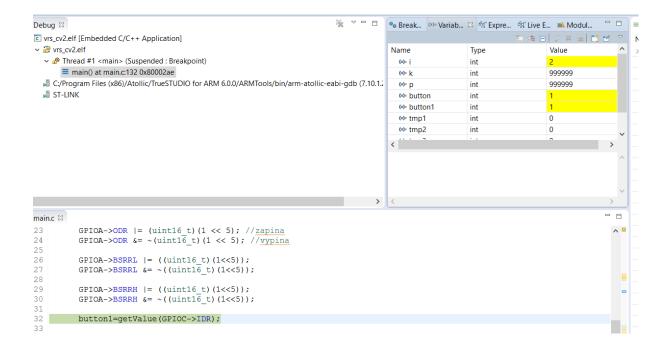
Úloha 2

button1 reprezentuje hodnotu tlacidla.

```
RCC_AHBPeriphClockCmd(RCC_AHBPeriph_GPIOC,ENABLE);

GPIOC->MODER &= ~((0b11) << (13*2));
GPIOC->OTYPER &= ~((0b1) << 13);
GPIOC->PUPDR &= ~((0b11) << (13*2));

button=getValue(GPIOC->IDR);
```



Úloha 3

Blikanie LED v intervale v nekonecnom while cykle.

```
GPIOA->ODR|=(uint16_t)((0b1)<<5);
for(k;k < 999999;k++) {}
GPIOA->ODR&= ~((uint16_t)((0b1)<<5));
for(p;p < 999999 ;p++) {}</pre>
```

Sledovanie tlacidla a zapinanie/vypinanie LED v nekonecnom while cykle.

```
button=getValue(GPIOC->IDR);
    if(button==1)
        GPIOA->ODR |=(uint16_t)(0b1<<5);
    else
    GPIOA->ODR &=~((uint16_t)(0b1<<5));</pre>
```

Po stlaceni tlacidla LED zmeni stav.

```
button1=getValue(GPIOC->IDR);
if (button1 == 1)
{
    while(counter < 6)
    {
        counter++; //5 "impulzov"
    }
    counter = 0;
}
button1=getValue(GPIOC->IDR);
```

```
if (button1 == 0)
      {
            while(counter < 6)</pre>
                 counter++; //5 "impulzov"
            }
            counter = 0;
      }
button1=getValue(GPIOC->IDR);
if (button1 == 1)
      {
            while(counter < 6)</pre>
                  counter++; //5 "impulzov"
            }
            counter = 0;
            if(tmp1 == 0)
                  GPIOA->ODR |=(uint16_t)(0b1<<5);</pre>
                  tmp1 = 1;
            }
            else if(tmp1 == 1)
                  GPIOA->ODR &= \sim (uint16_t) (0b1 << 5);
                  tmp1 = 0;
            }
      }
```