

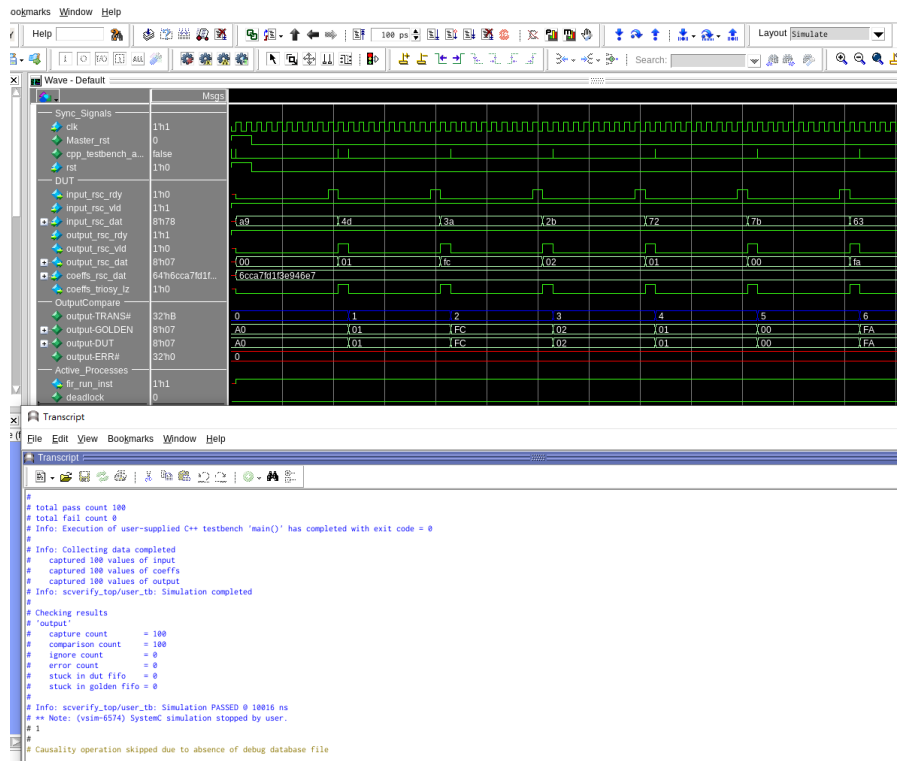
Advanced SoC Lab2-1

學號: 111061605

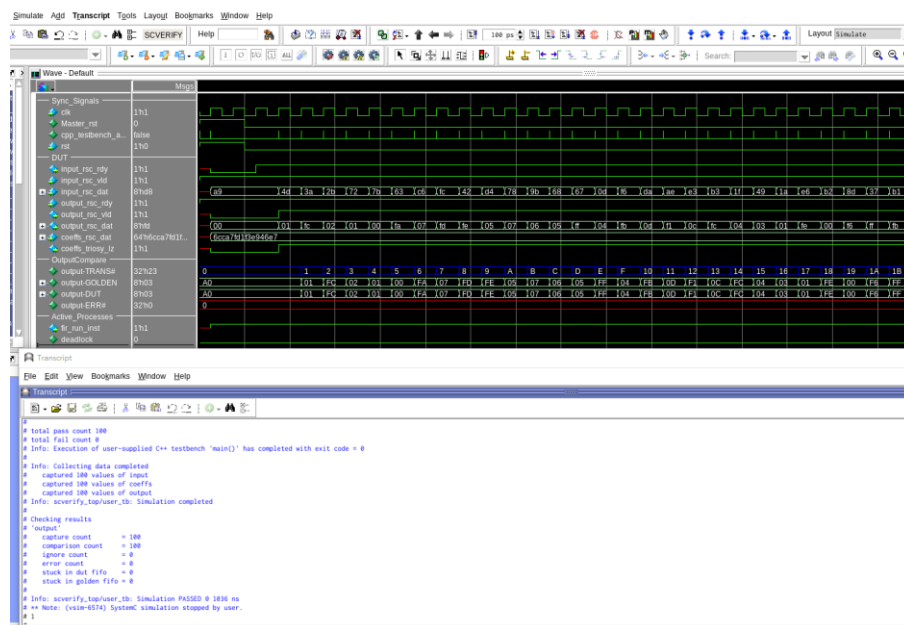
姓名: 周聖平

一、FIR

1. Normal



2. Loop Unroll and Pipeline



[illegible]