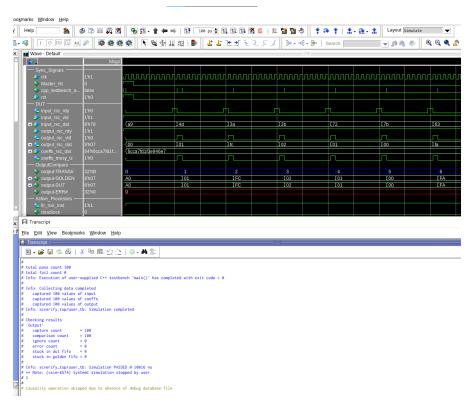
Advanced SoC Lab2-1

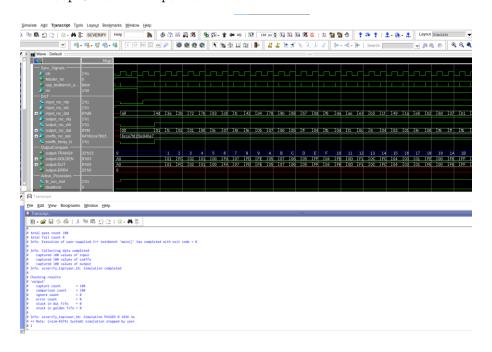
學號: 111061605 姓名:周聖平

→ · FIR

1. Normal

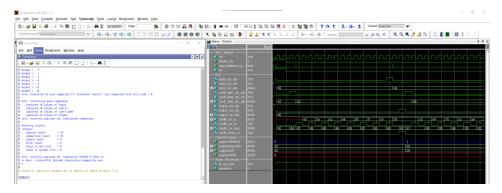


2. Loop Unroll and Pipeline

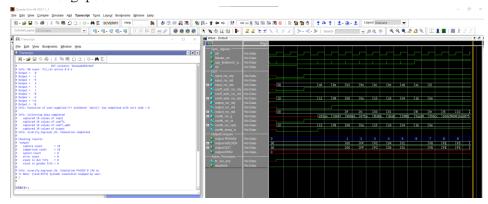


二、Memory Interface

1. Normal

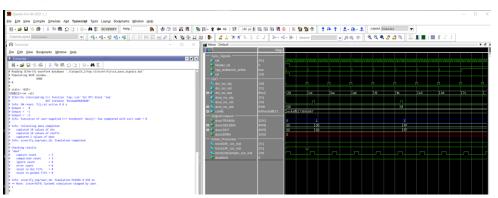


2. Throughput = 1



三、Multiple blocks

1. Normal



2. Throughput = 1

