Simple DDS Specification

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1 Overview

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Description: DDS core capable of sin and arbitrary waves

- 2 Architecture
- 2.1 TODO insert drawing of architecture
- 3 TODO Operation
- 4 TODO Registers
- 5 TODO Clocks
- 6 TODO IO Ports
- 7 TODO Verification
- 7.1 verification plan
- 7.2 Verification Method
- 7.3 Verification/TB Guide
- 8 Synthesis
- 8.1
- 9 Revision History

Table 1: Change history for this core Rev Date Author Description 0.1 30-10-22 phoey initial release

- 10 Appendices
- 10.1 Resources & References