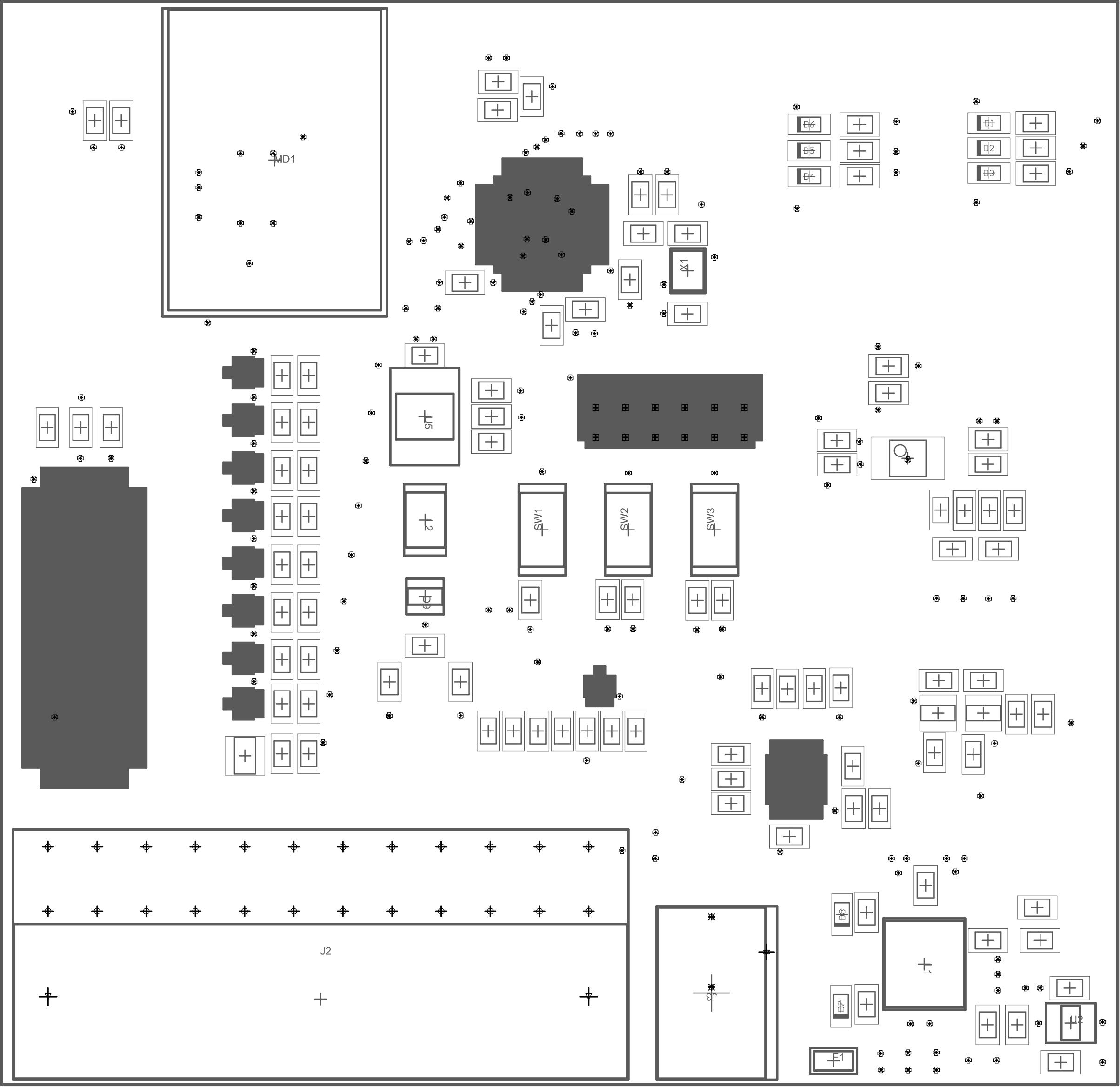
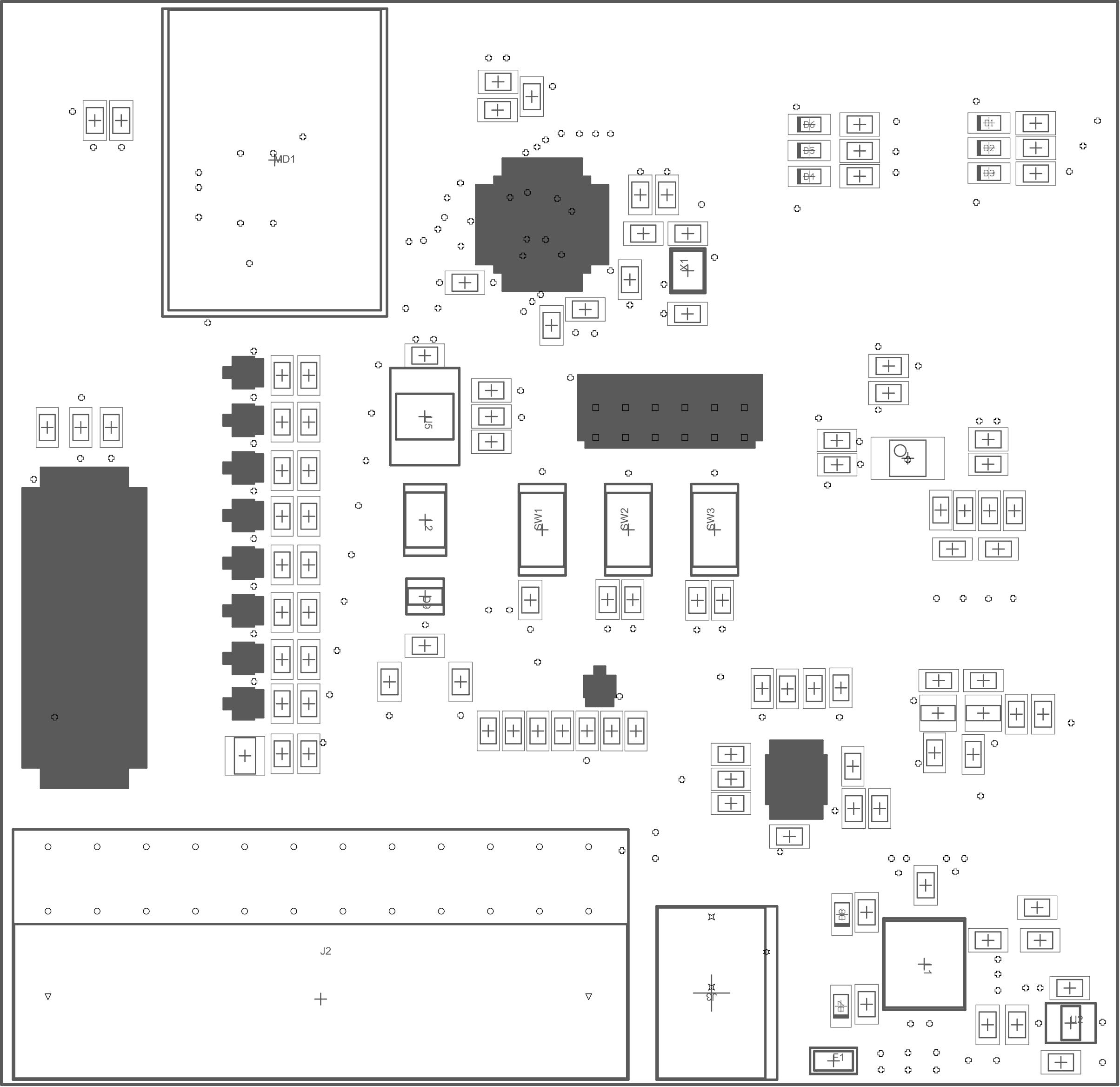
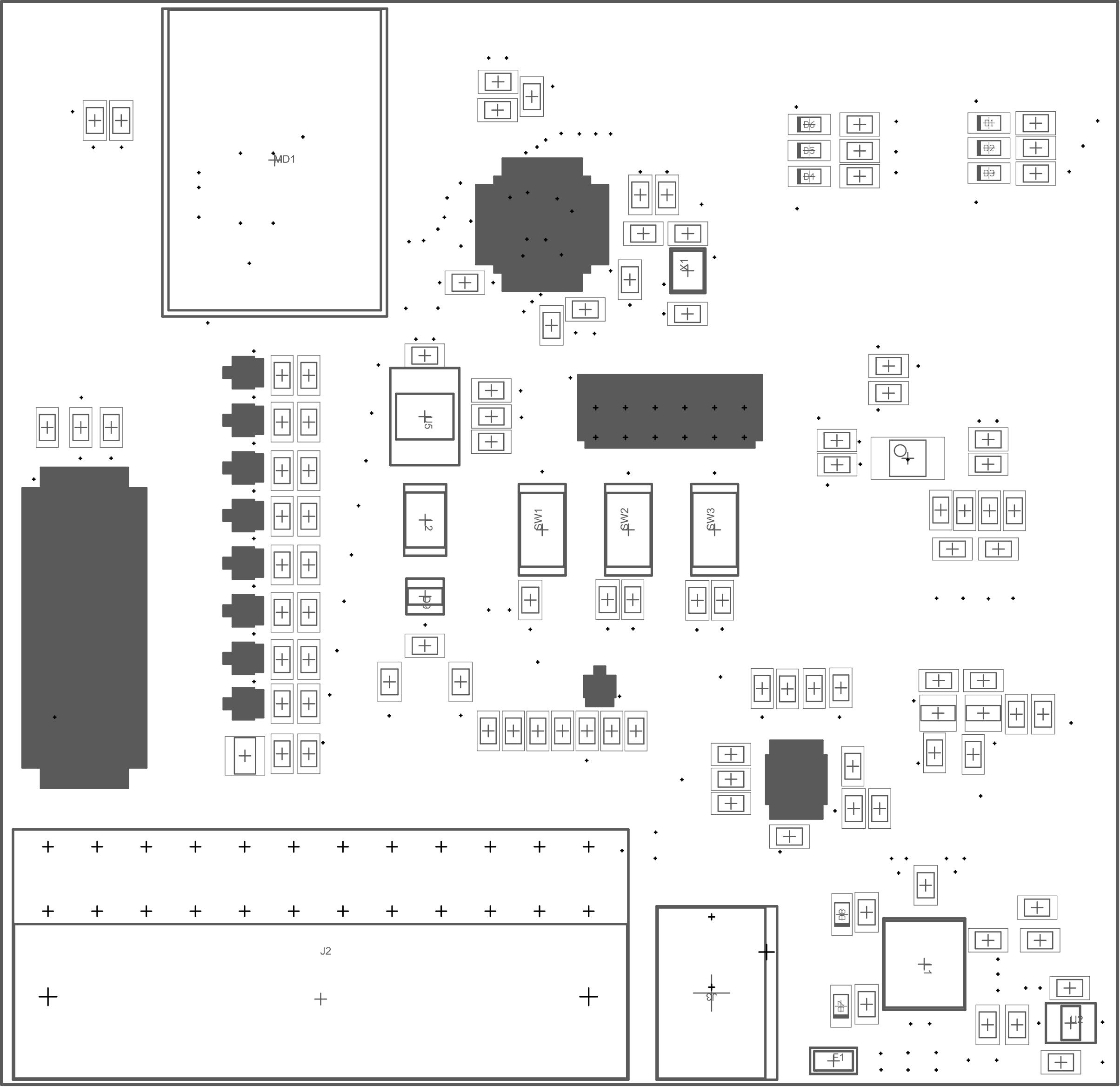


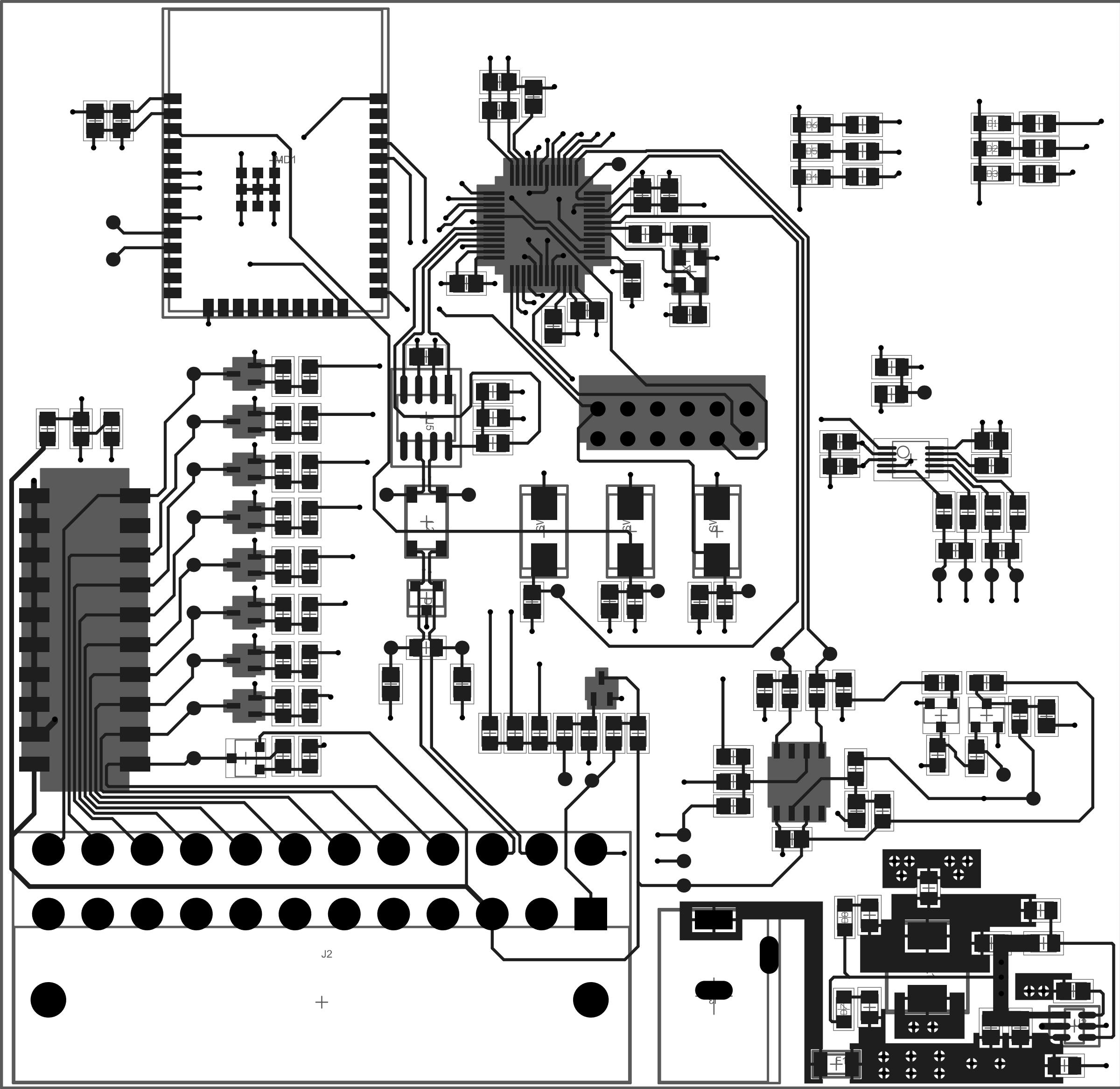
Board Stack Report

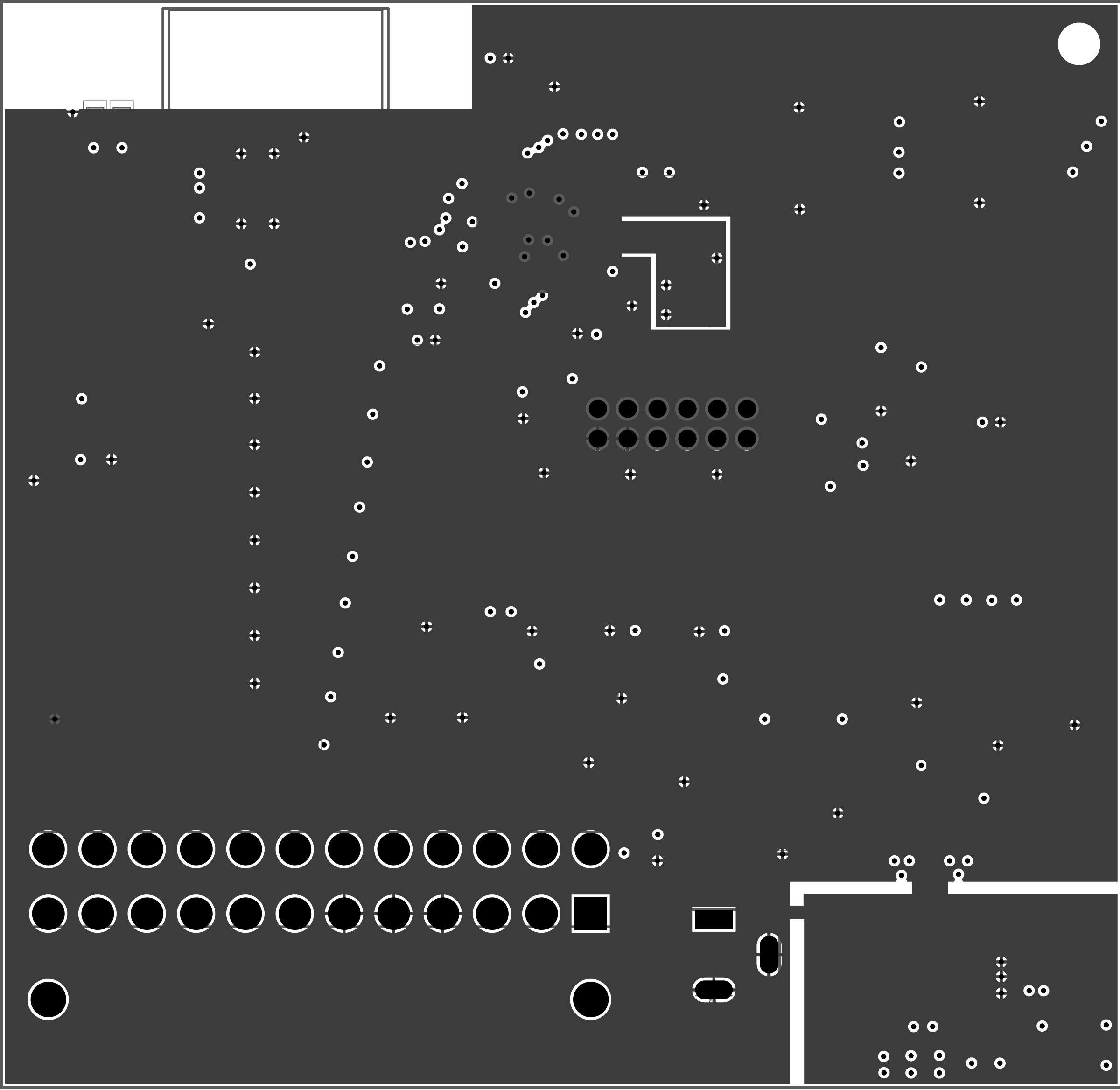
Stack Up		Layer Stack			
Layer	Board Layer Stack	Name	Material	Thickness	Constant
1		Top Overlay		0mil	
2		Top Solder	SM-001	1mil	4
3		Top Surface Finish	PbSn	0.787mil	
4		Top Layer	CF-004	1.378mil	
5		Dielectric 1	PP-017	3.91mil	4.3
6		Int1 (GND)	CF-004	0.689mil	
7		Dielectric 3	Core-039	48.8mil	4.8
8		Int2 (PWR)	CF-004	0.689mil	
9		Dielectric 5	PP-017	3.91mil	4.3
10		Bottom Layer	CF-004	1.378mil	
11		Bottom Surface Finish	PbSn	0.787mil	
12		Bottom Solder	SM-001	1mil	4
13		Bottom Overlay		0mil	
	Height : 64.328mil				

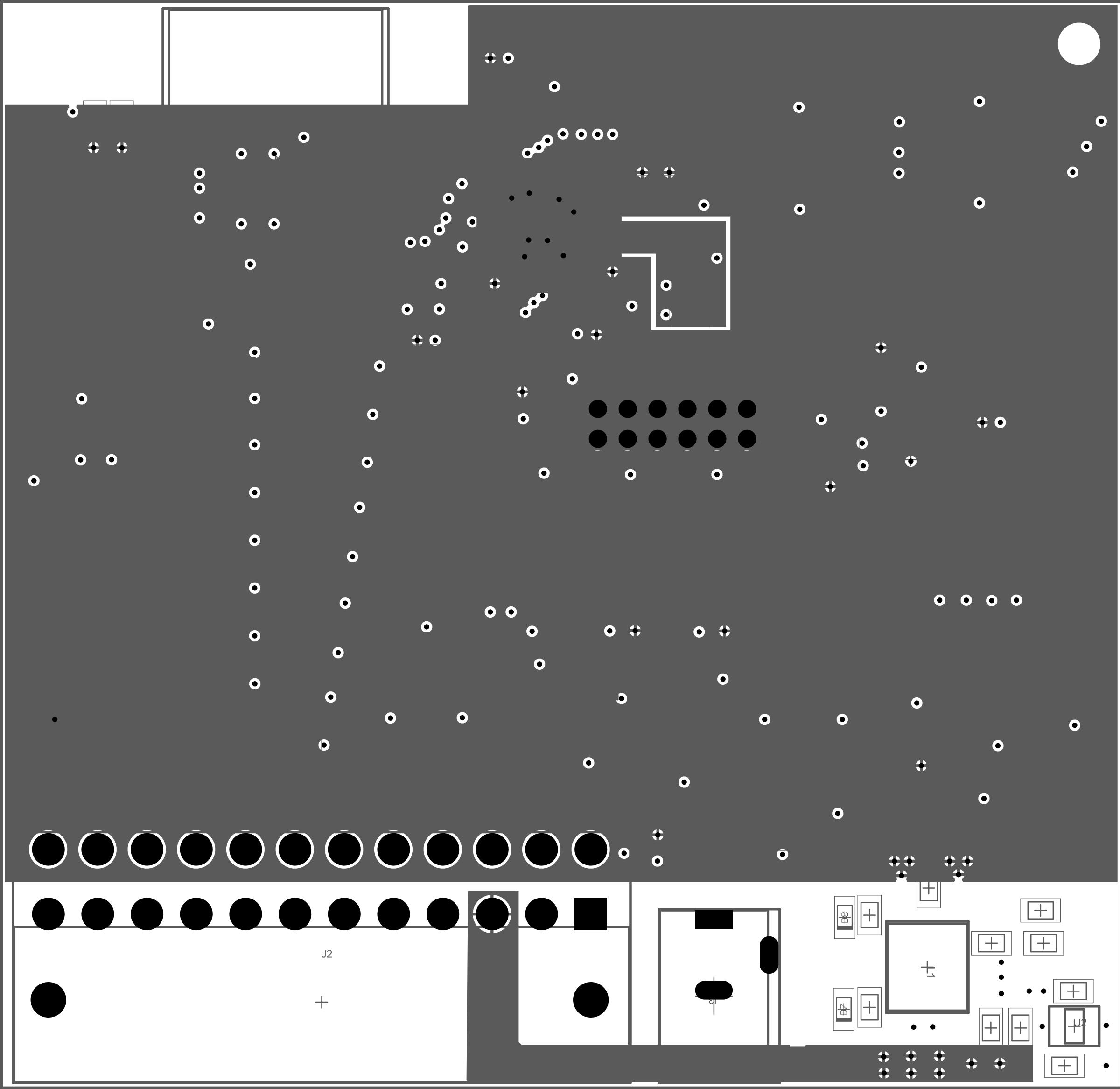


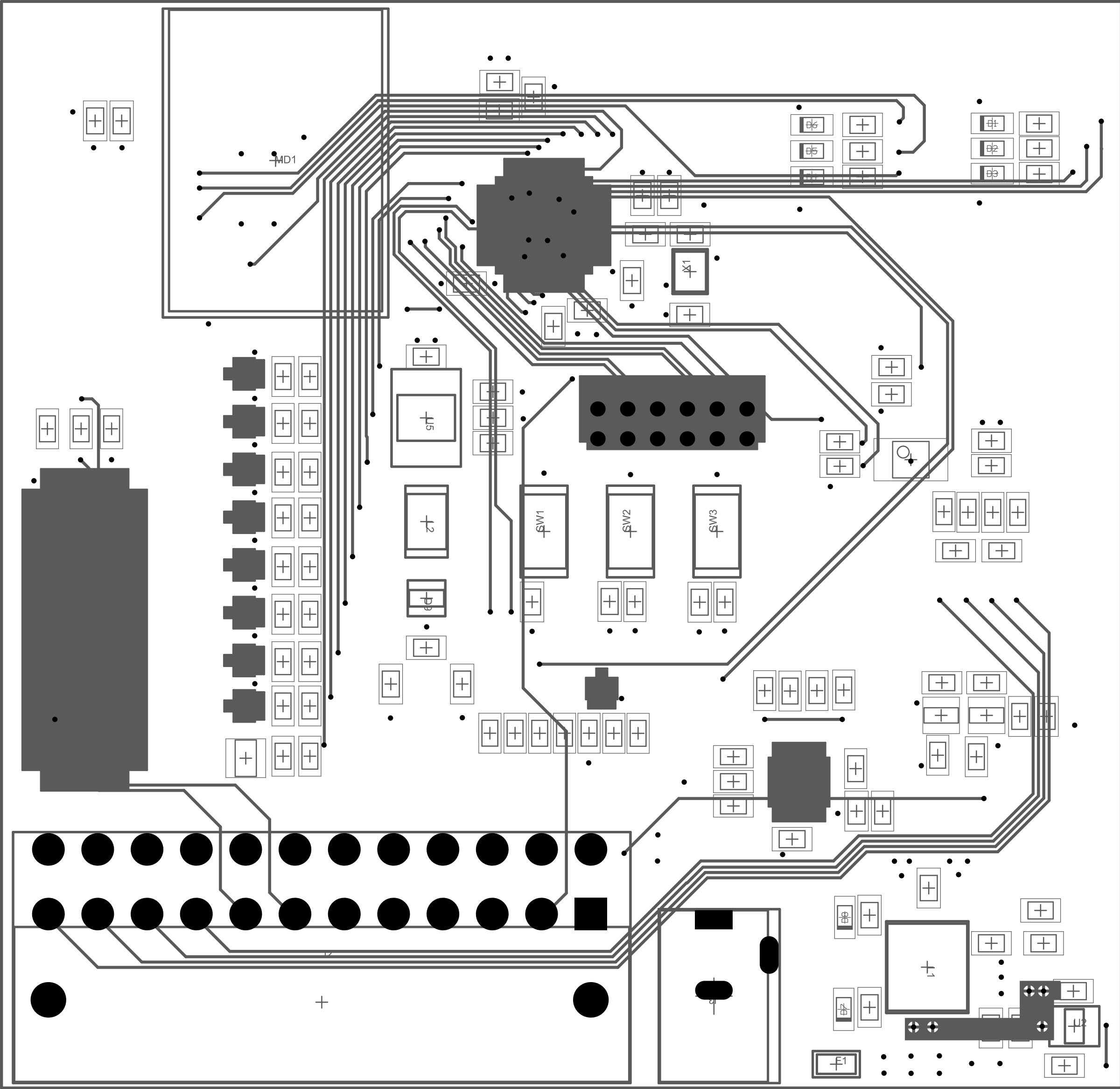


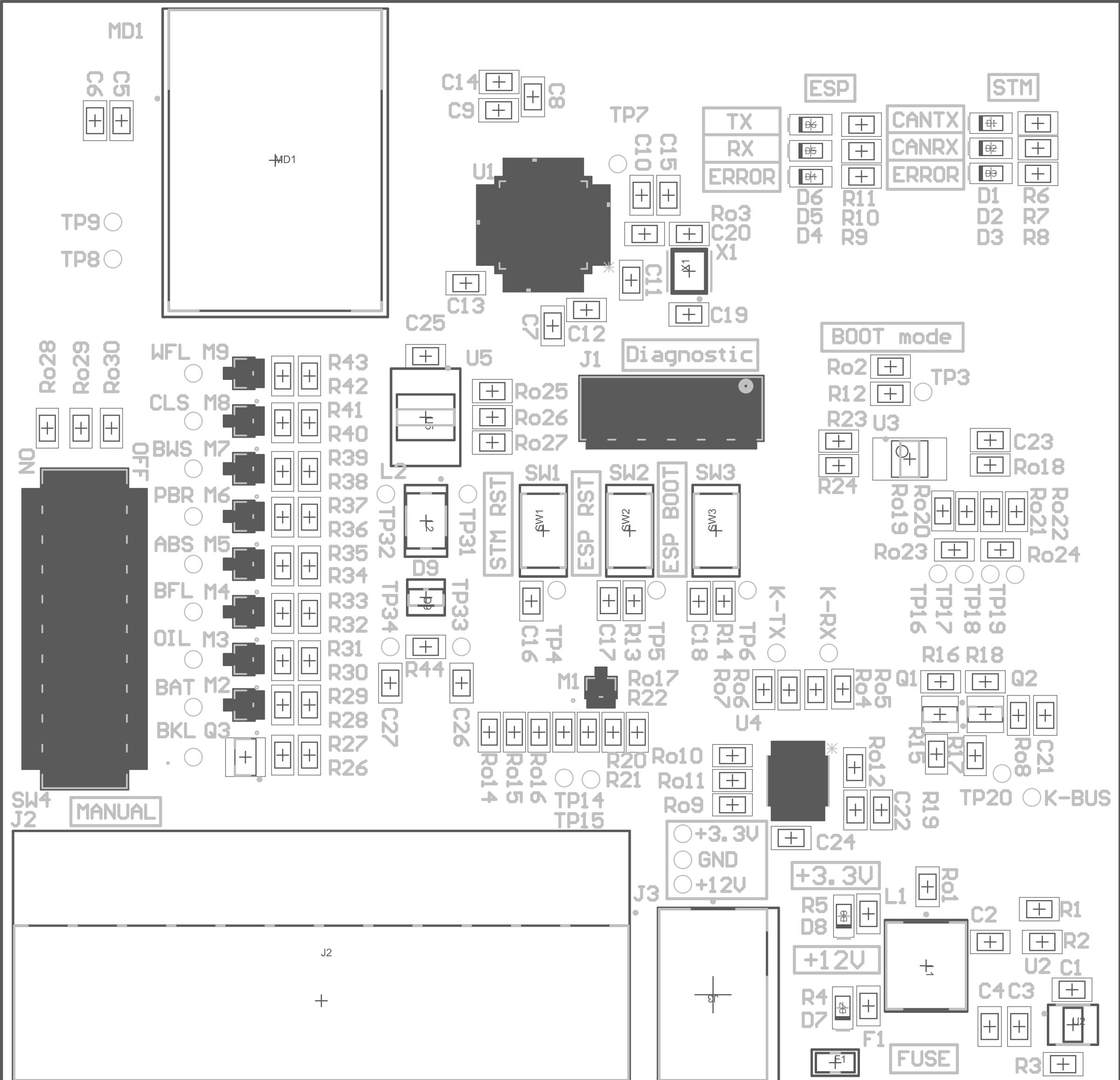


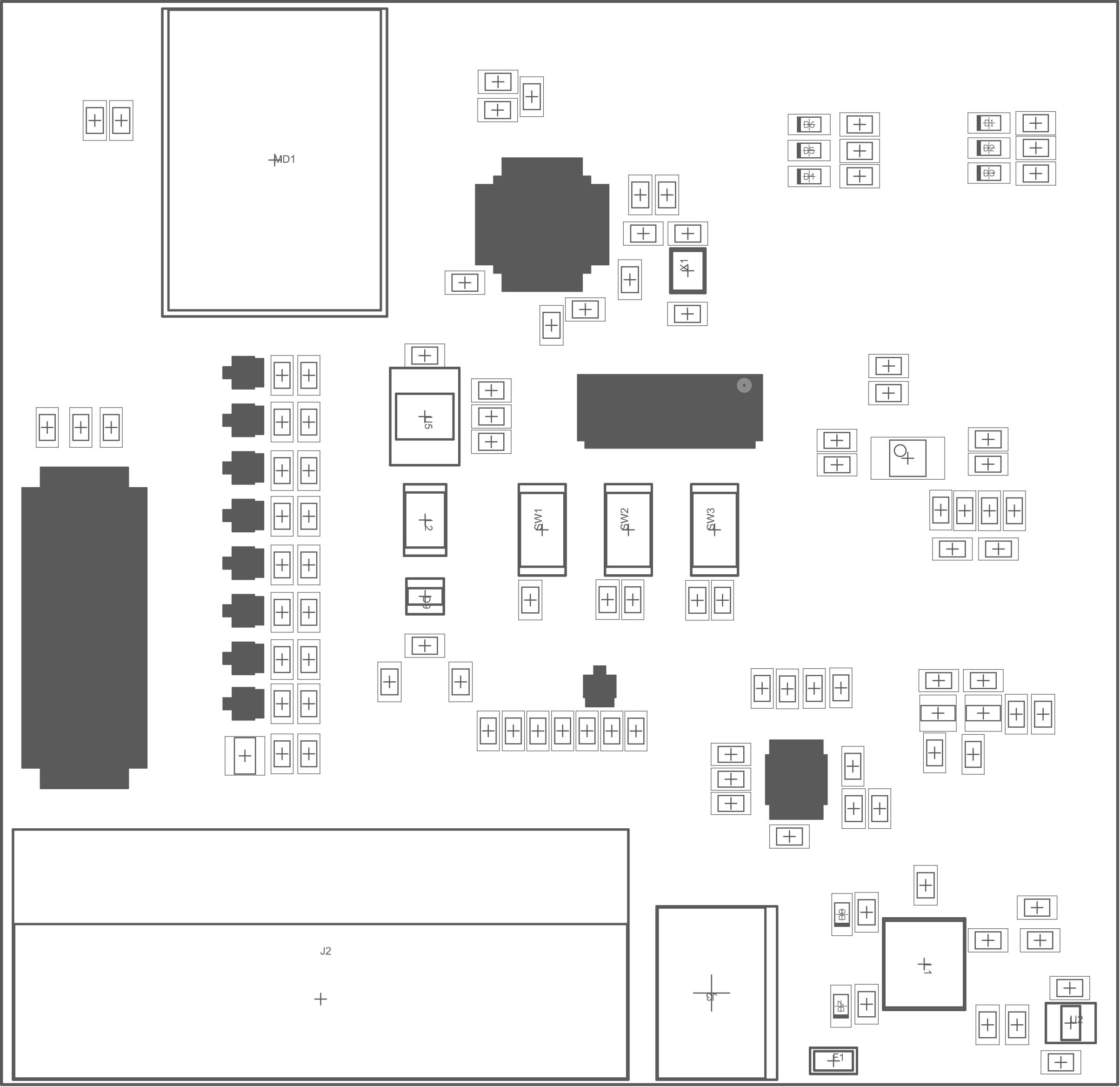


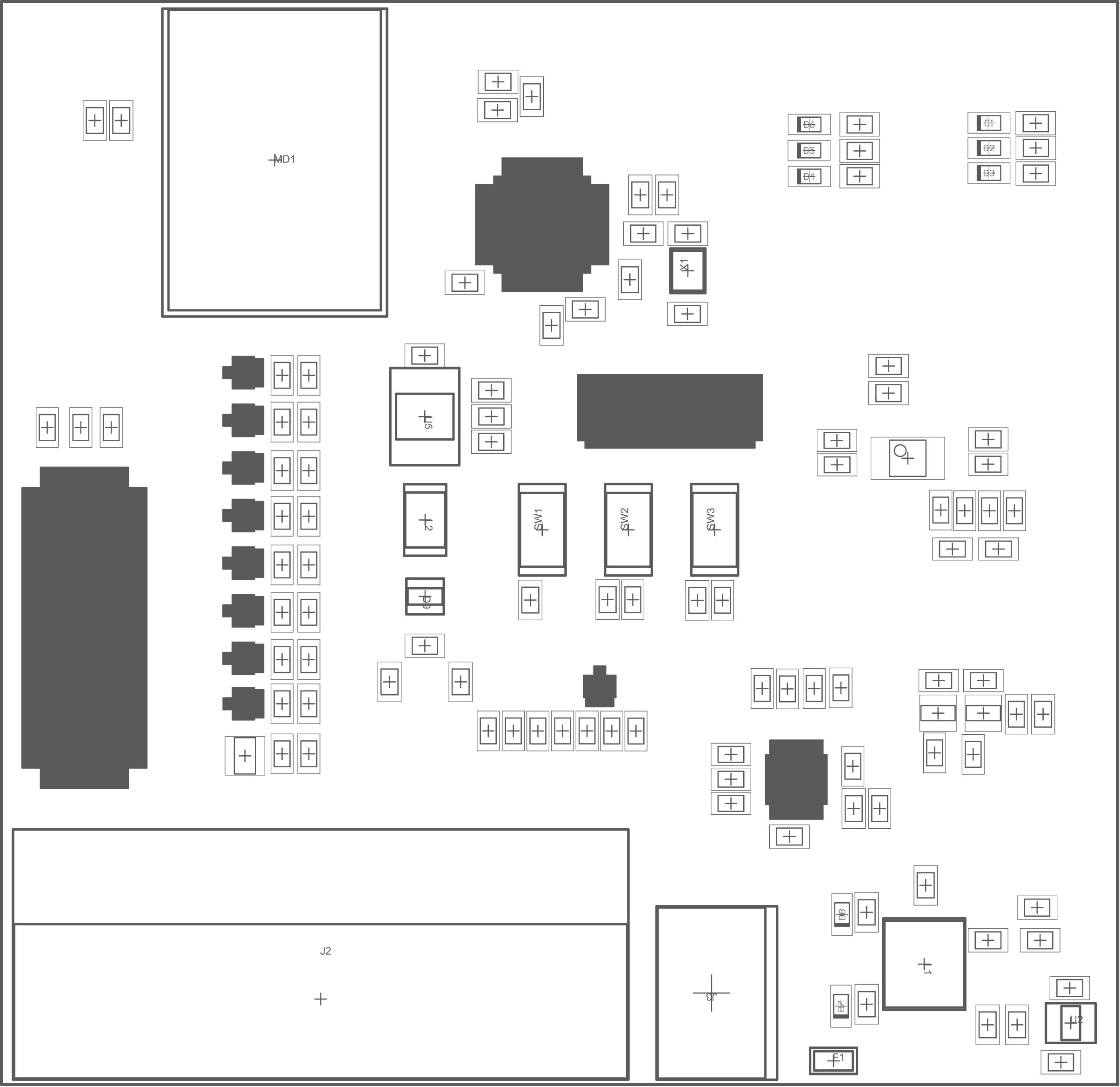


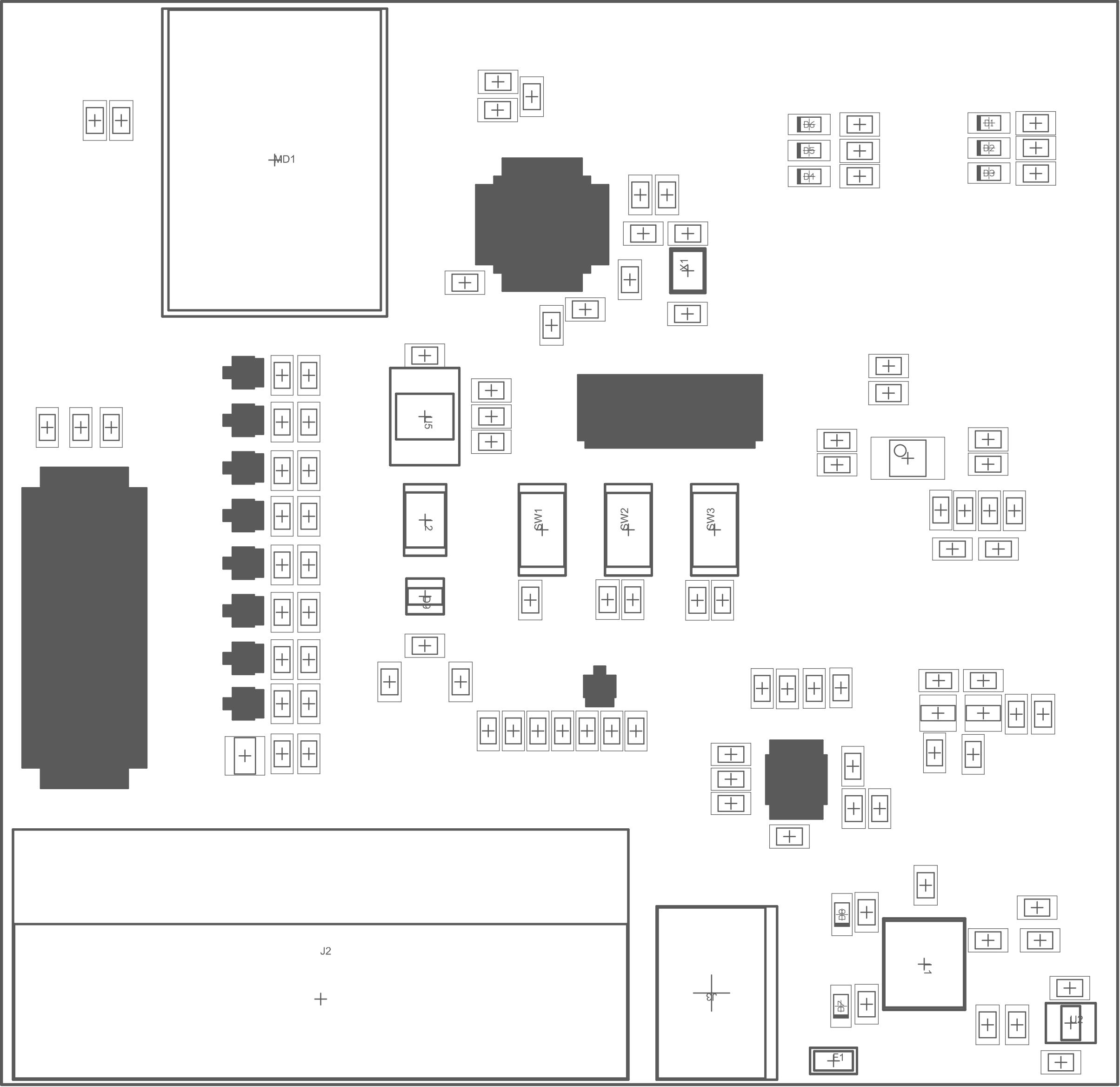


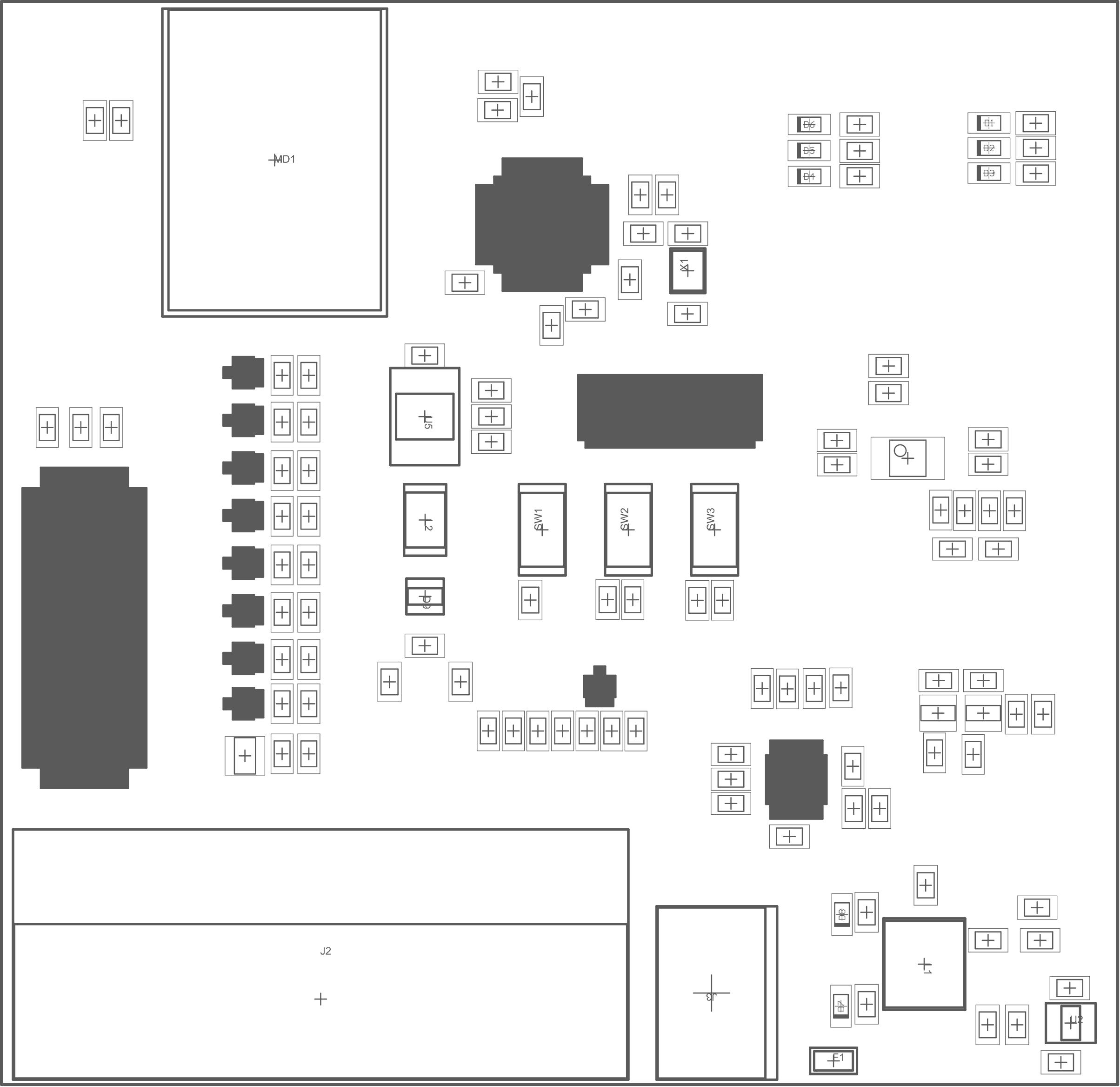


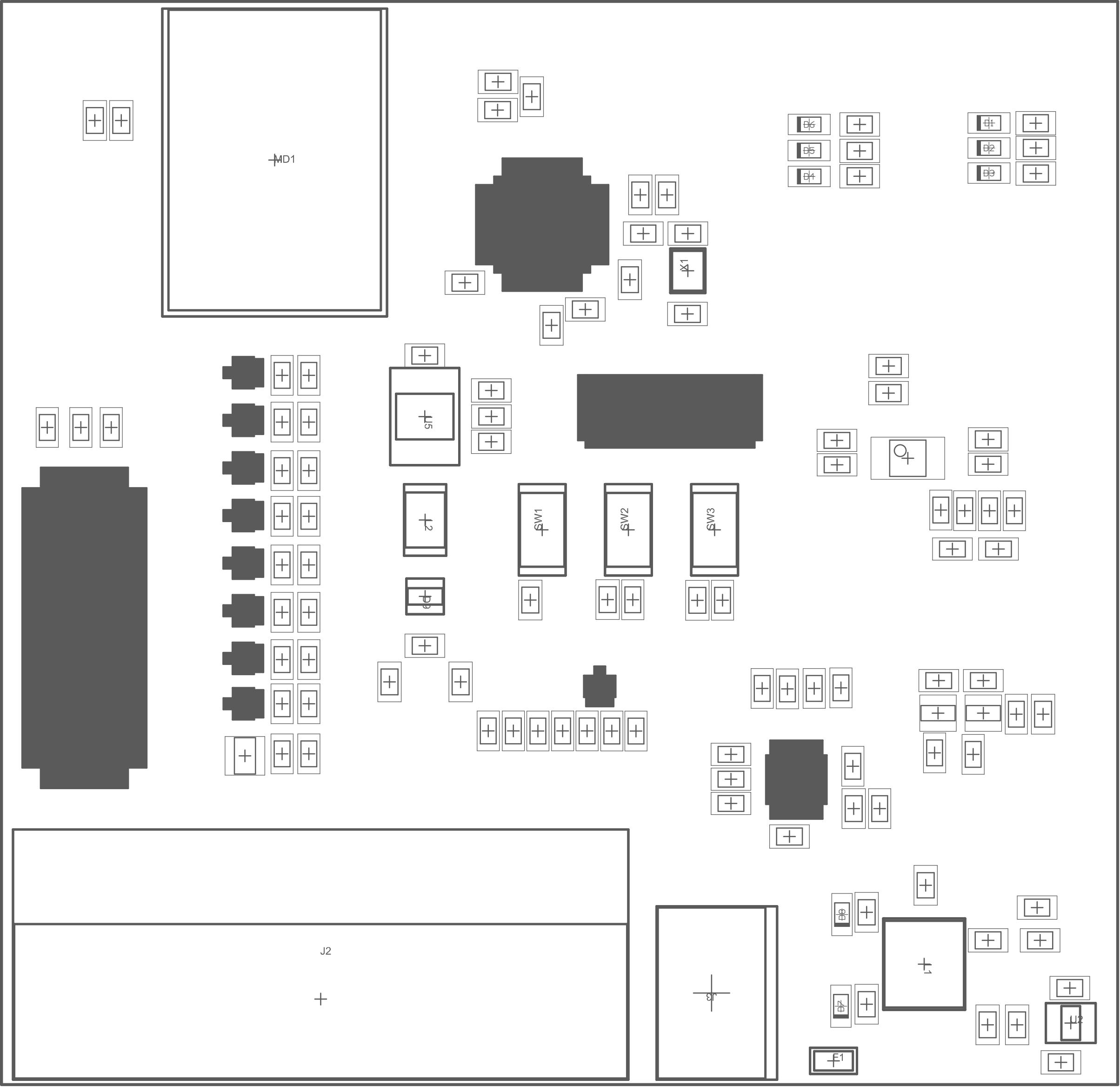


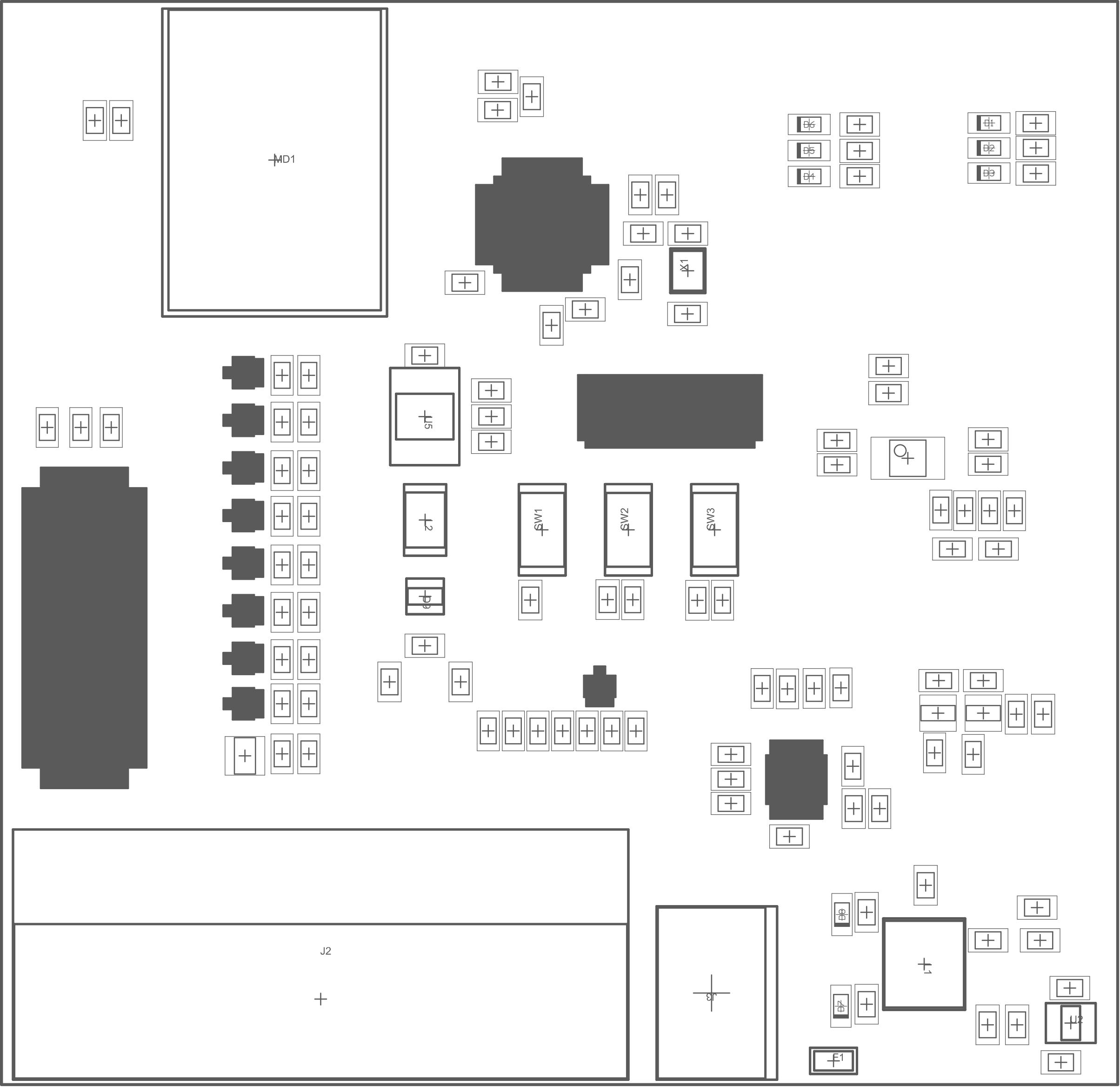


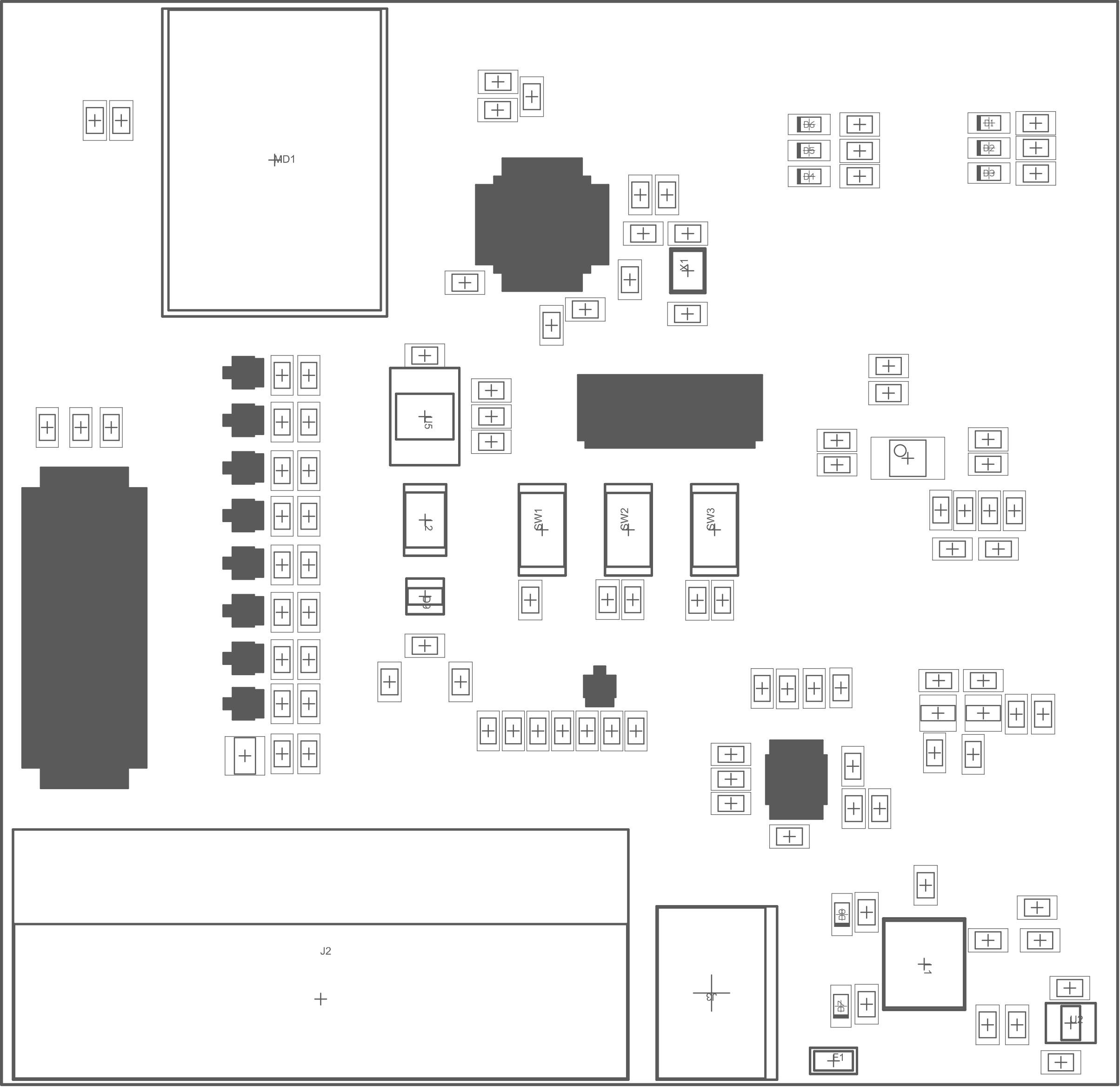


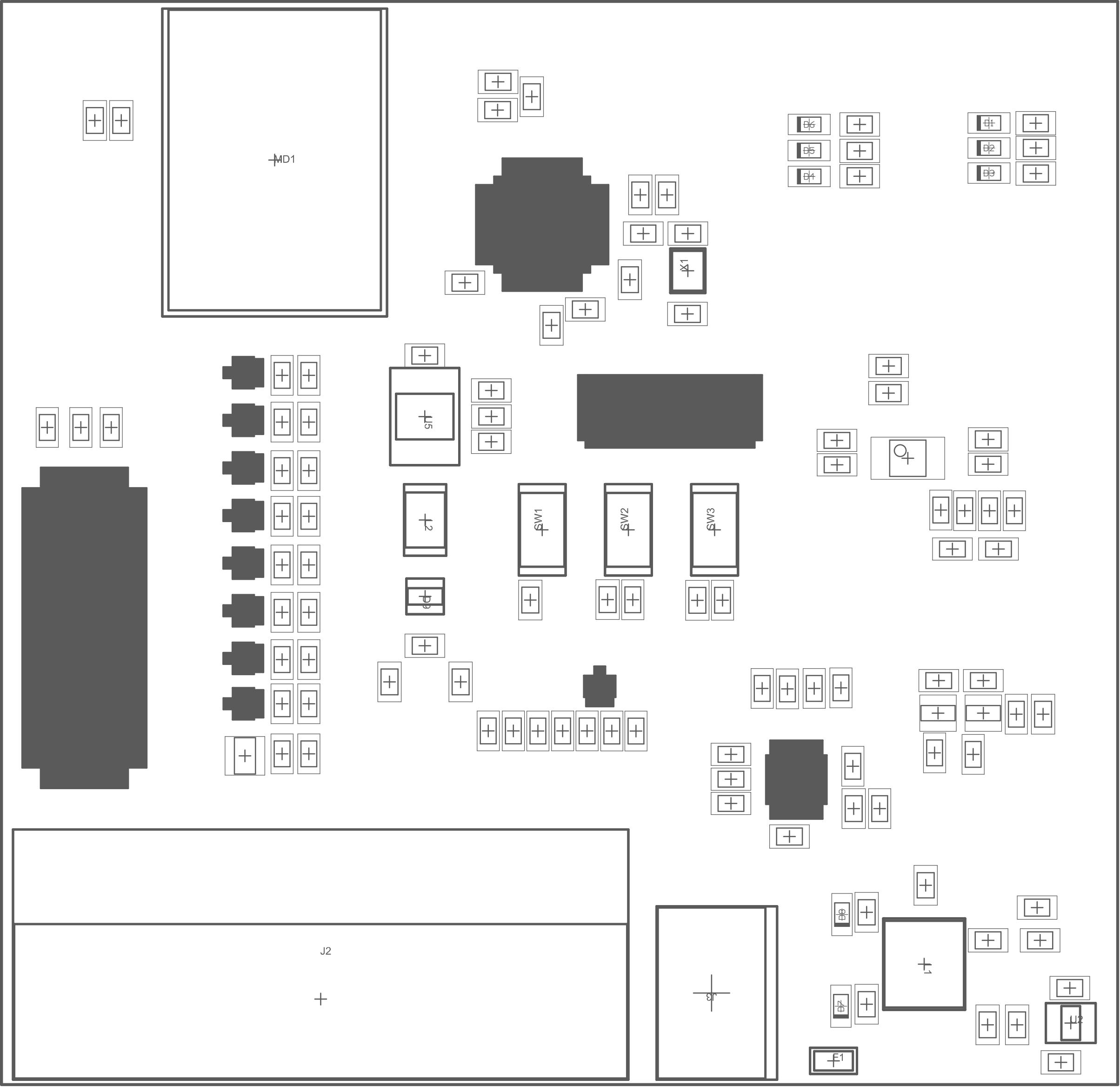


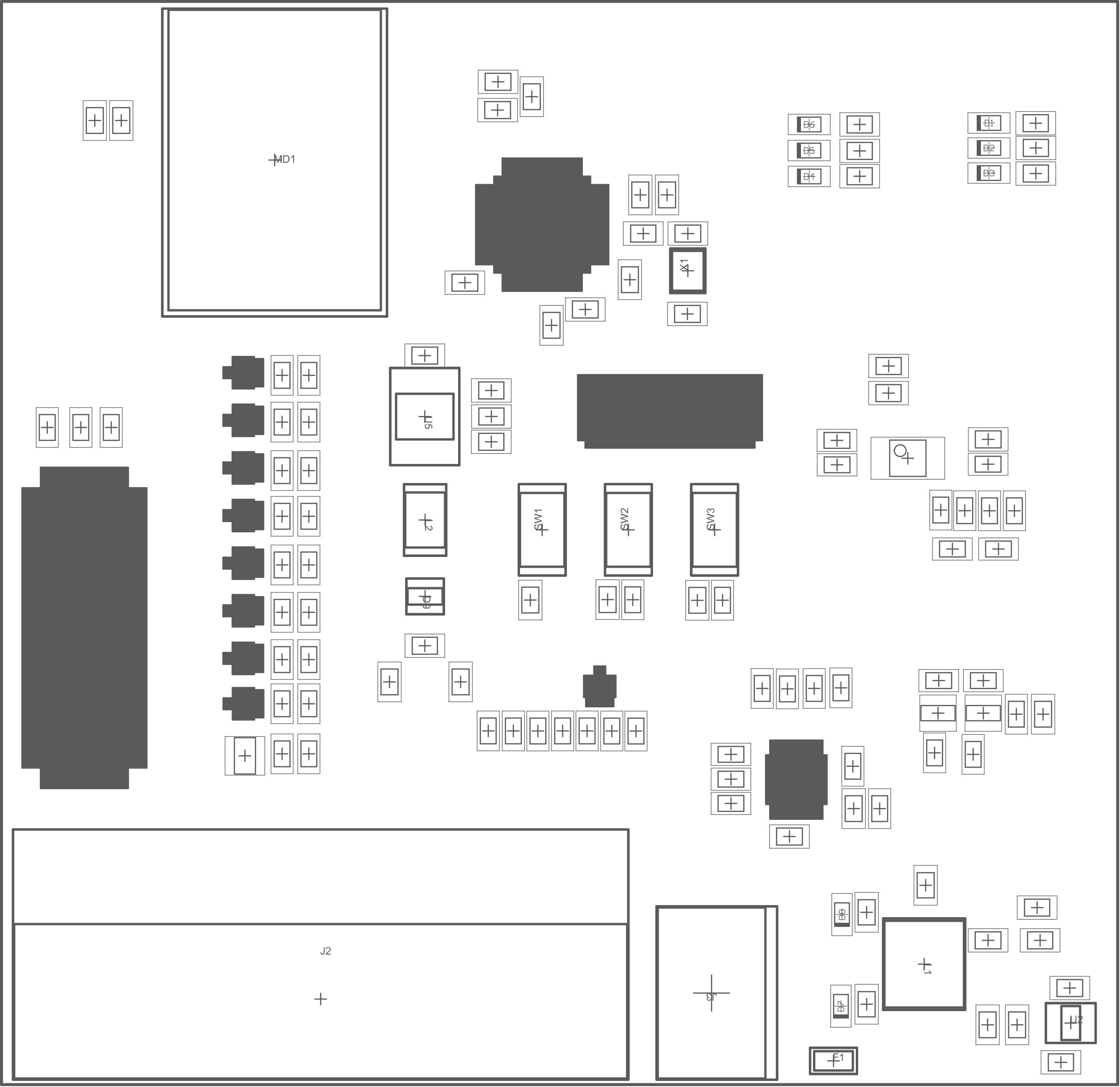


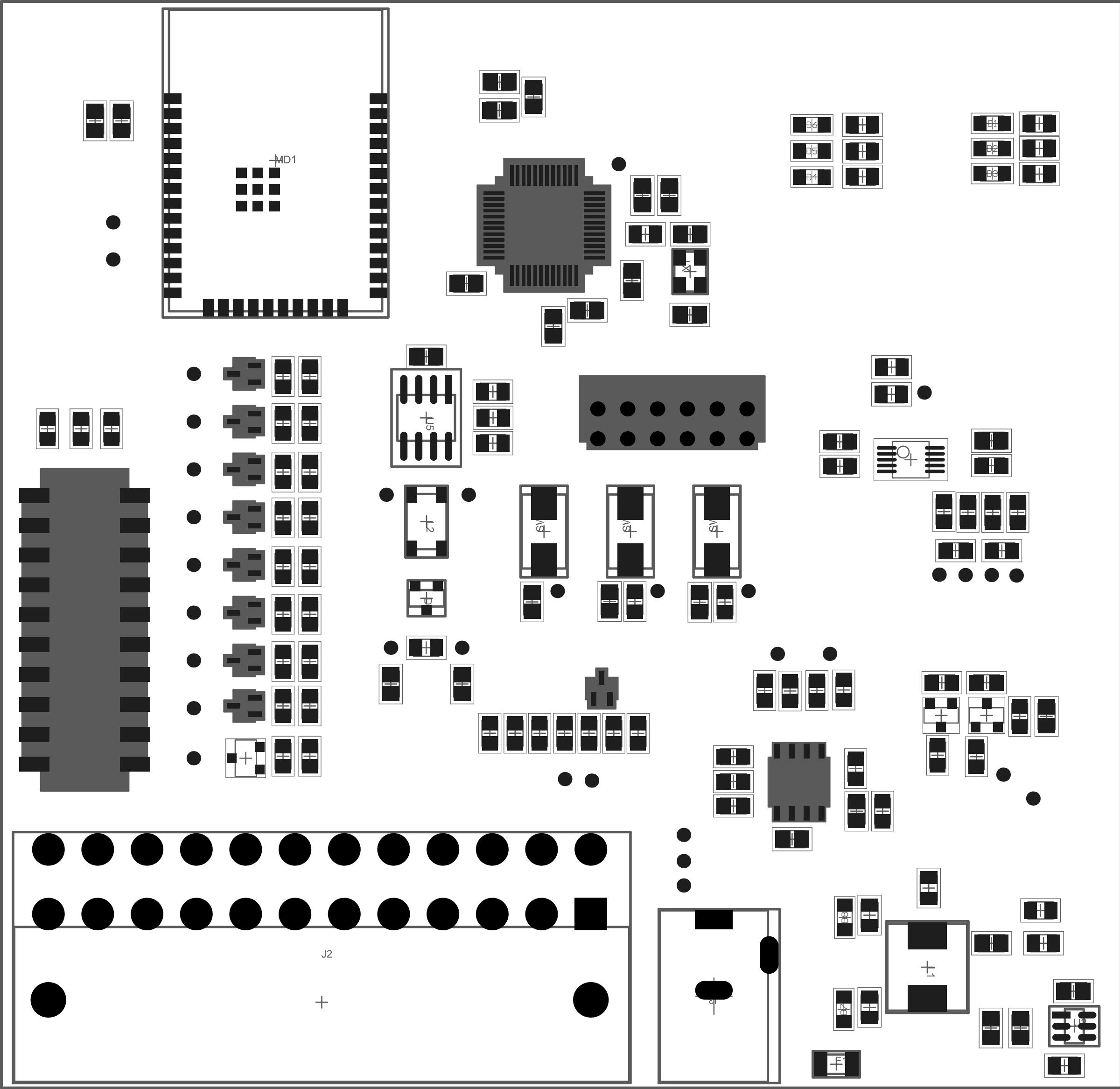


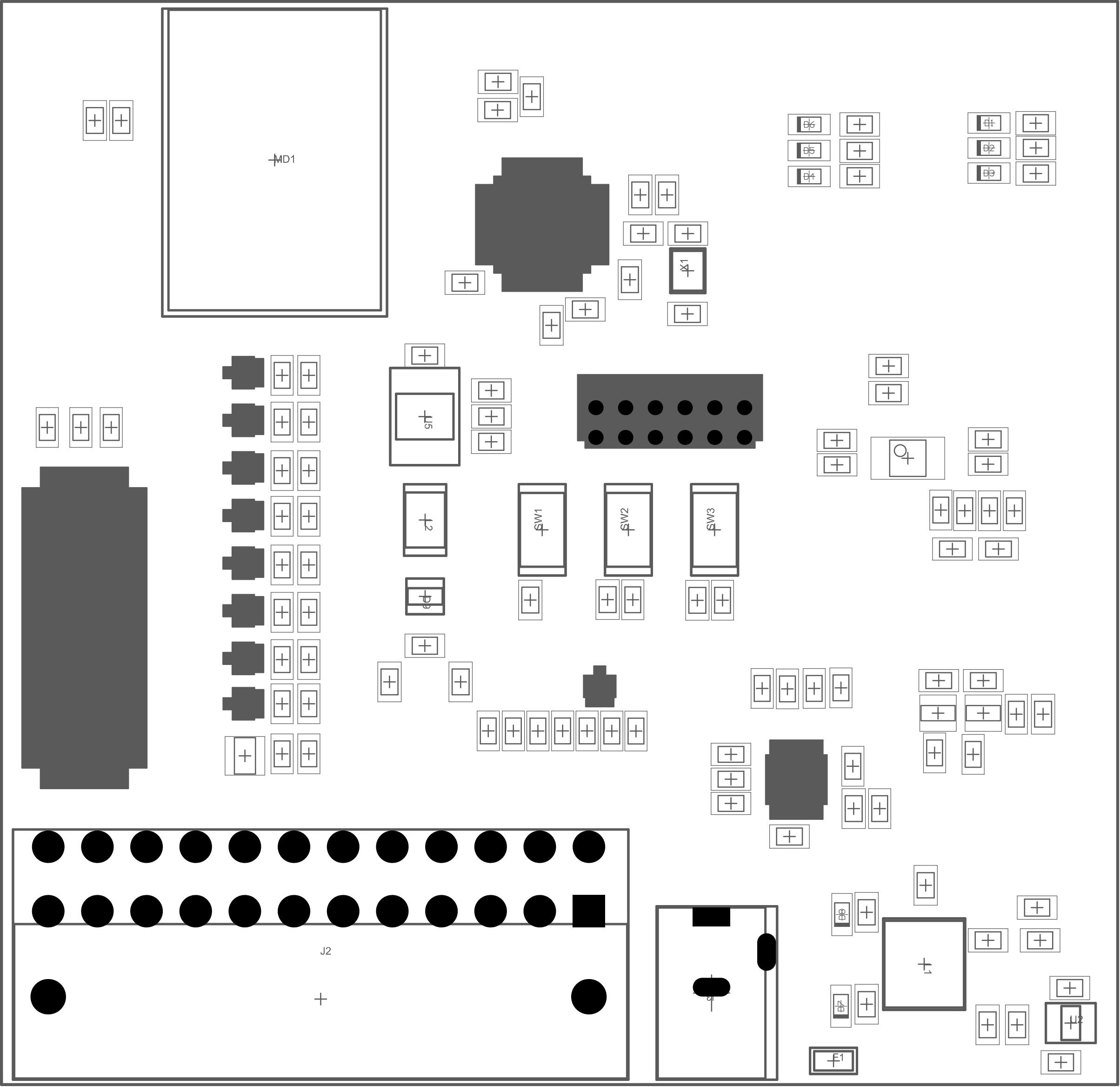


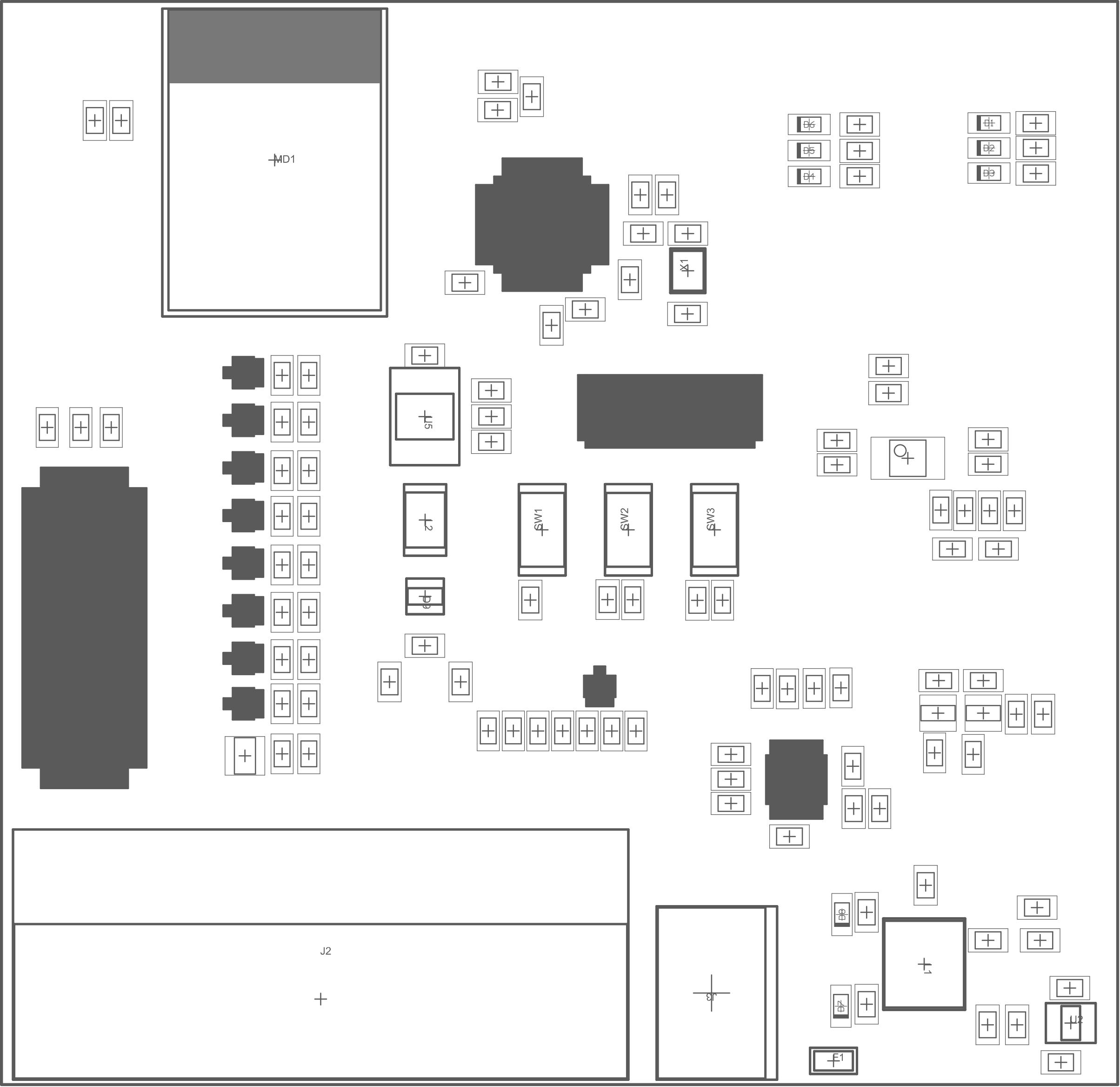


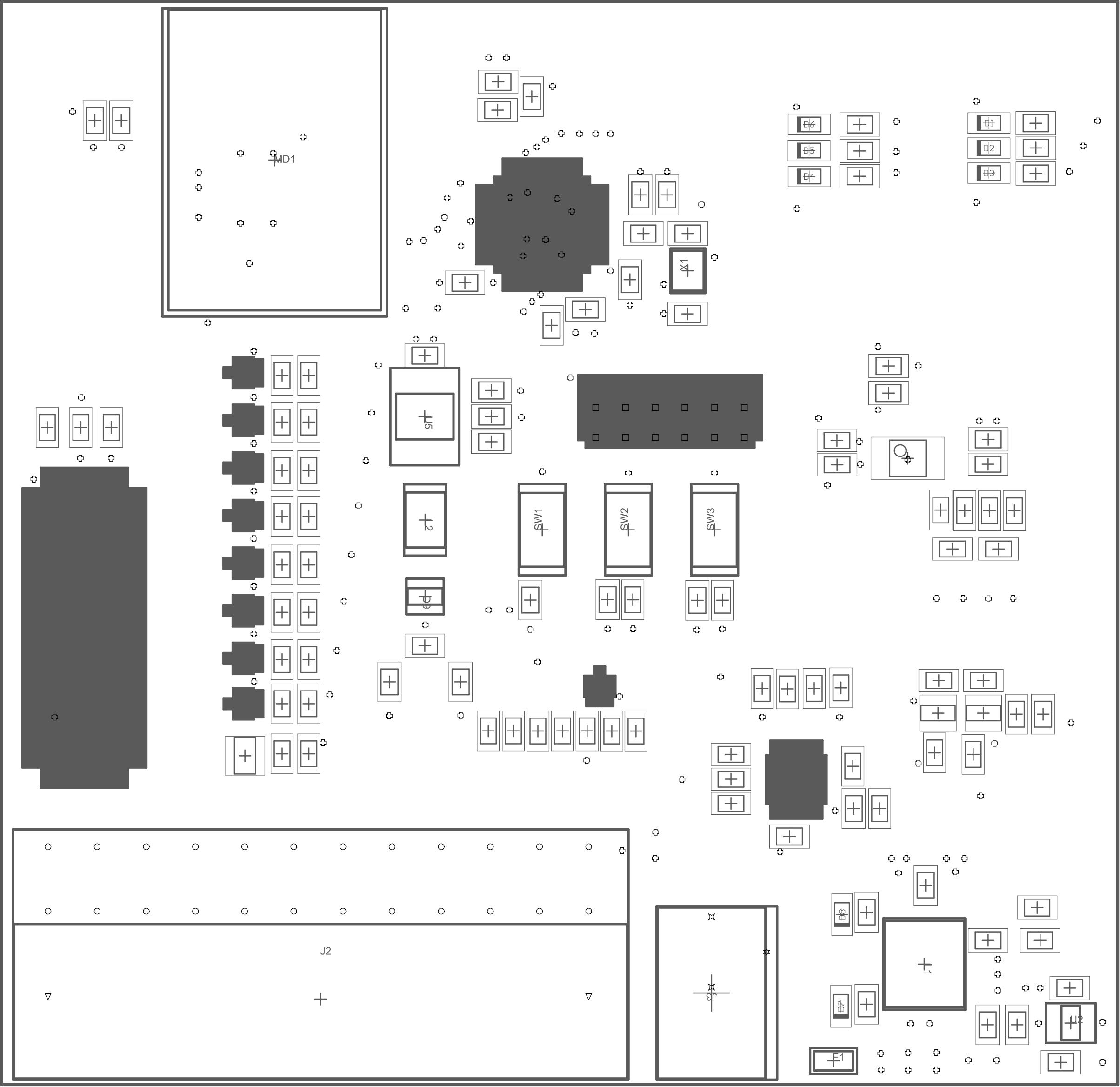


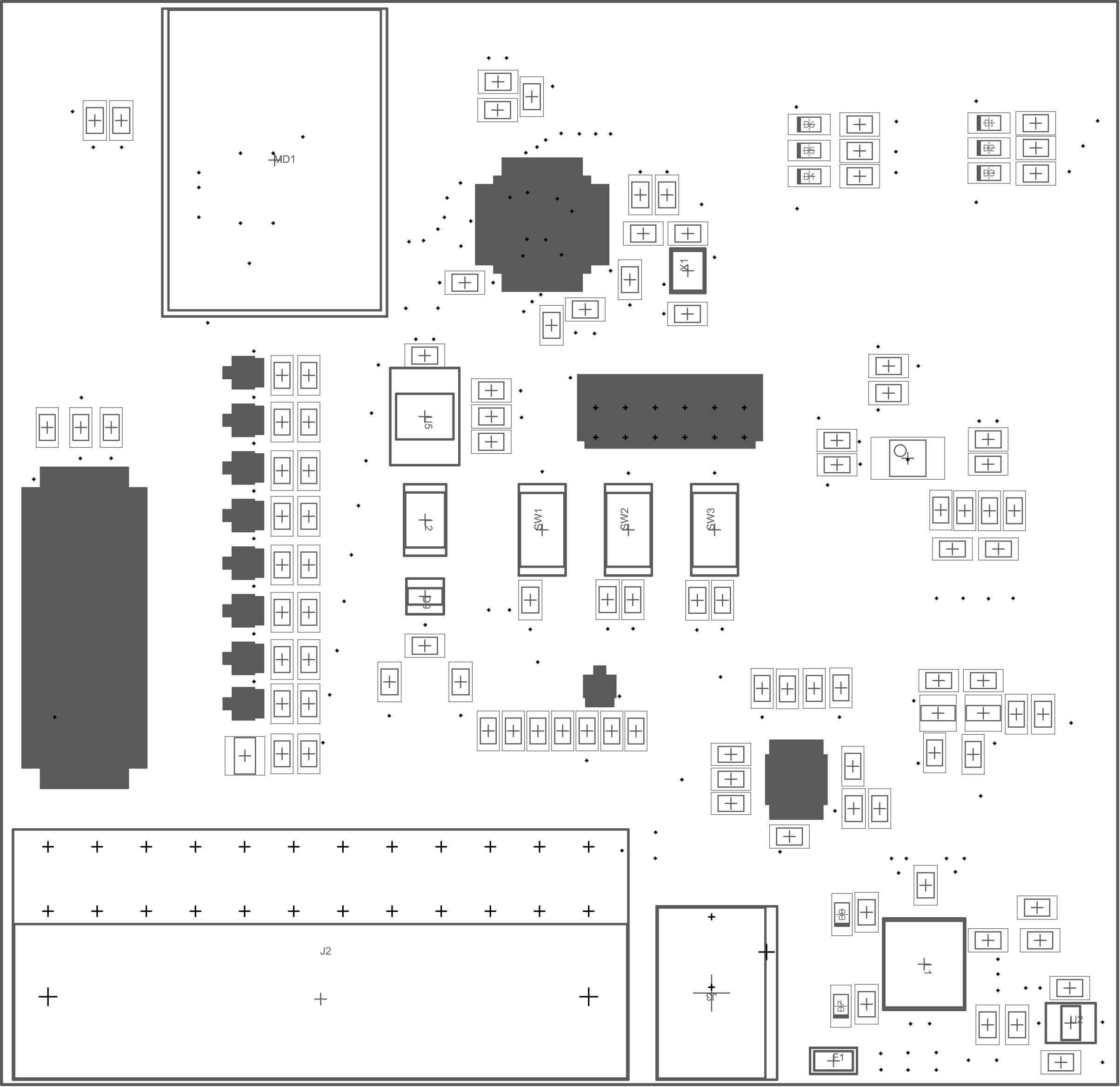


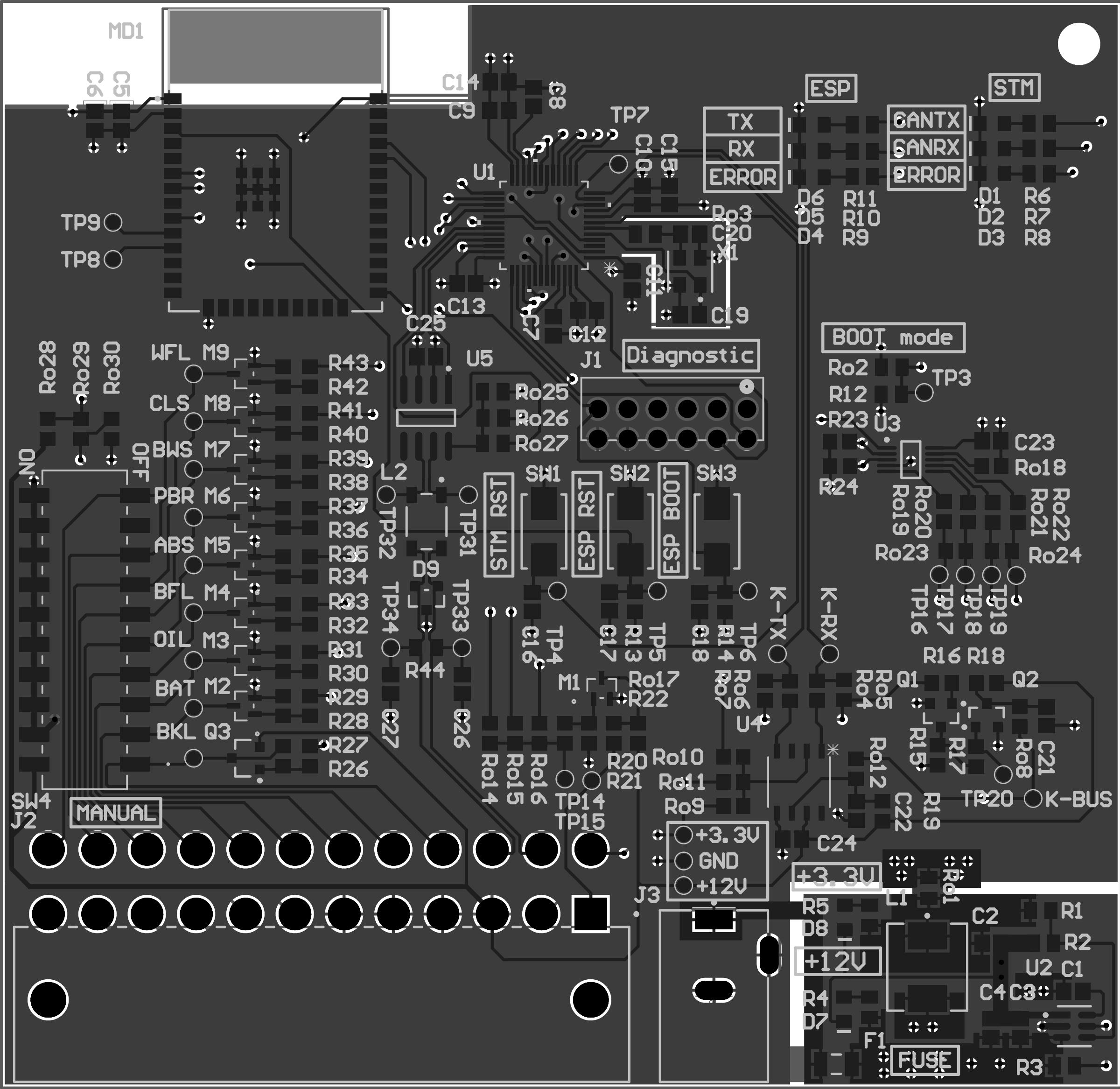


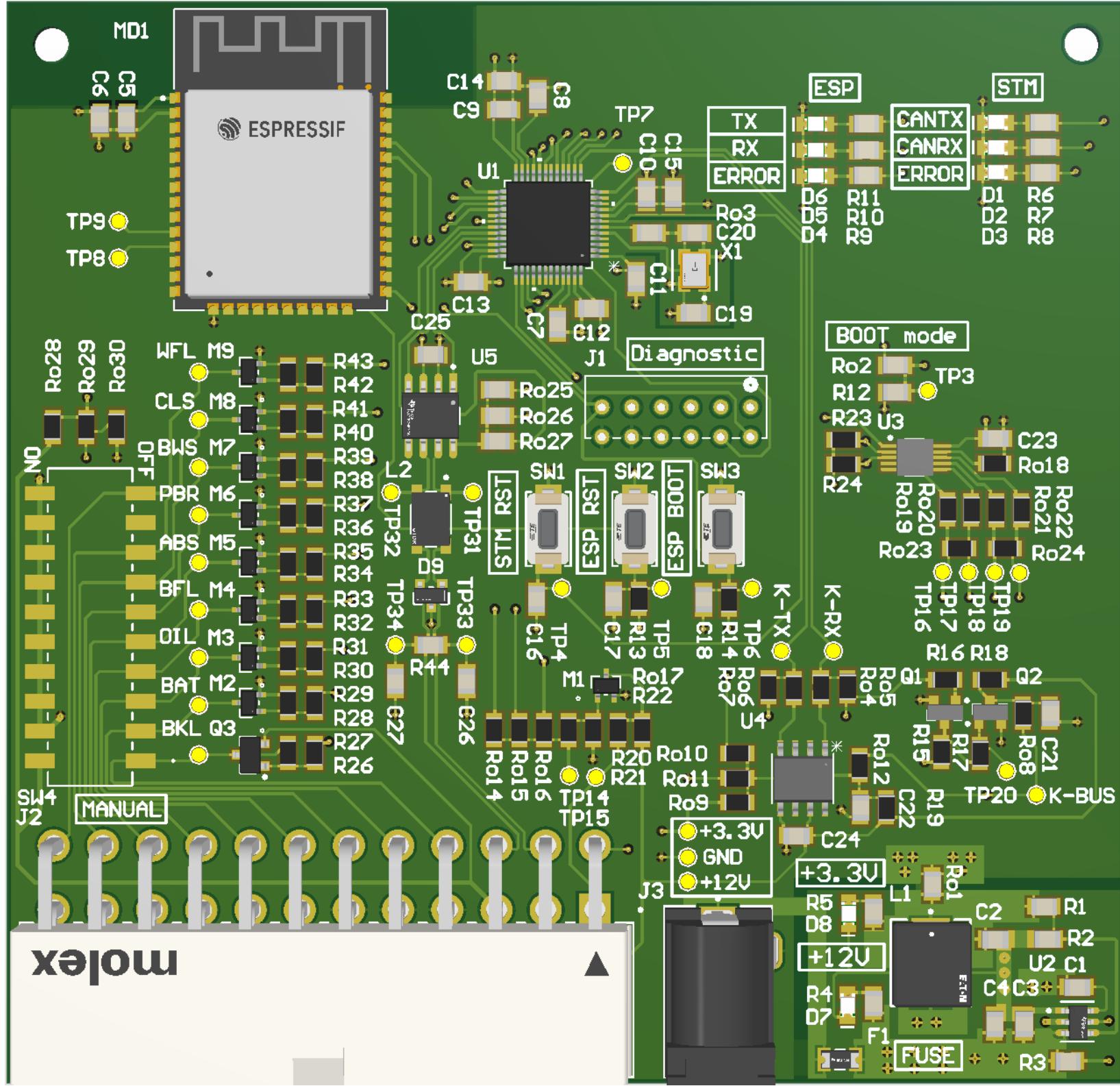


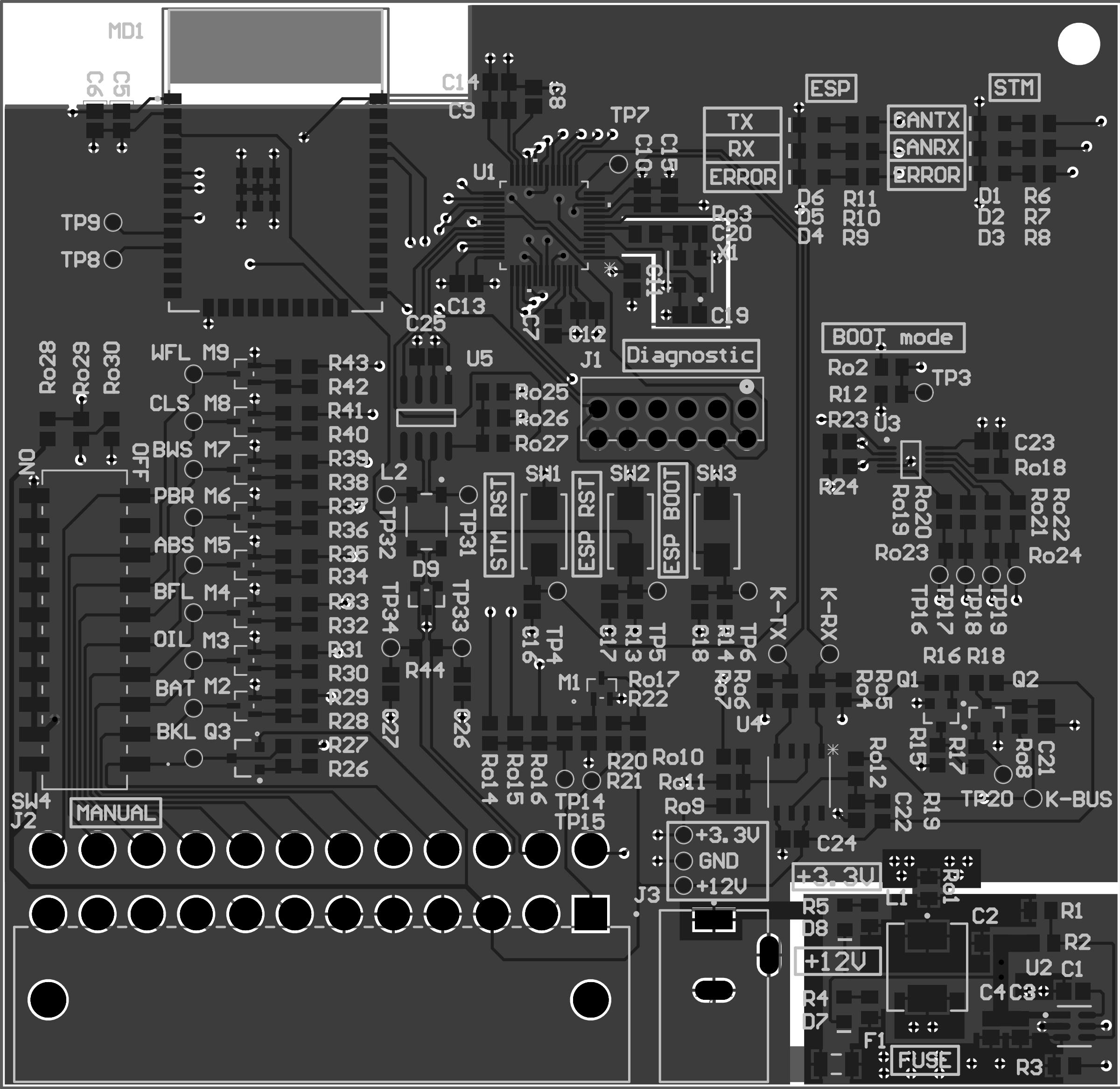




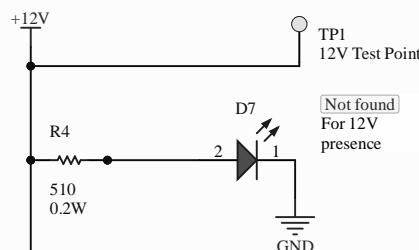




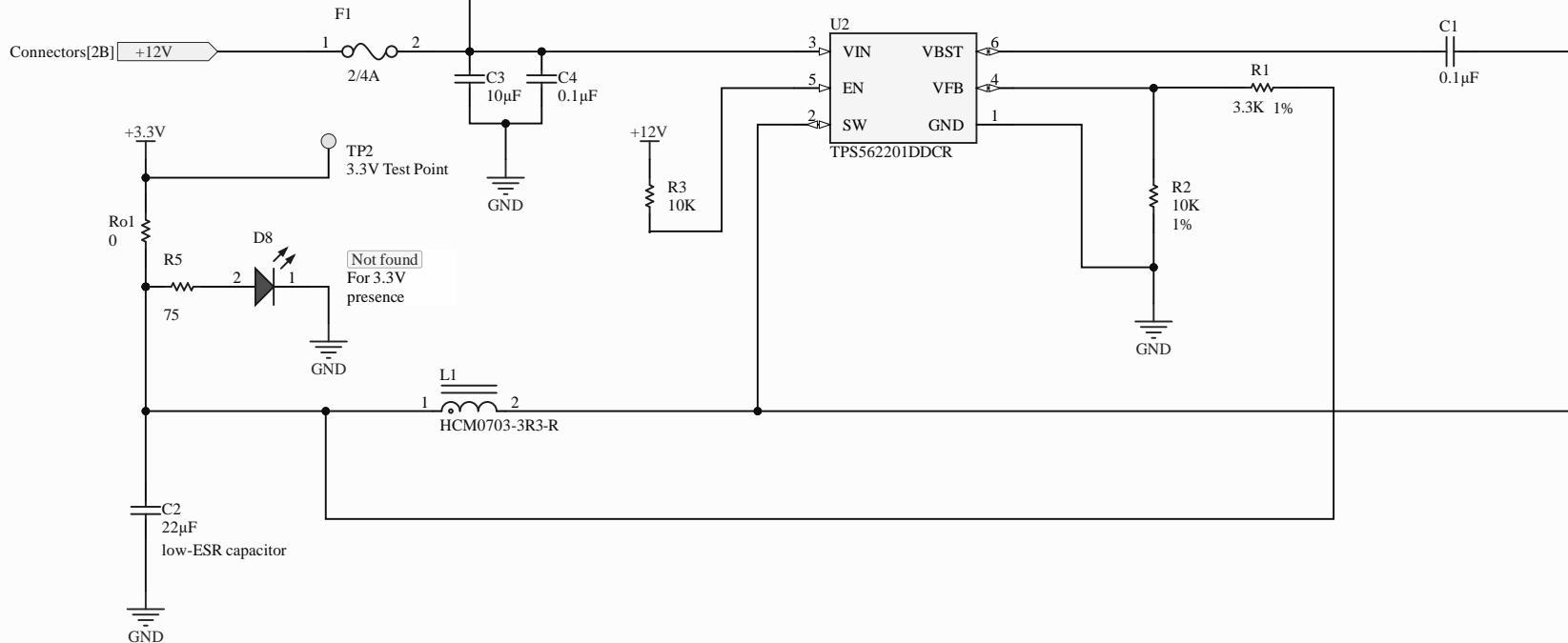




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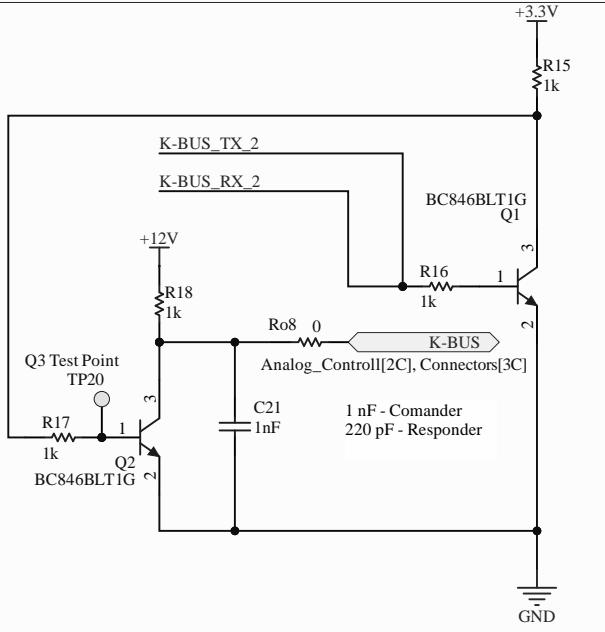
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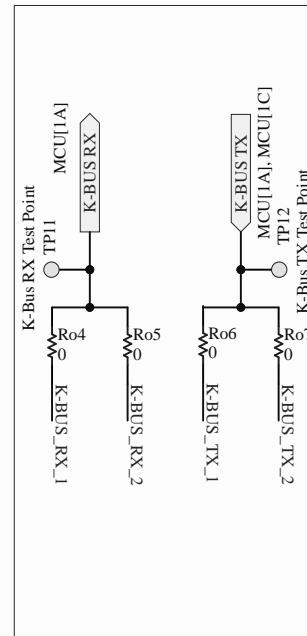
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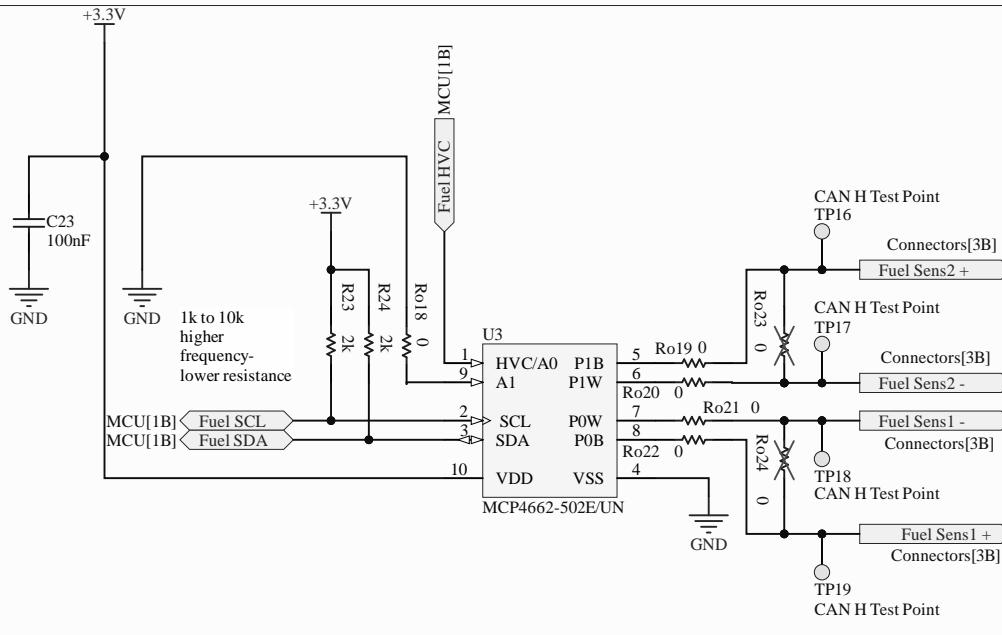
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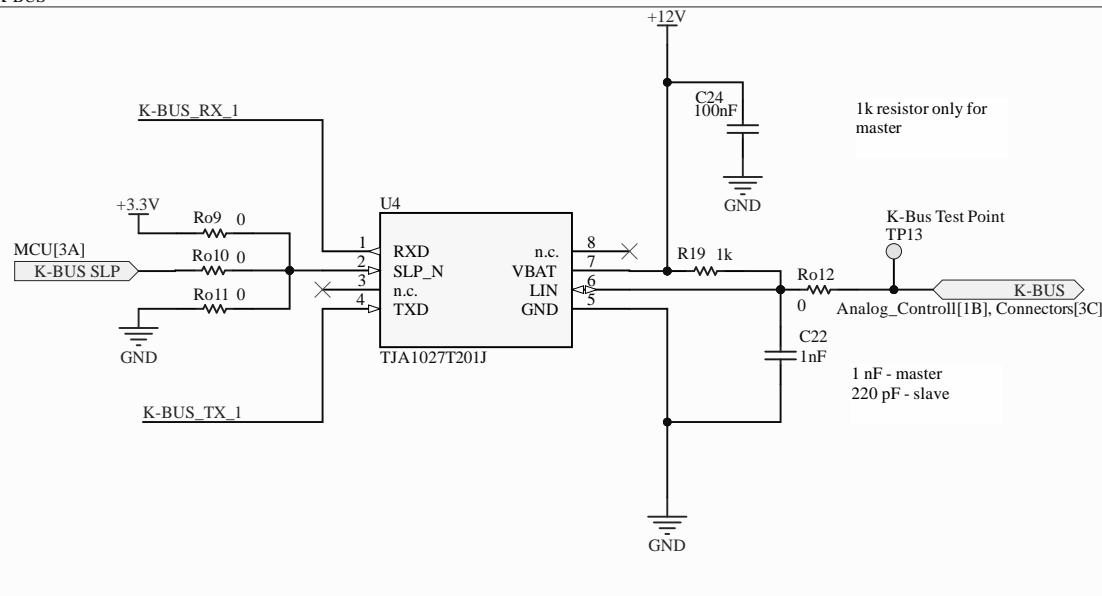
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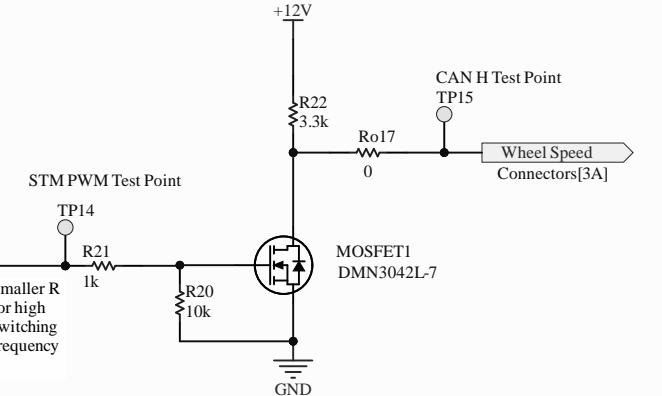
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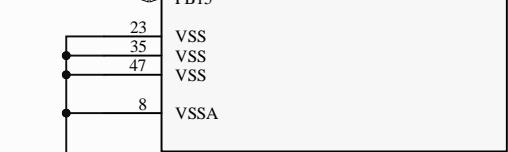
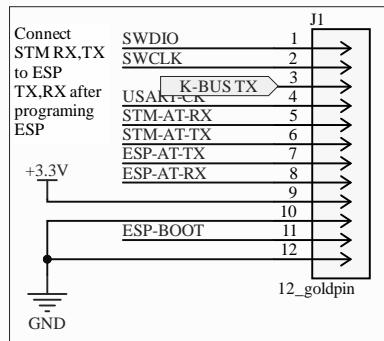
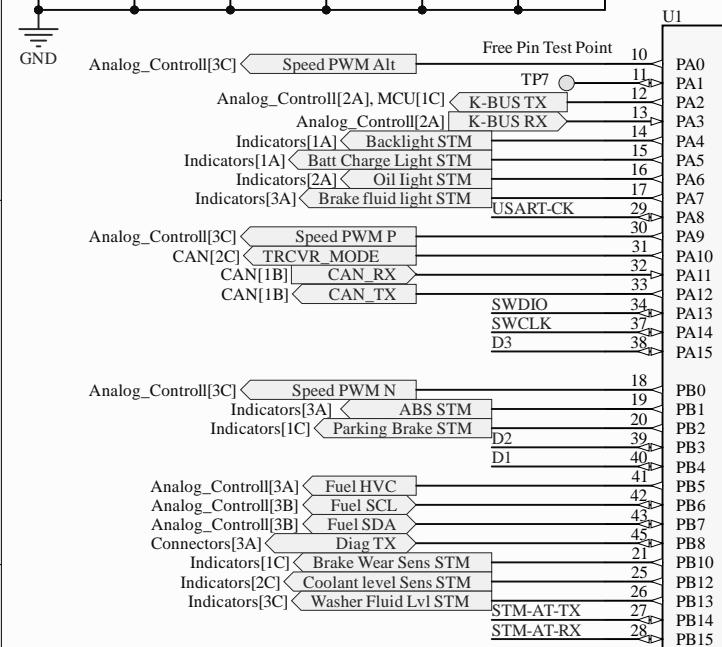
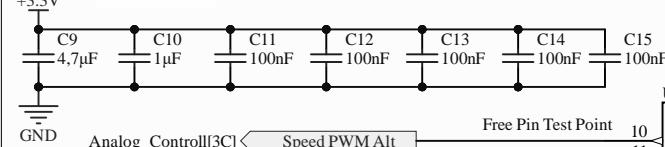
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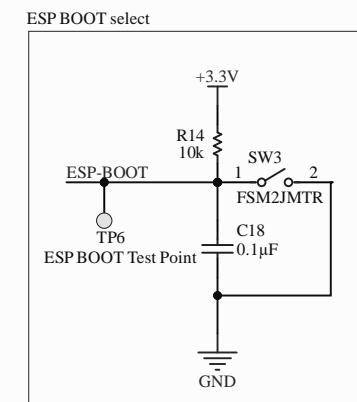
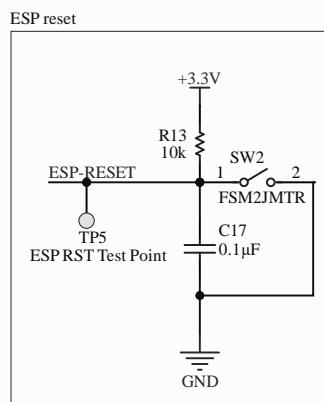
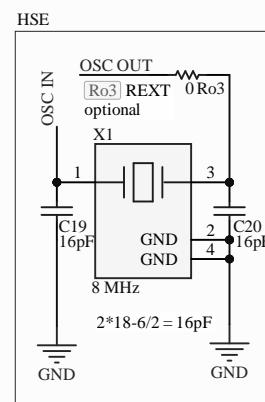
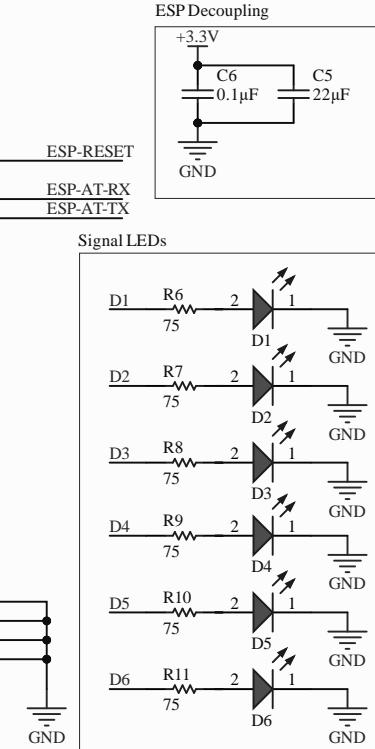
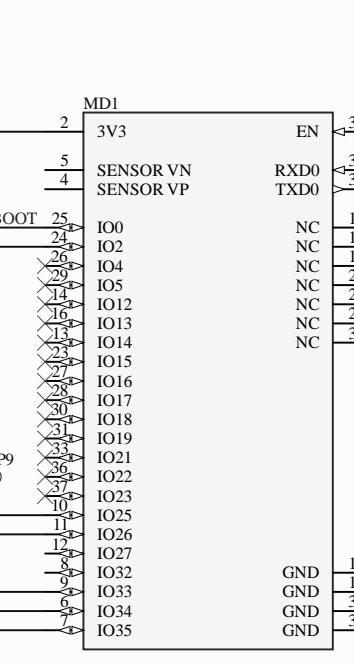
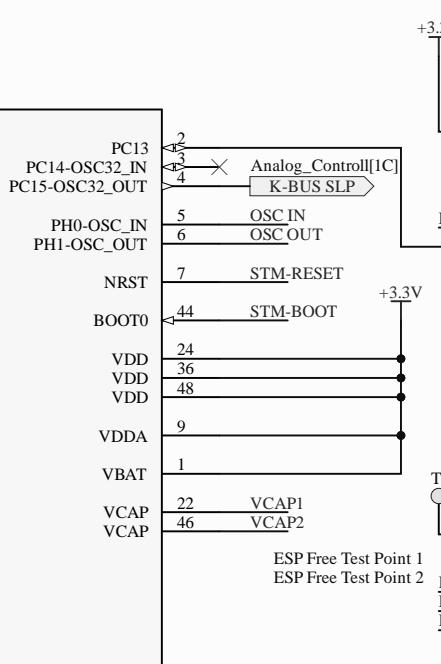
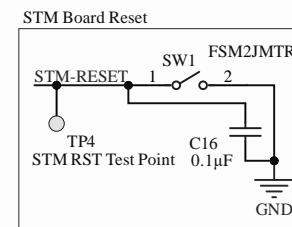
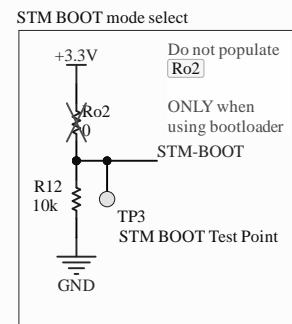
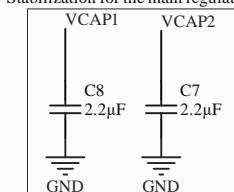
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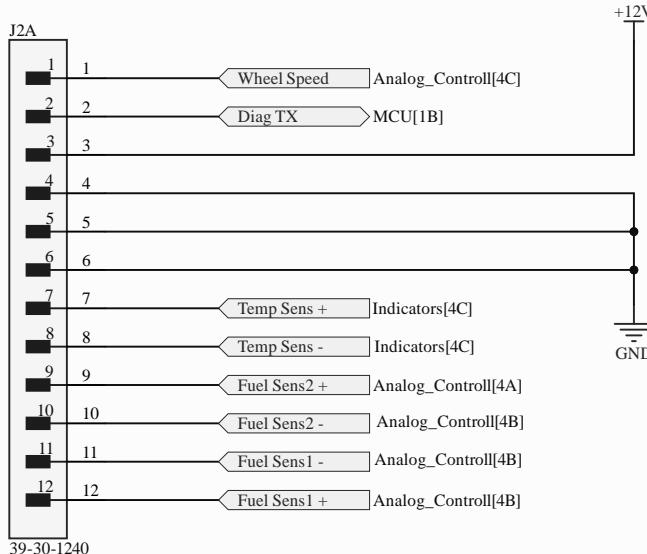
C10 to VDDA

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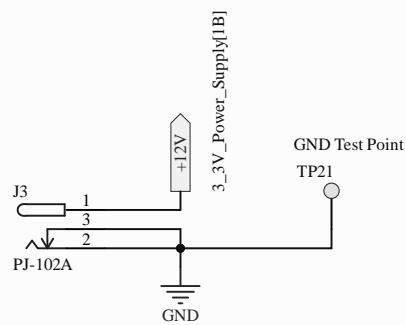


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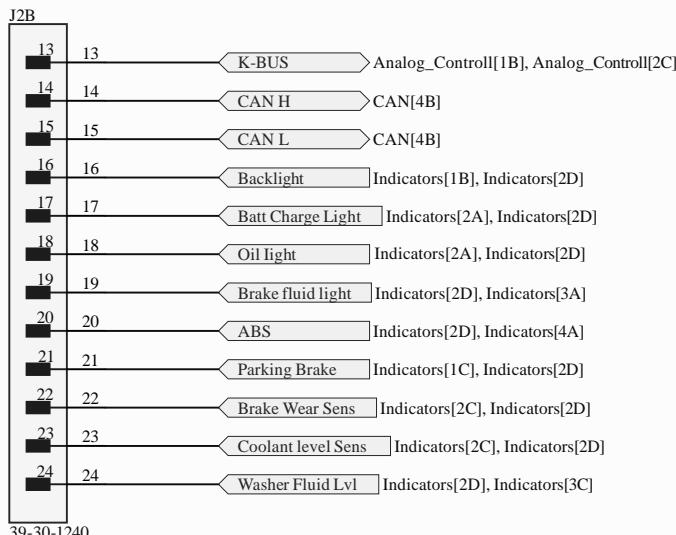
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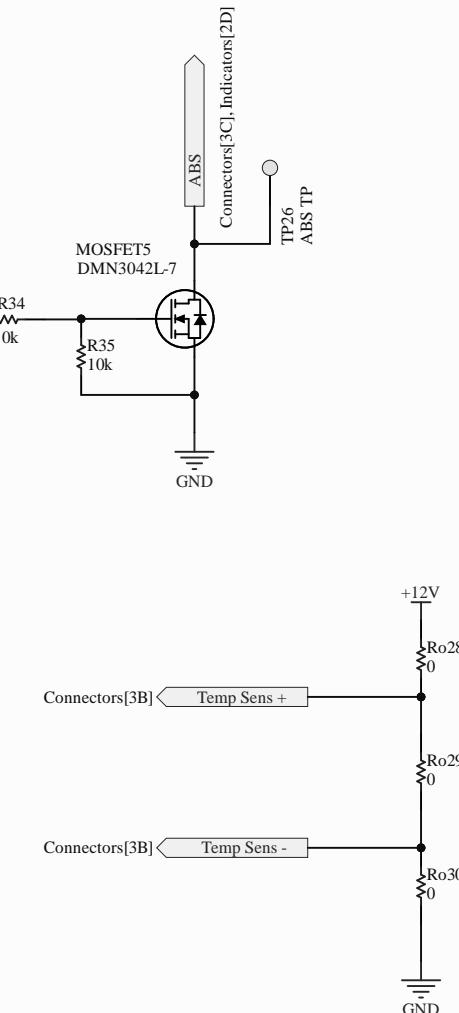
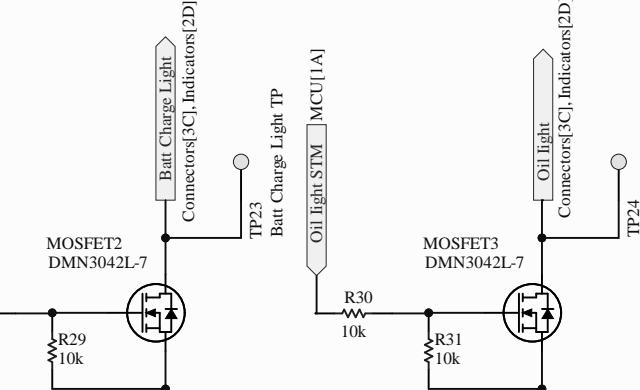
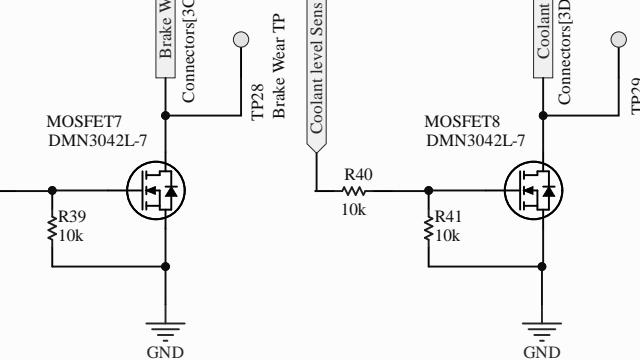
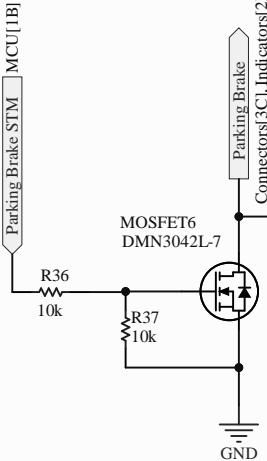
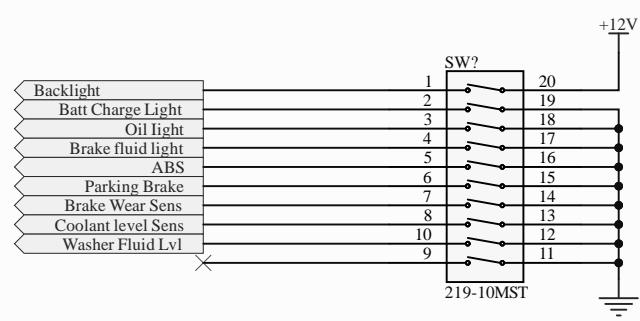
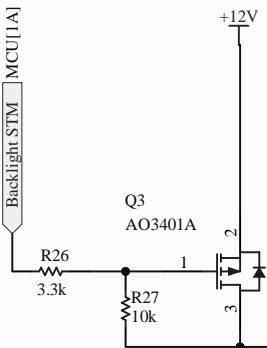


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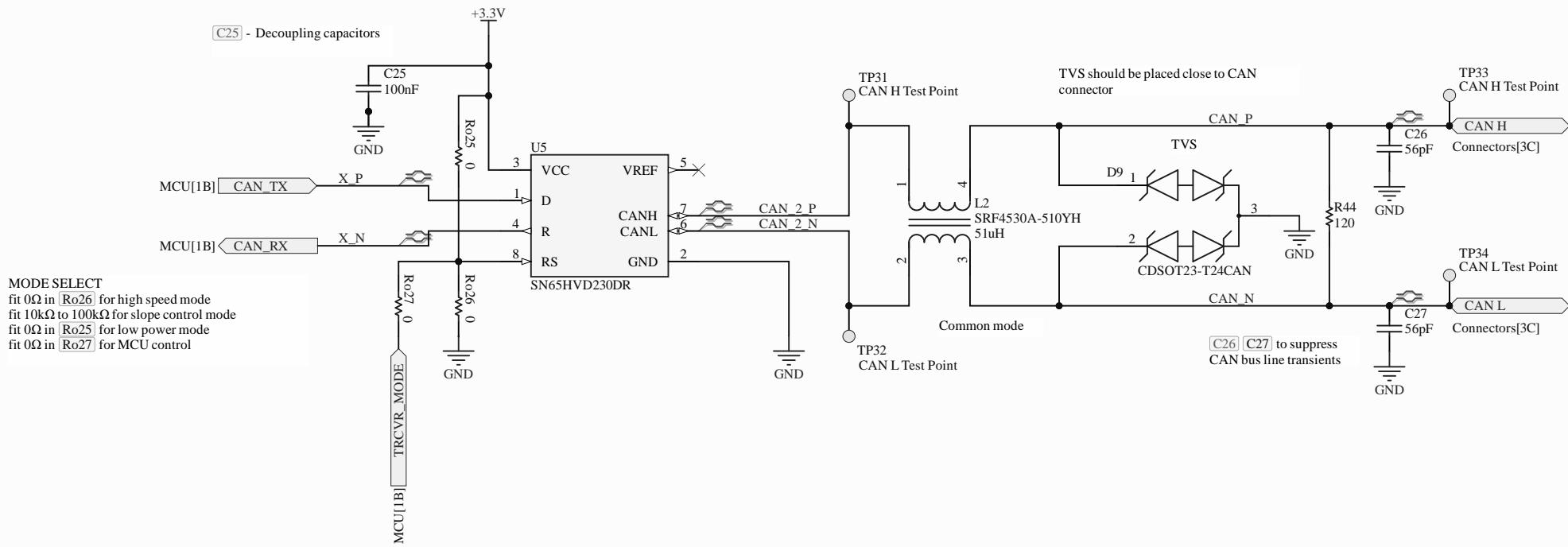
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