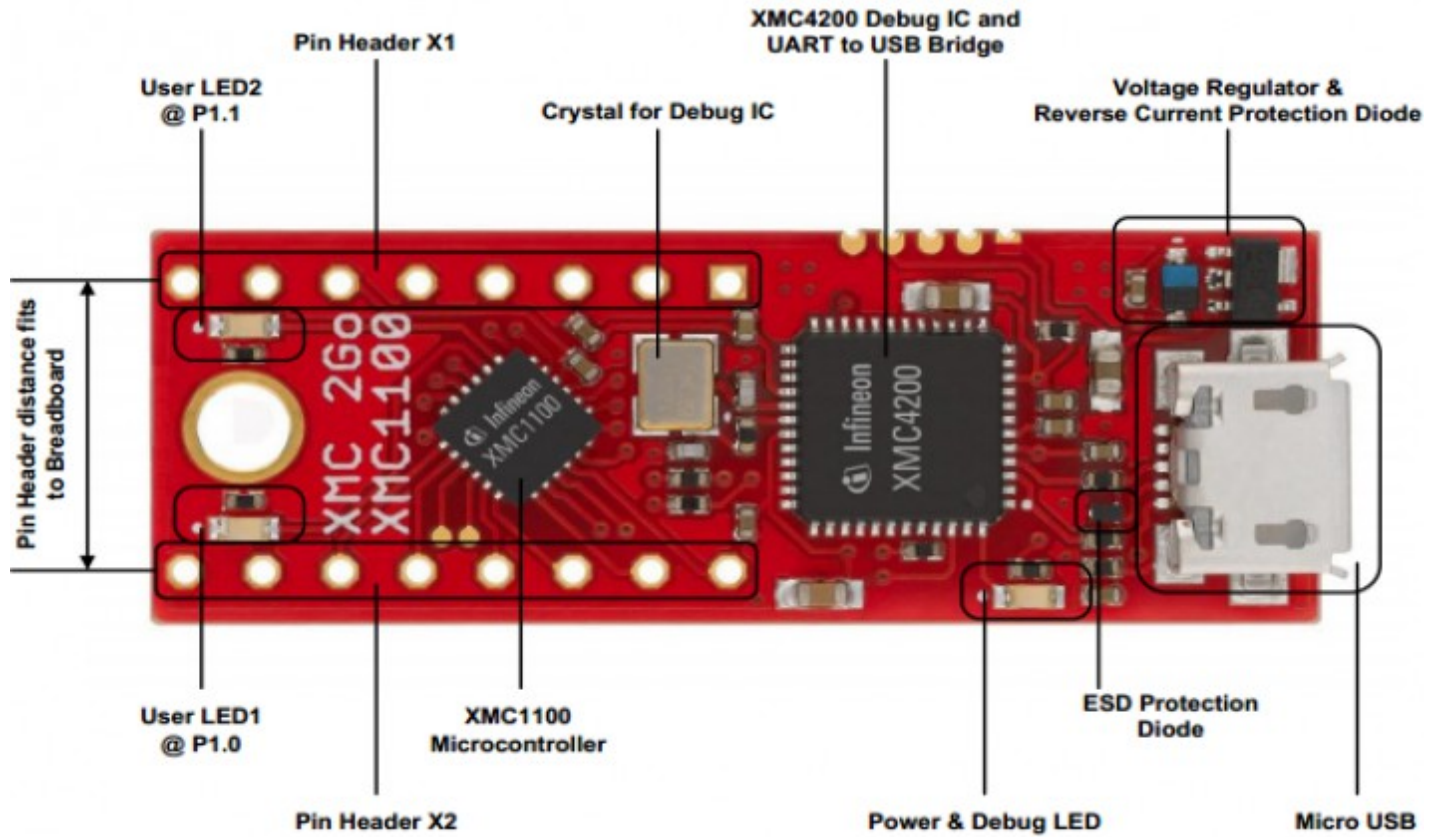
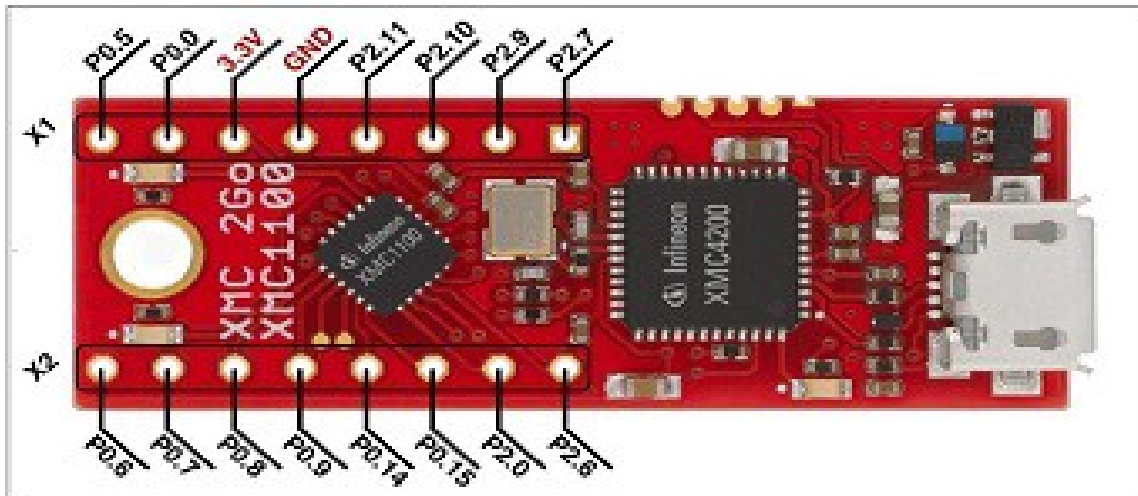


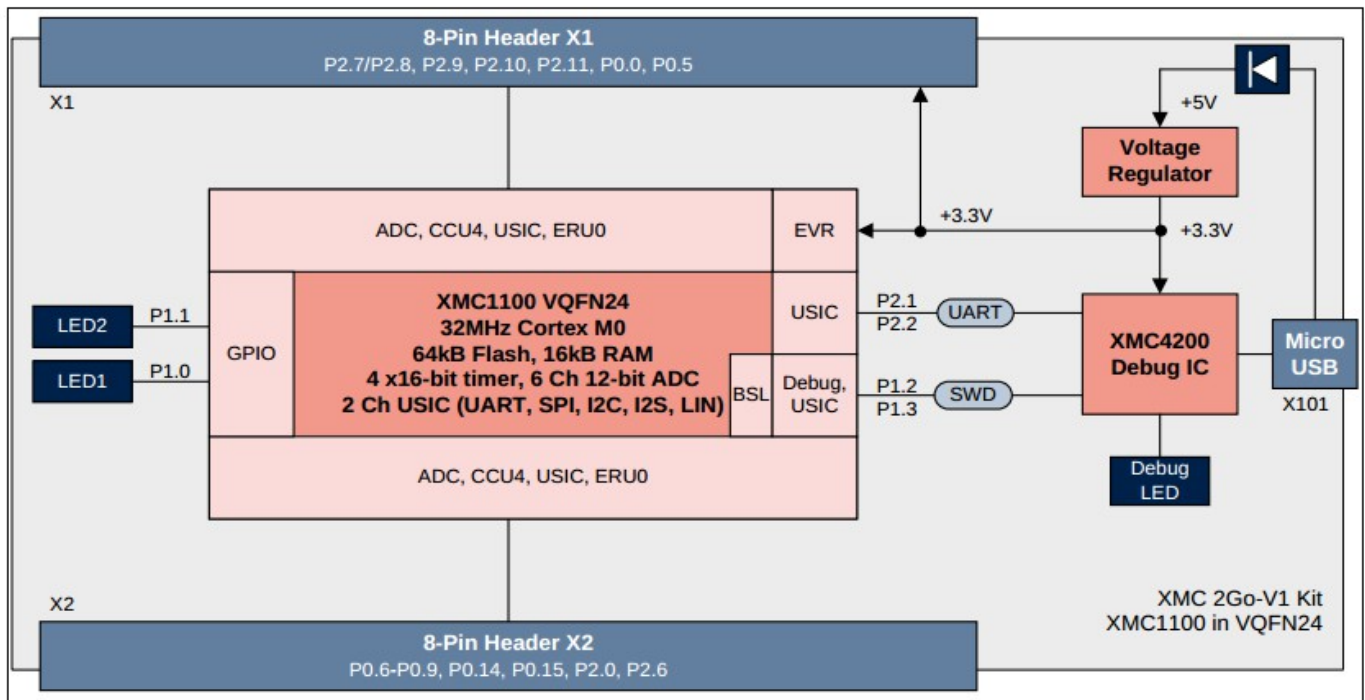


## Infineon XMC2GO XMC1100 Development Board :





## Infineon XMC2GO XMC1100 Development Board :



**XMC2GO Target - XMC1100 VQFN24 :**

- **ARM Cortex-M0 Micro-controller (32 MHz)**
- **64 kilobytes FLASH**
- **16 kilobytes of SRAM**
- **8 kilobytes of ROM – ASC/AES Bootloader**
- **Two (2) Universal Serial (UART/SPI/I2C/I2S/LIN)**
- **Four (4) Timers/Capture/Compare Units**
- **Six (6) Channel, Twelve (12) bit ADC Converter**
- **Sixteen (16) Pin Dual In-line Package (16-DIP)**
- **Twenty-two (22) GPIO pins**



## Infineon XMC2GO XMC1100 Development Board :

<b>I/O Port</b>	<b>I/O Pin</b>	<b>Function</b>	<b>Note</b>
P0_0	15	-	GPIO
P0_5	16	-	GPIO
P0_6	17	-	GPIO
P0_7	18	-	GPIO
P0_8	19	-	GPIO
P0_9	20	-	GPIO
P0_12	21	NC	GPIO
P0_13	22	NC	GPIO
P0_14	23	-	GPIO
P0_15	24	-	GPIO
P2_0	1	-	GPIO/AIN
P2_1	2	UART0_TXD	GPIO/AIN
P2_2	3	UART0_RXD	GPIO/AIN
P2_6	4	-	GPIO/AIN
P2_7	5	-	GPIO/AIN
P2_9	6	-	GPIO/AIN
P2_10	7	-	GPIO/AIN
P2_11	8	-	GPIO/AIN
Ground	9	Ground	Ground
VDD	10	Power	Power

I/O Port	I/O Pin	Function	Note
P1_3	11	JTAG_SWDIO	Hi Current
P1_2	12	JTAG_SWCLK	Hi Current
P1_1	13	LED2	Hi Current
P1_0	14	LED1	Hi Current

## Legend :

– (Available GPIO)

NC Not pinned out on module (No Connect)

## PinHeader XMC1100

