

**QuadroTeenia!**

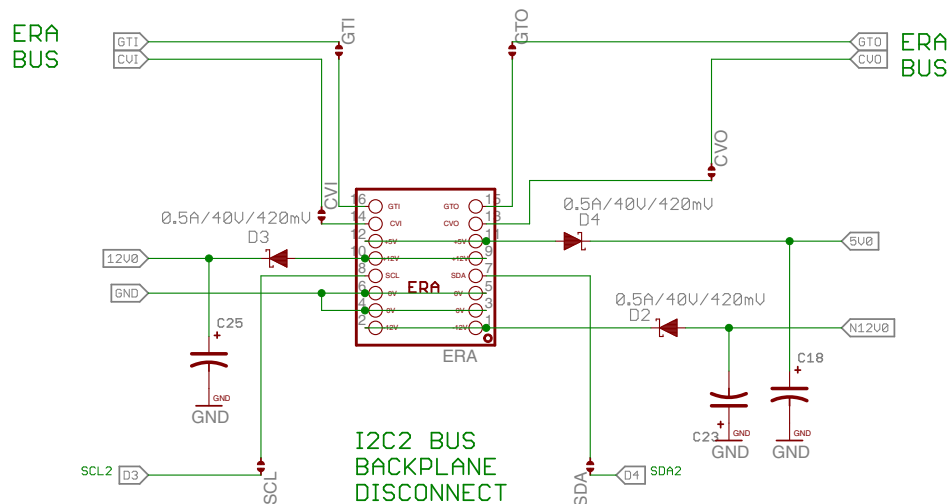
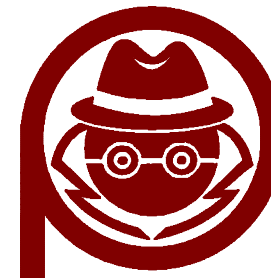
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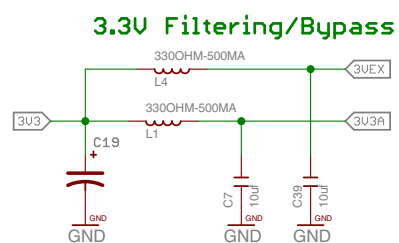
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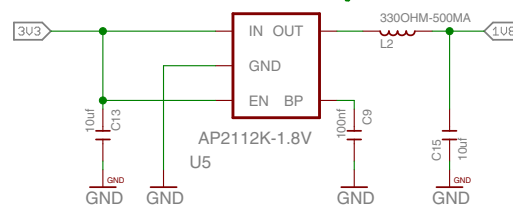
Sheet: 1/1



EURORACK/ERA BACKPLANE CONNECTOR



1.8V LD0/Regulator



## QuadroTeenia!

TITLE: QuadroTeenia

Document Number:

REV:

Date: 3/26/2018 1:47:38 PM

Sheet: 1/1

**NOTE:**

\*\*\* CUT USB TRACE On Teensy \*\*\*  
DIODES ALLOW EITHER USB  
OR EURORACK 5V POWER SOURCE

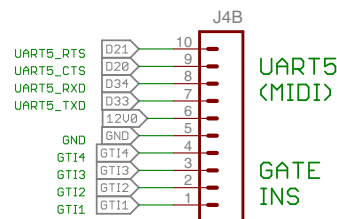
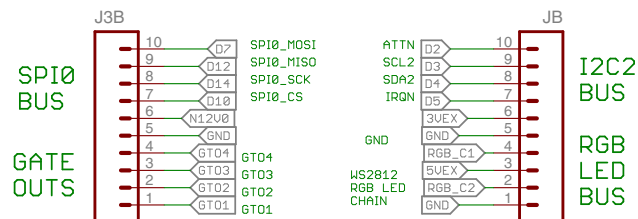
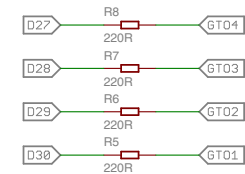
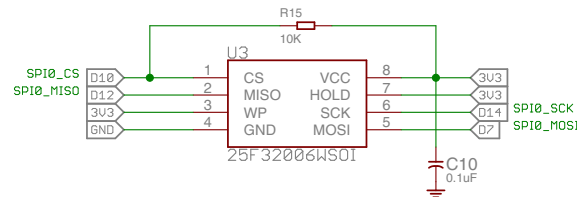
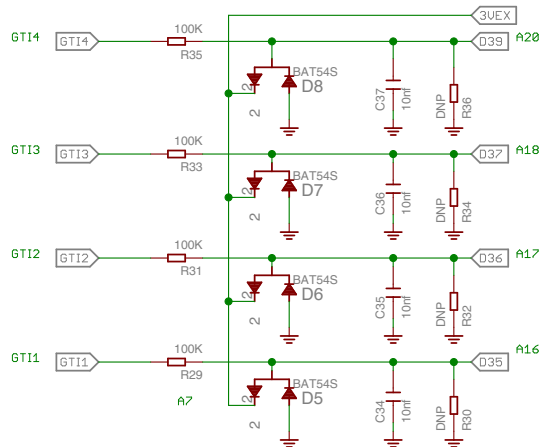
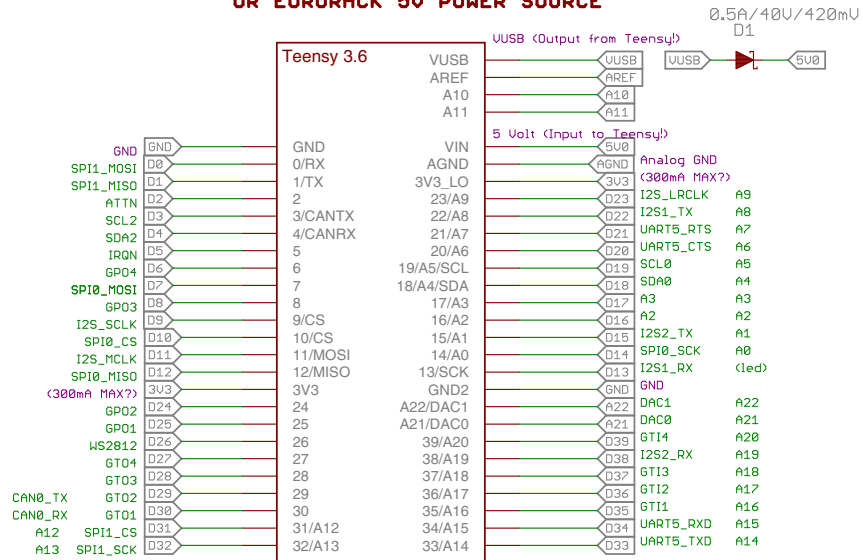
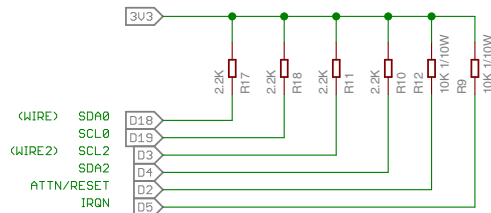
**I2C0 (WIRE) ADDRESS MAP**

SGTL5000 (LD) = 0x0A  
SGTL5000 (HI) = 0x4A  
ADS1015 ADC = 0x48  
DAC4728T DAC = 0x60

**I2C2 (WIRE2) ADDRESS MAP**

FM24W256G RAM = 0x50  
BACKPLANE MUX = 0x70

ATTN - Can be used for reset  
IRQN - I2C Interrupt

**QuadroTeenia!**

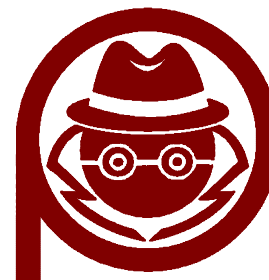
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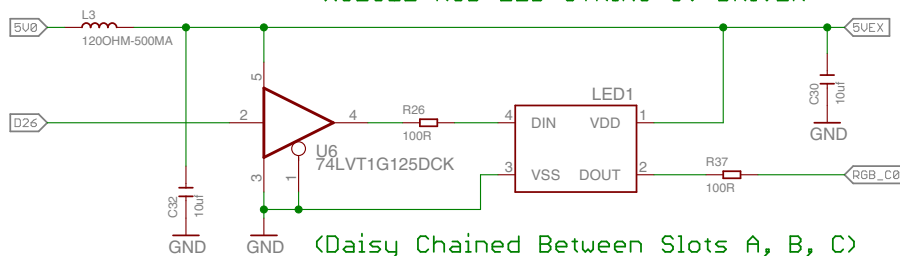
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Sheet: 1/1

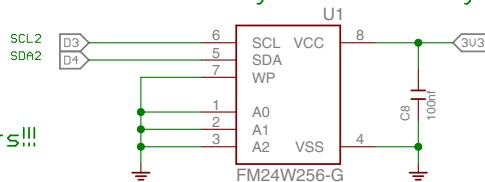


### WS2812 RGB LED STRING 5V DRIVER



(Daisy Chained Between Slots A, B, C)

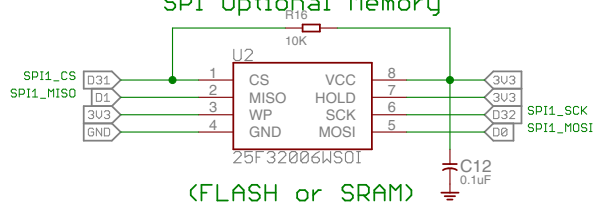
### I2C Configuration Memory



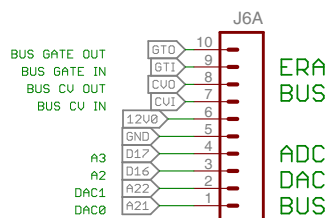
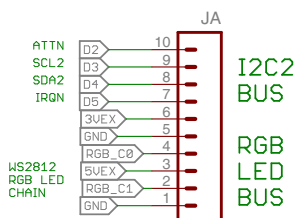
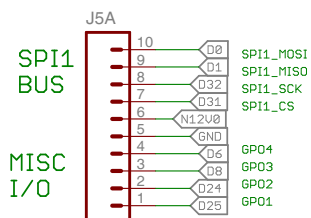
ADD Address Jumpers!!!

32K byte NVSRAM (or EEPROM)

### SPI Optional Memory



(FLASH or SRAM)



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Sheet: 1/1