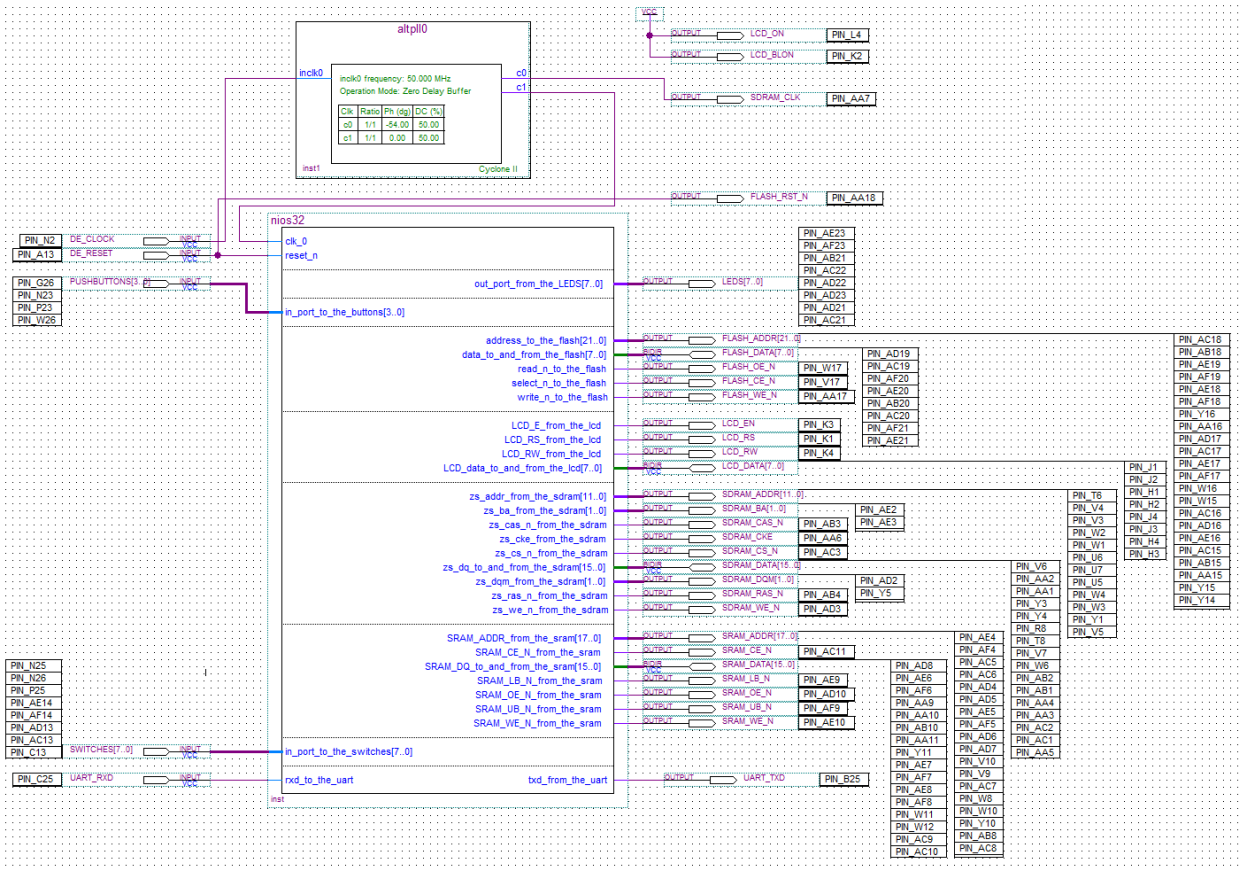
Paul Kafka

10/28/13

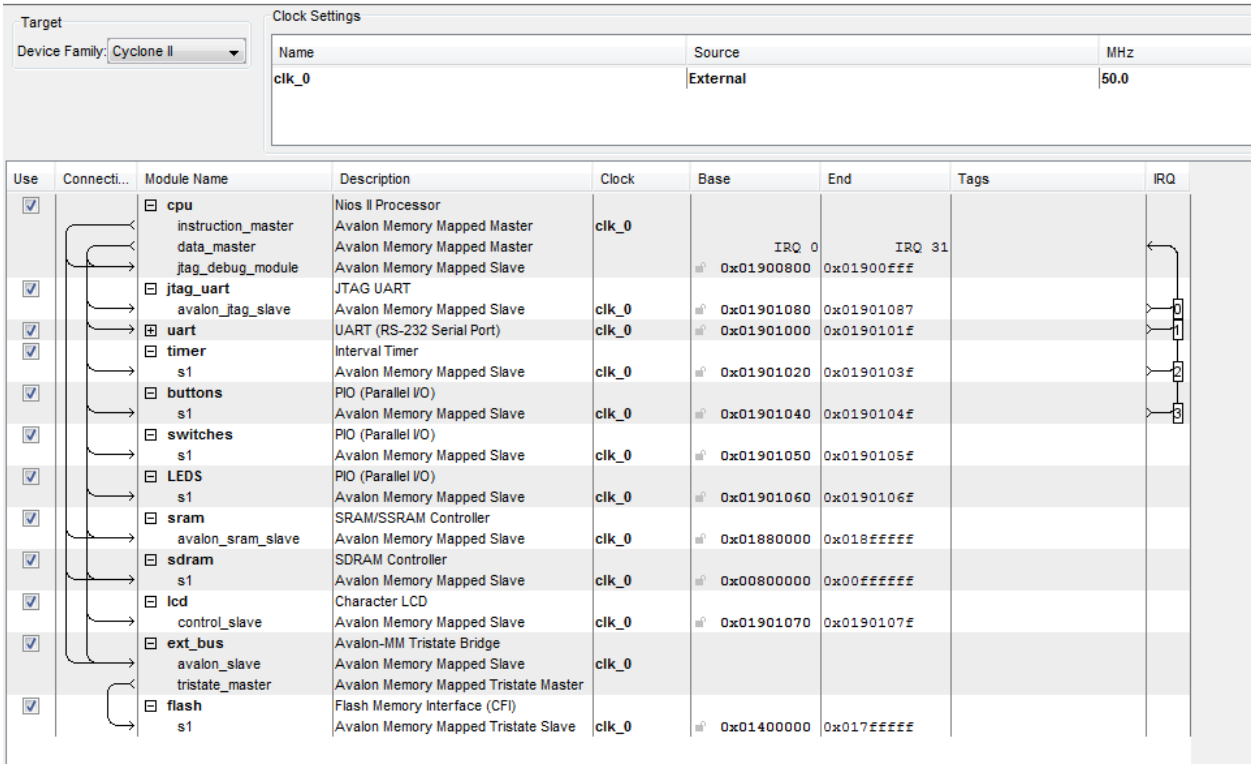
Embedded Micro System Design

Homework 6

Top Level BDF Graphic



SPOC Snapshot



Resource Data

Flow Status	Successful - Wed Oct 23 11:22:57 2013
Quartus II Version	9.1 Build 350 03/24/2010 SP 2 SJ Full Version
Revision Name	rpds17
Top-level Entity Name	rpds17
Family	Cyclone II
Device	EP2C35F672C6
Timing Models	Final
Met timing requirements	Yes
Total logic elements	3,535 / 33,216 (11 %)
Total combinational functions	3,287 / 33,216 (10 %)
Dedicated logic registers	2,044 / 33,216 (6 %)
Total registers	2145
Total pins	148 / 475 (31 %)
Total virtual pins	0
Total memory bits	46,720 / 483,840 (10 %)
Embedded Multiplier 9-bit elements	4 / 70 (6 %)
Total PLLs	1 / 4 (25 %)