

Async Processor (Demonstrator)

- Author: Paul Schulz
- Description: Processing data without global clock, using local dependancies.
- Language: Verilog

How it works

Use a fixed graph definition (processing matrix) to define adjacent nodes and dependancies.

How to test

TBD. Set the boundary values, then

Experiment by changing the boundary values.

IO

#	Input	Output	Bidirectional
0	input bit 0	segment a	second counter bit 0
1	input bit 1	segment b	second counter bit 1
2	input bit 2	segment c	second counter bit 2
3	input bit 3	segment d	second counter bit 3
4	load input 0	segment e	second counter bit 4
5	load input 1	segment f	second counter bit 5
6	load input 2	segment g	second counter bit 6
7	load input 3	dot	second counter bit 7

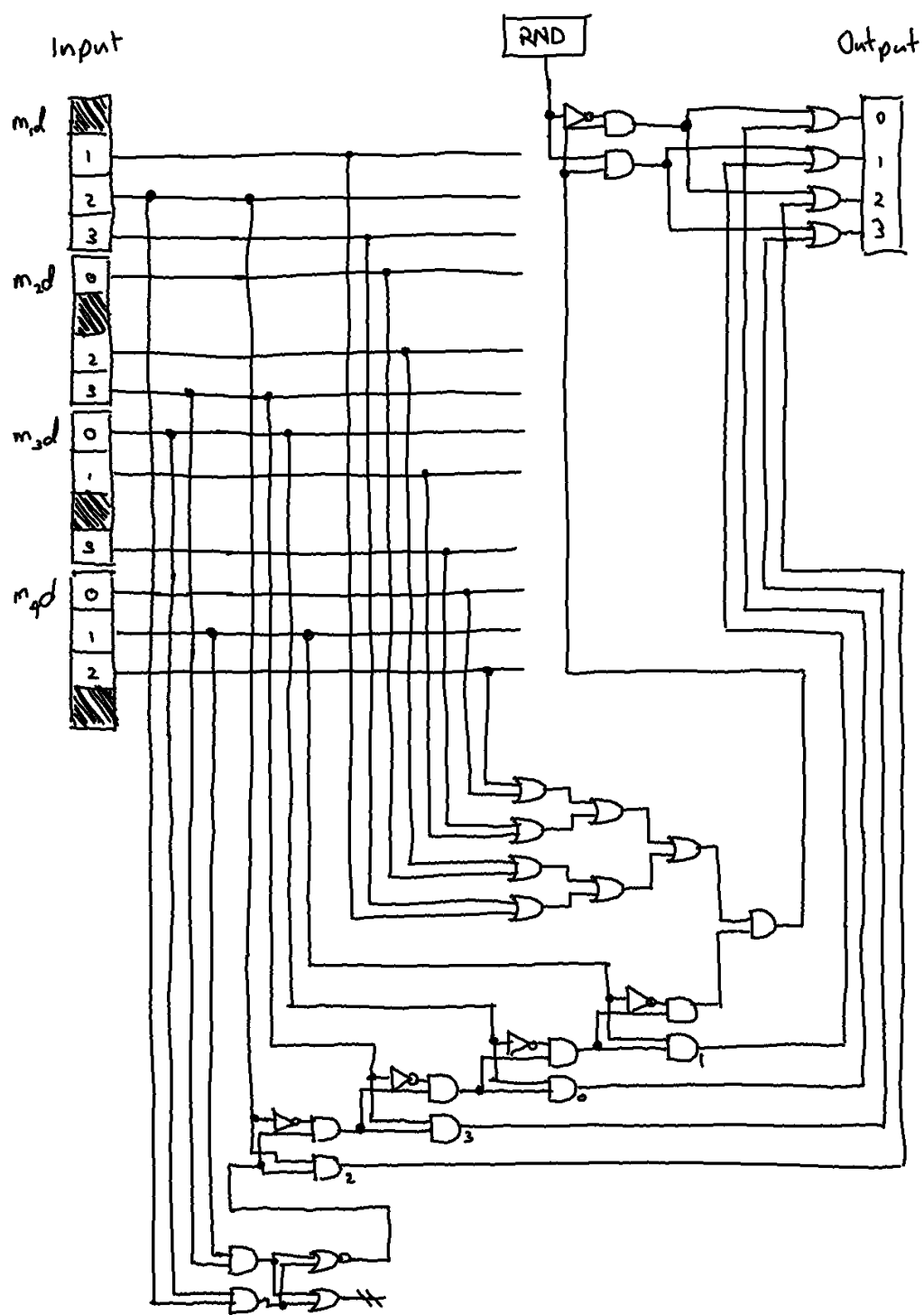


Figure 1: picture