

Contact

er.nsingh1@gmail.com

www.linkedin.com/in/neha-singh-a6979a59 (LinkedIn)

www.nehasingh.info/ (Personal)

Top Skills

Android Development

Java

Technical Writing

Languages

Hindi (Native or Bilingual)

English (Full Professional)

Honors-Awards

Top Writer-2018

Highest CGPA Award

STAR Contender of the Quarter
(July-September 2018)

Publications

Dual band circular polarized bow tie slotted patch antenna over high impedance surface for WiMAX application

Design Studies of Three Layer HIS Design for L-Band Frequencies

Neha Singh

Design Engineer | IIT Roorkee
India

Experience

Centre for Development of Advanced Computing (C-DAC)

Knowledge Associate

November 2017 - December 2018 (1 year 2 months)

Pune Area, India

Worked in server system hardware design with Intel's latest processors and chipsets; PCIe3.0, USB, SATA, SFI, and DDR4 interfaces; Experienced in schematics entry and CAD layout; Worked on managing SI and PI challenges, layer-stack creation, high speed via modelling, modelling and simulation of high-speed channels, S-parameter, crosstalk, IL, RL, and timing analysis; power plane analysis and power shape tuning, board bring-up and validation

Education

Indian Institute of Technology, Roorkee

Master of Technology (M.Tech.), RF and Microwaves · (2015 - 2017)

Kumaon Engineering College, Dwarahat, Almora

Bachelor of Technology (B. Tech.), Electronics and Communication

Engineering · (2010 - 2014)