- exam1-p1
 - link: https://github.com/PaulWang0513/Electronic-System-Level-Design-and-Synthesis/tree/main/exam1/exam1-p1
- exam1-p2
 - link: https://github.com/PaulWang0513/Electronic-System-Level-Design-and-Synthesis/tree/main/exam1/exam1-p2
 - result:

```
User@ubuntu:~/ee6470/Electronic-System-Level-Design-and-Synthesis/exam1/exam1-p2/build$ ./decimation

SystemC 2.3.3-Accellera --- Feb 16 2023 13:55:28
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Simulated start
Decimation filter start ...
All input provided

Info: /OSCI/SystemC: Simulation stopped by user.
0.429167 0.5575 0.606 0.654 0.700167 0.744333 0.7865 0.825333
0.8615 0.893833 0.922167 0.9465 0.966333 0.981667 0.992167 0.9985
0.9995 0.995833 0.986833 0.973167 0.9555 0.933 0.905833 0.875
0.840833 0.803167 0.762 0.718667 0.673333 0.626 0.578 0.528667
0.4795 0.430167 0.382 0.3345 0.288833 0.245167 0.2035 0.165167
0.130333 0.099 0.0713333 0.048 0.0295 0.01516670.005333330.000833333
0.001 0.0065 0.0161667 0.0306667 0.0498333 0.0735 0.101167 0.132833
0.1685 0.206833 0.2485 0.292333 0.338167 0.386 0.4345 0.4835

Simulated done
Simulated time == 321 ns
```