Paula Perdomo

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Education

University of Toronto **Expected Graduation: June 2024**

Bachelor of Applied Science & Engineering: Computer Engineering *Toronto, ON*

Minor in Artificial Intelligence & Engineering Business

Awards: Edward S. Rogers Sr. Admissions Scholarship, Dean's Merit Award, 2020 Fall-Dean's Honours List, and Natural Sciences and Engineering Research Council (NSERC) Undergraduate Student Awardee

Experience

Google May 2023 – August 2023

Software Engineer Intern - Chrome Extensions Security Team *Montreal, QC*

- Conducted in-depth research to identify and analyze potential instances of abuse within WebAssembly (WASM) in the context of Google Chrome Extensions.
- Engaged in proactive extension prototyping and experimentation to identify vulnerabilities, ensuring the early detection and prevention of potential WASM exploitation or malicious misuse.
- Created a pipeline to assess the use of WASM across thousands of Google Chrome Extensions, employing various detection patterns and parallel processing for enhanced efficiency.

Google September 2022 – December 2022

Software Engineer Intern - Fuchsia Operating Systems Team *Seattle, WA*

- Designed a USB peripheral test suite for Fuchsia OS to improve the quality and reliability of the USB stack, ensuring correct data transfers across various endpoints. The tests benefited 3P driver developers using Fuchsia OS-compatible devices.
- Implemented a factory setting CLI to help Fuchsia OS developers obtain hardware and system information.

Microsoft May 2022 – August 2022

Software Engineer Intern - Platform Firmware & Drivers Team *Redmond, WA*

- Implemented the Monte Carlo performance profiler in C code for the Unified Extensible Firmware Interface (UEFI), with the goal of optimizing the pre-OS Boot Flow performance and run time.
- Strengthened the UEFI boot flow by accurately modelling the UEFI boot phases using probability and CPU timing, aiming to reduce areas of code delay and enhance optimization.

University of Toronto May 2021 – September 2021

Undergraduate Student Researcher - Verilog to Routing (VTR) FPGA Research *Toronto, ON*

- Supported advanced FPGA research under the supervision of Professor Vaughn Betz, by assisting with new feature implementation, quality of results, documentation, and user performance.
- Developed theManual Moves functionality within VTR, enabling users to manually define placement adjustments for FPGA architecture and design.

Projects

Mapper Project

- Created a Google Maps inspired GIS software using OpenStreetMap database API (OSM Data), C++ programming language, GTK++, and EZGL Graphics Library.
- Made use of multiple graph and path finding algorithms to solve the Traveling Salesman Problem.

Personal Website

- Engaging in ongoing development and management of a personal website aimed at showcasing my professional profile, projects, and accomplishments.
- Utilizing industry-standard web technologies such as HTML, CSS, and JavaScript to design and implement an intuitive user interface while ensuring optimal performance and responsive design principles are upheld.

Skills

Languages & Tools: C/C++, Python, Rust, Java, JavaScript, HTML & CSS, TensorFlow, Verilog, ARM Assembly, SQL, WebAssembly, Git, Linux / UNIX Systems, Microsoft Office.

Interpersonal Skills: Teamwork Management, Leadership, Communication, Strong Work Ethic, Goal-Oriented, Task Driven, Critical Thinking, Innovation, Problem Solving.

Languages: Spanish - *fluent*, English - *fluent*