Nexys 2 Board FPGA Pin Assignments
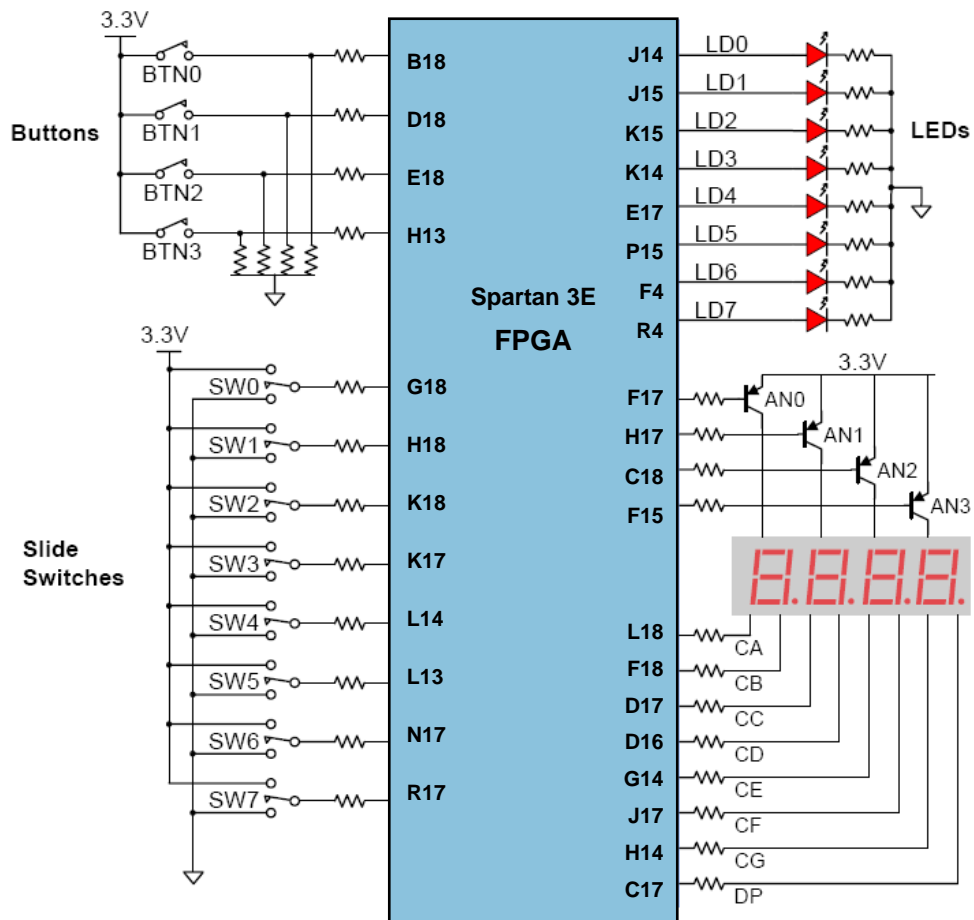
Buttons			Switches			LEDS			7 Segment Displays			7 Segment Displays		
BTN0	in	B18	SW0	in	G18	LD0	out	J14	CA	out	L18	AN0	out	F17
BTN1	in	D18	SW1	in	H18	LD1	out	J15	CB	out	F18	AN1	out	H17
BTN2	in	E18	SW2	in	K18	LD2	out	K15	CC	out	D17	AN2	out	C18
BTN3	in	H13	SW3	in	K17	LD3	out	K14	CD	out	D16	AN3	out	F15
			SW4	in	L14	LD4	out	E17	CE	out	G14			
Clock			SW5	in	L13	LD5	out	P15	CF	out	J17			
GCLK0	in	B8	SW6	in	N17	LD6	out	F4	CG	out	H14			
			SW7	in	R17	LD7	out	R4	CDP	out	C17			
signal	dir	pin	signal	dir	pin	signal	pin	dir	signal	dir	pin	signal	dir	pin