19 - Porta Universal

a) NAN pode pode implementar

Not A = A NAND A

AND = (A NAND B) NAND (A NAND B)

OR = (A NAND A) NAND (B NAND B)

b) NOR também

Not A = A NOR A

OR = (A NOR A) NOR (B NOR B)

AND = (A NOR B) NOR (A NOR B)

20 - Circuito Combinado

S = (A XOR B) NAN (C XNOR D)

tabela - Verdade

A	B	C	D	A	B	C = DS
0	0	0	0	0	1	1
0	0	0	1	0	0	1
0	0	1	0	0	0	1
0	0	1	1	0	0	1
0	0	0	0	1	1	0
0	1	0	1	1	0	0
0	1	1	0	1	0	1
0	1	1	1	1	1	0
1	1	0	0	1	1	0
1	0	0	1	1	0	1
1	0	1	0	1	0	1
1	0	1	1	1	1	0
1	1	0	0	0	1	1
1	1	0	1	0	0	1
1	1	1	0	0	0	1