

# Pruthviraj N

## Python Django Developer



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📍 #15A kruthika layout

in <https://www.linkedin.com/in/pruthviraj-n-703b5b1aa/>

🐙 <https://github.com/Pruthvirajn28>

🔗 <https://github.com/Pruthvirajn28/All-set.git>

### EDUCATION

#### BE / Electronics and Communication Engineering, KSIT / VTU

August 2017 – August 2021  
Bangalore, India

### CERTIFICATES

Participated in a 2-day workshop on "Image & Video Processing using Raspberry pi" organized at K S Institute of Technology, Bangalore from 1st to 2nd October 2019.

Pursuing "Python Full Stack Developer" course at Pyspiders, Bangalore.  
(October 2022 --- April 2022)

Completed "Python Programming Language" course from "Mind Luster". - June 2023.  
(<https://www.mindluster.com/student/certificate/5709172085>)

### BEHAVIORAL SKILLS

- Good Communication Skill
- Time Management
- Adaptable to Any Environment
- Problem Solving Skills

### OBJECTIVES

To secure a challenging position in a reputable organization to expand my learning, knowledge and skills.

### SKILLS

Python

Django

Django REST Framework

Oracle SQL

MySQL

HTML

CSS

OOPS

DATA STRUCTURES

Basic JS

### PUBLICATIONS

"Implementation of Low, High and Band Pass Filters using Verilog HDL", International Journal of Innovative Technology & Exploring Engineering, ISSN: 2278-3075 (online) Volume-9

December 2019

### PROJECTS

#### \* Ecommerce Website

June 2023 – June 2023

Built a on eCommerce website using Django framework - Includes functionalities Login Authentication, Product Category-wise, Add to Cart, Wishlist, Razorpay Payment Gateway, Add addresses - technologies used [python, HTML, CSS, JS]

#### \* TODO List Webapplication

June 2023 – June 2023

Created a simple ToDo-List project using Django Framework. Which has the functionality to add and remove scheduled Tasks.

#### \* Final project: Area Delay Power Efficient Carry Select Adder

Technology used: VLSI Cadence Tool..

Made an analysis on the logic operations involved in conventional CSLA and CSLA based on binary to excess-1 converter (CSLA-BEC) to study the data dependency and to find redundant logic operations. Instead of using 2 RCA and sum carry unit, we simulated with a HSG, 2 CG, CS and FSG. Which reduced the delay and proved to be power efficient than conventional CSLA.

### INTERNSHIP

Attended Online Internship Program on Industrial IOT, Machine, and Deep Learning organized by "V I Solutions."

September 2020 – October 2020 | Bangalore, India

### PROFESSIONAL EXPERIENCE

#### Mphasis, Tech Support Associate

January 2022 – August 2022 | Bangalore, India

Where I handled diverse semi-technical responsibilities such as Customer Support, Troubleshooting, Documentation and Tracking and etc., Also came across Azure directory, MFA and handled various other applications as well.

### DECLARATION

I do hereby declare that the above facts and information stated are true, correct, and complete to the best of my belief and knowledge.

Pruthviraj N  
Bangalore