



ABHYASA
SEMICON TECHNOLOGIES

Training Completion Certificate

Proudly Presented to

Maturu Pavan Kumar

Has successfully completed a 6-month training program in VLSI Design & Verification from September 18-09-2024 to 20-03-2025 and gained hands-on experience in the following areas:

- RTL Design.
- System Verilog Testbench Development .
- Coverage Analysis.
- AXI Protocol Verification with UVM methodology.

We commend their dedication and efforts in successfully completing this Training Program .

S. Visweswara Rao

Dr. VISWESWARA RAO SAMOJU
FOUNDER & MANAGING DIRECTOR



Validation Number: 010225006

(to validate this certificate mail to
hr.abhyasa1@gmail.com)



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