| Action Proposition Interior Main Number 1 Type OB Language LAD Interior Main Number | | | | | | | i | valli IUBT |
|--|-----|--------------------|--------------------|----------------|--------------------------------|-----------|--------------------------|-------------|
| Intial_Call Bool Constant etwork 1: Main Program Sweep (Cycle) Comment Family | | | | | | | | |
| Automatic formation lele "Main Program Sweep (Cy- cle)" unsoin 0.1 User-defined ID Initial_Call Bool Initial call of this OB Remanence Bool —True, if remanent data are available Temp Constant attwork 1: NoB2 | LAD | Languago | OP | Type | | Number | | eneral |
| title "Main Program Sweep (Cycle)" Author Comment Family | LAU | Language | ОВ | Туре | | Number | | umbering |
| cle)* Intial_Call Bool Initial call of this OB Remanence Bool —True, if remanent data are available Temp Constant atwork 1: ***DB2** **SEVENCA.0B** **SEVENCA** EN | | Family | | Comment | | Author | "Main Program Sweep (Cy- | |
| Data type Default value Comment Injut Initial_Call Bool Remanence Bool Temp Constant Etwork 1: False OFF_SQ S_NO | | , | | | | | cle)" | |
| Initial_Call Bool Initial call of this OB Remanence Bool —True, if remanent data are available Temp Constant **SERVENCA_DB** **SERVENCA_D | | | | | | | 0.1 | |
| Initial_Call | | | Comment | | Default valu | Data type | | |
| Temp Constant **DB2 **SEKVENCA_DB* **SEKVENCA' **B1 **SEKVENCA' false OFF_SQ S_NO false AKC_EF S_ACTIVE false S_NEXT AUTO_ON false S_NEXT AUTO_ON false S_W_AUTO TAP_ON false S_W_TAP TOP_ON | | | | | | | | Initial_Cal |
| ## SERVENCA_DB* SERVENCA_DB* SERVENCA* Talse | | data are available | =True, if remanent | | | Bool | nce | |
| "SEKVENCA_DB" "SEKVENCA" EN ENO false — OFF_SQ S_NO false — INIT_SQ S_MORE — false — ACK_EF S_ACTIVE — false — S_PREV ERR_FLT — false — S_NEXT AUTO_ON — false — SW_AUTO TAP_ON — false — SW_TAP TOP_ON — false — SW_MAN O — S_SEL false — S_OFF | | | | | | | | |
| "SEKVENCA_DB" "SEKVENCA" EN ENO false — OFF_SQ S_NO false — INIT_SQ S_MORE — false — ACK_EF S_ACTIVE — false — S_PREV ERR_FLT — false — S_NEXT AUTO_ON — false — SW_AUTO TAP_ON — false — SW_TAP TOP_ON — false — SW_MAN O — S_SEL false — S_OFF | | | | | | | | etwork 1: |
| ## SEKVENCA DB* WFB1 | | | | | | | | |
| ## ## ## ## ## ## ## ## ## ## ## ## ## | | | | | | | | |
| "SEKVENCA" EN ENO false — OFF_SQ | | | | DB" | "SEKVEN | | | |
| false — OFF_SQ S_NO — false — INIT_SQ S_MORE — false — ACK_EF S_ACTIVE — false — S_PREV ERR_FLT — false — S_NEXT AUTO_ON — false — SW_AUTO TAP_ON — false — SW_TAP TOP_ON — false — SW_MAN 0 — S_SEL false — S_ON false — S_OFF | | | | | "SEKV | | | |
| false — ACK_EF S_ACTIVE — false — S_PREV ERR_FLT — false — S_NEXT AUTO_ON — false — SW_AUTO TAP_ON — false — SW_TAP TOP_ON — false — SW_TOP MAN_ON — false — SW_MAN 0 — S_SEL false — S_ON false — S_OFF | | | | s_NO | false — OFF_SQ | | | |
| false — s_NEXT AUTO_ON → false — sw_AUTO TAP_ON → false — sw_TAP TOP_ON → false — sw_MAN O — s_sel false — s_ON false — s_OFF | | | | S_ACTIVE —I | false — ACK_EF | | | |
| false — SW_TAP TOP_ON — false — SW_TOP MAN_ON — false — SW_MAN 0 — S_SEL false — S_ON false — S_OFF | | | | AUTO_ON ─ ···· | false — S_NEXT | | | |
| false — sw_TOP | | | | | false — SW_AUTO false — SW_TAP | | | |
| 0 — S_SEL false — S_ON false — S_OFF | | | | | false — SW_TOP | | | |
| false — S_OFF | | | | | 0 — S_SEL | | | |
| | | | | | false — S_OFF | | | |
| | | | | | Talse — T_PUSH | | | |
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| Automation Portal | |
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SEKVENCA [FB1]

| SEKVENCA Prop | perties | | | | | | |
|---------------|-----------|-----------------|-----|---------------|------|----------|-------|
| General | | | | | | | |
| Name | SEKVENCA | Number | 1 | Туре | FB | Language | GRAPH |
| Numbering | Automatic | Network lan- | LAD | Block version | V5.0 | | |
| | | guage | | | | | |
| Information | | | | | | | |
| Title | | Author | | Comment | | Family | |
| Version | 0.1 | User-defined ID | | | | | |
| | | | | | | | |

| ame | Data type | Default value | Retain | Accessible from HMI/OPC UA | able | Visible in HMI engi- neering | | Supervi- sion | Comment |
|-----------------|-----------------------|---------------|------------|-------------------------------------|-------|------------------------------------|-------|------------------|---|
| ▼ Input | | | | | | | | | |
| OFF_SQ | Bool | false | Non-retain | False | False | False | False | | Turn sequence off |
| INIT_SQ | Bool | false | Non-retain | False | False | | False | | Set sequence to initial state |
| ACK_EF | Bool | false | Non-retain | False | False | False | False | | Acknowledge all errors and faults |
| S_PREV | Bool | false | Non-retain | False | False | False | False | | Output previous step in parameter S_NO |
| S_NEXT | Bool | false | Non-retain | False | False | False | False | | Indicate next step in parameter S_NO |
| SW_AUTO | Bool | false | Non-retain | False | False | False | False | | Automatic mode |
| SW_TAP | Bool | false | Non-retain | False | False | False | False | | Semiautomatic/switch with transition |
| SW_TOP | Bool | false | Non-retain | False | False | False | False | | Semiautomatic/ignore transition |
| SW_MAN | Bool | false | Non-retain | False | False | False | False | | Manual mode |
| S_SEL | Int | 0 | Non-retain | False | False | False | False | | Select step to be output to |
| S_ON | Bool | false | Non-retain | False | False | False | False | | S_NO Activate step indicated in |
| S_OFF | Bool | false | Non-retain | False | False | False | False | | S_NO Deactivate step indicated |
| T_PUSH | Bool | false | Non-retain | False | False | False | False | | S_NO Enable transition to switch |
| | 5001 | Tuise | Nonretuni | ruise | ruisc | T disc | Tuise | | in semi automatic mode |
| Output | | - | | | | | | | |
| S_NO | Int | 6-1 | Non-retain | False | False | | False | | Step number |
| S_MORE | Bool | false | Non-retain | False | False | | False | | More steps are available ar can be shown in S_NO |
| S_ACTIVE | Bool | false | Non-retain | False | False | | False | | Step indicated in S_NO is a tive |
| ERR_FLT | Bool | false | Non-retain | False | False | | False | | Interlock or supervision group error |
| AUTO_ON | Bool | false | Non-retain | False | False | | False | | Automatic mode is active |
| TAP_ON | Bool | false | Non-retain | False | False | False | False | | Semiautomatic mode/step with transition enabled |
| TOP_ON | Bool | false | Non-retain | False | False | False | False | | Semiautomatic mode/ignotransition enabled |
| MAN_ON | Bool | false | Non-retain | False | False | False | False | | Manual mode is active |
| InOut | | | | | | | | | |
| ▼ Static | | | | | | | | | |
| ▼ RT_DATA | G7_RTData- Plus_V5 | | Non-retain | False | False | False | False | | Internal data area |
| VERSION | String[10] | 'V5.0' | Non-retain | False | False | False | False | | Block version |
| S_DISPLAY | Int | 0 | Non-retain | False | False | False | False | | Internal display of output parameter S_NO |
| S_SEL_OLD | Int | 0 | Non-retain | False | False | False | False | | Previous value in S_SEL |
| S_DISPIDX | USInt | 255 | Non-retain | False | False | False | False | | Index of the step in S_NO |
| T_DISPIDX | USInt | 255 | Non-retain | False | False | False | False | | Index of the transition dis- played in T_NO |
| ▼ MOP_EDGE | G7_MOP- Plus_V5 | | Non-retain | False | False | False | False | | Mode in last cycle |
| AUTO | Bool | false | Non-retain | False | False | False | False | | Status: automatic mode |
| MAN | Bool | false | Non-retain | False | False | False | False | | Status: manual mode |
| TAP | Bool | false | Non-retain | False | False | False | False | | Status: semi automatic/ switch with transition |
| ТОР | Bool | false | Non-retain | False | False | False | False | | Status: semi automatic/ignore transition |
| ACK_S | Bool | false | Non-retain | False | False | False | False | | Request: acknowledge step at parameter S_NO |
| REG_S | Bool | false | Non-retain | False | False | False | False | | Request: register step indi- cated in S_NO |
| T_PREV | Bool | false | Non-retain | False | False | False | False | | Request: output previous valid transition in T_NO |
| T_NEXT | Bool | false | Non-retain | False | False | False | False | | Request: output next valid transition in T_NO |
| LOCK | Bool | false | Non-retain | False | False | False | False | | Status: interlocks activated |
| SUP | Bool | false | Non-retain | False | False | | False | | Status: supervisions activa- ted |
| ACKREQ | Bool | false | Non-retain | False | False | False | False | | Status: acknowledgment required |

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| Name | Data type | Default value | Retain | Accessible from HMI/OPC UA | able | Visible in HMI engi- neering | Setpoint | Supervi- sion | Comment |
|------------------|--------------------|----------------|-----------------------|-------------------------------------|-------|------------------------------------|----------------|------------------|---|
| SSKIP | Bool | false | Non-retain | False | False | False | False | | Status: "Skip steps" enabled |
| OFF | Bool | false | Non-retain | False | | False | False | | Request: deactivate all steps |
| INIT HALT | Bool | false | Non-retain Non-retain | False False | | False False | False False | | Request: set sequence to initial state Status: sequence halted |
| TMS_HALT | Bool | false | Non-retain | False | | False | False | | Status: all internal timers |
| | D I | £ 1. | Name of the | F 1- | F 1- | F 1. | F 1- | | held |
| OPS_ZERO | Bool | false | Non-retain | False | Faise | False | False | | Status: set all operands processed with N, L, D instructions to 0 |
| SACT_DISP | Bool | false | Non-retain | False | | False | False | | Status: display active steps only |
| SEF_DISP | Bool | false | Non-retain | False | False | False | False | | Status: display only steps with errors and disrupted steps |
| SALL_DISP | Bool | false | Non-retain | False | | False | False | | Status: display all steps |
| S_PREV | Bool | false | Non-retain | False | False | False | False | | Request: output previous step to S_NO |
| S_NEXT | Bool | false | Non-retain | False | | False | False | | Request: Output next step a S_NO parameter |
| S_SELOK | Bool | false | Non-retain | False | False | False | False | | Request: output step num- ber from S_SEL to S_NO |
| S_ON | Bool | false | Non-retain | False | False | False | False | | Request: activate step indi- cated in S_NO |
| S_OFF | Bool | false | Non-retain | False | False | False | False | | Request: deactivate step at parameter S_NO |
| T_PUSH | Bool | false | Non-retain | False | False | False | False | | Request: transition switch- ing enabled |
| REG | Bool | false | Non-retain | False | False | False | False | | Request: register all inter- lock and supervision errors |
| ACK | Bool | false | Non-retain | False | False | False | False | | Request: acknowledge all in- terlock and supervision er- rors |
| IL_PERM | Bool | false | Non-retain | False | False | False | False | | Status: permanent process- ing of all interlocks |
| T_PERM | Bool | false | Non-retain | False | False | False | False | | Status: permanent process- ing of all transitions |
| ILP_MAN | Bool | false | Non-retain | False | False | False | False | | Status: permanent processing of all interlocks in man- ual mode |
| LMODE | Bool | false | Non-retain | False | False | False | False | | Status: learning mode is acitve |
| RESET_CRIT | Bool | false | Non-retain | False | False | False | False | | Request: reset all initial val- ues recorded for interlocks and transitions |
| ▼ MOP | G7_MOP- Plus_V5 | | Non-retain | False | False | False | False | | Mode |
| AUTO | Bool | true | Non-retain | False | False | | False | | Status: automatic mode |
| MAN | Bool | false false | Non-retain | False False | | False False | False False | | Status: manual mode Status: semi automatic/ |
| TAP | Bool | false | Non-retain Non-retain | False | | False | False | | switch with transition Status: Semi automatic/ |
| ACK_S | Bool | false | Non-retain | False | | False | False | | ignore transition Request: acknowledge step |
| | | | | | | | | | at parameter S_NO |
| REG_S | Bool | false | Non-retain | False | | False | False | | Request: register step indicated in S_NO |
| T_PREV | Bool | false | Non-retain | False | False | False | False | | Request: output previous valid transition in T_NO |
| T_NEXT | Bool | false | Non-retain | False | | False | False | | Request: output next valid transition in T_NO |
| LOCK | Bool | true | Non-retain | False | | False | False | | Status: interlocks activated |
| SUP | Bool | true | Non-retain | False | | False | False | | Status: supervisions activa- ted |
| ACKREQ | Bool | true | Non-retain | False | False | False | False | | Status: acknowledgment required |
| SSKIP | Bool | false | Non-retain | False | | False | False | | Status: "Skip steps" enabled |
| OFF | Bool | false | Non-retain | False | - | False | False | | Request: deactivate all steps |
| INIT HALT | Bool | true false | Non-retain Non-retain | False False | False | False | False False | | Request: set sequence to initial state Status: sequence halted |
| TMS_HALT | Bool | false | Non-retain | False | | False | False | | Status: all internal timers |
| OPS_ZERO | Bool | false | Non-retain | False | False | False | False | | held Status: set all operands processed with N, L, D instruc- |
| SACT_DISP | Bool | true | Non-retain | False | False | False | False | | tions to 0 Status: display active steps |
| SEF_DISP | Bool | false | Non-retain | False | False | False | False | | only Status: display only steps with errors and disrupted |
| | | false | Non-retain | False | False | Falso | False | | steps Status: display all steps |
| SALL_DISP | Bool | laise | INCHERAIN | | | | | | |

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| ame | Data type | Default value | Retain | Accessible from HMI/OPC UA | able | Visible in HMI engi- neering | Setpoint | Supervi- sion | Comment |
|-------------|------------------------|----------------|--------------------------|-------------------------------------|----------------|------------------------------------|----------------|------------------|---|
| S_NEXT | Bool | false | Non-retain | False | False | False | False | | Request: Output next step |
| S_SELOK | Bool | false | Non-retain | False | False | False | False | | S_NO parameter Request: output step num |
| | | | | | | | | | ber from S_SEL to S_NO |
| S_ON | Bool | false | Non-retain | False | False | False | False | | Request: activate step indicated in S_NO |
| S_OFF | Bool | false | Non-retain | False | False | False | False | | Request: deactivate step a |
| T_PUSH | Bool | false | Non-retain | False | False | False | False | | parameter S_NO Request: transition switch |
| | | | | | | | | | ing enabled |
| REG | Bool | false | Non-retain | False | False | False | False | | Request: register all inter- lock and supervision errors |
| ACK | Bool | false | Non-retain | False | False | False | False | | Request: acknowledge all |
| | | | | | | | | | terlock and supervision errors |
| IL_PERM | Bool | false | Non-retain | False | False | False | False | | Status: permanent proces |
| T_PERM | Bool | false | Non-retain | False | False | False | False | | ing of all interlocks Status: permanent proces |
| | | | - | | | | | | ing of all transitions |
| ILP_MAN | Bool | false | Non-retain | False | False | False | False | | Status: permanent proces |
| | | | | | | | | | ual mode |
| LMODE | Bool | false | Non-retain | False | False | False | False | | Status: learning mode is acitve |
| RESET_CRIT | Bool | false | Non-retain | False | False | False | False | | Request: reset all initial va |
| | | | | | | | | | ues recorded for interlock and transitions |
| TIME_DELTA | Time | T#0ms | Non-retain | False | False | False | False | | Cycle time |
| ▼ SQ_FLAGS | G7_SQFlags- Plus_V5 | | Non-retain | False | False | False | False | | Sequence bit memory |
| ERR_FLT | Bool | false | Non-retain | False | False | False | False | | Interlock and supervision |
| ERROR | Bool | false | Non-retain | False | False | Falso | False | | group error Interlock group error |
| FAULT | Bool | false | Non-retain | False | False | | False | | Supervision group error |
| RT_FAIL | Bool | false | Non-retain | False | False | | False | | Runtime error |
| NO_SNO | Bool | false | Non-retain | False | False | | False | | Requested step number n |
| | | | | | | | | | found |
| NF_OFL | Bool | false | Non-retain | False | False | False | False | | Overflow: too many ON o OFF requests |
| SA_OFL | Bool | false | Non-retain | False | False | False | False | | Overflow: too many steps |
| TV_OFL | Bool | false | Non-retain | False | False | Falso | False | | active Overflow: too many valid |
| TV_OFL | ВООТ | laise | Non-retain | l dise | laise | i dise | laise | | transitions |
| NO_SWI | Bool | false | Non-retain | False | False | | False | | Do not switch in this cycle |
| CYC_OP | Bool | false | Non-retain | False | False | False | False | | Cyclic execution of the se quence after initialization |
| AS_MSG | Bool | true | Non-retain | False | False | False | False | | Alarms during runtime er bled or disabled by instru |
| SQ_BUSY | Bool | false | Non-retain | False | False | False | False | | tion Internal edge memory bit |
| | | | | | | | | | for sequence processing |
| SA_BUSY | Bool | false | Non-retain | False | False | False | False | | Internal edge memory bit for sequence processing |
| PRE_CNT | USInt | 1 | Non-retain | False | False | False | False | | Number of permanent in structions preceding the |
| POST_CNT | USInt | 1 | Non-retain | False | False | False | False | | quencer Number of permanent in- |
| | | | | | | | | | structions after the seque |
| SQ_CNT | USInt | 1 | Non-retain | False | False | False | False | | er Number of branch paths |
| S_CNT | USInt | 15 | Non-retain | False | False | | False | | Number of steps |
| LOCK_CNT | USInt | 4 | Non-retain | False | False | | False | | Number of interlocks |
| SUP_CNT | USInt | 0 | Non-retain | False | False | False | False | | Number of supervisions |
| T_CNT | USInt | 13 | Non-retain | False | False | False | False | | Number of transitions |
| SQ_PART_CNT | USInt | 8 | Non-retain | False | False | | False | | Number of branches |
| MAX_TVAL | USInt | 11 | Non-retain | False | False | False | False | | Max. number of simultan ously valid transitions |
| MAX_SACT | USInt | 7 | Non-retain | False | False | False | False | | Max. number of simultane ously active steps |
| AS_MSG | Byte | 16#65 | Non-retain | False | False | | False | | Alarm flags |
| ▼ EXEC_BITS | Array[0249] o Bool | † | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[| | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[| • | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[: | | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[| | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[4 | | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[| - | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[| - | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[| | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[| | false false | Non-retain Non-retain | False False | False False | | False False | | System-internal System-internal |
| EXEC_BITS[| • | false | Non-retain Non-retain | False | | | False | | System-internal System-internal |
| LVEC DII2 | 101 | . 4.50 | 14011 ICtulii | 1 4130 | . 4136 | . 4.30 | . 4.50 | | -, stem meeria |

| me | Data type | Default value | Retain | Accessible from HMI/OPC UA | able | Visible in HMI engi- neering | Setpoint | Supervi- sion | Comment |
|------------------------------|-----------|---------------|--|-------------------------------------|-------|------------------------------------|----------|------------------|-----------------|
| EXEC_BITS[11] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[12] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[13] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[14] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[15] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[16] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[17] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[18] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[19] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[20] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[21] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[22] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[23] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[24] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[25] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[26] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[27] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[28] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[29] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[30] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[31] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[32] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[33] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[34] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[35] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[36] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[37] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[38] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[39] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[40] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[41] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[42] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[43] | Bool | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[44] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[45] | Bool | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[46] | Bool | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[47] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[47] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[49] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[50] | Bool | false | Non-retain | False | | - | False | | System-internal |
| EXEC_BITS[50] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[51] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[52] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[54] | Bool | false | Non-retain | False | | False | False | | System-internal |
| | Bool | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[55] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[56] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[57] | | | | | | | - | | <u> </u> |
| EXEC_BITS[58] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[59] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[60] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[61] | Bool | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[62] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[63] | Bool | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[64] | Bool | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[65] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[66] | Bool | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[67] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[68] | Bool | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[69] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[70] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[71] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[72] | Bool | false | Non-retain | False | + | False | False | | System-internal |
| EXEC_BITS[73] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[74] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[75] | Bool | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[76] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[77] | Bool | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[78] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[79] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[80] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[81] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[82] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[83] | | | The second secon | | | | | | |
| EXEC_BITS[83] EXEC_BITS[84] | Bool | false | Non-retain | False | False | False | False | | System-internal |

| ame | Data type | Default value | Retain | Accessible from HMI/OPC UA | able | Visible in HMI engi- neering | Setpoint | Supervi- sion | Comment |
|----------------|-----------|---------------|------------|-------------------------------------|-------|------------------------------------|--------------|------------------|-----------------|
| EXEC_BITS[86] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[87] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[88] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[89] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[90] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[91] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[92] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[93] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[94] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[95] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[96] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[97] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[98] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[99] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[100] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[100] | Bool | false | Non-retain | False | False | | False | | System-internal |
| | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[102] | | | | | False | | False | | - |
| EXEC_BITS[103] | Bool | false | Non-retain | False | | | | | System-internal |
| EXEC_BITS[104] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[105] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[106] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[107] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[108] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[109] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[110] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[111] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[112] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[113] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[114] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[115] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[116] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[117] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[118] | Bool | false | Non-retain | False | False | | False | | System-internal |
| | | false | | False | | | False | | • |
| EXEC_BITS[119] | Bool | | Non-retain | | False | | | | System-internal |
| EXEC_BITS[120] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[121] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[122] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[123] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[124] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[125] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[126] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[127] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[128] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[129] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[130] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[131] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[132] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[133] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[134] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[134] | Bool | false | Non-retain | False | False | | False | | System-internal |
| | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[136] | | false | | False | | | | | - |
| EXEC_BITS[137] | Bool | | Non-retain | | False | | False | | System-internal |
| EXEC_BITS[138] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[139] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[140] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[141] | Bool | false | Non-retain | False | | False | False _ · | | System-internal |
| EXEC_BITS[142] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[143] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[144] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[145] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[146] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[147] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[148] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[149] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[150] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[151] | Bool | false | Non-retain | False | False | | False | | System-internal |
| | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[152] | | | | | | | | | - |
| EXEC_BITS[153] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[154] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[155] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[156] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[157] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[158] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[159] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[160] | Bool | false | Non-retain | False | False | Ealco | False | | System-internal |

| Name | Data type | Default value | Retain | Accessible from HMI/OPC UA | able | Visible in HMI engi- neering | Setpoint | Supervi- sion | Comment |
|----------------|-----------|---------------|------------|-------------------------------------|-------|------------------------------------|----------|------------------|-----------------|
| EXEC_BITS[161] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[162] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[163] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[164] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[165] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[166] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[167] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[168] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[169] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[170] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[171] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[172] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[173] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[174] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[175] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[176] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[177] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[178] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[179] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[180] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[181] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[182] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[183] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[184] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[185] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[186] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[187] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[188] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[189] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[190] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[191] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[192] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[193] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[194] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[195] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[196] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[197] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[198] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[199] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[200] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[201] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[202] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[203] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[204] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[205] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[206] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[207] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[208] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[209] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[210] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[211] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[212] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[213] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[214] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[214] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[216] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[217] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[218] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[218] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[219] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[220] | Bool | false | Non-retain | False | False | | False | | System-internal |
| | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[222] | Bool | false | Non-retain | False | False | | False | | , |
| EXEC_BITS[223] | | | | | False | | | | System-internal |
| EXEC_BITS[224] | Bool | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[225] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[226] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[227] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[228] | Bool | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[229] | Bool | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[230] | Bool | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[231] | Bool | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[232] | Bool | false | Non-retain | False | | | False | | System-internal |
| EXEC_BITS[233] | Bool | false | Non-retain | False | False | | False | | System-internal |
| EXEC_BITS[234] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[235] | Bool | false | Non-retain | False | False | | False | | System-internal |

| Totally Integ | rated |
|---------------|--------|
| Automation | Portal |

| ame | Data type | Default value | Retain | | able | Visible in HMI engi- neering | Setpoint | Supervi- sion | Comment |
|----------------|---------------------------|---------------|--------------------------|----------------|-------|------------------------------------|----------------|------------------|--|
| EXEC_BITS[236] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[237] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[238] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[239] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[240] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[241] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[242] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[243] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[244] | Bool | false | Non-retain | False | | False | False | | System-internal |
| | | false | | | | False | False | | - |
| EXEC_BITS[245] | Bool | | Non-retain | False | | | | | System-internal |
| EXEC_BITS[246] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[247] | Bool | false | Non-retain | False | | False | False | | System-internal |
| EXEC_BITS[248] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| EXEC_BITS[249] | Bool | false | Non-retain | False | False | False | False | | System-internal |
| ▼ OFFSETS | G7_Offsets- | | Non-retain | False | False | False | False | | Internal offsets |
| | Plus_V5 | | | | | | | | |
| SINI_OFFSET | UInt | 0 | Non-retain | False | False | False | False | | Offset of internal array SI |
| LSTT_OFFSET | UInt | 8 | Non-retain | False | False | False | False | | Offset of internal array LSTT[] |
| ATAJ_OFFSET | UInt | 21 | Non-retain | False | False | False | False | | Offset of internal array ATAJ[] |
| ATAB_OFFSET | UInt | 34 | Non-retain | False | False | False | False | | Offset of internal array ATAB[] |
| PSTT_OFFSET | UInt | 47 | Non-retain | False | False | False | False | | Offset of internal array PSTT[] |
| NSTT_OFFSET | UInt | 60 | Non-retain | False | | False | False | | Offset of internal array NSTT[] |
| ASSJ_OFFSET | UInt | 73 | Non-retain | False | False | False | False | | Offset of internal array ASSJ[] |
| ASSB_OFFSET | UInt | 88 | Non-retain | False | False | False | False | | Offset of internal array ASSB[] |
| PTTS_OFFSET | UInt | 103 | Non-retain | False | False | False | False | | Offset of internal array PTTS[] |
| NTTS_OFFSET | UInt | 118 | Non-retain | False | False | False | False | | Offset of internal array NTTS[] |
| SW_SQTS_OFFSET | UInt | 133 | Non-retain | False | False | False | False | | Offset of internal array SW_SQTS[] |
| SWITCH_OFFSET | UInt | 148 | Non-retain | False | False | False | False | | Offset of internal array SWITCH[] |
| TVX_OFFSET | UInt | 156 | Non-retain | False | False | False | False | | Offset of internal array T |
| TTX_OFFSET | UInt | 168 | Non-retain | False | False | False | False | | Offset of internal array T |
| TSX_OFFSET | UInt | 180 | Non-retain | False | False | False | False | | Offset of internal array TS |
| S00X_OFFSET | UInt | 188 | Non-retain | False | False | False | False | | Offset of internal array |
| SOFFX_OFFSET | UInt | 196 | Non-retain | False | False | False | False | | S00X[] Offset of internal array |
| SONX_OFFSET | UInt | 204 | Non-retain | False | False | False | False | | SOFFX[] Offset of internal array |
| SAX_OFFSET | UInt | 212 | Non-retain | False | False | False | False | | SONX[] Offset of internal array |
| SERRX_OFFSET | UInt | 220 | Non-retain | False | False | False | False | | SAX[] Offset of internal array |
| SMX_OFFSET | UInt | 236 | Non-retain | False | False | False | False | | SERRX[] Offset of internal array |
| S0X_OFFSET | UInt | 252 | Non-retain | False | False | False | False | | SMX[] Offset of internal array S |
| S1X_OFFSET | UInt | 268 | Non-retain | False | False | False | False | | Offset of internal array S |
| THRESHOLD_SUP | USInt | 0 | Non-retain | False | | False | False | | Threshold for step activa |
| THRESHOLD_WARN | USInt | 0 | Non-retain | False | False | False | False | | Threshold for step activa time (warning only) |
| ▼ GC_FLAGS | G7_GCFlags- Plus_V5 | | Non-retain | False | False | False | False | | Compiler flags |
| COND_ED | USInt | 16#E3 | Non-retain | False | False | False | False | | Language in networks |
| SSKIP_ON | Bool | false | Non-retain | False | | False | False | | Skip steps |
| ACK_REQ | Bool | true | Non-retain | False | | False | False | | Acknowledgement requi |
| ILP_MAN | Bool | false | Non-retain | False | False | False | False | | Permanent processing of interlocks in manual mod |
| SWM_LOCKED | Bool | false | Non-retain | False | False | False | False | | Lock operating mode sel |
| SET_ENO | Bool | false | Non-retain | False | False | False | False | | Set ENO automatically |
| IL_REACT_CAT | USInt | 1 | Non-retain | False | | False | False | | Category for interlock an reaction |
| WARN_CAT | USInt | 2 | Non-retain | False | False | False | False | | Category for warnings |
| CRIT_ON | Bool | false | Non-retain | False | | False | False | | Criteria analysis activated |
| | | idise | | | | | | | * |
| ▼ Trans1 | G7_Transition- Plus_V5 | | Non-retain | False | raise | False | False | | Transition structure |
| TV | Bool | false | Non-retain | False | Falco | False | False | | Transition is valid |
| | Bool | | Non-retain | | | False | | | Transition is satisfied |
| TT TS | | false | | False | | | False | | |
| 18 | Bool | false | Non-retain Non-retain | False False | | False False | False False | | Transition switches Indicates the user-define |

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| ne | Data type | Default value | Retain | Accessible from HMI/OPC UA | able from HMI/ OPC | Visible in HMI engi- neering | Setpoint | Supervi- sion | Comment |
|----------|---------------------------|---------------|--------------------------|-------------------------------------|-----------------------------|------------------------------------|----------------|------------------|---|
| CRIT | DWord | 16#0 | Non-retain | False | UA False | False | False | | Status of the maximum |
| | | | | | | | | | LAD/FBD elements of the transition in the current cessing cycle |
| CRIT_OLD | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum |
| | | | | | | | | | LAD/FBD elements of the transition in the previou processing cycle |
| CRIT_FLT | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT if an error of curs |
| ▼ Trans2 | G7_Transition- Plus_V5 | | Non-retain | False | False | False | False | | Transition structure |
| TV | Bool | false | Non-retain | False | False | | False | | Transition is valid |
| TT | Bool | false | Non-retain | False | False | | False | | Transition is satisfied |
| TS | Bool | false | Non-retain | False | False | | False | | Transition switches |
| TNO | Int | 2 | Non-retain | False | False | False | False | | Indicates the user-define transition number |
| CRIT | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum LAD/FBD elements of the transition in the current cessing cycle |
| CRIT_OLD | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum |
| CMT_OLD | Sword . | 10,10 | Non retuin | raise | , disc | Taise | i disc | | LAD/FBD elements of the transition in the previou processing cycle |
| CRIT_FLT | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT if an error |
| ▼ Trans3 | G7_Transition- Plus_V5 | | Non-retain | False | False | False | False | | Transition structure |
| TV | Bool | false | Non-retain | False | False | False | False | | Transition is valid |
| TT | Bool | false | Non-retain | False | False | False | False | | Transition is satisfied |
| TS | Bool | false | Non-retain | False | False | False | False | | Transition switches |
| TNO | Int | 3 | Non-retain | False | False | | False | | Indicates the user-defin transition number |
| CRIT | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum LAD/FBD elements of the transition in the current cessing cycle |
| CRIT_OLD | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum LAD/FBD elements of th transition in the previous processing cycle |
| CRIT_FLT | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT if an error curs |
| ▼ Trans4 | G7_Transition- Plus_V5 | | Non-retain | False | False | | False | | Transition structure |
| TV | Bool | false | Non-retain | False | False | | False | | Transition is valid |
| TT | Bool | false | Non-retain | False | False | | False | | Transition is satisfied |
| TS | Bool | false | Non-retain | False | False | | False | | Transition switches |
| TNO | Int DWord | 16#0 | Non-retain Non-retain | False False | False False | | False False | | Indicates the user-defir transition number Status of the maximum |
| | | | | | | | | | LAD/FBD elements of the transition in the current cessing cycle |
| CRIT_OLD | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum LAD/FBD elements of the transition in the previous processing cycle |
| CRIT_FLT | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT if an error curs |
| ▼ Trans5 | G7_Transition- Plus_V5 | | Non-retain | False | False | | False | | Transition structure |
| TV | Bool | false | Non-retain | False | | False | False | | Transition is valid |
| TT | Bool | false | Non-retain | False | False | | False | | Transition is satisfied |
| TS | Bool | false 5 | Non-retain Non-retain | False | False | | False False | | Transition switches Indicates the user-defir |
| TNO | Int | J | ivon-retain | False | raise | False | raise | | transition number |
| CRIT | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum LAD/FBD elements of the transition in the current cessing cycle |
| CRIT_OLD | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum LAD/FBD elements of th transition in the previous processing cycle |
| CRIT_FLT | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT if an error |
| ▼ Trans6 | G7_Transition- Plus_V5 | | Non-retain | False | False | False | False | | Transition structure |
| TV | Bool | false | Non-retain | False | False | False | False | | Transition is valid |
| TT | Bool | false | Non-retain | False | False | | False | | Transition is satisfied |
| <u>l</u> | | | | | | False | | | |

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| Automation Portal |

| ame | Data type | Default value | Retain | Accessible from HMI/OPC UA | able from HMI/ OPC | Visible in HMI engi- neering | Setpoint | Supervi- sion | Comment |
|-----------------|---------------------------|---------------|------------|-------------------------------------|-----------------------------|------------------------------------|----------|------------------|---|
| CRIT | DWord | 16#0 | Non-retain | False | UA False | False | False | | Status of the maximum 32 |
| | | | | | | | | | LAD/FBD elements of the transition in the current processing cycle |
| CRIT_OLD | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 32 LAD/FBD elements of the transition in the previous |
| CRIT_FLT | DWord | 16#0 | Non-retain | False | False | False | False | | processing cycle Copy of CRIT if an error oc |
| ▼ Trans7 | G7_Transition- Plus_V5 | | Non-retain | False | False | False | False | | Transition structure |
| TV | Bool | false | Non-retain | False | False | False | False | | Transition is valid |
| TT | Bool | false | Non-retain | False | False | False | False | | Transition is satisfied |
| TS | Bool | false | Non-retain | False | False | | False | | Transition switches |
| TNO | Int | 7 | Non-retain | False | False | False | False | | Indicates the user-defined transition number |
| CRIT | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 3. LAD/FBD elements of the transition in the current p cessing cycle |
| CRIT_OLD | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle |
| CRIT_FLT | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT if an error occurs |
| ▼ Trans8 | G7_Transition- Plus_V5 | | Non-retain | False | False | False | False | | Transition structure |
| TV | Bool | false | Non-retain | False | False | False | False | | Transition is valid |
| TT | Bool | false | Non-retain | False | False | False | False | | Transition is satisfied |
| TS | Bool | false | Non-retain | False | False | | False | | Transition switches |
| TNO | Int | 8 | Non-retain | False | False | False | False | | Indicates the user-define transition number |
| CRIT | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 3 LAD/FBD elements of the transition in the current p cessing cycle |
| CRIT_OLD | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 3 LAD/FBD elements of the transition in the previous processing cycle |
| CRIT_FLT | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT if an error of |
| ▼ Trans9 | G7_Transition- Plus_V5 | | Non-retain | False | False | False | False | | Transition structure |
| TV | Bool | false | Non-retain | False | False | False | False | | Transition is valid |
| TT | Bool | false | Non-retain | False | False | False | False | | Transition is satisfied |
| TS | Bool | false | Non-retain | False | False | | False | | Transition switches |
| TNO | Int | 9 | Non-retain | False | False | False | False | | Indicates the user-define transition number |
| CRIT | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 3 LAD/FBD elements of the transition in the current cessing cycle |
| CRIT_OLD | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 3 LAD/FBD elements of the transition in the previous processing cycle |
| CRIT_FLT | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT if an error or curs |
| ▼ Trans10 | G7_Transition- Plus_V5 | | Non-retain | False | False | False | False | | Transition structure |
| TV | Bool | false | Non-retain | False | False | False | False | | Transition is valid |
| TT | Bool | false | Non-retain | False | False | False | False | | Transition is satisfied |
| TS | Bool | false | Non-retain | False | False | False | False | | Transition switches |
| TNO | Int | 10 | Non-retain | False | False | False | False | | Indicates the user-define transition number |
| CRIT | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 3 LAD/FBD elements of the transition in the current cessing cycle |
| CRIT_OLD | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 3 LAD/FBD elements of the transition in the previous processing cycle |
| CRIT_FLT | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT if an error o |
| ▼ Trans11 | G7_Transition- Plus_V5 | | Non-retain | False | False | False | False | | Transition structure |
| TV | Bool | false | Non-retain | False | False | | False | | Transition is valid |
| TT | Bool | false | Non-retain | False | False | | False | | Transition is satisfied |
| TS | Bool | false | Non-retain | False | False | | False | | Transition switches |
| TNO | Int | 11 | Non-retain | False | False | False | False | 1 | Indicates the user-define |

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| Automation Portal | |

| Name | Data type | Default value | Retain | Accessible from | | Visible in HMI engi- | Setpoint | Supervi- sion | Comment |
|-------------------|----------------|---------------|------------|-----------------|-----------|-------------------------|----------|------------------|---|
| | | | | HMI/OPC | from | neering | | | |
| | | | | UA | HMI/ | | | | |
| | | | | | OPC UA | | | | |
| CRIT | DWord | 16#0 | Non-retain | False | | False | False | | Status of the maximum 32 |
| | | | | | | | | | LAD/FBD elements of the |
| | | | | | | | | | transition in the current pro- |
| CRIT_OLD | DWord | 16#0 | Non-retain | False | False | False | False | | cessing cycle Status of the maximum 32 |
| CKI1_OLD | DVVOIG | 10#0 | Non-retain | laise | i dise | laise | i dise | | LAD/FBD elements of the |
| | | | | | | | | | transition in the previous |
| | | | | | | | | | processing cycle |
| CRIT_FLT | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT if an error oc- |
| ▼ Trans12 | G7_Transition- | | Non-retain | False | False | False | False | | curs Transition structure |
| ▼ ITalis12 | Plus_V5 | | Non-retain | raise | raise | raise | raise | | Transition structure |
| TV | Bool | false | Non-retain | False | False | False | False | | Transition is valid |
| TT | Bool | false | Non-retain | False | | False | False | | Transition is satisfied |
| TS | Bool | false | Non-retain | False | False | False | False | | Transition switches |
| TNO | Int | 12 | Non-retain | False | False | False | False | | Indicates the user-defined |
| | | | | | | | | | transition number |
| CRIT | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 32 |
| | | | | | | | | | LAD/FBD elements of the |
| | | | | | | | | | transition in the current pro- cessing cycle |
| CRIT_OLD | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 32 |
| 55.22 | | | | | | | | | LAD/FBD elements of the |
| | | | | | | | | | transition in the previous |
| CDIT ELE | D111 | 1640 | NI · · | F 1 | F ' | F-1- | F_1- | | processing cycle |
| CRIT_FLT | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT if an error oc- curs |
| ▼ Trans13 | G7_Transition- | | Non-retain | False | Falso | False | False | | Transition structure |
| ▼ ITalis15 | Plus_V5 | | Non-retain | raise | raise | raise | raise | | Transition structure |
| TV | Bool | false | Non-retain | False | False | False | False | | Transition is valid |
| TT | Bool | false | Non-retain | False | | False | False | | Transition is satisfied |
| TS | Bool | false | Non-retain | False | | False | False | | Transition switches |
| TNO | Int | 13 | Non-retain | False | | False | False | | Indicates the user-defined |
| | | | | | | | | | transition number |
| CRIT | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 32 |
| | | | | | | | | | LAD/FBD elements of the |
| | | | | | | | | | transition in the current pro- |
| CRIT_OLD | DWord | 16#0 | Non-retain | False | Ealco | False | False | | cessing cycle Status of the maximum 32 |
| CRIT_OLD | Dvvord | 10#0 | Non-retain | raise | raise | raise | raise | | LAD/FBD elements of the |
| | | | | | | | | | transition in the previous |
| | | | | | | | | | processing cycle |
| CRIT_FLT | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT if an error oc- |
| INICHALIZACHA | C7 Stan Dive V | <u> </u> | Non votoin | Гаја | ГаІса | Гаја | Гајаа | | Curs |
| ▼ INICIJALIZACIJA | G7_StepPlus_V | | Non-retain | False | | False | False | | Step structure |
| S1 | Bool | false | Non-retain | False | False | False | False | | Step is activated |
| L1 | Bool | false | Non-retain | False | False | False | False | | interlock leaving state |
| V1 | Bool | false | Non-retain | False | False | False | False | | Supervision entering state |
| R1 | Bool | false | Non-retain | False | | False | False | | Reserved |
| A1 | Bool | false | Non-retain | False | False | False | False | | Error is acknowledged |
| SO | Bool | false | Non-retain | False | False | False | False | | Step is deactivated |
| LO | Bool | false | Non-retain | False | False | False | False | | Interlock entering state |
| VO | Bool | false | Non-retain | False | False | False | False | | Supervision leaving state |
| X | Bool | false | Non-retain | False | False | False | False | | Step is active |
| LA | Bool | false | Non-retain | False | False | False | False | | Interlock is not satisfied |
| VA | Bool | false | Non-retain | False | False | False | False | | Supervision active |
| RA | Bool | false | Non-retain | False | False | False | False | | Reserved |
| AA | Bool | false | Non-retain | False | | False | False | | Reserved |
| SS | Bool | false | Non-retain | False | False | False | False | | System-internal |
| LS | Bool | true | Non-retain | False | False | False | False | | Direct result of the program- |
| | | | | | - : | | | | med interlock |
| VS | Bool | false | Non-retain | False | False | False | False | | Direct result of the program- |
| CNO | Int | 1 | Non ratain | Ealca | Eala- | Ealco | Ealco | | med supervision |
| SNO | Int | T#Opas | Non-retain | False | | False | False | | User step number |
| T | Time | T#0ms | Non-retain | False | | False | False | | Total step activation time |
| U | Time | T#0ms | Non-retain | False | raise | False | False | | Step activation time without disturbance |
| T_MAX | Time | T#10S | Non-retain | False | False | False | False | | Maximal step activation time |
| T_WARN | Time | T#7S | Non-retain | False | | False | False | | Warning time |
| CRIT_LOC | DWord | 16#0 | Non-retain | False | | False | False | | Status of the maximum 32 |
| CIMI_LOC | DVVOIG | | Non retuin | i disc | . 4130 | , 4150 | . 4130 | | LAD/FBD elements in the in- |
| | | | | | | | | | terlock in the current pro- |
| | | 4.505 | | | - : | | | | cessing cycle |
| CRIT_LOC_ERR | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT_LOC when the |
| CNA | Dool | false | Non ratain | Ealaa | Eala- | Ealca | Ealca | | interlock leaves the state |
| SM | Bool | false | Non-retain | False | | False | False | | System-internal |
| H_IL_ERR | Byte | 16#0 | Non-retain | False | | False | False | | System-internal |
| H_SV_FLT | Byte | 16#04 | Non-retain | False | | False | False | | System-internal |
| ▼ Step1 | G7_StepPlus_V | | Non-retain | False | | False | False | | Step structure |
| S1 | Bool | false | Non-retain | False | | False | False | | Step is activated |
| L1 | Bool | false | Non-retain | False | False | False | False | | interlock leaving state |
| V1 | Bool | false | Non-retain | False | False | False | False | | Supervision entering state |
| | | | | | | | | | |
| | | | | | | | | | |

| Totally Integrated Automation Portal | |
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| Name | Data type | Default value | Retain | Accessible from HMI/OPC UA | able | Visible in HMI engi- neering | Setpoint | Supervi- sion | Comment |
|--------------------------|----------------|---------------|-----------------------|-------------------------------------|----------------|------------------------------------|----------------|------------------|---|
| R1 | Bool | false | Non-retain | False | False | False | False | | Reserved |
| A1 | Bool | false | Non-retain | False | False | False | False | | Error is acknowledged |
| S0 | Bool | false | Non-retain | False | False | False | False | | Step is deactivated |
| LO | Bool | false | Non-retain | False | False | False | False | | Interlock entering state |
| V0 | Bool | false | Non-retain | False | False | False | False | | Supervision leaving state |
| X | Bool | false | Non-retain | False | False | False | False | | Step is active |
| LA | Bool | false | Non-retain | False | False | False | False | | Interlock is not satisfied |
| VA | Bool | false | Non-retain | False | False | False | False | | Supervision active |
| RA | Bool | false | Non-retain | False | False | False | False | | Reserved |
| AA | Bool | false | Non-retain | False | False | False | False | | Reserved |
| SS | Bool | false | Non-retain | False | False | False | False | | System-internal |
| LS | Bool | true | Non-retain | False | False | False | False | | Direct result of the program- med interlock |
| VS | Bool | false | Non-retain | False | False | | False | | Direct result of the program- med supervision |
| SNO | Int | 2 | Non-retain | False | False | False | False | | User step number |
| Т | Time | T#0ms | Non-retain | False | False | False | False | | Total step activation time |
| U | Time | T#0ms | Non-retain | False | False | | False | | Step activation time without disturbance |
| T_MAX | Time | T#10S | Non-retain | False | False | False | False | | Maximal step activation time |
| T_WARN | Time | T#7S | Non-retain | False | False | False | False | | Warning time |
| CRIT_LOC | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 32 LAD/FBD elements in the in- terlock in the current pro- |
| CRIT_LOC_ERR | DWord | 16#0 | Non-retain | False | False | False | False | | cessing cycle Copy of CRIT_LOC when the interlock leaves the state |
| SM | Bool | false | Non-retain | False | False | False | False | | System-internal |
| H_IL_ERR | Byte | 16#0 | Non-retain | False | False | | False | | System-internal |
| H_SV_FLT | Byte | 16#04 | Non-retain | False | _ | | False | | System-internal |
| ▼ Step2 | G7_StepPlus_V5 | | Non-retain | False | False | | False | | Step structure |
| <u> </u> | | | | | | | | | ' |
| S1 | Bool | false | Non-retain | False | False | | False | | Step is activated |
| L1 | Bool | false | Non-retain | False | False | | False | | interlock leaving state |
| V1 | Bool | false | Non-retain | False | False | | False | | Supervision entering state |
| R1 | Bool | false | Non-retain | False | False | | False | | Reserved |
| A1 | Bool | false | Non-retain | False | False | | False | | Error is acknowledged |
| S0 | Bool | false | Non-retain | False | False | _ | False | | Step is deactivated |
| LO | Bool | false | Non-retain | False | False | | False | | Interlock entering state |
| VO | Bool | false | Non-retain | False | False | | False | | Supervision leaving state |
| X | Bool | false | Non-retain | False | | | False | | Step is active |
| LA | Bool | false | Non-retain | False | False | | False | | Interlock is not satisfied |
| VA | Bool | false | Non-retain | False | False | | False | | Supervision active |
| RA | Bool | false | Non-retain | False | False | | False | | Reserved |
| AA | Bool | false | Non-retain | False | False | | False | | Reserved |
| SS | Bool | false | Non-retain | False | False | | False | | System-internal |
| LS | | true | Non-retain | False | False | | False False | | Direct result of the program- med interlock Direct result of the program- |
| VS SNO | Bool | 3 | Non-retain Non-retain | False False | False False | | False | | med supervision User step number |
| T | Time | T#0ms | Non-retain | False | | | False | | Total step activation time |
| U | Time | T#0ms | Non-retain | False | | | False | | Step activation time without disturbance |
| T_MAX | Time | T#10S | Non-retain | False | False | False | False | | Maximal step activation time |
| T_WARN | Time | T#7S | Non-retain | False | False | | False | | Warning time |
| CRIT_LOC | DWord | 16#0 | Non-retain | False | | | False | | Status of the maximum 32 LAD/FBD elements in the in- terlock in the current pro- cessing cycle |
| CRIT_LOC_ERR | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT_LOC when the interlock leaves the state |
| SM | Bool | false | Non-retain | False | False | False | False | | System-internal |
| H_IL_ERR | Byte | 16#0 | Non-retain | False | False | False | False | | System-internal |
| H_SV_FLT | Byte | 16#04 | Non-retain | False | False | False | False | | System-internal |
| ▼ ODRZAVANJE_KLIP_4_LEVO | - | | Non-retain | False | | | False | | Step structure |
| S1 | Bool | false | Non-retain | False | | | False | | Step is activated |
| L1 | Bool | false | Non-retain | False | | | False | | interlock leaving state |
| V1 | Bool | false | Non-retain | False | | | False | | Supervision entering state |
| R1 | Bool | false | Non-retain | False | | | False | | Reserved |
| A1 | Bool | false | Non-retain | False | | | False | | Error is acknowledged |
| SO | Bool | false | Non-retain | False | False | | False | | Step is deactivated |
| LO | Bool | false | Non-retain | False | | | False | | Interlock entering state |
| V0 | Bool | false | Non-retain | False | | | False | | Supervision leaving state |
| V | Bool | false | Non-retain | False | | | False | | Step is active |
| X | | IC I | INT and the transfer | I = 1 | Falce | False | False | 1 | Interlock is not satisfied |
| LA | Bool | false | Non-retain | False | | | | | |
| LA VA | Bool | false | Non-retain | False | False | False | False | | Supervision active |
| LA | | | | | False False | False | | | |

| Totally Integrated Automation Portal | |
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| Name | Data type | Default value | Retain | Accessible from HMI/OPC UA | able | Visible in HMI engi- neering | Setpoint | Supervi- sion | Comment |
|------------------------|-----------------------|----------------|--------------------------|-------------------------------------|-------|------------------------------------|----------------|------------------|--|
| SS | Bool | false | Non-retain | False | False | False | False | | System-internal |
| LS | Bool | true | Non-retain | False | False | False | False | | Direct result of the program |
| VS | Bool | false | Non-retain | False | False | False | False | | med interlock Direct result of the program med supervision |
| SNO | Int | 4 | Non-retain | False | False | False | False | | User step number |
| T | Time | T#0ms | Non-retain | False | False | False | False | | Total step activation time |
| U | Time | T#0ms | Non-retain | False | False | False | False | | Step activation time withou disturbance |
| T_MAX | Time | T#10S | Non-retain | False | False | False | False | | Maximal step activation tim |
| T_WARN | Time | T#7S | Non-retain | False | False | False | False | | Warning time |
| CRIT_LOC | DWord | 16#0 | Non-retain | False | | False | False | | Status of the maximum 32 LAD/FBD elements in the in terlock in the current pro- cessing cycle |
| CRIT_LOC_ERR | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT_LOC when the interlock leaves the state |
| SM | Bool | false | Non-retain | False | False | False | False | | System-internal |
| H_IL_ERR | Byte | 16#0 | Non-retain | False | | False | False | | System-internal |
| H_SV_FLT | Byte | 16#04 | Non-retain | False | False | False | False | | System-internal |
| ▼ ODRZAVANJE_KLIP_4_DE | - | ' 5 | Non-retain | False | False | False | False | | Step structure |
| S1 | Bool | false | Non-retain | False | False | False | False | | Step is activated |
| L1 | Bool | false | Non-retain | False | False | | False | | interlock leaving state |
| V1 | Bool | false | Non-retain | False | False | | False | | Supervision entering state |
| R1 | Bool | false | Non-retain | False | False | False | False | | Reserved |
| A1 | Bool | false | Non-retain | False | False | False | False | | Error is acknowledged |
| S0 | Bool | false | Non-retain | False | False | False | False | | Step is deactivated |
| LO | Bool | false | Non-retain | False | False | False | False | | Interlock entering state |
| V0 | Bool | false | Non-retain | False | False | False | False | | Supervision leaving state |
| Χ | Bool | false | Non-retain | False | False | | False | | Step is active |
| LA | Bool | false | Non-retain | False | False | | False | | Interlock is not satisfied |
| VA | Bool | false | Non-retain | False | | False | False | | Supervision active |
| RA | Bool | false | Non-retain | False | False | | False | | Reserved |
| AA | Bool | false | Non-retain | False | False | | False | | Reserved |
| SS | Bool | false | Non-retain | False | False | | False | | System-internal |
| LS | Bool | true | Non-retain | False | | False | False | | Direct result of the programmed interlock |
| VS SNO | Bool | false 5 | Non-retain Non-retain | False False | False | False | False False | | Direct result of the program med supervision User step number |
| T | Time | T#0ms | Non-retain | False | False | | False | | Total step activation time |
| U | Time | T#0ms | Non-retain | False | | False | False | | Step activation time withou disturbance |
| T_MAX | Time | T#10S | Non-retain | False | False | False | False | | Maximal step activation tim |
| T_WARN | Time | T#7S | Non-retain | False | False | False | False | | Warning time |
| CRIT_LOC | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 32 LAD/FBD elements in the in terlock in the current pro- cessing cycle |
| CRIT_LOC_ERR | DWord | 16#0 | Non-retain | False | False | | False | | Copy of CRIT_LOC when the interlock leaves the state |
| SM | Bool | false | Non-retain | False | | False | False | | System-internal |
| H_IL_ERR | Byte | 16#0 16#04 | Non-retain Non-retain | False False | | False False | False False | | System-internal |
| H_SV_FLT ▼ Step3 | Byte G7_StepPlus_\ | | Non-retain | False | | False | False | | System-internal Step structure |
| · | · | | | | | | | | · · |
| S1 | Bool | false false | Non-retain | False | False | | False False | | Step is activated |
| L1 V1 | Bool Bool | false | Non-retain Non-retain | False False | False | False | False | | interlock leaving state Supervision entering state |
| R1 | Bool | false | Non-retain | False | | False | False | | Reserved |
| A1 | Bool | false | Non-retain | False | False | | False | | Error is acknowledged |
| SO | Bool | false | Non-retain | False | False | | False | | Step is deactivated |
| LO | Bool | false | Non-retain | False | | False | False | | Interlock entering state |
| V0 | Bool | false | Non-retain | False | False | | False | | Supervision leaving state |
| X | Bool | false | Non-retain | False | False | | False | | Step is active |
| LA | Bool | false | Non-retain | False | False | False | False | | Interlock is not satisfied |
| VA | Bool | false | Non-retain | False | False | False | False | | Supervision active |
| RA | Bool | false | Non-retain | False | | False | False | | Reserved |
| AA | Bool | false | Non-retain | False | | False | False | | Reserved |
| SS | Bool | false | Non-retain | False | | False | False | | System-internal |
| LS | Bool | true | Non-retain | False | | False | False | | Direct result of the programmed interlock |
| VS | Bool | false | Non-retain | False | | False | False | | Direct result of the programmed supervision |
| SNO | Int | 6 | Non-retain | False | | False | False | | User step number |
| T U | Time Time | T#0ms T#0ms | Non-retain Non-retain | False False | | False False | False False | | Total step activation time Step activation time without |
| | 1 | | | | | I | | | disturbance |
| T_MAX | Time | T#10S | Non-retain | False | F. | False | False | | Maximal step activation tim |

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| ne | Data type | Default value | Retain | Accessible from HMI/OPC UA | able | Visible in HMI engi- neering | Setpoint | Supervi- sion | Comment |
|--------------------|--------------------|----------------|--------------------------|-------------------------------------|----------------|------------------------------------|----------------|------------------|--|
| T_WARN | Time | T#7S | Non-retain | False | False | False | False | | Warning time |
| CRIT_LOC | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 3 LAD/FBD elements in the terlock in the current pro- cessing cycle |
| CRIT_LOC_E | RR DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT_LOC when t interlock leaves the state |
| SM | Bool | false | Non-retain | False | False | | False | | System-internal |
| H_IL_ERR | Byte | 16#0 | Non-retain | False | False | | False | | System-internal |
| H_SV_FLT | Byte G7_StepPlu | 16#04 | Non-retain Non-retain | False False | False False | | False False | | System-internal Step structure |
| ▼ Step4 | | _ | | | | | | | <u>'</u> |
| S1 L1 | Bool Bool | false false | Non-retain Non-retain | False False | False False | | False False | | Step is activated interlock leaving state |
| V1 | Bool | false | Non-retain | False | False | | False | | Supervision entering state |
| R1 | Bool | false | Non-retain | False | False | | False | | Reserved |
| A1 | Bool | false | Non-retain | False | False | False | False | | Error is acknowledged |
| S0 | Bool | false | Non-retain | False | False | False | False | | Step is deactivated |
| LO | Bool | false | Non-retain | False | False | | False | | Interlock entering state |
| V0 | Bool | false | Non-retain | False | False | | False | | Supervision leaving state |
| X | Bool | false | Non-retain | False | False | | False | | Step is active |
| LA VA | Bool Bool | false false | Non-retain Non-retain | False False | False False | | False False | | Interlock is not satisfied Supervision active |
| RA | Bool | false | Non-retain | False | False | | False | | Reserved |
| AA | Bool | false | Non-retain | False | False | | False | | Reserved |
| SS | Bool | false | Non-retain | False | False | False | False | | System-internal |
| LS | Bool | true | Non-retain | False | False | False | False | | Direct result of the progr med interlock |
| VS | Bool | false | Non-retain | False | False | | False | | Direct result of the progr med supervision |
| SNO | Int | 7 | Non-retain Non-retain | False | False | | False False | | User step number |
| U | Time Time | T#0ms T#0ms | Non-retain | False False | False False | | False | | Total step activation time Step activation time with disturbance |
| T_MAX | Time | T#10S | Non-retain | False | False | False | False | | Maximal step activation |
| T_WARN | Time | T#7S | Non-retain | False | False | | False | | Warning time |
| CRIT_LOC | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 3 LAD/FBD elements in the terlock in the current pro |
| CRIT_LOC_E | RR DWord | 16#0 | Non-retain | False | False | False | False | | cessing cycle Copy of CRIT_LOC when interlock leaves the state |
| SM | Bool | false | Non-retain | False | False | False | False | | System-internal |
| H_IL_ERR | Byte | 16#0 | Non-retain | False | False | False | False | | System-internal |
| H_SV_FLT | Byte | 16#04 | Non-retain | False | False | | False | | System-internal |
| ▼ Step5 | G7_StepPlu | | Non-retain | False | False | | False | | Step structure |
| S1 | Bool | false | Non-retain | False | False | | False | | Step is activated |
| L1 | Bool | false | Non-retain | False | False | | False | | interlock leaving state |
| V1 R1 | Bool Bool | false false | Non-retain Non-retain | False False | False False | | False False | | Supervision entering star Reserved |
| A1 | Bool | false | Non-retain | False | False | | False | | Error is acknowledged |
| S0 | Bool | false | Non-retain | False | False | | False | | Step is deactivated |
| LO | Bool | false | Non-retain | False | False | | False | | Interlock entering state |
| VO | Bool | false | Non-retain | False | False | False | False | | Supervision leaving state |
| Χ | Bool | false | Non-retain | False | False | False | False | | Step is active |
| LA | Bool | false | Non-retain | False | False | _ | False | | Interlock is not satisfied |
| VA | Bool | false | Non-retain | False | False | | False | | Supervision active |
| RA | Bool Bool | false false | Non-retain Non-retain | False False | False False | | False False | | Reserved Reserved |
| AA SS | Bool | false | Non-retain | False | False | | False | | System-internal |
| LS | Bool | true | Non-retain | False | False | | False | | Direct result of the progr med interlock |
| VS | Bool | false | Non-retain | False | False | False | False | | Direct result of the progr med supervision |
| SNO | Int | 8 | Non-retain | False | False | | False | | User step number |
| Т | Time | T#0ms | Non-retain | False | False | | False | | Total step activation time |
| U | Time | T#0ms | Non-retain | False | False | False | False | | Step activation time with disturbance |
| T_MAX | Time | T#10S | Non-retain | False | False | | False | | Maximal step activation |
| T_WARN CRIT_LOC | Time DWord | T#7S 16#0 | Non-retain Non-retain | False False | False False | | False False | | Warning time Status of the maximum 3 LAD/FBD elements in the terlock in the current processing cycle |
| CRIT_LOC_E | | 16#0 | Non-retain | False | False | | False | | Copy of CRIT_LOC when interlock leaves the state |
| SM | Bool | false | Non-retain | False | False | | False | | System-internal |
| H_IL_ERR | Byte | 16#0 | Non-retain | False | False | | False | | System-internal |
| H_SV_FLT | Byte | 16#04 | Non-retain | False | False | raise | False | | System-internal |

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| ame | Data type | Default value | Retain | Accessible from HMI/OPC UA | able | Visible in HMI engi- neering | Setpoint | Supervi- sion | Comment |
|-----------------------------|----------------------|----------------------------------|--|-------------------------------------|-------------------------|------------------------------------|-------------------------|------------------|--|
| ▼ ODRZAVANJE_KLIP_1_DESNO | G7_StepPlus_V5 | | Non-retain | False | False | False | False | | Step structure |
| S1 | Bool | false | Non-retain | False | False | False | False | | Step is activated |
| L1 | Bool | false | Non-retain | False | | | False | | interlock leaving state |
| V1 | Bool | false | Non-retain | False | False | | False | | Supervision entering state |
| R1 | Bool | false | Non-retain | False | False | | False | | Reserved |
| | | | | | | | | | |
| A1 | Bool | false | Non-retain | False | False | | False | | Error is acknowledged |
| SO | Bool | false | Non-retain | False | False | | False | | Step is deactivated |
| LO | Bool | false | Non-retain | False | False | | False | | Interlock entering state |
| V0 | Bool | false | Non-retain | False | False | | False | | Supervision leaving state |
| X | Bool | false | Non-retain | False | | | False | | Step is active |
| LA | Bool | false | Non-retain | False | False | False | False | | Interlock is not satisfied |
| VA | Bool | false | Non-retain | False | False | False | False | | Supervision active |
| RA | Bool | false | Non-retain | False | False | False | False | | Reserved |
| AA | Bool | false | Non-retain | False | False | False | False | | Reserved |
| SS | Bool | false | Non-retain | False | False | False | False | | System-internal |
| LS | Bool | true | Non-retain | False | False | False | False | | Direct result of the progra |
| VS | Bool | false | Non-retain | False | | | False | | med interlock Direct result of the progra |
| | | | | | | | | | med supervision |
| SNO | Int | 9 | Non-retain | False | | | False | | User step number |
| Т | Time | T#0ms | Non-retain | False | False | | False | | Total step activation time |
| U | Time | T#0ms | Non-retain | False | False | False | False | | Step activation time with |
| | | | | | | | | | disturbance |
| T_MAX | Time | T#10S | Non-retain | False | False | | False | | Maximal step activation ti |
| T_WARN | Time | T#7S | Non-retain | False | | | False | | Warning time |
| CRIT_LOC | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 3 LAD/FBD elements in the terlock in the current pro- cessing cycle |
| CRIT_LOC_ERR | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT_LOC when the interlock leaves the state |
| SM | Bool | false | Non-retain | False | False | False | False | | System-internal |
| H_IL_ERR | Byte | 16#0 | Non-retain | False | False | False | False | | System-internal |
| H_SV_FLT | Byte | 16#04 | Non-retain | False | False | | False | | System-internal |
| | G7_StepPlus_V5 | | Non-retain | False | False | | False | | Step structure |
| | · | | | | | | | | · |
| S1 | Bool | false | Non-retain | False | False | | False | | Step is activated |
| L1 | Bool | false | Non-retain | False | False | False | False | | interlock leaving state |
| V1 | Bool | false | Non-retain | False | False | False | False | | Supervision entering state |
| R1 | Bool | false | Non-retain | False | False | False | False | | Reserved |
| A1 | Bool | false | Non-retain | False | False | False | False | | Error is acknowledged |
| S0 | Bool | false | Non-retain | False | False | False | False | | Step is deactivated |
| LO | Bool | false | Non-retain | False | False | False | False | | Interlock entering state |
| V0 | Bool | false | Non-retain | False | | | False | | Supervision leaving state |
| X | Bool | false | Non-retain | False | False | | False | | Step is active |
| | | | | | | | False | | Interlock is not satisfied |
| LA | Bool | false | Non-retain | False | False | | | | |
| VA | Bool | false | Non-retain | False | False | | False | | Supervision active |
| RA | Bool | false | Non-retain | False | False | | False | | Reserved |
| AA | Bool | false | Non-retain | False | False | | False | | Reserved |
| SS | Bool | false | Non-retain | False | False | | False | | System-internal |
| LS | Bool | true | Non-retain | False | False | False | False | | Direct result of the programed interlock |
| VS | Bool | false | Non-retain | False | False | False | False | | Direct result of the programed supervision |
| SNO | Int | 10 | Non-retain | False | False | False | False | | User step number |
| T | Time | T#0ms | Non-retain | False | False | | False | | Total step activation time |
| <u>'</u> | | | | | | | | | · |
| U | Time | T#0ms | Non-retain | False | False | | False | | Step activation time with disturbance |
| T_MAX | Time | T#10S | Non-retain | False | False | | False | | Maximal step activation t |
| T_WARN | Time | T#7S | Non-retain | False | False | False | False | | Warning time |
| CRIT_LOC | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 3 LAD/FBD elements in the terlock in the current pro- cessing cycle |
| CRIT_LOC_ERR | DWord | 16#0 | Non-retain | False | False | | False | | Copy of CRIT_LOC when t interlock leaves the state |
| SM | Bool | false | Non-retain | False | False | False | False | | System-internal |
| H_IL_ERR | Byte | 16#0 | Non-retain | False | False | False | False | | System-internal |
| | Byte | 16#04 | Non-retain | False | False | False | False | | System-internal |
| H_SV_FLT | G7_StepPlus_V5 | | Non-retain | False | False | | False | | Step structure |
| H_SV_FLT ▼ Step6 | ' | | Non-retain | False | | | False | | Step is activated |
| ▼ Step6 | Rool | | INOH-TELAITI | raise | | | False | | • |
| ▼ Step6 | Bool | false | | E a La | I Palco | F3100 | L-21C0 | | interlock leaving state |
| ▼ Step6 S1 L1 | Bool | false | Non-retain | False | | | | | |
| ▼ Step6 S1 L1 V1 | Bool Bool | false false | Non-retain Non-retain | False | False | False | False | | Supervision entering stat |
| ▼ Step6 S1 L1 | Bool | false | Non-retain | | False False | False False | | | |
| ▼ Step6 S1 L1 V1 | Bool Bool | false false | Non-retain Non-retain | False | False False | False False | False | | Supervision entering stat |
| ▼ Step6 S1 L1 V1 R1 | Bool Bool | false false false | Non-retain Non-retain Non-retain | False False | False False | False False False | False False | | Supervision entering stat Reserved |
| ▼ Step6 S1 L1 V1 R1 A1 | Bool Bool Bool | false false false false | Non-retain Non-retain Non-retain Non-retain | False False | False False False | False False False False | False False False | | Supervision entering stat Reserved Error is acknowledged |

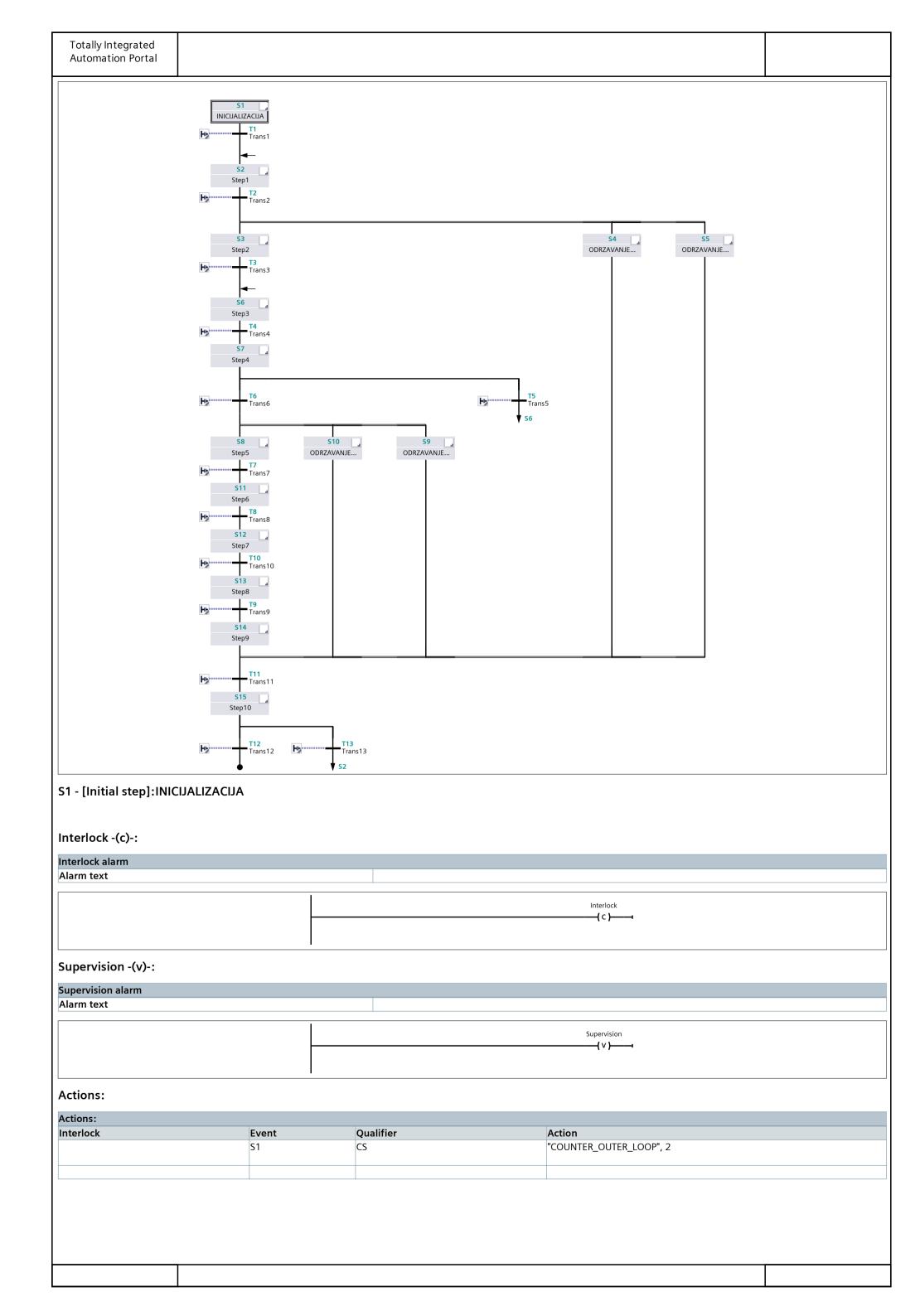
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| lame | Data type | Default value | Retain | Accessible from HMI/OPC UA | able | Visible in HMI engi- neering | Setpoint | Supervi- sion | Comment |
|----------------------|---------------|----------------|--------------------------|-------------------------------------|----------------|------------------------------------|----------------|------------------|--|
| X | Bool | false | Non-retain | False | False | | False | | Step is active |
| LA | Bool | false | Non-retain | False | False | | False | | Interlock is not satisfied |
| VA | Bool | false | Non-retain | False | False | | False | | Supervision active |
| RA | Bool | false | Non-retain | False | False | | False | | Reserved |
| AA | Bool | false | Non-retain | False | False | | False | | Reserved |
| SS | Bool | false | Non-retain | False | False | | False | | System-internal |
| LS | Bool | true | Non-retain | False | False | | False | | Direct result of the programmed interlock |
| VS | Bool | false | Non-retain | False | False | | False | | Direct result of the programmed supervision |
| SNO T | Int Time | 11 T#0ms | Non-retain Non-retain | False False | False False | | False False | | User step number Total step activation time |
| U | Time | T#0ms | Non-retain | False | False | False | False | | Step activation time witho |
| T_MAX | Time | T#10S | Non-retain | False | False | False | False | | Maximal step activation tir |
| T_WARN | Time | T#7S | Non-retain | False | False | False | False | | Warning time |
| CRIT_LOC | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum 32 LAD/FBD elements in the ir terlock in the current pro- cessing cycle |
| CRIT_LOC_ERR | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT_LOC when th |
| CNA | Dool | false | Non ratein | Enlac | False | Ealco | False | | interlock leaves the state |
| SM H II ERR | Bool Byte | 16#0 | Non-retain Non-retain | False False | False | | False False | | System-internal System-internal |
| H_IL_ERR H_SV_FLT | Byte Byte | 16#04 | Non-retain Non-retain | False | False | | False False | | System-internal System-internal |
| ▼ Step7 | G7_StepPlus_\ | | Non-retain | False | False | | False | | Step structure |
| <u> </u> | - | | | | False | | False | | · |
| S1 | Bool | false | Non-retain | False | False | | | | Step is activated interlock leaving state |
| L1 | Bool | false false | Non-retain | False False | False | | False False | | Supervision entering state |
| V1 R1 | Bool Bool | false | Non-retain Non-retain | False | False | | False | | Reserved |
| A1 | Bool | false | Non-retain | False | False | | False | | Error is acknowledged |
| SO SO | Bool | false | Non-retain | False | False | | False | | Step is deactivated |
| L0 | Bool | false | Non-retain | False | False | | False | | Interlock entering state |
| VO | Bool | false | Non-retain | False | False | | False | | Supervision leaving state |
| X | Bool | false | Non-retain | False | False | | False | | Step is active |
| LA | Bool | false | Non-retain | False | False | | False | | Interlock is not satisfied |
| VA | Bool | false | Non-retain | False | False | False | False | | Supervision active |
| RA | Bool | false | Non-retain | False | False | False | False | | Reserved |
| AA | Bool | false | Non-retain | False | False | False | False | | Reserved |
| SS | Bool | false | Non-retain | False | False | False | False | | System-internal |
| LS | Bool | true | Non-retain | False | False | | False | | Direct result of the programmed interlock |
| VS | Bool | false | Non-retain | False | False | | False | | Direct result of the programmed supervision |
| SNO | Int | 12 | Non-retain | False | False | | False | | User step number |
| I | Time | T#0ms | Non-retain | False | False | | False | | Total step activation time |
| U | Time | T#0ms | Non-retain | False | False | | False | | Step activation time witho disturbance |
| T_MAX | Time | T#10S | Non-retain | False | False | | False | | Maximal step activation tir |
| T_WARN | Time | T#7S | Non-retain | False | False | | False | | Warning time |
| CRIT_LOC | DWord | 16#0 | Non-retain | False | False | Faise | False | | Status of the maximum 32 LAD/FBD elements in the interlock in the current pro- cessing cycle |
| CRIT_LOC_ERR | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT_LOC when the interlock leaves the state |
| SM | Bool | false | Non-retain | False | False | | False | | System-internal |
| H_IL_ERR | Byte | 16#0 | Non-retain | False | False | | False | | System-internal |
| H_SV_FLT | Byte | 16#04 | Non-retain | False | False | | False | | System-internal |
| ▼ Step8 | G7_StepPlus_\ | /5 | Non-retain | False | False | False | False | | Step structure |
| S1 | Bool | false | Non-retain | False | False | | False | | Step is activated |
| L1 | Bool | false | Non-retain | False | False | | False | | interlock leaving state |
| V1 | Bool | false | Non-retain | False | False | | False | | Supervision entering state |
| R1 | Bool | false | Non-retain | False | False | | False | | Reserved |
| A1 | Bool | false | Non-retain | False | False | | False | | Error is acknowledged |
| S0 | Bool | false | Non-retain | False | False | | False | | Step is deactivated |
| LO | Bool | false | Non-retain | False | | | False | | Interlock entering state |
| V0 | Bool | false | Non-retain | False | | | False | | Supervision leaving state |
| X | Bool | false | Non-retain | False | | | False | | Step is active |
| LA VA | Bool | false | Non-retain | False | | | False | | Interlock is not satisfied |
| VA | Bool | false false | Non-retain Non-retain | False False | | | False False | | Supervision active Reserved |
| RA A A | Bool Bool | false | Non-retain Non-retain | False | False | | False | | Reserved |
| AA | Bool | false | Non-retain Non-retain | False | | | False False | | System-internal |
| cc | DUUI | IUISC | INOTIFIELAIII | li aise | ı aıse | ו מוזכ | ו מוזכ | | System-miternal |
| SS LS | Bool | true | Non-retain | False | False | False | False | | Direct result of the progra med interlock |

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| ne | Data type | Default value | Retain | Accessible from HMI/OPC UA | able | Visible in HMI engi- neering | Setpoint | Supervi- sion | Comment |
|--------------|---------------|---------------|--------------------------|-------------------------------------|----------------|------------------------------------|----------------|------------------|---|
| SNO | Int | 13 | Non-retain | False | False | False | False | | User step number |
| T | Time | T#0ms | Non-retain | False | False | False | False | | Total step activation tin |
| U | Time | T#0ms | Non-retain | False | False | False | False | | Step activation time wi |
| | | | | | | | | | disturbance |
| T_MAX | Time | T#10S | Non-retain | False | False | False | False | | Maximal step activation |
| T_WARN | Time | T#7S | Non-retain | False | False | False | False | | Warning time |
| CRIT_LOC | DWord | 16#0 | Non-retain | False | False | False | False | | Status of the maximum LAD/FBD elements in the terlock in the current processing cycle |
| CRIT_LOC_ERR | DWord | 16#0 | Non-retain | False | False | | False | | Copy of CRIT_LOC when interlock leaves the sta |
| SM | Bool | false | Non-retain | False | False | | False | | System-internal |
| H_IL_ERR | Byte | 16#0 | Non-retain | False | False | | False | | System-internal |
| H_SV_FLT | Byte | 16#04 | Non-retain | False | False | False | False | | System-internal |
| ▼ Step9 | G7_StepPlus_\ | /5 | Non-retain | False | False | False | False | | Step structure |
| S1 | Bool | false | Non-retain | False | False | False | False | | Step is activated |
| | Bool | false | Non-retain | False | False | | False | | interlock leaving state |
| L1 | | | | | | | | | |
| V1 | Bool | false | Non-retain | False | False | | False | | Supervision entering st |
| R1 | Bool | false | Non-retain | False | False | | False | | Reserved |
| A1 | Bool | false | Non-retain | False | False | | False | | Error is acknowledged |
| S0 | Bool | false | Non-retain | False | False | | False | | Step is deactivated |
| LO | Bool | false | Non-retain | False | False | | False | | Interlock entering state |
| V0 | Bool | false | Non-retain | False | False | False | False | | Supervision leaving sta |
| Χ | Bool | false | Non-retain | False | False | False | False | | Step is active |
| LA | Bool | false | Non-retain | False | False | False | False | | Interlock is not satisfied |
| VA | Bool | false | Non-retain | False | False | | False | | Supervision active |
| RA | Bool | false | Non-retain | False | False | | False | | Reserved |
| AA | Bool | false | Non-retain | False | False | | False | | Reserved |
| | Bool | false | Non-retain | False | False | | False | | System-internal |
| SS | | | | | | | | | • |
| LS VS | Bool | true false | Non-retain Non-retain | False False | False False | | False False | | Direct result of the pro- med interlock Direct result of the pro- |
| SNO | Int | 14 | Non-retain | False | False | | False | | med supervision User step number |
| T | Time | T#0ms | Non-retain | False | False | | False | | Total step activation tir |
| U | Time | T#0ms | Non-retain | False | False | | False | | Step activation time wi |
| U | Tillle | 1#01115 | Non-retain | raise | гаізе | raise | raise | | disturbance |
| T_MAX | Time | T#10S | Non-retain | False | False | False | False | | Maximal step activation |
| T_WARN | Time | T#7S | Non-retain | False | False | | False | | Warning time |
| CRIT_LOC | DWord | 16#0 | Non-retain | False | False | | False | | Status of the maximum LAD/FBD elements in the terlock in the current pressing cycle |
| CRIT_LOC_ERR | DWord | 16#0 | Non-retain | False | False | | False | | Copy of CRIT_LOC when interlock leaves the sta |
| SM | Bool | false | Non-retain | False | False | | False | | System-internal |
| H_IL_ERR | Byte | 16#0 | Non-retain | False | False | False | False | | System-internal |
| H_SV_FLT | Byte | 16#04 | Non-retain | False | False | False | False | | System-internal |
| Step10 | G7_StepPlus_\ | /5 | Non-retain | False | False | False | False | | Step structure |
| | Bool | false | Non-retain | False | False | Falco | False | | Step is activated |
| S1 | Bool | false | Non-retain Non-retain | False | False | | False | | interlock leaving state |
| L1 | | | | | | | | | |
| V1 | Bool | false | Non-retain | False | False | | False | | Supervision entering st |
| R1 | Bool | false | Non-retain | False | False | | False | | Reserved |
| A1 | Bool | false | Non-retain | False | False | | False | | Error is acknowledged |
| S0 | Bool | false | Non-retain | False | False | | False | | Step is deactivated |
| L0 | Bool | false | Non-retain | False | False | | False | | Interlock entering state |
| V0 | Bool | false | Non-retain | False | False | False | False | | Supervision leaving sta |
| Χ | Bool | false | Non-retain | False | False | False | False | | Step is active |
| LA | Bool | false | Non-retain | False | False | False | False | | Interlock is not satisfied |
| VA | Bool | false | Non-retain | False | False | False | False | | Supervision active |
| RA | Bool | false | Non-retain | False | False | | False | | Reserved |
| AA | Bool | false | Non-retain | False | False | | False | | Reserved |
| SS | Bool | false | Non-retain | False | False | | False | | System-internal |
| LS | Bool | true | Non-retain | False | False | | False | | Direct result of the pro- |
| VS | Bool | false | Non-retain | False | False | False | False | | Direct result of the prog |
| SNO | Int | 15 | Non-retain | False | False | False | False | | User step number |
| T | Time | T#0ms | Non-retain | False | False | | False | | Total step activation tir |
| U | Time | T#0ms | Non-retain | False | | False | False | | Step activation time wi |
| T_MAX | Time | T#10S | Non-retain | False | False | False | False | | Maximal step activation |
| T_WARN | Time | T#7S | Non-retain | False | False | | False | | Warning time |
| CRIT_LOC | DWord | 16#0 | Non-retain | False | | False | False | | Status of the maximum LAD/FBD elements in the terlock in the current p |

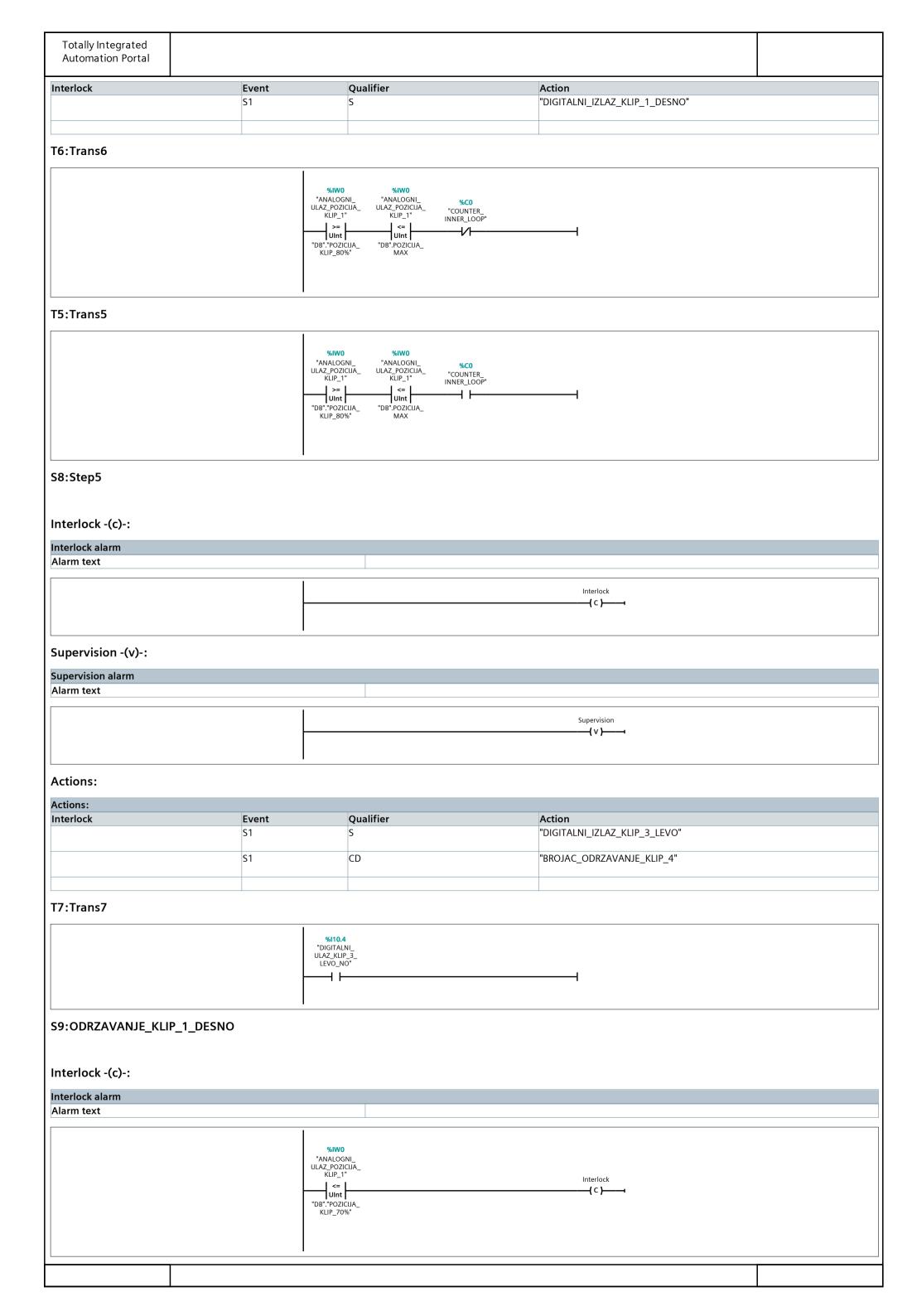
| ame | Data type | Default value | Retain | Accessible from HMI/OPC UA | able | Visible in HMI engi- neering | Setpoint | Supervi- sion | Comment |
|---|-----------|---------------|------------------|-------------------------------------|--------|------------------------------------|----------|------------------|---|
| CRIT_LOC_ERR | DWord | 16#0 | Non-retain | False | False | False | False | | Copy of CRIT_LOC when the |
| SM | Bool | false | Non-retain | False | False | False | False | | interlock leaves the state System-internal |
| H_IL_ERR | Byte | 16#0 | Non-retain | | False | | False | | System-internal |
| H_SV_FLT Temp | Byte | 16#04 | Non-retain | False | False | False | False | | System-internal |
| Constant | | | | | | | | | |
| arms | | | | | | | | | |
| able alarms | | True | | | | | | | |
| tegory | Category | enabler | | | | splay class | 5 | | |
| or arning | | | | | 0 | | | | |
| tegory 4 | | | | | 0 | | | | |
| tegory 5 | | | | | 0 | | | | |
| tegory 6 tegory 7 | | | | | 0 | | | | |
| tegory 8 | | | | | 0 | | | | |
| ntegory for interlocks and su- ervisions | Error | | | Category for GRAPH w | arning | s: Warr | ning | | |
| rmbol | Address | ר | ⁻ уре | Com | ment | | | | |
| ermanent pre-instructions | 5 | | | | | | | | |
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| quences (1) | | | | | | | | | |



| Totally Integrated Automation Portal | | | | |
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| T1:Trans1 | | | | |
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| S3:Step2 | | | | |
| Interlock -(c)-: | | | | |
| Interlock alarm | | | | |
| Alarm text | | | | |
| | _ | | Interlock C }—— | |
| | | | | |
| Supervision -(v)-: | | | | |
| Supervision alarm Alarm text | | | | |
| | | | Supervision | |
| | | | (∨) | |
| | l | | | |
| Actions: | | | | |
| Actions: Interlock | Event | Qualifier | Action | |
| | S1 | S | "DIGITALNI_IZLAZ_KLIP_2_LEVO" | |
| | S1 | R | "DIGITALNI_IZLAZ_KLIP_2_DESNO" | |
| | S1 | CD | "BROJAC_ODRZAVANJE_KLIP_4" | |
| T3:Trans3 | | | | |
| | | %I10.2 "DIGITALNI_ | | |
| | | ULAZ_KLIP_2_ LEVO_NO" | | |
| | | 11 | ' | |
| S5:ODRZAVANJE_KLIP_4_0 | DESNO | | | |
| | | | | |
| Interlock -(c)-: | | | | |
| Interlock alarm Alarm text | | | | |
| | | | | |
| | | %IW2 "ANALOGNI_ ULAZ_POZICIJA_ KLIP_4" | | |
| | - | <= UInt | Interlock ← C → ← | |
| | | "DB","POZICIJA_ KLIP_30%" | | |
| | | | | |
| Supervision -(v)-: | | | | |
| Supervision alarm | | | | |
| Alarm text | | | | |
| | _ | | Supervision (V) | |
| | | | | |
| Actions: | | | | |
| Actions: Interlock | Event | Qualifier | Action | |
| C | | R | "DIGITALNI_IZLAZ_KLIP_4_LEVO" | |
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| Totally Integrated Automation Portal | | | | | |
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| T11:Trans11 | | | | | |
| | | %I10.7 "DIGITALNI_ ULAZ_KLIP_4_ DESNO_NO" | | I | |
| S4:ODRZAVANJE_KLIF | P_4_LEVO | | | | |
| Interlock -(c)-: | | | | | |
| Interlock alarm Alarm text | | | | | |
| | | ' | | | |
| | - | ### ### ############################## | %C2 "BROJAC_ ODRZAVANJE_ KLIP_4" | Interlock (C) | |
| Supervision -(v)-: | | | | | |
| Supervision alarm | | | | | |
| Alarm text | | | | | |
| | | | | Supervision (V) | |
| Actions: | | | | | |
| Actions: | | - 1151 | | | |
| Interlock C | Event | Qualifier S | | Action "DIGITALNI_IZLAZ_KLIP_4_LEVO" | |
| T11:Trans11 | | | | | |
| | | %I10.7 "DIGITALNI_ ULAZ_KLIP_4_ DESNO_NO" | | | |
| S2:Step1 | | | | | |
| Interlock -(c)-: | | | | | |
| Interlock alarm Alarm text | | | | | |
| | | | | Interlock | |
| Supervision -(v)-: | | | | | |
| Supervision alarm Alarm text | | | | | |
| | | , | | Supervision V | |
| Actions: | | | | | |
| Actions: Interlock | Event | Qualifier | | Action | |
| milenock | S1 | Qualifier S | | "DIGITALNI_IZLAZ_KLIP_4_LEVO" | |
| | S1 | CD | | "COUNTER_OUTER_LOOP" | |
| | S1 | CS | | "COUNTER_INNER_LOOP", 2 | |
| | S1 | CS | | "BROJAC_ODRZAVANJE_KLIP_4", 6 | |
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| Totally Integrated Automation Portal | | | | |
|---|----------|--|--------------------------------|--|
| nterlock | Event | Qualifier | Action | |
| Γ2:Trans2 | | | | |
| | | %IW2 | | |
| | | "ANALOGNI_ ULAZ_POZICIJA_ KLIP_4" <= | | |
| | | UINT "DB"."POZICIJA_ KLIP_40%" | | |
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| S6:Step3 | ı | | | |
| Interlock -(c)-: | | | | |
| Interlock alarm | | | | |
| Alarm text | | | | |
| | | | Interlock C } | |
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| Supervision -(v)-: | | | | |
| Supervision alarm Alarm text | | | | |
| | | | Supervision | |
| | | | (∨)——₁ | |
| Actions: | | | | |
| Actions: Interlock | Event | Qualifier | Action | |
| menock | S1 | S | "DIGITALNI_IZLAZ_KLIP_1_LEVO" | |
| | S1 | R | "DIGITALNI_IZLAZ_KLIP_1_DESNO" | |
| | S1 | CD | "COUNTER_INNER_LOOP" | |
| T4:Trans4 | | | | |
| | | %110.0 %110.1 | | |
| | | "DIGITALNI_ "DIGITALNI_ ULAZ_KLIP_1_ ULAZ_KLIP_1_ LEVO_NC" DESNO_NC" | | |
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| S7:Step4 | | | | |
| Interlock -(c)-: | | | | |
| Interlock alarm | | | | |
| Alarm text | | | | |
| | _ | | Interlock ← C → | |
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| Supervision -(v)-: | | | | |
| Supervision alarm Alarm text | | | | |
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| Actions: | <u> </u> | | | |
| Actions: Interlock | Event | Qualifier | Action | |
| | S1 | R | "DIGITALNI_IZLAZ_KLIP_1_LEVO" | |
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| Totally Integrated Automation Portal | | | | |
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| Supervision -(v)-: | | | | |
| Supervision alarm | | | | |
| Alarm text | | | | |
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| Actions: | | | | |
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| Interlock | Event | Qualifier | Action | |
| С | | S | "DIGITALNI_IZLAZ_KLIP_1_DESNO" | |
| С | | R | "DIGITALNI_IZLAZ_KLIP_1_LEVO" | |
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| T11:Trans11 | | | | |
| | | 1 | | |
| | | %I10.7 "DIGITALNI_ | | |
| | | "DIGITALNI_ ULAZ_KLIP_4_ DESNO_NO" | | |
| | | | | |
| | | 1 | | |
| S10:ODRZAVANJE_KLIP | _1_LEVO | | | |
| | | | | |
| Interlock -(c)-: | | | | |
| Interlock alarm | | | | |
| Alarm text | | | | |
| | | | | |
| | | %IW0 %IW0 "ANALOGNI_ "ANALOGNI_ | | |
| | | ULAZ_POZICIJA_ ULAZ_POZICIJA_ KLIP_1" KLIP_1" | Interlock | |
| | | >= <= UInt UInt "DR" "POZICIJA "DR" POZICIJA | (c)—— | |
| | | "DB"."POZICIJA_ "DB".POZICIJA_ KLIP_90%" MAX | | |
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| | | <u> </u> | | |
| Supervision -(v)-: | | | | |
| Supervision alarm | | | | |
| Alarm text | | | | |
| | | | Supervision (∨)—— | |
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| | | 1 | | |
| Actions: | | | | |
| Actions: | | - 1151 | | |
| Interlock C | Event | Qualifier S | Action "DIGITALNI_IZLAZ_KLIP_1_LEVO" | |
| C | | R | "DIGITALNI_IZLAZ_KLIP_1_DESNO" | |
| | | N | DIGITALNI_IZLAZ_KLIF_I_DESINO | |
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| T11:Trans11 | | | | |
| | | %110.7 | | |
| | | "DIGITALNI_ ULAZ_KLIP_4_ DESNO_NO" | | |
| | | DESNO_NO" | | |
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| C11.6+==-6 | | · | | |
| S11:Step6 | | | | |
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| Interlock -(c)-: | | | | |
| Interlock alarm | | | | |
| Alarm text | | | | |
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| T. Control of the Con | | | Interlock | |
| | | | Interlock ← C } | |
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| Totally Integrated Automation Portal | | | | |
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| Supervision -(v)-: | | | | |
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| Supervision alarm Alarm text | | | | |
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| | - | | Supervision (V) | |
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| Actions: | | | | |
| Actions: | | | | |
| Interlock | Event S1 | Qualifier | Action | |
| | | TL | "TIMER", S5T#12S | |
| | S1 | CD | "BROJAC_ODRZAVANJE_KLIP_4" | |
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| T8:Trans8 | | | | |
| | | %TO "TIMER" | | |
| | _ | VI | | |
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| S12:Step7 | | | | |
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| Interlock -(c)-: | | | | |
| Interlock alarm | | | | |
| Alarm text | | | | |
| | 1 | | Interlock | |
| | _ | | (c) | |
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| Supervision -(v)-: | | | | |
| Supervision alarm | | | | |
| Alarm text | | | | |
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| Actions: | | | | |
| Actions: | - | | | |
| Interlock | Event S1 | Qualifier S | Action "DIGITALNI_IZLAZ_KLIP_2_DESNO" | |
| | S1 | R | "DIGITALNI_IZLAZ_KLIP_2_LEVO" | |
| | | | | |
| | S1 | CD | "BROJAC_ODRZAVANJE_KLIP_4" | |
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| T10:Trans10 | | | | |
| | | %I10.3 | | |
| | | %110.3 "DIGITALNI_ ULAZ_KLIP_2_ DESNO_NC" | | |
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| S13:Step8 | | | | |
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| Interlock -(c)-: | | | | |
| Interlock alarm | | | | |
| Alarm text | | | | |
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| Supervision -(v)-: | | | | |
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| Supervision alarm | | | | |
| Supervision alarm Alarm text | | | | |
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| 9:Trans9 14:Step9 | |
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| 9:Trans9 Srios Popraise Po | |
| 14:Step9 | |
| 14:Step9 Interlock -{c}-: terlock alarm larm text | |
| 14:Step9 | |
| 14:Step9 sterlock -(c)-: terlock alarm larm text | |
| tetrlock -(c)-: tetrlock alarm larm text Interlock (c) | |
| terlock alarm larm text tererlock {c} upervision -(v)-: upervision alarm larm text Supervision {v} ctions: terlock Event S1 R DIGITALNI_URAR_RUP_4 BROJAC_ODRZAVANJE_KLIP_4* 11:Trans11 | |
| terlock alarm larm text tererlock {c} upervision -(v)-: upervision alarm larm text Supervision {v} ctions: terlock Event S1 R DIGITALNI_URAR_RUP_4 BROJAC_ODRZAVANJE_KLIP_4* 11:Trans11 | |
| Interlock (c) | |
| C | |
| pervision alarm larm text Supervision | |
| pervision alarm larm text Supervision | |
| ctions: ctions: terlock Event Qualifier Action S1 R "DIGITALNI_IZLAZ_KLIP_4_LEVO" S1 CD "BROJAC_ODRZAVANJE_KLIP_4" 11:Trans11 | |
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| ctions: Interlock Event S1 R "DIGITALNI_IZLAZ_KLIP_4_LEVO" S1 CD "BROJAC_ODRZAVANJE_KLIP_4" 11:Trans11 | |
| ctions: Interlock Event S1 R "DIGITALNI_IZLAZ_KLIP_4_LEVO" S1 CD "BROJAC_ODRZAVANJE_KLIP_4" 11:Trans11 | |
| \$1 R "DIGITALNI_IZLAZ_KLIP_4_LEVO" \$1 CD "BROJAC_ODRZAVANJE_KLIP_4" 11:Trans11 **10.7 *DIGITALNI_ ULAZ_KLIP_4_ DESNO_NO* — I DESNO_NO* | |
| 11:Trans11 %10.7 "DIGITALNI_ ULAZ_KLIP_4_ DESNO_NO" | |
| %I10.7 "DIGITALNI_ ULAZ_KLIP_4_ DESNO_NO" | |
| %I10.7 "DIGITALNI_ ULAZ_KLIP_4_ DESNO_NO" | |
| "DIGITALNI_ ULAZ_KLIP_4_ DESNO_NO" | |
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| 15:Step10 | |
| 15:Step10 | |
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| stantante (a) . | |
| terlock -(c)-: | |
| larm text | |
| Interlock | |
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| upervision -(v)-: | |
| upervision alarm | |
| Supervision | |
| (V) | |
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| Totally Integrated Automation Portal | | | | |
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| Actions: | | | | |
| Actions: Interlock | Event | Qualifier | Action | |
| | S1 | S | "DIGITALNI_IZLAZ_KLIP_1_DESNO" | |
| | S1 | R | "DIGITALNI_IZLAZ_KLIP_1_LEVO" | |
| | S1 | CS | "BROJAC_ODRZAVANJE_KLIP_4", 10 | |
| T42 Tax = 42 | | | | |
| T12:Trans12 | | | | |
| | | %C1 "COUNTER_ OUTER_LOOP" | | |
| | | —-V | | |
| T13:Trans13 | ı | | | |
| 113.11411313 | | 0.01 | | |
| | | %C1 "COUNTER_ OUTER_LOOP" | | |
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| Permanent post-instructions | | | | |
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| [DB1] | | | | | | | | | | | | |
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| operties | | | | | | | | | | | | |
| ral e | DB | N | umber 1 | | Туре | DI | В | | Langu | age | DB | |
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| mation | | | ıthor | | Comment | | | | Family | | | |
| on | 0.1 | | ser-defined ID | | Comment | | | | Family | <u> </u> | | |
| 9 | | Data type | Start value | Retain | Accessible | Writ- | Visible in | Setpoint | Supervi- | Comme | ent | |
| | | Satu type | 3.01.10.00 | | from HMI/OPC UA | able from HMI/ OPC | HMI engi- neering | Setpoint | sion | | | |
| atic | | | | | | UA | | | | | | |
| | A_KLIP_30% | Word | 8310 | False | True | True | True | False | | | | |
| | A_KLIP_40% | Word | 11080 | False | True | True | | False | | | | |
| | A_KLIP_50% | Word | 13850 | False | True | True | | False | | | | |
| POZICIJA | 4_KLIP_70% | Word | 19390 | False | True | True | True | False | | | | |
| POZICIJA | 4_KLIP_90% | Word | 24930 | False | True | True | | False | | | | |
| | A_KLIP_80% | Word | 22160 | False | True | True | | False | | | | |
| POZICIJA | A_MAX | Word | 27700 | False | True | True | True | False | | | | |
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