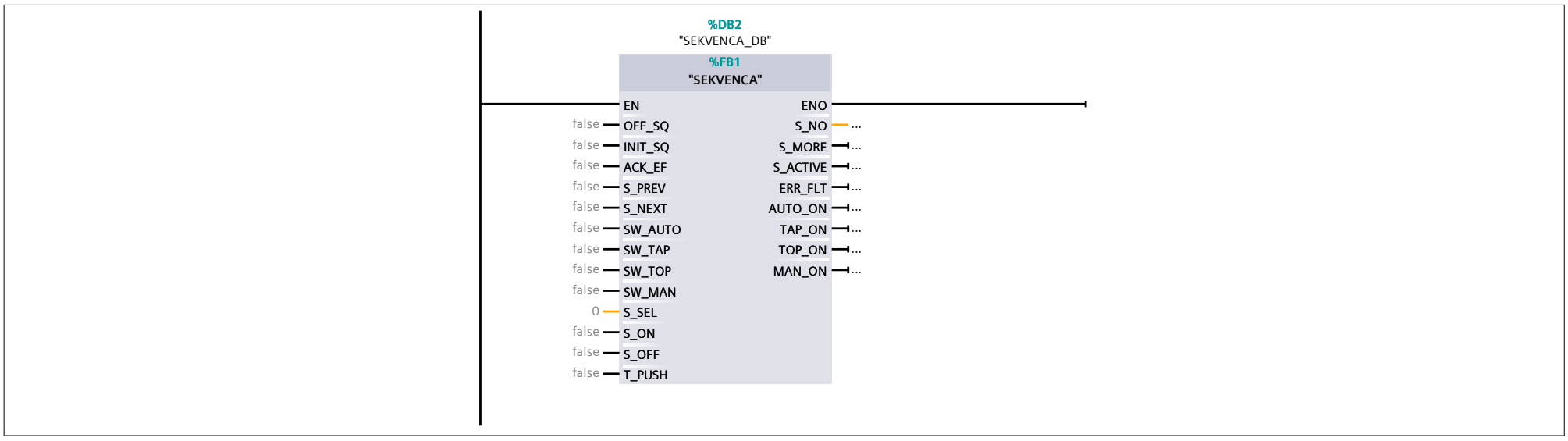
Main [OB1]

Main Properties							
General							
Name	Main	**Number**	1	**Type**	OB	**Language**	LAD
Numbering	Automatic						
Information							
Title	"Main Program Sweep (Cycle)"	**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Default value	Comment
▼ Input			
Initial_Call	Bool		Initial call of this OB
Remanence	Bool		=True, if remanent data are available
Temp			
Constant			

Network 1:



SEKVENCA [FB1]

SEKVENCA Properties							
General							
Name	SEKVENCA	**Number**	1	**Type**	FB	**Language**	GRAPH
Numbering	Automatic	**Network language**	LAD	**Block version**	V5.0		
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
▼ Input									
OFF_SQ	Bool	false	Non-retain	False	False	False	False		Turn sequence off
INIT_SQ	Bool	false	Non-retain	False	False	False	False		Set sequence to initial state
ACK_EF	Bool	false	Non-retain	False	False	False	False		Acknowledge all errors and faults
S_PREV	Bool	false	Non-retain	False	False	False	False		Output previous step in parameter S_NO
S_NEXT	Bool	false	Non-retain	False	False	False	False		Indicate next step in parameter S_NO
SW_AUTO	Bool	false	Non-retain	False	False	False	False		Automatic mode
SW_TAP	Bool	false	Non-retain	False	False	False	False		Semiautomatic/switch with transition
SW_TOP	Bool	false	Non-retain	False	False	False	False		Semiautomatic/ignore transition
SW_MAN	Bool	false	Non-retain	False	False	False	False		Manual mode
S_SEL	Int	0	Non-retain	False	False	False	False		Select step to be output to S_NO
S_ON	Bool	false	Non-retain	False	False	False	False		Activate step indicated in S_NO
S_OFF	Bool	false	Non-retain	False	False	False	False		Deactivate step indicated S_NO
T_PUSH	Bool	false	Non-retain	False	False	False	False		Enable transition to switch in semi automatic mode
▼ Output									
S_NO	Int	0	Non-retain	False	False	False	False		Step number
S_MORE	Bool	false	Non-retain	False	False	False	False		More steps are available and can be shown in S_NO
S_ACTIVE	Bool	false	Non-retain	False	False	False	False		Step indicated in S_NO is active
ERR_FLT	Bool	false	Non-retain	False	False	False	False		Interlock or supervision group error
AUTO_ON	Bool	false	Non-retain	False	False	False	False		Automatic mode is active
TAP_ON	Bool	false	Non-retain	False	False	False	False		Semiautomatic mode/step with transition enabled
TOP_ON	Bool	false	Non-retain	False	False	False	False		Semiautomatic mode/ignore transition enabled
MAN_ON	Bool	false	Non-retain	False	False	False	False		Manual mode is active
InOut									
▼ Static									
▼ RT_DATA	G7_RTData-Plus_V5		Non-retain	False	False	False	False		Internal data area
VERSION	String[10]	'V5.0'	Non-retain	False	False	False	False		Block version
S_DISPLAY	Int	0	Non-retain	False	False	False	False		Internal display of output parameter S_NO
S_SEL_OLD	Int	0	Non-retain	False	False	False	False		Previous value in S_SEL
S_DISPIDX	USInt	255	Non-retain	False	False	False	False		Index of the step in S_NO
T_DISPIDX	USInt	255	Non-retain	False	False	False	False		Index of the transition displayed in T_NO
▼ MOP_EDGE	G7_MOP-Plus_V5		Non-retain	False	False	False	False		Mode in last cycle
AUTO	Bool	false	Non-retain	False	False	False	False		Status: automatic mode
MAN	Bool	false	Non-retain	False	False	False	False		Status: manual mode
TAP	Bool	false	Non-retain	False	False	False	False		Status: semi automatic/ switch with transition
TOP	Bool	false	Non-retain	False	False	False	False		Status: semi automatic/ ignore transition
ACK_S	Bool	false	Non-retain	False	False	False	False		Request: acknowledge step at parameter S_NO
REG_S	Bool	false	Non-retain	False	False	False	False		Request: register step indicated in S_NO
T_PREV	Bool	false	Non-retain	False	False	False	False		Request: output previous valid transition in T_NO
T_NEXT	Bool	false	Non-retain	False	False	False	False		Request: output next valid transition in T_NO
LOCK	Bool	false	Non-retain	False	False	False	False		Status: interlocks activated
SUP	Bool	false	Non-retain	False	False	False	False		Status: supervisions activated
ACKREQ	Bool	false	Non-retain	False	False	False	False		Status: acknowledgment required

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
SSKIP	Bool	false	Non-retain	False	False	False	False		Status: "Skip steps" enabled
OFF	Bool	false	Non-retain	False	False	False	False		Request: deactivate all steps
INIT	Bool	false	Non-retain	False	False	False	False		Request: set sequence to initial state
HALT	Bool	false	Non-retain	False	False	False	False		Status: sequence halted
TMS_HALT	Bool	false	Non-retain	False	False	False	False		Status: all internal timers held
OPS_ZERO	Bool	false	Non-retain	False	False	False	False		Status: set all operands processed with N, L, D instructions to 0
SACT_DISP	Bool	false	Non-retain	False	False	False	False		Status: display active steps only
SEF_DISP	Bool	false	Non-retain	False	False	False	False		Status: display only steps with errors and disrupted steps
SALL_DISP	Bool	false	Non-retain	False	False	False	False		Status: display all steps
S_PREV	Bool	false	Non-retain	False	False	False	False		Request: output previous step to S_NO
S_NEXT	Bool	false	Non-retain	False	False	False	False		Request: Output next step at S_NO parameter
S_SELOK	Bool	false	Non-retain	False	False	False	False		Request: output step number from S_SEL to S_NO
S_ON	Bool	false	Non-retain	False	False	False	False		Request: activate step indicated in S_NO
S_OFF	Bool	false	Non-retain	False	False	False	False		Request: deactivate step at parameter S_NO
T_PUSH	Bool	false	Non-retain	False	False	False	False		Request: transition switching enabled
REG	Bool	false	Non-retain	False	False	False	False		Request: register all interlock and supervision errors
ACK	Bool	false	Non-retain	False	False	False	False		Request: acknowledge all interlock and supervision errors
IL_PERM	Bool	false	Non-retain	False	False	False	False		Status: permanent processing of all interlocks
T_PERM	Bool	false	Non-retain	False	False	False	False		Status: permanent processing of all transitions
ILP_MAN	Bool	false	Non-retain	False	False	False	False		Status: permanent processing of all interlocks in manual mode
LMODE	Bool	false	Non-retain	False	False	False	False		Status: learning mode is acitve
RESET_CRIT	Bool	false	Non-retain	False	False	False	False		Request: reset all initial values recorded for interlocks and transitions
▼ MOP	G7_MOP-Plus_V5		Non-retain	False	False	False	False		Mode
AUTO	Bool	true	Non-retain	False	False	False	False		Status: automatic mode
MAN	Bool	false	Non-retain	False	False	False	False		Status: manual mode
TAP	Bool	false	Non-retain	False	False	False	False		Status: semi automatic/ switch with transition
TOP	Bool	false	Non-retain	False	False	False	False		Status: semi automatic/ ignore transition
ACK_S	Bool	false	Non-retain	False	False	False	False		Request: acknowledge step at parameter S_NO
REG_S	Bool	false	Non-retain	False	False	False	False		Request: register step indicated in S_NO
T_PREV	Bool	false	Non-retain	False	False	False	False		Request: output previous valid transition in T_NO
T_NEXT	Bool	false	Non-retain	False	False	False	False		Request: output next valid transition in T_NO
LOCK	Bool	true	Non-retain	False	False	False	False		Status: interlocks activated
SUP	Bool	true	Non-retain	False	False	False	False		Status: supervisions activated
ACKREQ	Bool	true	Non-retain	False	False	False	False		Status: acknowledgment required
SSKIP	Bool	false	Non-retain	False	False	False	False		Status: "Skip steps" enabled
OFF	Bool	false	Non-retain	False	False	False	False		Request: deactivate all steps
INIT	Bool	true	Non-retain	False	False	False	False		Request: set sequence to initial state
HALT	Bool	false	Non-retain	False	False	False	False		Status: sequence halted
TMS_HALT	Bool	false	Non-retain	False	False	False	False		Status: all internal timers held
OPS_ZERO	Bool	false	Non-retain	False	False	False	False		Status: set all operands processed with N, L, D instructions to 0
SACT_DISP	Bool	true	Non-retain	False	False	False	False		Status: display active steps only
SEF_DISP	Bool	false	Non-retain	False	False	False	False		Status: display only steps with errors and disrupted steps
SALL_DISP	Bool	false	Non-retain	False	False	False	False		Status: display all steps
S_PREV	Bool	false	Non-retain	False	False	False	False		Request: output previous step to S_NO

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
S_NEXT	Bool	false	Non-retain	False	False	False	False		Request: Output next step at S_NO parameter
S_SELOK	Bool	false	Non-retain	False	False	False	False		Request: output step number from S_SEL to S_NO
S_ON	Bool	false	Non-retain	False	False	False	False		Request: activate step indicated in S_NO
S_OFF	Bool	false	Non-retain	False	False	False	False		Request: deactivate step at parameter S_NO
T_PUSH	Bool	false	Non-retain	False	False	False	False		Request: transition switching enabled
REG	Bool	false	Non-retain	False	False	False	False		Request: register all interlock and supervision errors
ACK	Bool	false	Non-retain	False	False	False	False		Request: acknowledge all interlock and supervision errors
IL_PERM	Bool	false	Non-retain	False	False	False	False		Status: permanent processing of all interlocks
T_PERM	Bool	false	Non-retain	False	False	False	False		Status: permanent processing of all transitions
ILP_MAN	Bool	false	Non-retain	False	False	False	False		Status: permanent processing of all interlocks in manual mode
LMODE	Bool	false	Non-retain	False	False	False	False		Status: learning mode is acitve
RESET_CRIT	Bool	false	Non-retain	False	False	False	False		Request: reset all initial values recorded for interlocks and transitions
TIME_DELTA	Time	T#0ms	Non-retain	False	False	False	False		Cycle time
▼ SQ_FLAGS	G7_SQFlags-Plus_V5		Non-retain	False	False	False	False		Sequence bit memory
ERR_FLT	Bool	false	Non-retain	False	False	False	False		Interlock and supervision group error
ERROR	Bool	false	Non-retain	False	False	False	False		Interlock group error
FAULT	Bool	false	Non-retain	False	False	False	False		Supervision group error
RT_FAIL	Bool	false	Non-retain	False	False	False	False		Runtime error
NO_SNO	Bool	false	Non-retain	False	False	False	False		Requested step number not found
NF_OFL	Bool	false	Non-retain	False	False	False	False		Overflow: too many ON or OFF requests
SA_OFL	Bool	false	Non-retain	False	False	False	False		Overflow: too many steps active
TV_OFL	Bool	false	Non-retain	False	False	False	False		Overflow: too many valid transitions
NO_SWI	Bool	false	Non-retain	False	False	False	False		Do not switch in this cycle
CYC_OP	Bool	false	Non-retain	False	False	False	False		Cyclic execution of the sequence after initialization
AS_MSG	Bool	true	Non-retain	False	False	False	False		Alarms during runtime enabled or disabled by instruction
SQ_BUSY	Bool	false	Non-retain	False	False	False	False		Internal edge memory bit for sequence processing
SA_BUSY	Bool	false	Non-retain	False	False	False	False		Internal edge memory bit for sequence processing
PRE_CNT	USInt	1	Non-retain	False	False	False	False		Number of permanent instructions preceding the sequencer
POST_CNT	USInt	1	Non-retain	False	False	False	False		Number of permanent instructions after the sequencer
SQ_CNT	USInt	1	Non-retain	False	False	False	False		Number of branch paths
S_CNT	USInt	15	Non-retain	False	False	False	False		Number of steps
LOCK_CNT	USInt	4	Non-retain	False	False	False	False		Number of interlocks
SUP_CNT	USInt	0	Non-retain	False	False	False	False		Number of supervisions
T_CNT	USInt	13	Non-retain	False	False	False	False		Number of transitions
SQ_PART_CNT	USInt	8	Non-retain	False	False	False	False		Number of branches
MAX_TVAL	USInt	11	Non-retain	False	False	False	False		Max. number of simultaneously valid transitions
MAX_SACT	USInt	7	Non-retain	False	False	False	False		Max. number of simultaneously active steps
AS_MSG	Byte	16#65	Non-retain	False	False	False	False		Alarm flags
▼ EXEC_BITS	Array[0..249] of Bool		Non-retain	False	False	False	False		System-internal
EXEC_BITS[0]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[1]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[2]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[3]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[4]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[5]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[6]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[7]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[8]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[9]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[10]	Bool	false	Non-retain	False	False	False	False		System-internal

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
EXEC_BITS[11]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[12]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[13]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[14]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[15]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[16]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[17]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[18]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[19]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[20]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[21]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[22]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[23]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[24]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[25]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[26]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[27]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[28]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[29]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[30]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[31]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[32]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[33]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[34]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[35]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[36]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[37]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[38]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[39]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[40]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[41]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[42]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[43]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[44]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[45]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[46]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[47]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[48]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[49]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[50]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[51]	Bool	false	Non-retain	False	False	False	False		System-internal
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EXEC_BITS[55]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[56]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[57]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[58]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[59]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[60]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[61]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[62]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[63]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[64]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[65]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[66]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[67]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[68]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[69]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[70]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[71]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[72]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[73]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[74]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[75]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[76]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[77]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[78]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[79]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[80]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[81]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[82]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[83]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[84]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[85]	Bool	false	Non-retain	False	False	False	False		System-internal

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
EXEC_BITS[86]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[87]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[88]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[89]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[90]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[91]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[92]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[93]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[94]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[95]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[96]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[97]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[98]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[99]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[100]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[101]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[102]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[103]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[104]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[105]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[106]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[107]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[108]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[109]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[110]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[111]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[112]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[113]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[114]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[115]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[116]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[117]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[118]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[119]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[120]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[121]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[122]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[123]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[124]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[125]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[126]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[127]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[128]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[129]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[130]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[131]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[132]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[133]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[134]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[135]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[136]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[137]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[138]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[139]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[140]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[141]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[142]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[143]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[144]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[145]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[146]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[147]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[148]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[149]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[150]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[151]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[152]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[153]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[154]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[155]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[156]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[157]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[158]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[159]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[160]	Bool	false	Non-retain	False	False	False	False		System-internal

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
EXEC_BITS[161]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[162]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[163]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[164]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[165]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[166]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[167]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[168]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[169]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[170]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[171]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[172]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[173]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[174]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[175]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[176]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[177]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[178]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[179]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[180]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[181]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[182]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[183]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[184]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[185]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[186]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[187]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[188]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[189]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[190]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[191]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[192]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[193]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[194]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[195]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[196]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[197]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[198]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[199]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[200]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[201]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[202]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[203]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[204]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[205]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[206]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[207]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[208]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[209]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[210]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[211]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[212]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[213]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[214]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[215]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[216]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[217]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[218]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[219]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[220]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[221]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[222]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[223]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[224]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[225]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[226]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[227]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[228]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[229]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[230]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[231]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[232]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[233]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[234]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[235]	Bool	false	Non-retain	False	False	False	False		System-internal

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
EXEC_BITS[236]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[237]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[238]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[239]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[240]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[241]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[242]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[243]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[244]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[245]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[246]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[247]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[248]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_BITS[249]	Bool	false	Non-retain	False	False	False	False		System-internal
▼ OFFSETS	G7_Offsets-Plus_V5		Non-retain	False	False	False	False		Internal offsets
SINI_OFFSET	UInt	0	Non-retain	False	False	False	False		Offset of internal array SINI[]
LSTT_OFFSET	UInt	8	Non-retain	False	False	False	False		Offset of internal array LSTT[]
ATAJ_OFFSET	UInt	21	Non-retain	False	False	False	False		Offset of internal array ATAJ[]
ATAB_OFFSET	UInt	34	Non-retain	False	False	False	False		Offset of internal array ATAB[]
PSTT_OFFSET	UInt	47	Non-retain	False	False	False	False		Offset of internal array PSTT[]
NSTT_OFFSET	UInt	60	Non-retain	False	False	False	False		Offset of internal array NSTT[]
ASSJ_OFFSET	UInt	73	Non-retain	False	False	False	False		Offset of internal array ASSJ[]
ASSB_OFFSET	UInt	88	Non-retain	False	False	False	False		Offset of internal array ASSB[]
PTTS_OFFSET	UInt	103	Non-retain	False	False	False	False		Offset of internal array PTTS[]
NTTS_OFFSET	UInt	118	Non-retain	False	False	False	False		Offset of internal array NTTS[]
SW_SQTS_OFFSET	UInt	133	Non-retain	False	False	False	False		Offset of internal array SW_SQTS[]
SWITCH_OFFSET	UInt	148	Non-retain	False	False	False	False		Offset of internal array SWITCH[]
TVX_OFFSET	UInt	156	Non-retain	False	False	False	False		Offset of internal array TVX[]
TTX_OFFSET	UInt	168	Non-retain	False	False	False	False		Offset of internal array TTX[]
TSX_OFFSET	UInt	180	Non-retain	False	False	False	False		Offset of internal array TSX[]
S00X_OFFSET	UInt	188	Non-retain	False	False	False	False		Offset of internal array S00X[]
SOFFX_OFFSET	UInt	196	Non-retain	False	False	False	False		Offset of internal array SOFFX[]
SONX_OFFSET	UInt	204	Non-retain	False	False	False	False		Offset of internal array SONX[]
SAX_OFFSET	UInt	212	Non-retain	False	False	False	False		Offset of internal array SAX[]
SERRX_OFFSET	UInt	220	Non-retain	False	False	False	False		Offset of internal array SERRX[]
SMX_OFFSET	UInt	236	Non-retain	False	False	False	False		Offset of internal array SMX[]
S0X_OFFSET	UInt	252	Non-retain	False	False	False	False		Offset of internal array S0X[]
S1X_OFFSET	UInt	268	Non-retain	False	False	False	False		Offset of internal array S1X[]
THRESHOLD_SUP	USInt	0	Non-retain	False	False	False	False		Threshold for step activation time
THRESHOLD_WARN	USInt	0	Non-retain	False	False	False	False		Threshold for step activation time (warning only)
▼ GC_FLAGS	G7_GCFlags-Plus_V5		Non-retain	False	False	False	False		Compiler flags
COND_ED	USInt	16#E3	Non-retain	False	False	False	False		Language in networks
SSKIP_ON	Bool	false	Non-retain	False	False	False	False		Skip steps
ACK_REQ	Bool	true	Non-retain	False	False	False	False		Acknowledgement required for reaction errors
ILP_MAN	Bool	false	Non-retain	False	False	False	False		Permanent processing of all interlocks in manual mode
SWM_LOCKED	Bool	false	Non-retain	False	False	False	False		Lock operating mode selection
SET_ENO	Bool	false	Non-retain	False	False	False	False		Set ENO automatically
IL_REACT_CAT	USInt	1	Non-retain	False	False	False	False		Category for interlock and reaction
WARN_CAT	USInt	2	Non-retain	False	False	False	False		Category for warnings
CRIT_ON	Bool	false	Non-retain	False	False	False	False		Criteria analysis activated
▼ Trans1	G7_Transition-Plus_V5		Non-retain	False	False	False	False		Transition structure
TV	Bool	false	Non-retain	False	False	False	False		Transition is valid
TT	Bool	false	Non-retain	False	False	False	False		Transition is satisfied
TS	Bool	false	Non-retain	False	False	False	False		Transition switches
TNO	Int	1	Non-retain	False	False	False	False		Indicates the user-defined transition number

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans2	G7_Transition-Plus_V5		Non-retain	False	False	False	False		Transition structure
TV	Bool	false	Non-retain	False	False	False	False		Transition is valid
TT	Bool	false	Non-retain	False	False	False	False		Transition is satisfied
TS	Bool	false	Non-retain	False	False	False	False		Transition switches
TNO	Int	2	Non-retain	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans3	G7_Transition-Plus_V5		Non-retain	False	False	False	False		Transition structure
TV	Bool	false	Non-retain	False	False	False	False		Transition is valid
TT	Bool	false	Non-retain	False	False	False	False		Transition is satisfied
TS	Bool	false	Non-retain	False	False	False	False		Transition switches
TNO	Int	3	Non-retain	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans4	G7_Transition-Plus_V5		Non-retain	False	False	False	False		Transition structure
TV	Bool	false	Non-retain	False	False	False	False		Transition is valid
TT	Bool	false	Non-retain	False	False	False	False		Transition is satisfied
TS	Bool	false	Non-retain	False	False	False	False		Transition switches
TNO	Int	4	Non-retain	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans5	G7_Transition-Plus_V5		Non-retain	False	False	False	False		Transition structure
TV	Bool	false	Non-retain	False	False	False	False		Transition is valid
TT	Bool	false	Non-retain	False	False	False	False		Transition is satisfied
TS	Bool	false	Non-retain	False	False	False	False		Transition switches
TNO	Int	5	Non-retain	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans6	G7_Transition-Plus_V5		Non-retain	False	False	False	False		Transition structure
TV	Bool	false	Non-retain	False	False	False	False		Transition is valid
TT	Bool	false	Non-retain	False	False	False	False		Transition is satisfied
TS	Bool	false	Non-retain	False	False	False	False		Transition switches
TNO	Int	6	Non-retain	False	False	False	False		Indicates the user-defined transition number

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans7	G7_Transition-Plus_V5		Non-retain	False	False	False	False		Transition structure
TV	Bool	false	Non-retain	False	False	False	False		Transition is valid
TT	Bool	false	Non-retain	False	False	False	False		Transition is satisfied
TS	Bool	false	Non-retain	False	False	False	False		Transition switches
TNO	Int	7	Non-retain	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans8	G7_Transition-Plus_V5		Non-retain	False	False	False	False		Transition structure
TV	Bool	false	Non-retain	False	False	False	False		Transition is valid
TT	Bool	false	Non-retain	False	False	False	False		Transition is satisfied
TS	Bool	false	Non-retain	False	False	False	False		Transition switches
TNO	Int	8	Non-retain	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans9	G7_Transition-Plus_V5		Non-retain	False	False	False	False		Transition structure
TV	Bool	false	Non-retain	False	False	False	False		Transition is valid
TT	Bool	false	Non-retain	False	False	False	False		Transition is satisfied
TS	Bool	false	Non-retain	False	False	False	False		Transition switches
TNO	Int	9	Non-retain	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans10	G7_Transition-Plus_V5		Non-retain	False	False	False	False		Transition structure
TV	Bool	false	Non-retain	False	False	False	False		Transition is valid
TT	Bool	false	Non-retain	False	False	False	False		Transition is satisfied
TS	Bool	false	Non-retain	False	False	False	False		Transition switches
TNO	Int	10	Non-retain	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans11	G7_Transition-Plus_V5		Non-retain	False	False	False	False		Transition structure
TV	Bool	false	Non-retain	False	False	False	False		Transition is valid
TT	Bool	false	Non-retain	False	False	False	False		Transition is satisfied
TS	Bool	false	Non-retain	False	False	False	False		Transition switches
TNO	Int	11	Non-retain	False	False	False	False		Indicates the user-defined transition number

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans12	G7_Transition-Plus_V5		Non-retain	False	False	False	False		Transition structure
TV	Bool	false	Non-retain	False	False	False	False		Transition is valid
TT	Bool	false	Non-retain	False	False	False	False		Transition is satisfied
TS	Bool	false	Non-retain	False	False	False	False		Transition switches
TNO	Int	12	Non-retain	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans13	G7_Transition-Plus_V5		Non-retain	False	False	False	False		Transition structure
TV	Bool	false	Non-retain	False	False	False	False		Transition is valid
TT	Bool	false	Non-retain	False	False	False	False		Transition is satisfied
TS	Bool	false	Non-retain	False	False	False	False		Transition switches
TNO	Int	13	Non-retain	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT if an error occurs
▼ INICIJALIZACIJA	G7_StepPlus_V5		Non-retain	False	False	False	False		Step structure
S1	Bool	false	Non-retain	False	False	False	False		Step is activated
L1	Bool	false	Non-retain	False	False	False	False		interlock leaving state
V1	Bool	false	Non-retain	False	False	False	False		Supervision entering state
R1	Bool	false	Non-retain	False	False	False	False		Reserved
A1	Bool	false	Non-retain	False	False	False	False		Error is acknowledged
S0	Bool	false	Non-retain	False	False	False	False		Step is deactivated
L0	Bool	false	Non-retain	False	False	False	False		Interlock entering state
V0	Bool	false	Non-retain	False	False	False	False		Supervision leaving state
X	Bool	false	Non-retain	False	False	False	False		Step is active
LA	Bool	false	Non-retain	False	False	False	False		Interlock is not satisfied
VA	Bool	false	Non-retain	False	False	False	False		Supervision active
RA	Bool	false	Non-retain	False	False	False	False		Reserved
AA	Bool	false	Non-retain	False	False	False	False		Reserved
SS	Bool	false	Non-retain	False	False	False	False		System-internal
LS	Bool	true	Non-retain	False	False	False	False		Direct result of the programmed interlock
VS	Bool	false	Non-retain	False	False	False	False		Direct result of the programmed supervision
SNO	Int	1	Non-retain	False	False	False	False		User step number
T	Time	T#0ms	Non-retain	False	False	False	False		Total step activation time
U	Time	T#0ms	Non-retain	False	False	False	False		Step activation time without disturbance
T_MAX	Time	T#10S	Non-retain	False	False	False	False		Maximal step activation time
T_WARN	Time	T#7S	Non-retain	False	False	False	False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
SM	Bool	false	Non-retain	False	False	False	False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False	False	False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False	False	False		System-internal
▼ Step1	G7_StepPlus_V5		Non-retain	False	False	False	False		Step structure
S1	Bool	false	Non-retain	False	False	False	False		Step is activated
L1	Bool	false	Non-retain	False	False	False	False		interlock leaving state
V1	Bool	false	Non-retain	False	False	False	False		Supervision entering state

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
R1	Bool	false	Non-retain	False	False	False	False		Reserved
A1	Bool	false	Non-retain	False	False	False	False		Error is acknowledged
S0	Bool	false	Non-retain	False	False	False	False		Step is deactivated
L0	Bool	false	Non-retain	False	False	False	False		Interlock entering state
V0	Bool	false	Non-retain	False	False	False	False		Supervision leaving state
X	Bool	false	Non-retain	False	False	False	False		Step is active
LA	Bool	false	Non-retain	False	False	False	False		Interlock is not satisfied
VA	Bool	false	Non-retain	False	False	False	False		Supervision active
RA	Bool	false	Non-retain	False	False	False	False		Reserved
AA	Bool	false	Non-retain	False	False	False	False		Reserved
SS	Bool	false	Non-retain	False	False	False	False		System-internal
LS	Bool	true	Non-retain	False	False	False	False		Direct result of the programmed interlock
VS	Bool	false	Non-retain	False	False	False	False		Direct result of the programmed supervision
SNO	Int	2	Non-retain	False	False	False	False		User step number
T	Time	T#0ms	Non-retain	False	False	False	False		Total step activation time
U	Time	T#0ms	Non-retain	False	False	False	False		Step activation time without disturbance
T_MAX	Time	T#10S	Non-retain	False	False	False	False		Maximal step activation time
T_WARN	Time	T#7S	Non-retain	False	False	False	False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
SM	Bool	false	Non-retain	False	False	False	False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False	False	False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False	False	False		System-internal
▼ Step2	G7_StepPlus_V5		Non-retain	False	False	False	False		Step structure
S1	Bool	false	Non-retain	False	False	False	False		Step is activated
L1	Bool	false	Non-retain	False	False	False	False		interlock leaving state
V1	Bool	false	Non-retain	False	False	False	False		Supervision entering state
R1	Bool	false	Non-retain	False	False	False	False		Reserved
A1	Bool	false	Non-retain	False	False	False	False		Error is acknowledged
S0	Bool	false	Non-retain	False	False	False	False		Step is deactivated
L0	Bool	false	Non-retain	False	False	False	False		Interlock entering state
V0	Bool	false	Non-retain	False	False	False	False		Supervision leaving state
X	Bool	false	Non-retain	False	False	False	False		Step is active
LA	Bool	false	Non-retain	False	False	False	False		Interlock is not satisfied
VA	Bool	false	Non-retain	False	False	False	False		Supervision active
RA	Bool	false	Non-retain	False	False	False	False		Reserved
AA	Bool	false	Non-retain	False	False	False	False		Reserved
SS	Bool	false	Non-retain	False	False	False	False		System-internal
LS	Bool	true	Non-retain	False	False	False	False		Direct result of the programmed interlock
VS	Bool	false	Non-retain	False	False	False	False		Direct result of the programmed supervision
SNO	Int	3	Non-retain	False	False	False	False		User step number
T	Time	T#0ms	Non-retain	False	False	False	False		Total step activation time
U	Time	T#0ms	Non-retain	False	False	False	False		Step activation time without disturbance
T_MAX	Time	T#10S	Non-retain	False	False	False	False		Maximal step activation time
T_WARN	Time	T#7S	Non-retain	False	False	False	False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
SM	Bool	false	Non-retain	False	False	False	False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False	False	False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False	False	False		System-internal
▼ ODRZAVANJE_KLIP_4_LEVO	G7_StepPlus_V5		Non-retain	False	False	False	False		Step structure
S1	Bool	false	Non-retain	False	False	False	False		Step is activated
L1	Bool	false	Non-retain	False	False	False	False		interlock leaving state
V1	Bool	false	Non-retain	False	False	False	False		Supervision entering state
R1	Bool	false	Non-retain	False	False	False	False		Reserved
A1	Bool	false	Non-retain	False	False	False	False		Error is acknowledged
S0	Bool	false	Non-retain	False	False	False	False		Step is deactivated
L0	Bool	false	Non-retain	False	False	False	False		Interlock entering state
V0	Bool	false	Non-retain	False	False	False	False		Supervision leaving state
X	Bool	false	Non-retain	False	False	False	False		Step is active
LA	Bool	false	Non-retain	False	False	False	False		Interlock is not satisfied
VA	Bool	false	Non-retain	False	False	False	False		Supervision active
RA	Bool	false	Non-retain	False	False	False	False		Reserved
AA	Bool	false	Non-retain	False	False	False	False		Reserved

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
SS	Bool	false	Non-retain	False	False	False	False		System-internal
LS	Bool	true	Non-retain	False	False	False	False		Direct result of the programmed interlock
VS	Bool	false	Non-retain	False	False	False	False		Direct result of the programmed supervision
SNO	Int	4	Non-retain	False	False	False	False		User step number
T	Time	T#0ms	Non-retain	False	False	False	False		Total step activation time
U	Time	T#0ms	Non-retain	False	False	False	False		Step activation time without disturbance
T_MAX	Time	T#10S	Non-retain	False	False	False	False		Maximal step activation time
T_WARN	Time	T#7S	Non-retain	False	False	False	False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
SM	Bool	false	Non-retain	False	False	False	False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False	False	False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False	False	False		System-internal
ODRZAVANJE_KLIP_4_DESNO	G7_StepPlus_V5		Non-retain	False	False	False	False		Step structure
S1	Bool	false	Non-retain	False	False	False	False		Step is activated
L1	Bool	false	Non-retain	False	False	False	False		interlock leaving state
V1	Bool	false	Non-retain	False	False	False	False		Supervision entering state
R1	Bool	false	Non-retain	False	False	False	False		Reserved
A1	Bool	false	Non-retain	False	False	False	False		Error is acknowledged
S0	Bool	false	Non-retain	False	False	False	False		Step is deactivated
L0	Bool	false	Non-retain	False	False	False	False		Interlock entering state
V0	Bool	false	Non-retain	False	False	False	False		Supervision leaving state
X	Bool	false	Non-retain	False	False	False	False		Step is active
LA	Bool	false	Non-retain	False	False	False	False		Interlock is not satisfied
VA	Bool	false	Non-retain	False	False	False	False		Supervision active
RA	Bool	false	Non-retain	False	False	False	False		Reserved
AA	Bool	false	Non-retain	False	False	False	False		Reserved
SS	Bool	false	Non-retain	False	False	False	False		System-internal
LS	Bool	true	Non-retain	False	False	False	False		Direct result of the programmed interlock
VS	Bool	false	Non-retain	False	False	False	False		Direct result of the programmed supervision
SNO	Int	5	Non-retain	False	False	False	False		User step number
T	Time	T#0ms	Non-retain	False	False	False	False		Total step activation time
U	Time	T#0ms	Non-retain	False	False	False	False		Step activation time without disturbance
T_MAX	Time	T#10S	Non-retain	False	False	False	False		Maximal step activation time
T_WARN	Time	T#7S	Non-retain	False	False	False	False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
SM	Bool	false	Non-retain	False	False	False	False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False	False	False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False	False	False		System-internal
Step3	G7_StepPlus_V5		Non-retain	False	False	False	False		Step structure
S1	Bool	false	Non-retain	False	False	False	False		Step is activated
L1	Bool	false	Non-retain	False	False	False	False		interlock leaving state
V1	Bool	false	Non-retain	False	False	False	False		Supervision entering state
R1	Bool	false	Non-retain	False	False	False	False		Reserved
A1	Bool	false	Non-retain	False	False	False	False		Error is acknowledged
S0	Bool	false	Non-retain	False	False	False	False		Step is deactivated
L0	Bool	false	Non-retain	False	False	False	False		Interlock entering state
V0	Bool	false	Non-retain	False	False	False	False		Supervision leaving state
X	Bool	false	Non-retain	False	False	False	False		Step is active
LA	Bool	false	Non-retain	False	False	False	False		Interlock is not satisfied
VA	Bool	false	Non-retain	False	False	False	False		Supervision active
RA	Bool	false	Non-retain	False	False	False	False		Reserved
AA	Bool	false	Non-retain	False	False	False	False		Reserved
SS	Bool	false	Non-retain	False	False	False	False		System-internal
LS	Bool	true	Non-retain	False	False	False	False		Direct result of the programmed interlock
VS	Bool	false	Non-retain	False	False	False	False		Direct result of the programmed supervision
SNO	Int	6	Non-retain	False	False	False	False		User step number
T	Time	T#0ms	Non-retain	False	False	False	False		Total step activation time
U	Time	T#0ms	Non-retain	False	False	False	False		Step activation time without disturbance
T_MAX	Time	T#10S	Non-retain	False	False	False	False		Maximal step activation time

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
T_WARN	Time	T#7S	Non-retain	False	False	False	False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
SM	Bool	false	Non-retain	False	False	False	False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False	False	False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False	False	False		System-internal
▼ Step4	G7_StepPlus_V5		Non-retain	False	False	False	False		Step structure
S1	Bool	false	Non-retain	False	False	False	False		Step is activated
L1	Bool	false	Non-retain	False	False	False	False		interlock leaving state
V1	Bool	false	Non-retain	False	False	False	False		Supervision entering state
R1	Bool	false	Non-retain	False	False	False	False		Reserved
A1	Bool	false	Non-retain	False	False	False	False		Error is acknowledged
S0	Bool	false	Non-retain	False	False	False	False		Step is deactivated
L0	Bool	false	Non-retain	False	False	False	False		Interlock entering state
V0	Bool	false	Non-retain	False	False	False	False		Supervision leaving state
X	Bool	false	Non-retain	False	False	False	False		Step is active
LA	Bool	false	Non-retain	False	False	False	False		Interlock is not satisfied
VA	Bool	false	Non-retain	False	False	False	False		Supervision active
RA	Bool	false	Non-retain	False	False	False	False		Reserved
AA	Bool	false	Non-retain	False	False	False	False		Reserved
SS	Bool	false	Non-retain	False	False	False	False		System-internal
LS	Bool	true	Non-retain	False	False	False	False		Direct result of the programmed interlock
VS	Bool	false	Non-retain	False	False	False	False		Direct result of the programmed supervision
SNO	Int	7	Non-retain	False	False	False	False		User step number
T	Time	T#0ms	Non-retain	False	False	False	False		Total step activation time
U	Time	T#0ms	Non-retain	False	False	False	False		Step activation time without disturbance
T_MAX	Time	T#10S	Non-retain	False	False	False	False		Maximal step activation time
T_WARN	Time	T#7S	Non-retain	False	False	False	False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
SM	Bool	false	Non-retain	False	False	False	False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False	False	False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False	False	False		System-internal
▼ Step5	G7_StepPlus_V5		Non-retain	False	False	False	False		Step structure
S1	Bool	false	Non-retain	False	False	False	False		Step is activated
L1	Bool	false	Non-retain	False	False	False	False		interlock leaving state
V1	Bool	false	Non-retain	False	False	False	False		Supervision entering state
R1	Bool	false	Non-retain	False	False	False	False		Reserved
A1	Bool	false	Non-retain	False	False	False	False		Error is acknowledged
S0	Bool	false	Non-retain	False	False	False	False		Step is deactivated
L0	Bool	false	Non-retain	False	False	False	False		Interlock entering state
V0	Bool	false	Non-retain	False	False	False	False		Supervision leaving state
X	Bool	false	Non-retain	False	False	False	False		Step is active
LA	Bool	false	Non-retain	False	False	False	False		Interlock is not satisfied
VA	Bool	false	Non-retain	False	False	False	False		Supervision active
RA	Bool	false	Non-retain	False	False	False	False		Reserved
AA	Bool	false	Non-retain	False	False	False	False		Reserved
SS	Bool	false	Non-retain	False	False	False	False		System-internal
LS	Bool	true	Non-retain	False	False	False	False		Direct result of the programmed interlock
VS	Bool	false	Non-retain	False	False	False	False		Direct result of the programmed supervision
SNO	Int	8	Non-retain	False	False	False	False		User step number
T	Time	T#0ms	Non-retain	False	False	False	False		Total step activation time
U	Time	T#0ms	Non-retain	False	False	False	False		Step activation time without disturbance
T_MAX	Time	T#10S	Non-retain	False	False	False	False		Maximal step activation time
T_WARN	Time	T#7S	Non-retain	False	False	False	False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
SM	Bool	false	Non-retain	False	False	False	False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False	False	False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False	False	False		System-internal

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
ODRZAVANJE_KLIP_1_DESNO	G7_StepPlus_V5		Non-retain	False	False	False	False		Step structure
S1	Bool	false	Non-retain	False	False	False	False		Step is activated
L1	Bool	false	Non-retain	False	False	False	False		interlock leaving state
V1	Bool	false	Non-retain	False	False	False	False		Supervision entering state
R1	Bool	false	Non-retain	False	False	False	False		Reserved
A1	Bool	false	Non-retain	False	False	False	False		Error is acknowledged
S0	Bool	false	Non-retain	False	False	False	False		Step is deactivated
L0	Bool	false	Non-retain	False	False	False	False		Interlock entering state
V0	Bool	false	Non-retain	False	False	False	False		Supervision leaving state
X	Bool	false	Non-retain	False	False	False	False		Step is active
LA	Bool	false	Non-retain	False	False	False	False		Interlock is not satisfied
VA	Bool	false	Non-retain	False	False	False	False		Supervision active
RA	Bool	false	Non-retain	False	False	False	False		Reserved
AA	Bool	false	Non-retain	False	False	False	False		Reserved
SS	Bool	false	Non-retain	False	False	False	False		System-internal
LS	Bool	true	Non-retain	False	False	False	False		Direct result of the programmed interlock
VS	Bool	false	Non-retain	False	False	False	False		Direct result of the programmed supervision
SNO	Int	9	Non-retain	False	False	False	False		User step number
T	Time	T#0ms	Non-retain	False	False	False	False		Total step activation time
U	Time	T#0ms	Non-retain	False	False	False	False		Step activation time without disturbance
T_MAX	Time	T#10S	Non-retain	False	False	False	False		Maximal step activation time
T_WARN	Time	T#7S	Non-retain	False	False	False	False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
SM	Bool	false	Non-retain	False	False	False	False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False	False	False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False	False	False		System-internal
ODRZAVANJE_KLIP_1_LEVO	G7_StepPlus_V5		Non-retain	False	False	False	False		Step structure
S1	Bool	false	Non-retain	False	False	False	False		Step is activated
L1	Bool	false	Non-retain	False	False	False	False		interlock leaving state
V1	Bool	false	Non-retain	False	False	False	False		Supervision entering state
R1	Bool	false	Non-retain	False	False	False	False		Reserved
A1	Bool	false	Non-retain	False	False	False	False		Error is acknowledged
S0	Bool	false	Non-retain	False	False	False	False		Step is deactivated
L0	Bool	false	Non-retain	False	False	False	False		Interlock entering state
V0	Bool	false	Non-retain	False	False	False	False		Supervision leaving state
X	Bool	false	Non-retain	False	False	False	False		Step is active
LA	Bool	false	Non-retain	False	False	False	False		Interlock is not satisfied
VA	Bool	false	Non-retain	False	False	False	False		Supervision active
RA	Bool	false	Non-retain	False	False	False	False		Reserved
AA	Bool	false	Non-retain	False	False	False	False		Reserved
SS	Bool	false	Non-retain	False	False	False	False		System-internal
LS	Bool	true	Non-retain	False	False	False	False		Direct result of the programmed interlock
VS	Bool	false	Non-retain	False	False	False	False		Direct result of the programmed supervision
SNO	Int	10	Non-retain	False	False	False	False		User step number
T	Time	T#0ms	Non-retain	False	False	False	False		Total step activation time
U	Time	T#0ms	Non-retain	False	False	False	False		Step activation time without disturbance
T_MAX	Time	T#10S	Non-retain	False	False	False	False		Maximal step activation time
T_WARN	Time	T#7S	Non-retain	False	False	False	False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
SM	Bool	false	Non-retain	False	False	False	False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False	False	False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False	False	False		System-internal
Step6	G7_StepPlus_V5		Non-retain	False	False	False	False		Step structure
S1	Bool	false	Non-retain	False	False	False	False		Step is activated
L1	Bool	false	Non-retain	False	False	False	False		interlock leaving state
V1	Bool	false	Non-retain	False	False	False	False		Supervision entering state
R1	Bool	false	Non-retain	False	False	False	False		Reserved
A1	Bool	false	Non-retain	False	False	False	False		Error is acknowledged
S0	Bool	false	Non-retain	False	False	False	False		Step is deactivated
L0	Bool	false	Non-retain	False	False	False	False		Interlock entering state
V0	Bool	false	Non-retain	False	False	False	False		Supervision leaving state

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
X	Bool	false	Non-retain	False	False	False	False		Step is active
LA	Bool	false	Non-retain	False	False	False	False		Interlock is not satisfied
VA	Bool	false	Non-retain	False	False	False	False		Supervision active
RA	Bool	false	Non-retain	False	False	False	False		Reserved
AA	Bool	false	Non-retain	False	False	False	False		Reserved
SS	Bool	false	Non-retain	False	False	False	False		System-internal
LS	Bool	true	Non-retain	False	False	False	False		Direct result of the programmed interlock
VS	Bool	false	Non-retain	False	False	False	False		Direct result of the programmed supervision
SNO	Int	11	Non-retain	False	False	False	False		User step number
T	Time	T#0ms	Non-retain	False	False	False	False		Total step activation time
U	Time	T#0ms	Non-retain	False	False	False	False		Step activation time without disturbance
T_MAX	Time	T#10S	Non-retain	False	False	False	False		Maximal step activation time
T_WARN	Time	T#7S	Non-retain	False	False	False	False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
SM	Bool	false	Non-retain	False	False	False	False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False	False	False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False	False	False		System-internal
▼ Step7	G7_StepPlus_V5		Non-retain	False	False	False	False		Step structure
S1	Bool	false	Non-retain	False	False	False	False		Step is activated
L1	Bool	false	Non-retain	False	False	False	False		interlock leaving state
V1	Bool	false	Non-retain	False	False	False	False		Supervision entering state
R1	Bool	false	Non-retain	False	False	False	False		Reserved
A1	Bool	false	Non-retain	False	False	False	False		Error is acknowledged
S0	Bool	false	Non-retain	False	False	False	False		Step is deactivated
L0	Bool	false	Non-retain	False	False	False	False		Interlock entering state
V0	Bool	false	Non-retain	False	False	False	False		Supervision leaving state
X	Bool	false	Non-retain	False	False	False	False		Step is active
LA	Bool	false	Non-retain	False	False	False	False		Interlock is not satisfied
VA	Bool	false	Non-retain	False	False	False	False		Supervision active
RA	Bool	false	Non-retain	False	False	False	False		Reserved
AA	Bool	false	Non-retain	False	False	False	False		Reserved
SS	Bool	false	Non-retain	False	False	False	False		System-internal
LS	Bool	true	Non-retain	False	False	False	False		Direct result of the programmed interlock
VS	Bool	false	Non-retain	False	False	False	False		Direct result of the programmed supervision
SNO	Int	12	Non-retain	False	False	False	False		User step number
T	Time	T#0ms	Non-retain	False	False	False	False		Total step activation time
U	Time	T#0ms	Non-retain	False	False	False	False		Step activation time without disturbance
T_MAX	Time	T#10S	Non-retain	False	False	False	False		Maximal step activation time
T_WARN	Time	T#7S	Non-retain	False	False	False	False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
SM	Bool	false	Non-retain	False	False	False	False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False	False	False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False	False	False		System-internal
▼ Step8	G7_StepPlus_V5		Non-retain	False	False	False	False		Step structure
S1	Bool	false	Non-retain	False	False	False	False		Step is activated
L1	Bool	false	Non-retain	False	False	False	False		interlock leaving state
V1	Bool	false	Non-retain	False	False	False	False		Supervision entering state
R1	Bool	false	Non-retain	False	False	False	False		Reserved
A1	Bool	false	Non-retain	False	False	False	False		Error is acknowledged
S0	Bool	false	Non-retain	False	False	False	False		Step is deactivated
L0	Bool	false	Non-retain	False	False	False	False		Interlock entering state
V0	Bool	false	Non-retain	False	False	False	False		Supervision leaving state
X	Bool	false	Non-retain	False	False	False	False		Step is active
LA	Bool	false	Non-retain	False	False	False	False		Interlock is not satisfied
VA	Bool	false	Non-retain	False	False	False	False		Supervision active
RA	Bool	false	Non-retain	False	False	False	False		Reserved
AA	Bool	false	Non-retain	False	False	False	False		Reserved
SS	Bool	false	Non-retain	False	False	False	False		System-internal
LS	Bool	true	Non-retain	False	False	False	False		Direct result of the programmed interlock
VS	Bool	false	Non-retain	False	False	False	False		Direct result of the programmed supervision

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
SNO	Int	13	Non-retain	False	False	False	False		User step number
T	Time	T#0ms	Non-retain	False	False	False	False		Total step activation time
U	Time	T#0ms	Non-retain	False	False	False	False		Step activation time without disturbance
T_MAX	Time	T#10S	Non-retain	False	False	False	False		Maximal step activation time
T_WARN	Time	T#7S	Non-retain	False	False	False	False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
SM	Bool	false	Non-retain	False	False	False	False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False	False	False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False	False	False		System-internal
▼ Step9	G7_StepPlus_V5		Non-retain	False	False	False	False		Step structure
S1	Bool	false	Non-retain	False	False	False	False		Step is activated
L1	Bool	false	Non-retain	False	False	False	False		interlock leaving state
V1	Bool	false	Non-retain	False	False	False	False		Supervision entering state
R1	Bool	false	Non-retain	False	False	False	False		Reserved
A1	Bool	false	Non-retain	False	False	False	False		Error is acknowledged
S0	Bool	false	Non-retain	False	False	False	False		Step is deactivated
L0	Bool	false	Non-retain	False	False	False	False		Interlock entering state
V0	Bool	false	Non-retain	False	False	False	False		Supervision leaving state
X	Bool	false	Non-retain	False	False	False	False		Step is active
LA	Bool	false	Non-retain	False	False	False	False		Interlock is not satisfied
VA	Bool	false	Non-retain	False	False	False	False		Supervision active
RA	Bool	false	Non-retain	False	False	False	False		Reserved
AA	Bool	false	Non-retain	False	False	False	False		Reserved
SS	Bool	false	Non-retain	False	False	False	False		System-internal
LS	Bool	true	Non-retain	False	False	False	False		Direct result of the programmed interlock
VS	Bool	false	Non-retain	False	False	False	False		Direct result of the programmed supervision
SNO	Int	14	Non-retain	False	False	False	False		User step number
T	Time	T#0ms	Non-retain	False	False	False	False		Total step activation time
U	Time	T#0ms	Non-retain	False	False	False	False		Step activation time without disturbance
T_MAX	Time	T#10S	Non-retain	False	False	False	False		Maximal step activation time
T_WARN	Time	T#7S	Non-retain	False	False	False	False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
SM	Bool	false	Non-retain	False	False	False	False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False	False	False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False	False	False		System-internal
▼ Step10	G7_StepPlus_V5		Non-retain	False	False	False	False		Step structure
S1	Bool	false	Non-retain	False	False	False	False		Step is activated
L1	Bool	false	Non-retain	False	False	False	False		interlock leaving state
V1	Bool	false	Non-retain	False	False	False	False		Supervision entering state
R1	Bool	false	Non-retain	False	False	False	False		Reserved
A1	Bool	false	Non-retain	False	False	False	False		Error is acknowledged
S0	Bool	false	Non-retain	False	False	False	False		Step is deactivated
L0	Bool	false	Non-retain	False	False	False	False		Interlock entering state
V0	Bool	false	Non-retain	False	False	False	False		Supervision leaving state
X	Bool	false	Non-retain	False	False	False	False		Step is active
LA	Bool	false	Non-retain	False	False	False	False		Interlock is not satisfied
VA	Bool	false	Non-retain	False	False	False	False		Supervision active
RA	Bool	false	Non-retain	False	False	False	False		Reserved
AA	Bool	false	Non-retain	False	False	False	False		Reserved
SS	Bool	false	Non-retain	False	False	False	False		System-internal
LS	Bool	true	Non-retain	False	False	False	False		Direct result of the programmed interlock
VS	Bool	false	Non-retain	False	False	False	False		Direct result of the programmed supervision
SNO	Int	15	Non-retain	False	False	False	False		User step number
T	Time	T#0ms	Non-retain	False	False	False	False		Total step activation time
U	Time	T#0ms	Non-retain	False	False	False	False		Step activation time without disturbance
T_MAX	Time	T#10S	Non-retain	False	False	False	False		Maximal step activation time
T_WARN	Time	T#7S	Non-retain	False	False	False	False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
SM	Bool	false	Non-retain	False	False	False	False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False	False	False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False	False	False		System-internal
Temp									
Constant									

Alarms

Enable alarms	True

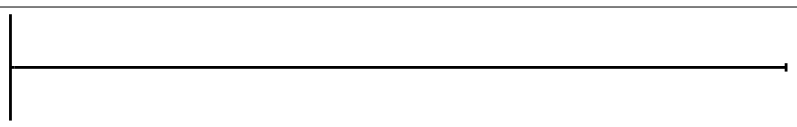
Category	Category enabler	Display class
Error		0
Warning		0
Info		0
Category 4		0
Category 5		0
Category 6		0
Category 7		0
Category 8		0

Category for interlocks and supervisions	Error	**Category for GRAPH warnings:**	Warning

Symbol	Address	Type	Comment

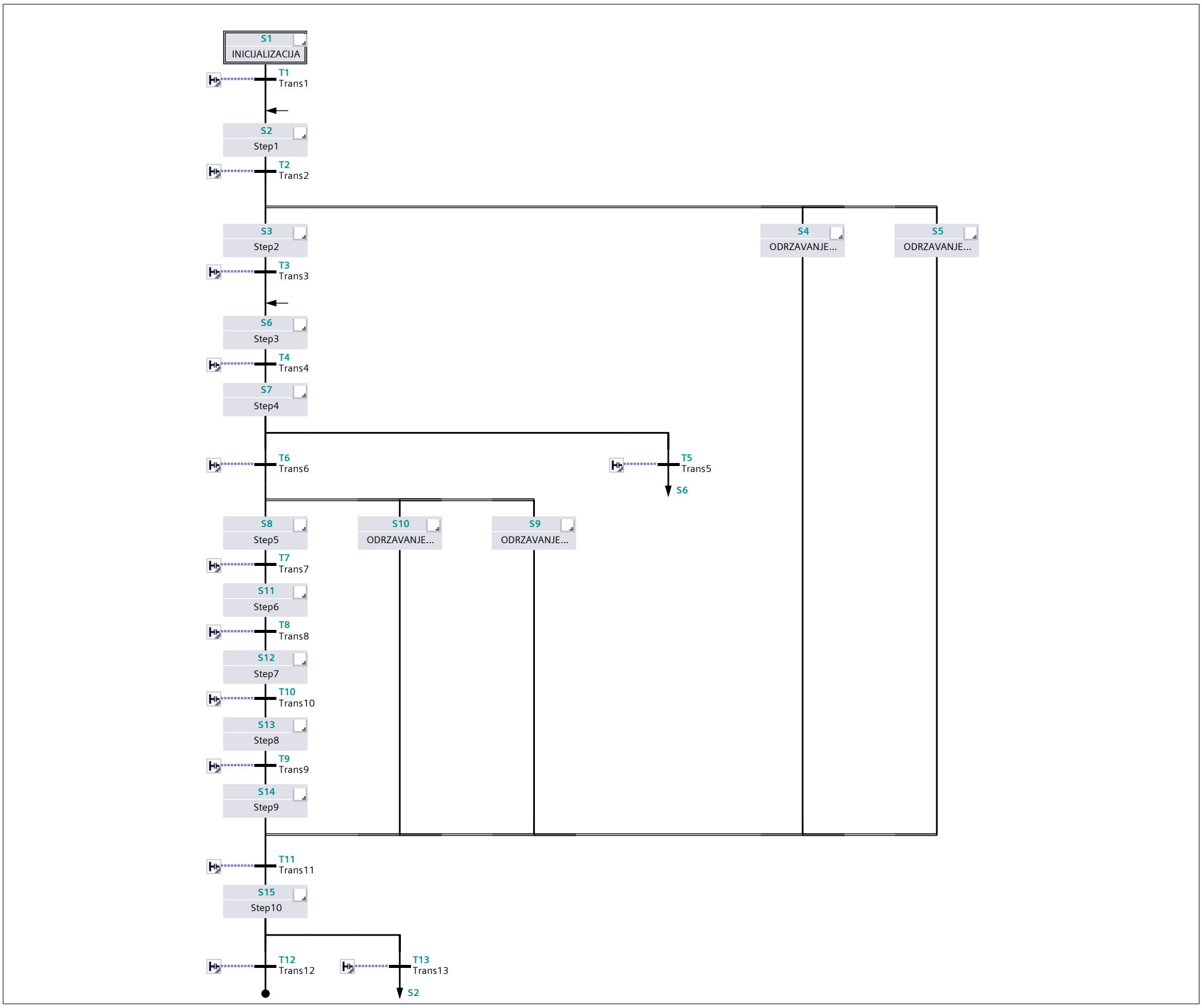
Permanent pre-instructions

1:



Sequences (1)

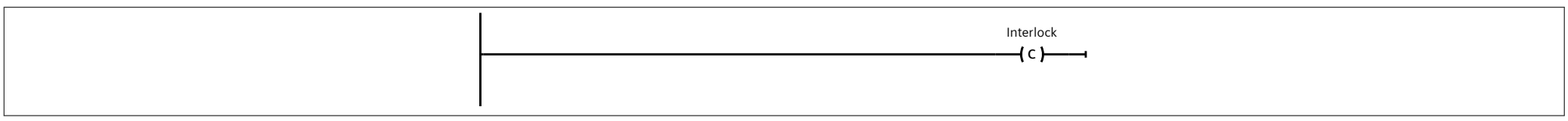
1:



S1 - [Initial step]:INICIJALIZACIJA

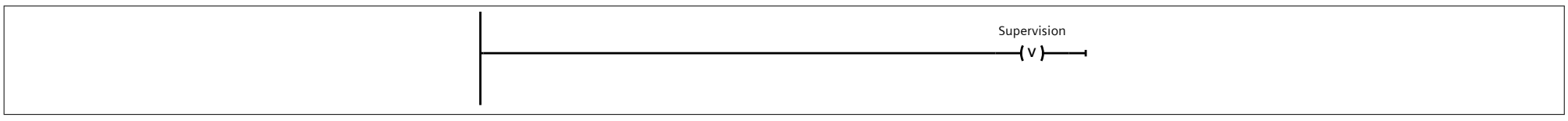
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

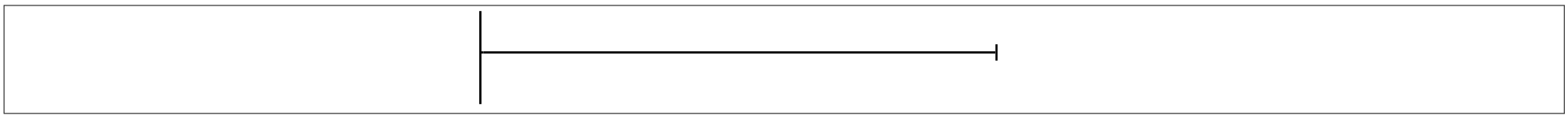
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
	S1	CS	"COUNTER_OUTER_LOOP", 2

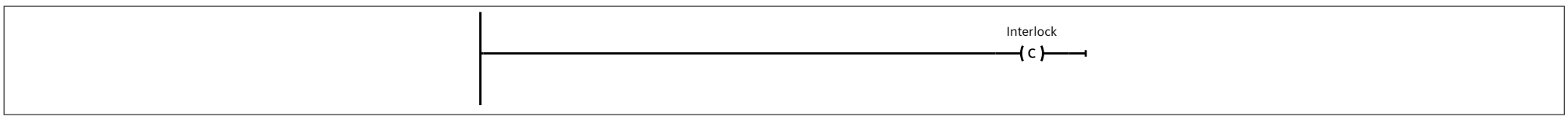
T1:Trans1



S3:Step2

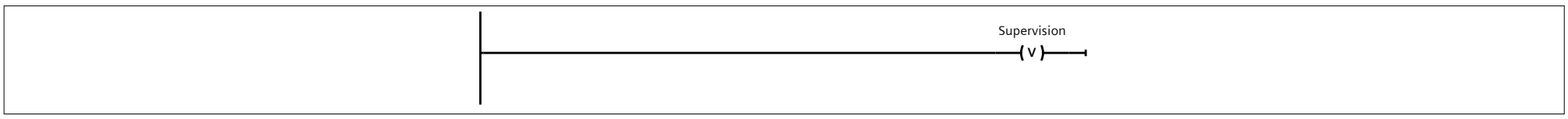
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

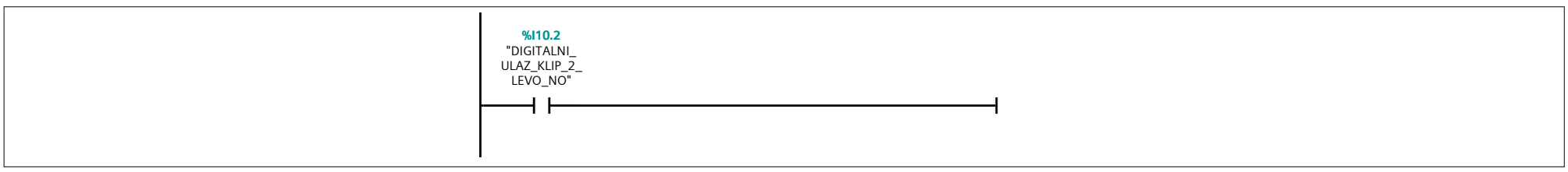
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
	S1	S	"DIGITALNI_IZLAZ_KLIP_2_LEVO"
	S1	R	"DIGITALNI_IZLAZ_KLIP_2_DESNO"
	S1	CD	"BROJAC_ODRZAVANJE_KLIP_4"

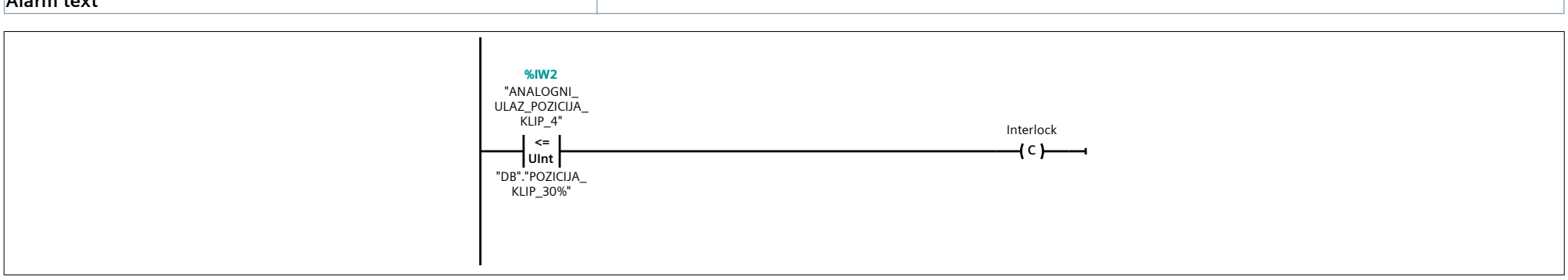
T3:Trans3



S5:ODRZAVANJE_KLIP_4_DESNO

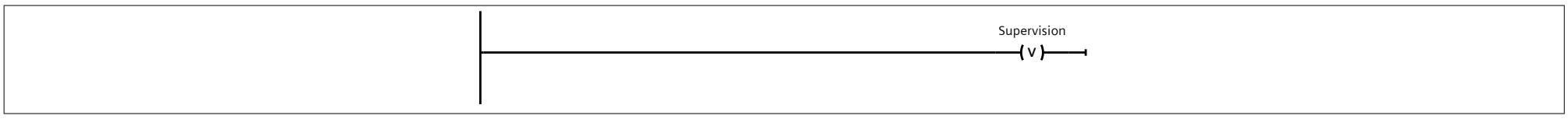
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

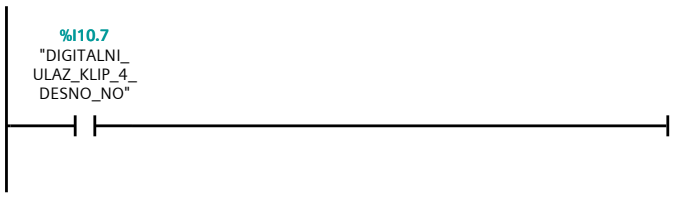
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
C		R	"DIGITALNI_IZLAZ_KLIP_4_LEVO"

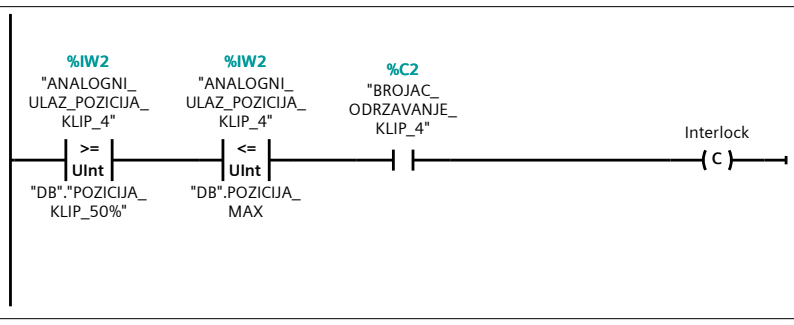
T11:Trans11



S4:ODRZAVANJE_KLIP_4_LEVO

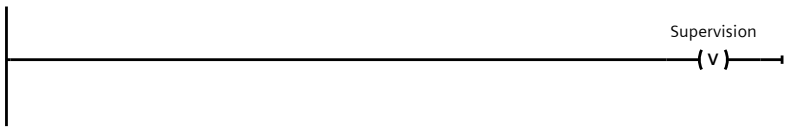
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

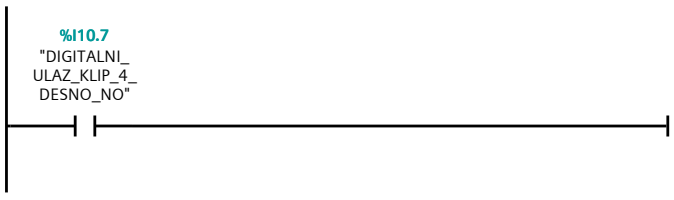
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
C		S	"DIGITALNI_IZLAZ_KLIP_4_LEVO"

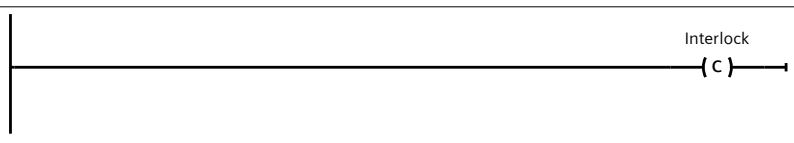
T11:Trans11



S2:Step1

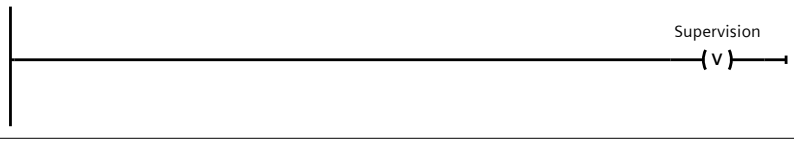
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

Supervision alarm	
Alarm text	

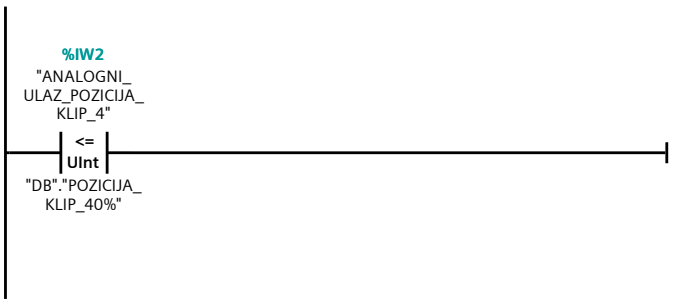


Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
	S1	S	"DIGITALNI_IZLAZ_KLIP_4_LEVO"
	S1	CD	"COUNTER_OUTER_LOOP"
	S1	CS	"COUNTER_INNER_LOOP", 2
	S1	CS	"BROJAC_ODRZAVANJE_KLIP_4", 6

Interlock	Event	Qualifier	Action

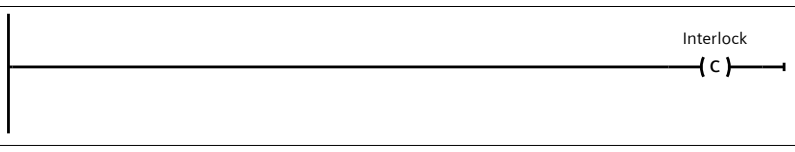
T2:Trans2



S6:Step3

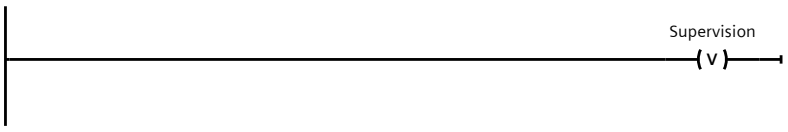
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

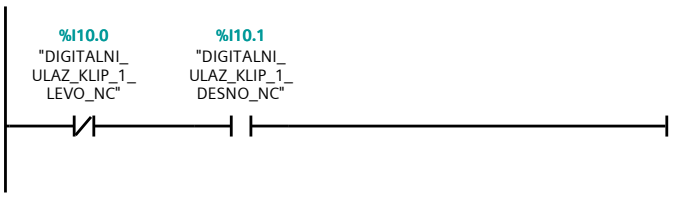
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
	S1	S	"DIGITALNI_IZLAZ_KLIP_1_LEVO"
	S1	R	"DIGITALNI_IZLAZ_KLIP_1_DESNO"
	S1	CD	"COUNTER_INNER_LOOP"

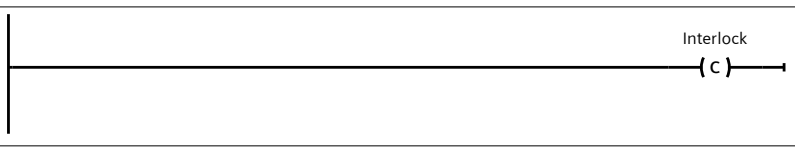
T4:Trans4



S7:Step4

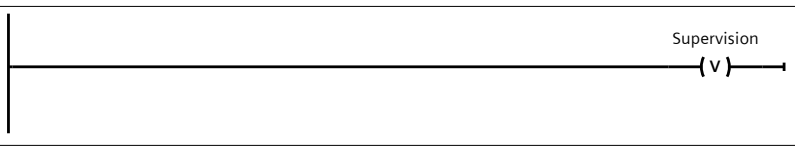
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

Supervision alarm	
Alarm text	

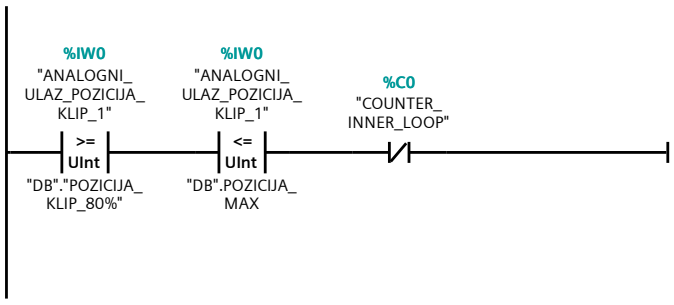


Actions:

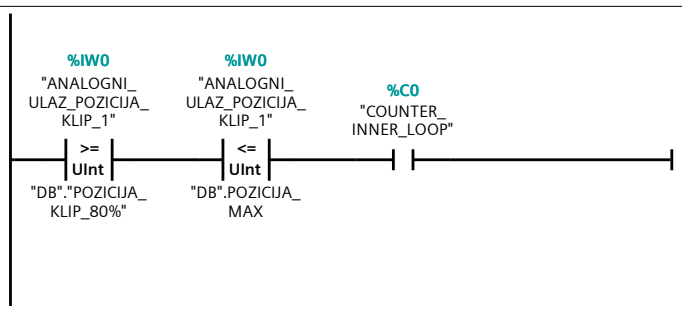
Actions:			
Interlock	**Event**	**Qualifier**	**Action**
	S1	R	"DIGITALNI_IZLAZ_KLIP_1_LEVO"

Interlock	Event	Qualifier	Action
	S1	S	"DIGITALNI_IZLAZ_KLIP_1_DESNO"

T6:Trans6



T5:Trans5



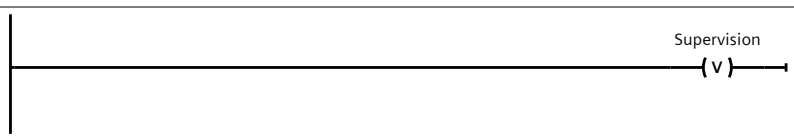
S8:Step5

Interlock -(c)-:

Interlock alarm	
Alarm text	

Supervision -(v)-:

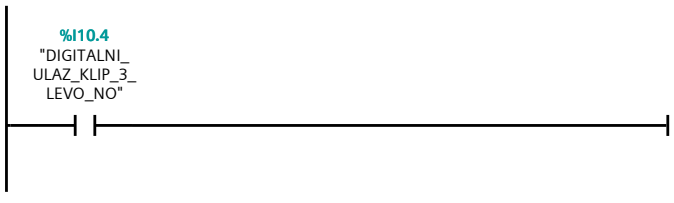
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
	S1	S	"DIGITALNI_IZLAZ_KLIP_3_LEVO"
	S1	CD	"BROJAC_ODRZAVANJE_KLIP_4"

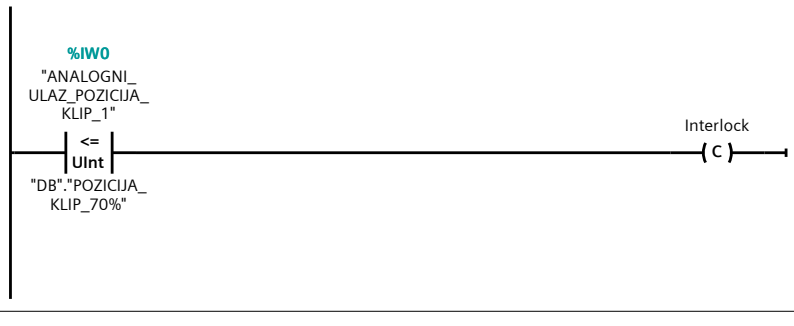
T7:Trans7



S9:ODRZAVANJE_KLIP_1_DESNO

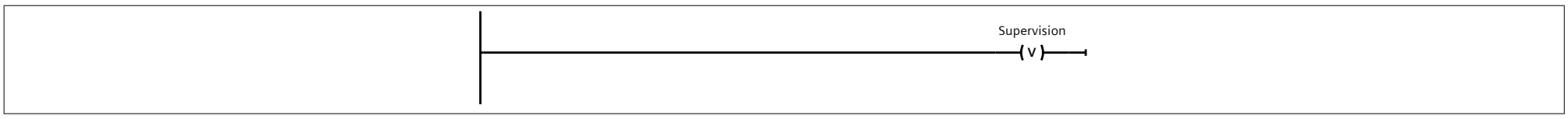
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

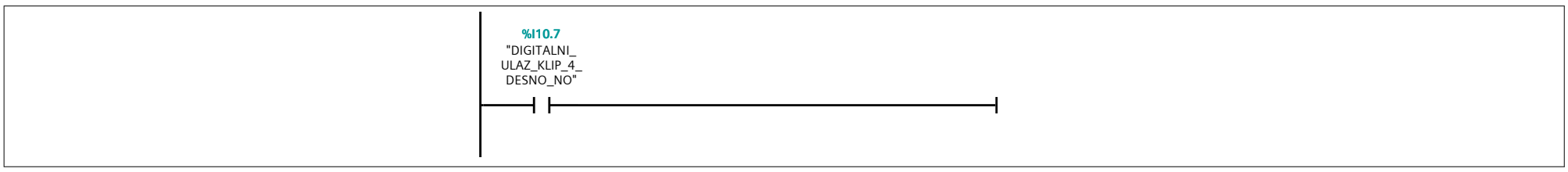
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
C		S	"DIGITALNI_IZLAZ_KLIP_1_DESNO"
C		R	"DIGITALNI_IZLAZ_KLIP_1_LEVO"

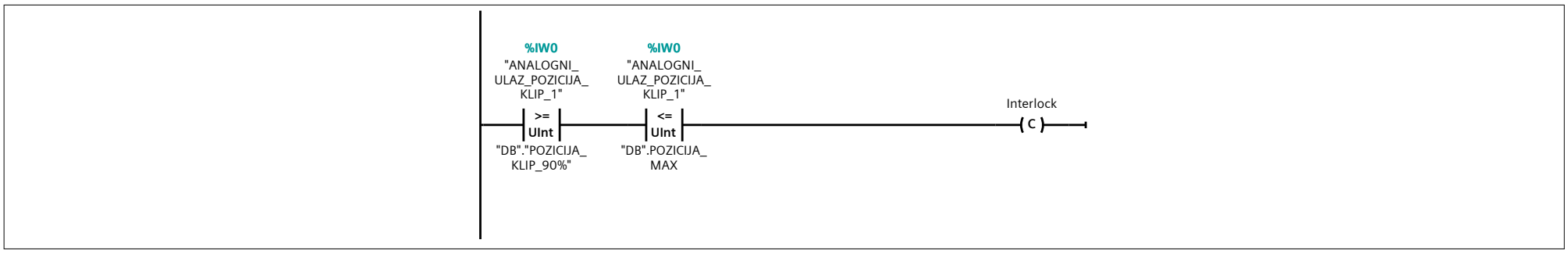
T11:Trans11



S10:ODRZAVANJE_KLIP_1_LEVO

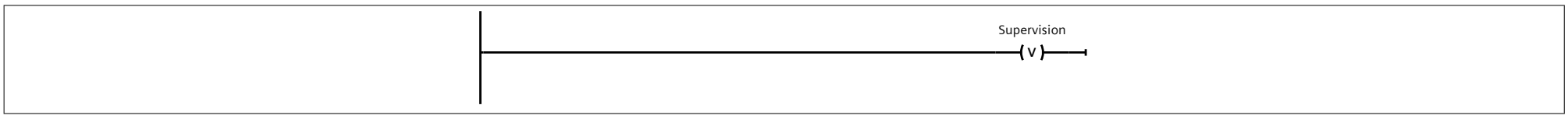
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

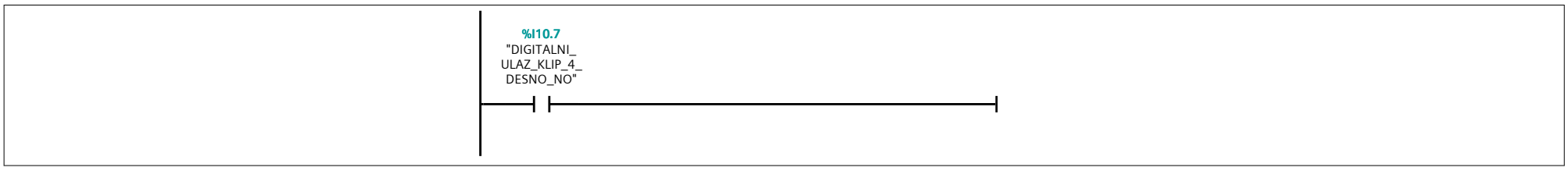
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
C		S	"DIGITALNI_IZLAZ_KLIP_1_LEVO"
C		R	"DIGITALNI_IZLAZ_KLIP_1_DESNO"

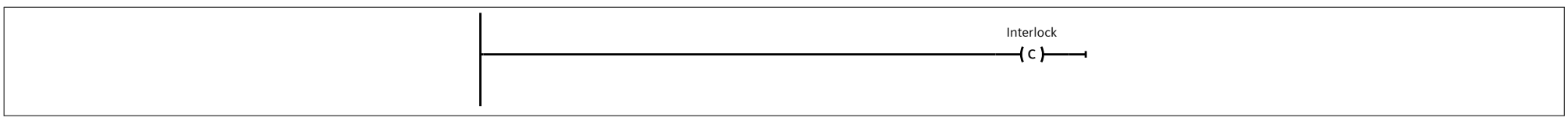
T11:Trans11



S11:Step6

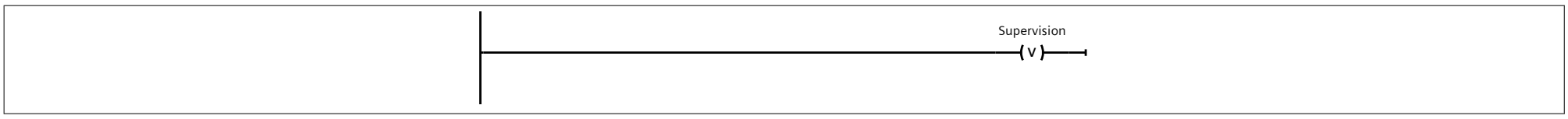
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

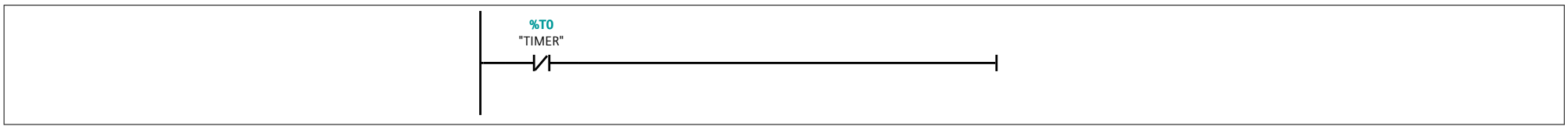
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
	S1	TL	"TIMER", S5T#12S
	S1	CD	"BROJAC_ODRZAVANJE_KLIP_4"

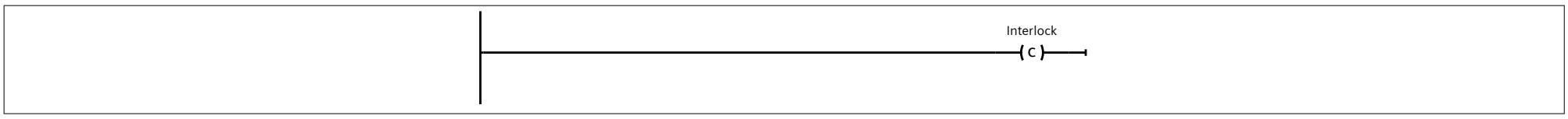
T8:Trans8



S12:Step7

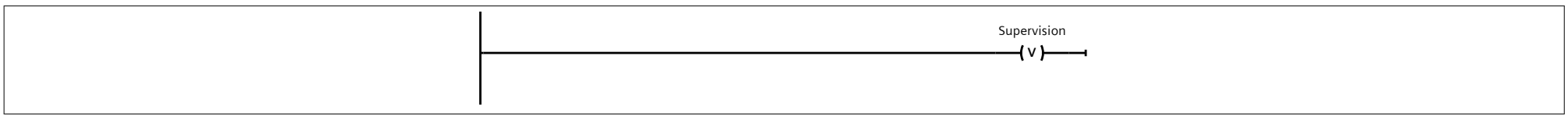
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

Supervision alarm	
Alarm text	



Actions:

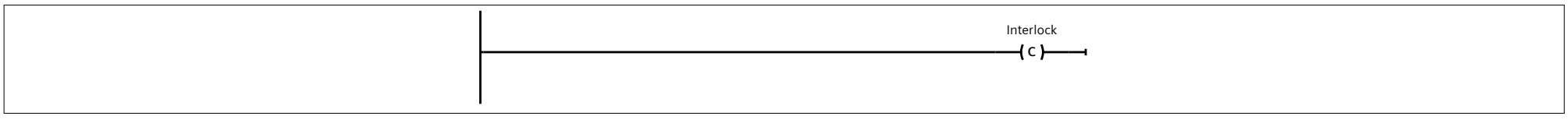
Actions:			
Interlock	**Event**	**Qualifier**	**Action**
	S1	S	"DIGITALNI_IZLAZ_KLIP_2_DESNO"
	S1	R	"DIGITALNI_IZLAZ_KLIP_2_LEVO"
	S1	CD	"BROJAC_ODRZAVANJE_KLIP_4"

T10:Trans10

S13:Step8

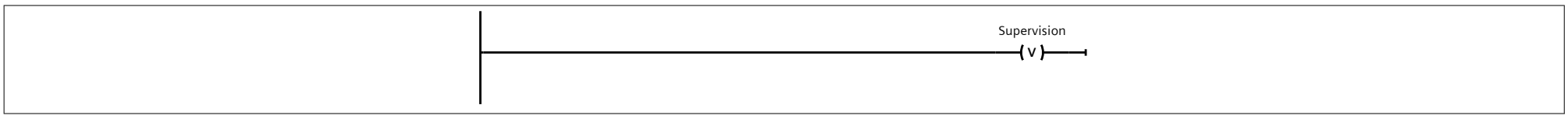
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

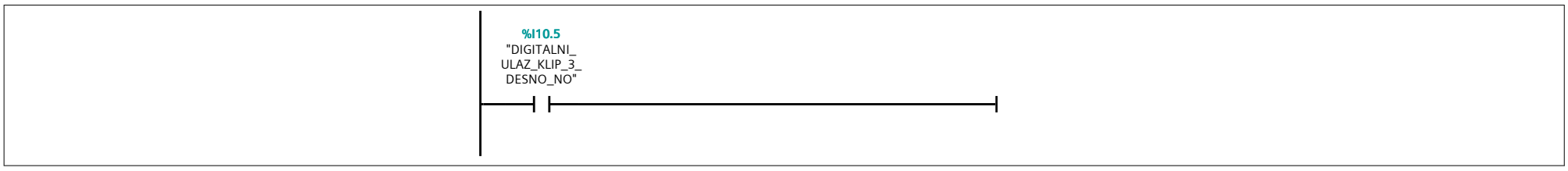
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
	S1	R	"DIGITALNI_IZLAZ_KLIP_3_LEVO"
	S1	CD	"BROJAC_ODRZAVANJE_KLIP_4"

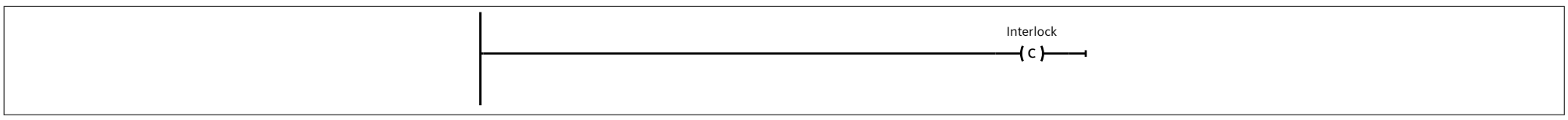
T9:Trans9



S14:Step9

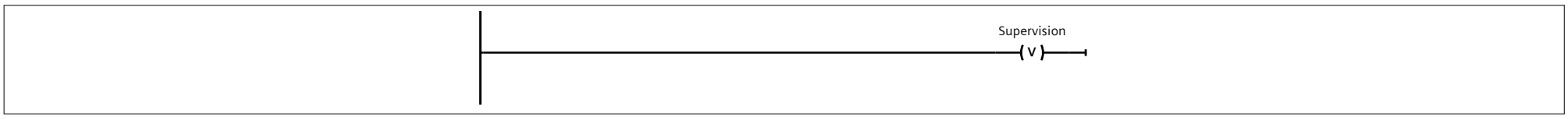
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

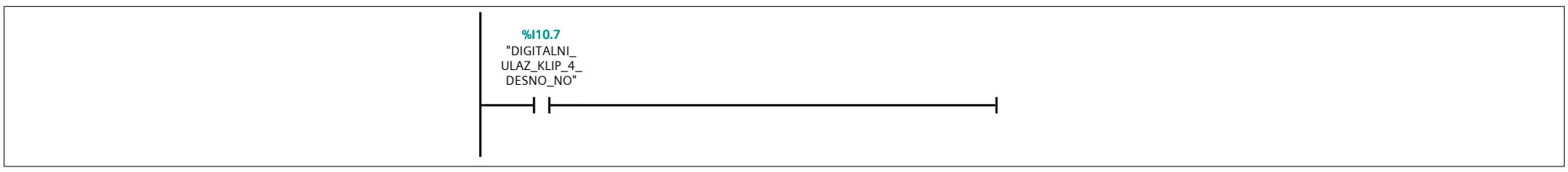
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
	S1	R	"DIGITALNI_IZLAZ_KLIP_4_LEVO"
	S1	CD	"BROJAC_ODRZAVANJE_KLIP_4"

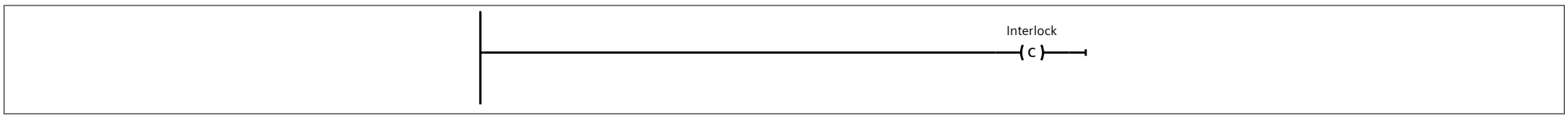
T11:Trans11



S15:Step10

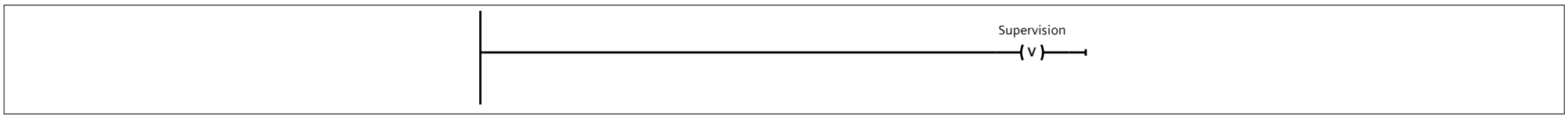
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

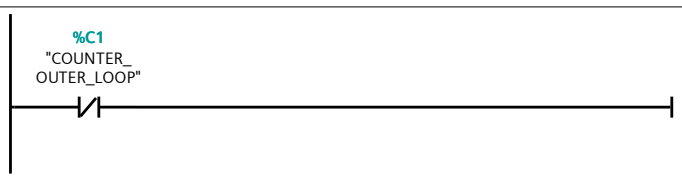
Supervision alarm	
Alarm text	



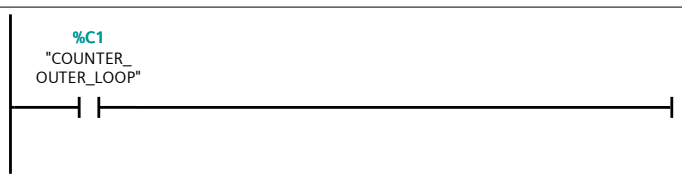
Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
	S1	S	"DIGITALNI_IZLAZ_KLIP_1_DESNO"
	S1	R	"DIGITALNI_IZLAZ_KLIP_1_LEVO"
	S1	CS	"BROJAC_ODRZAVANJE_KLIP_4", 10

T12:Trans12

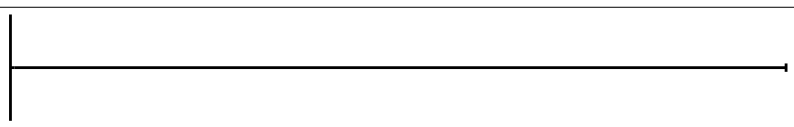


T13:Trans13



Permanent post-instructions

1:



DB [DB1]

DB Properties							
General							
Name	DB	**Number**	1	**Type**	DB	**Language**	DB
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Start value	Retain	Accessible from HMI/OPC UA	Writable from HMI/OPC UA	Visible in HMI engineering	Setpoint	Supervision	Comment
▼ Static									
POZICIJA_KLIP_30%	Word	8310	False	True	True	True	False		
POZICIJA_KLIP_40%	Word	11080	False	True	True	True	False		
POZICIJA_KLIP_50%	Word	13850	False	True	True	True	False		
POZICIJA_KLIP_70%	Word	19390	False	True	True	True	False		
POZICIJA_KLIP_90%	Word	24930	False	True	True	True	False		
POZICIJA_KLIP_80%	Word	22160	False	True	True	True	False		
POZICIJA_MAX	Word	27700	False	True	True	True	False		