

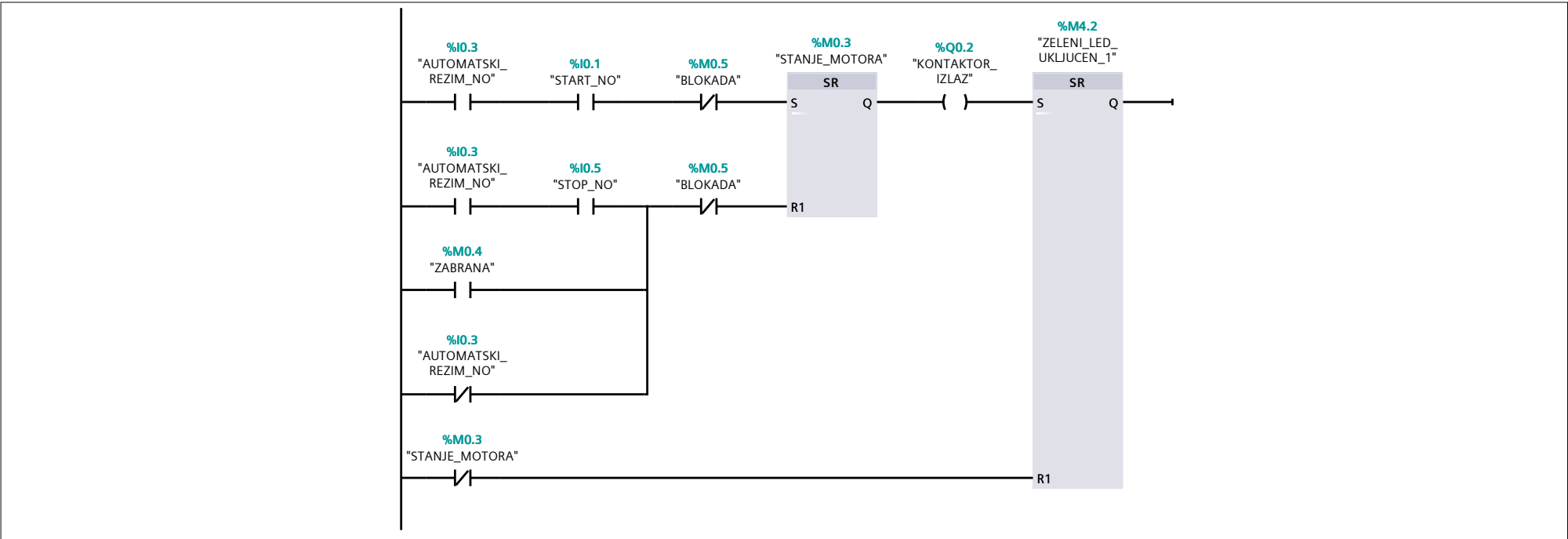
lab1 / PLC_1 [CPU 1214C AC/DC/Rly] / Program blocks

Main [OB1]

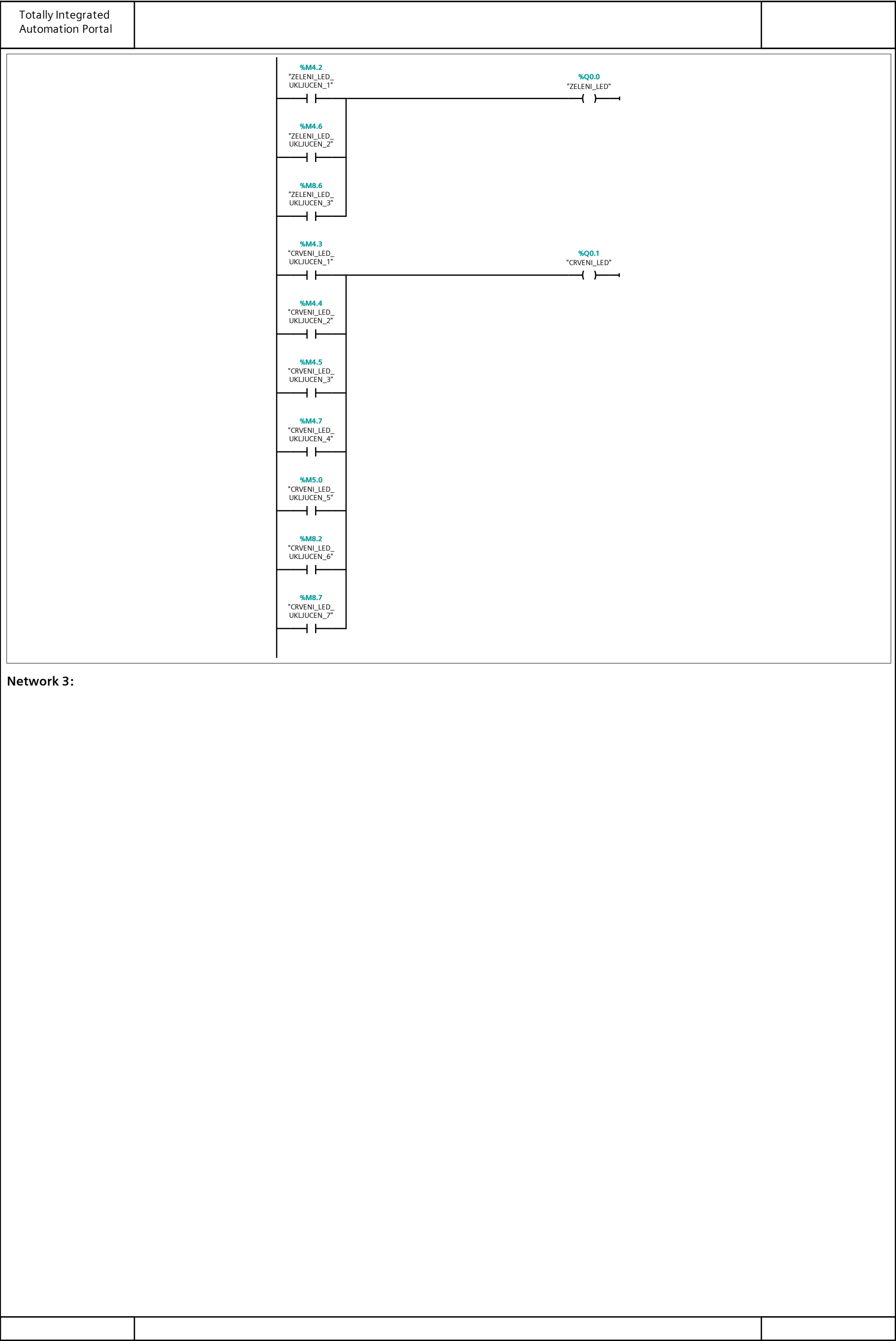
Main Properties							
General							
Name	Main	Number	1	Type	OB	Language	LAD
Numbering	Automatic						
Information							
Title	"Main Program Sweep (Cycle)"	Author		Comment		Family	
Version	0.1	User-defined ID					

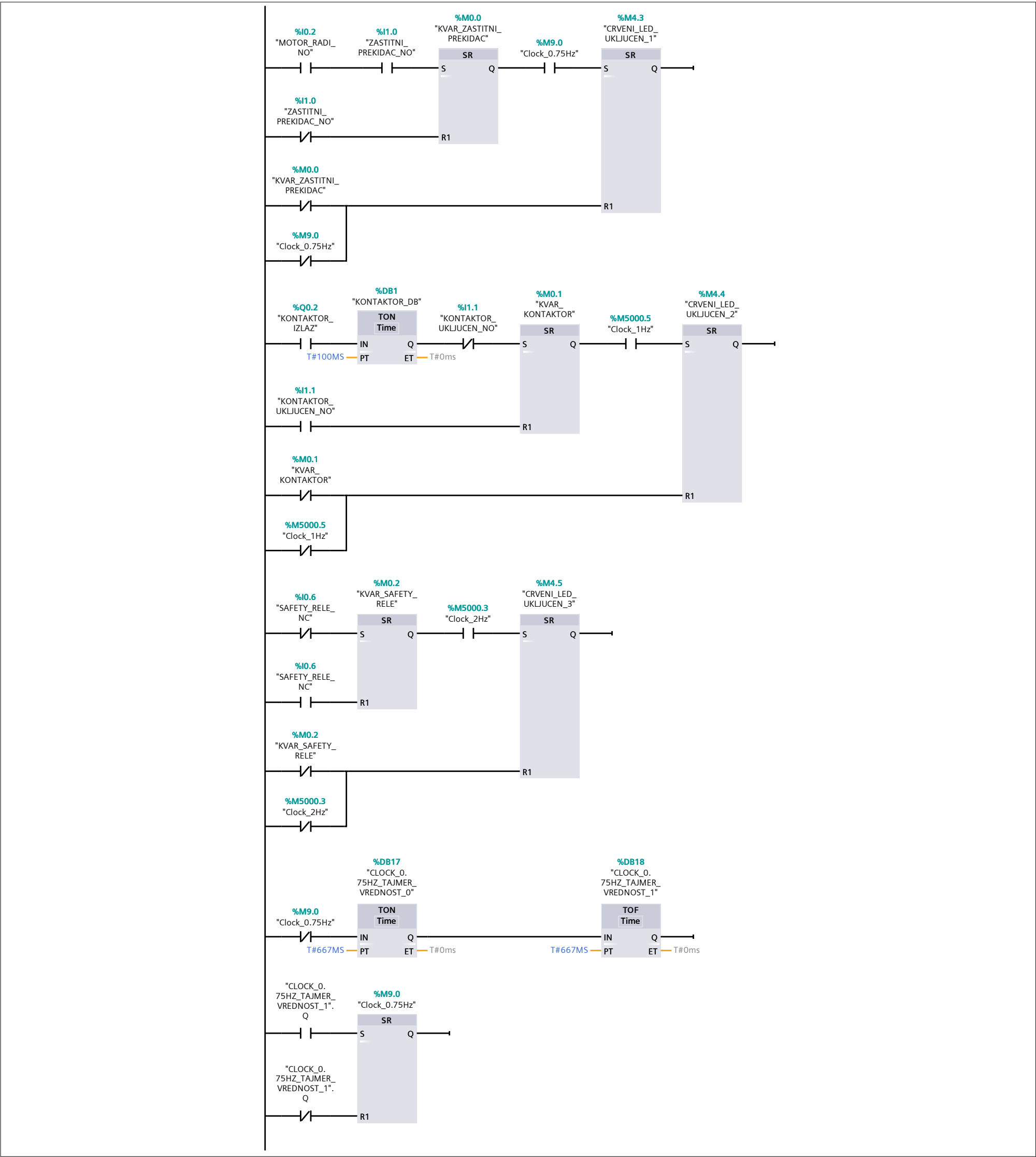
Main			
Name	Data type	Default value	Comment
▼ Input			
Initial_Call	Bool		Initial call of this OB
Remanence	Bool		=True, if remanent data are available
Temp			
Constant			

Network 1:



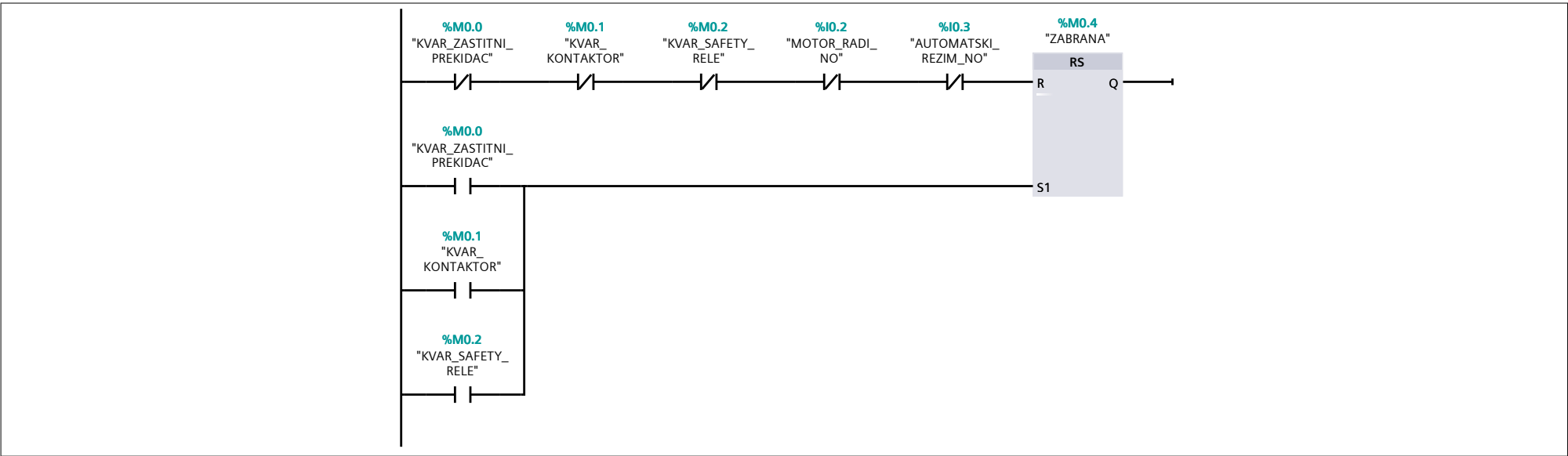
Network 2:



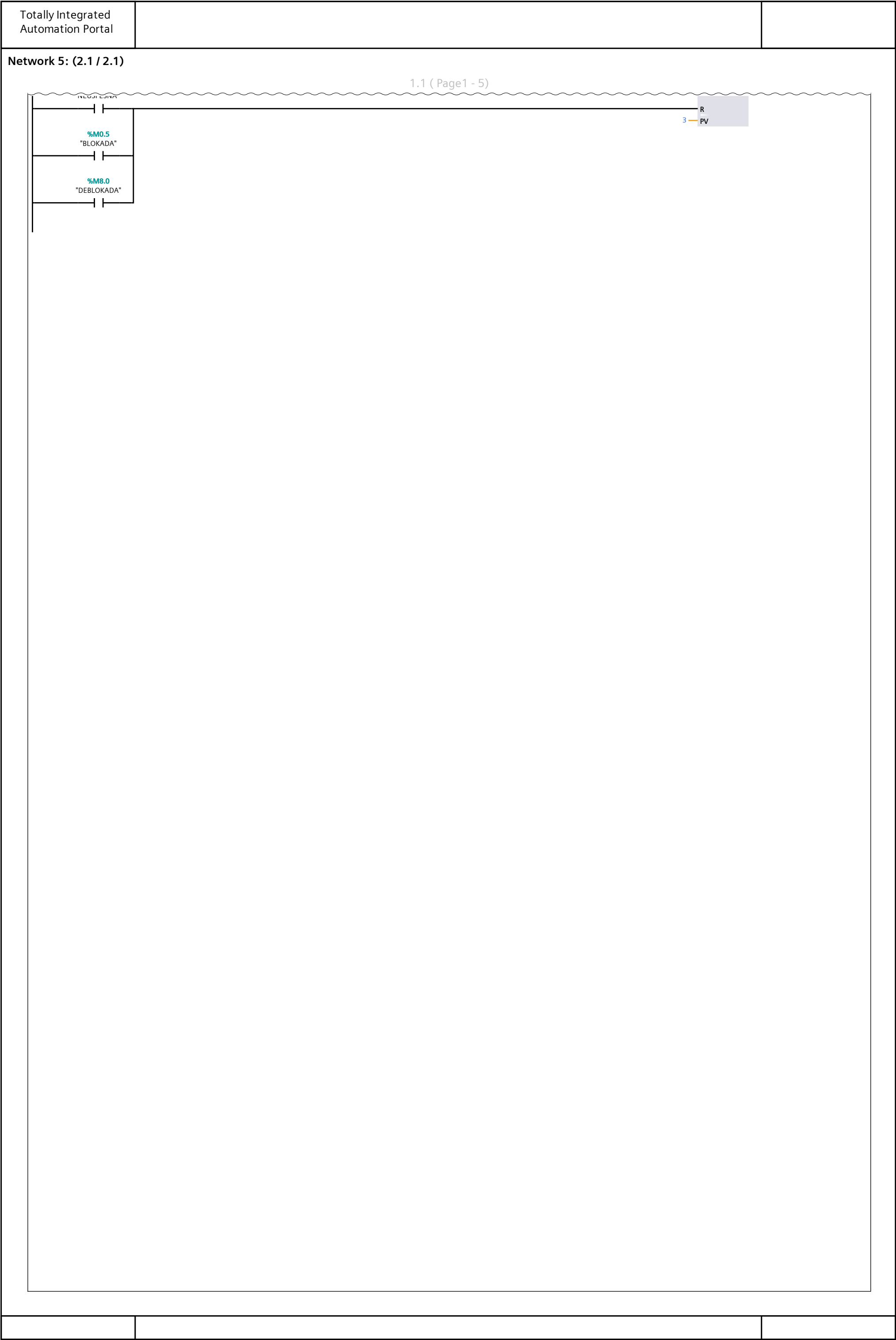


Network 4:

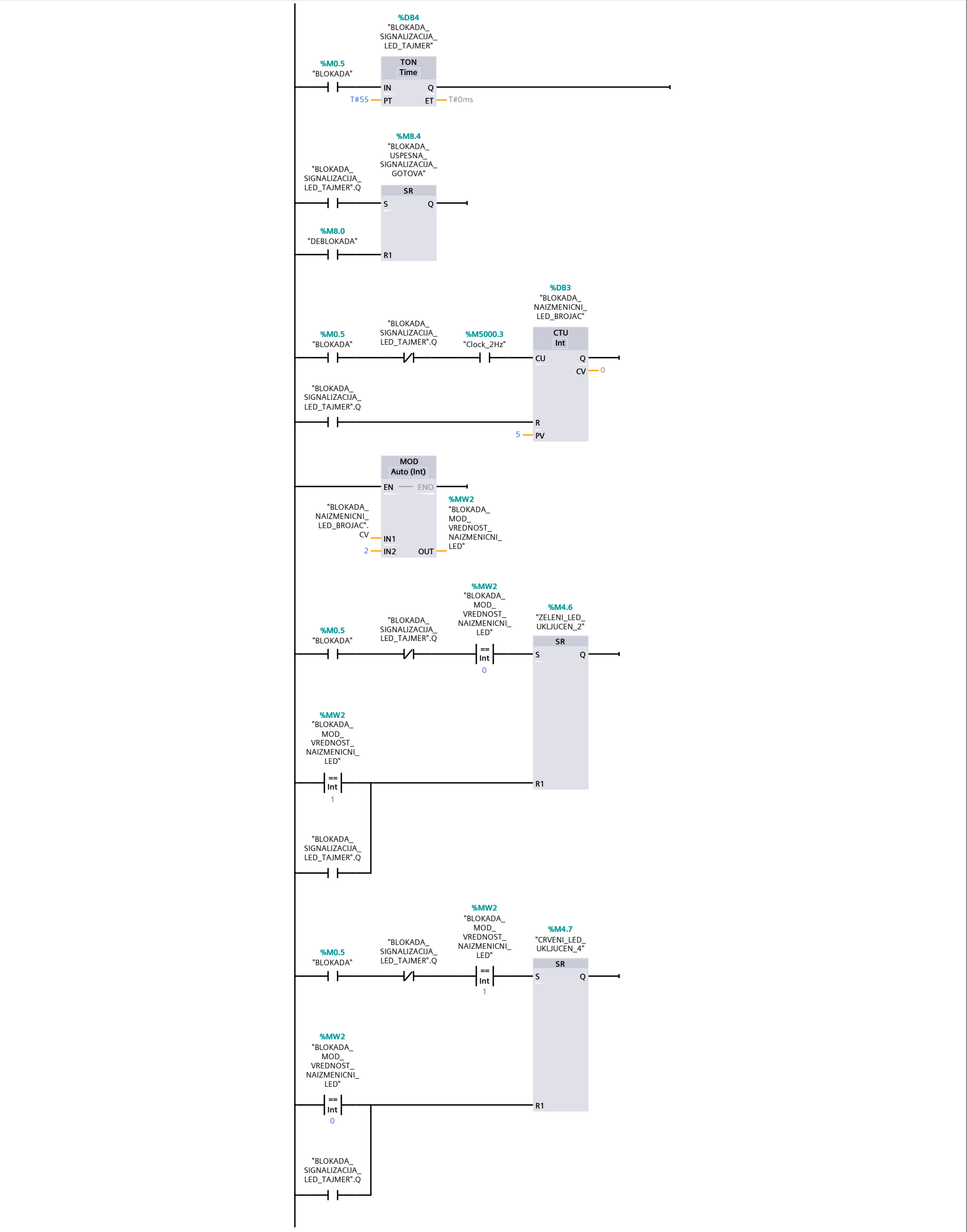
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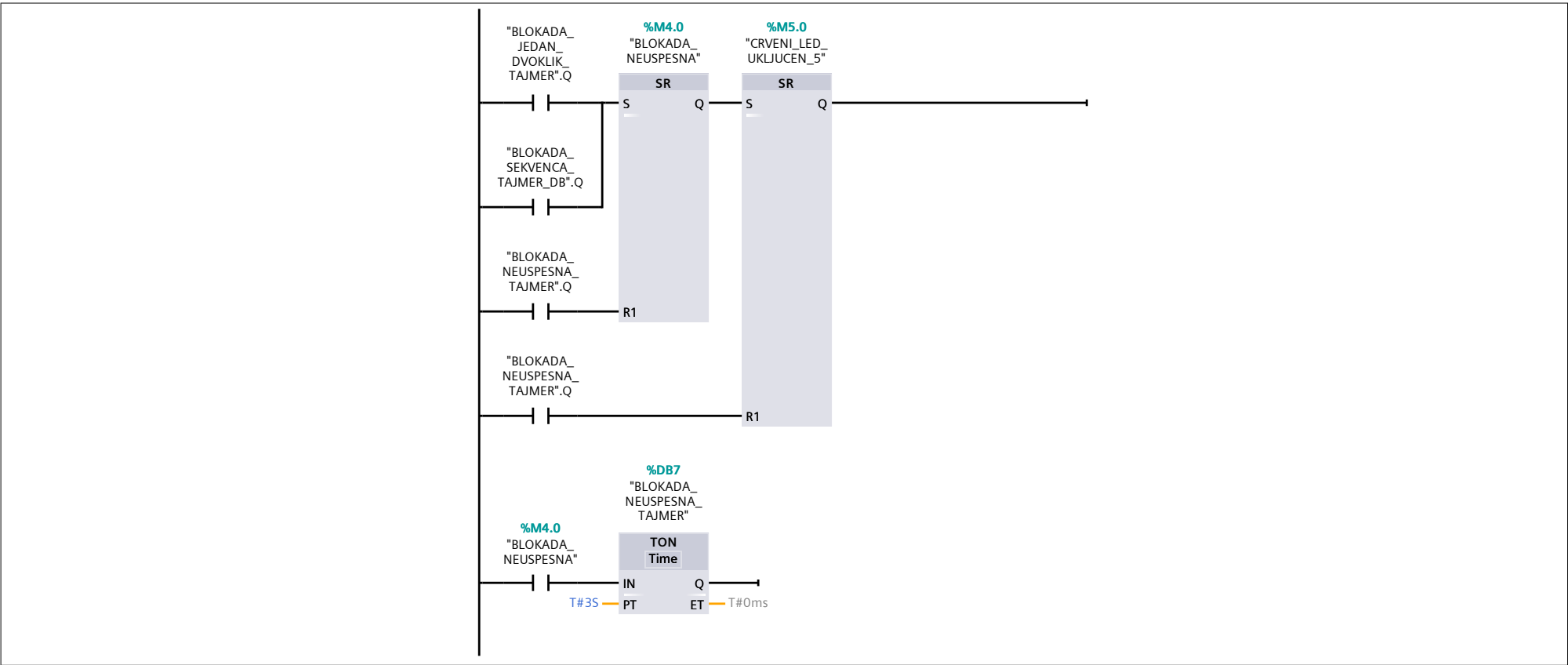
[illegible]



Network 6:



Network 7:

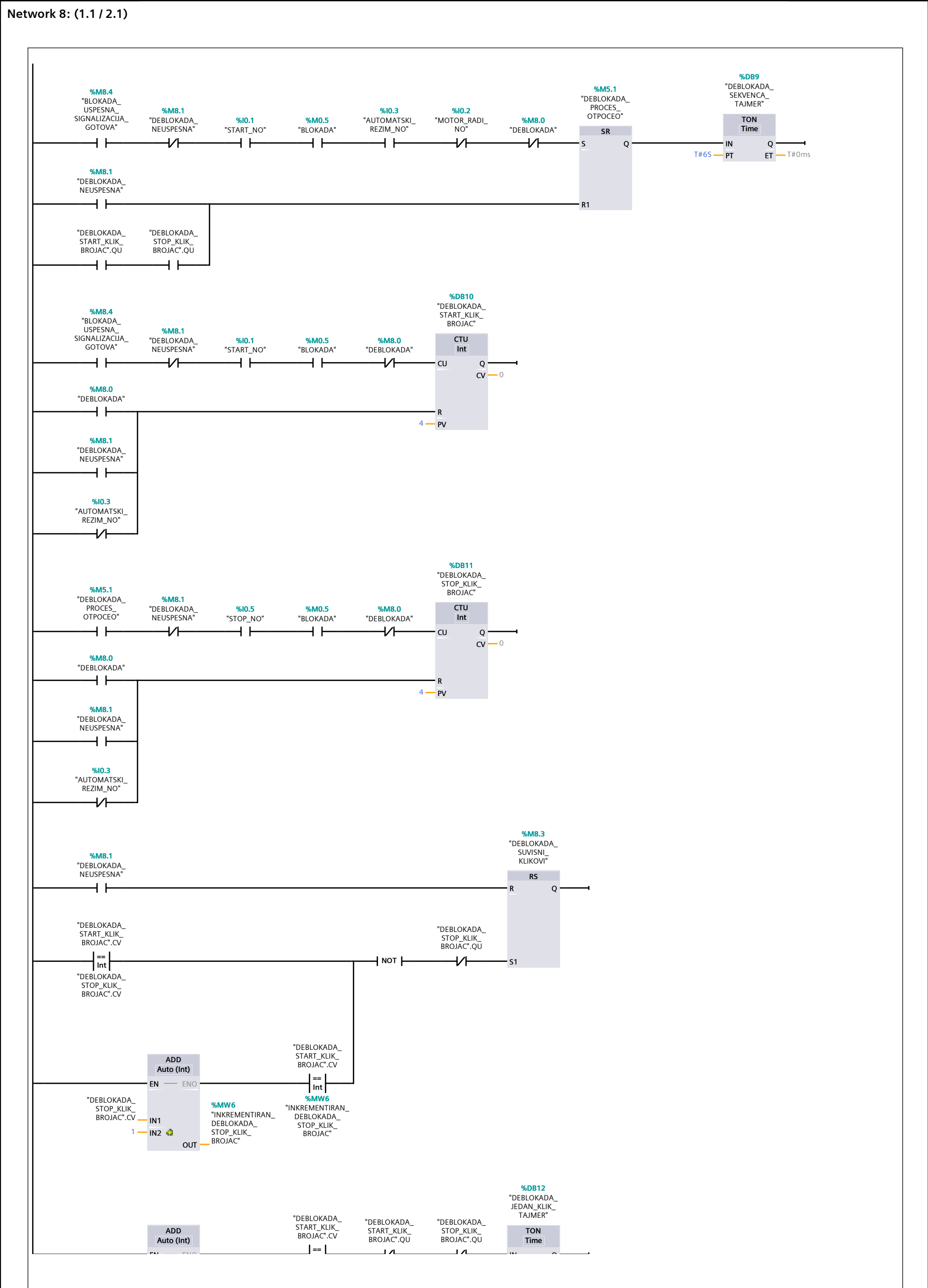


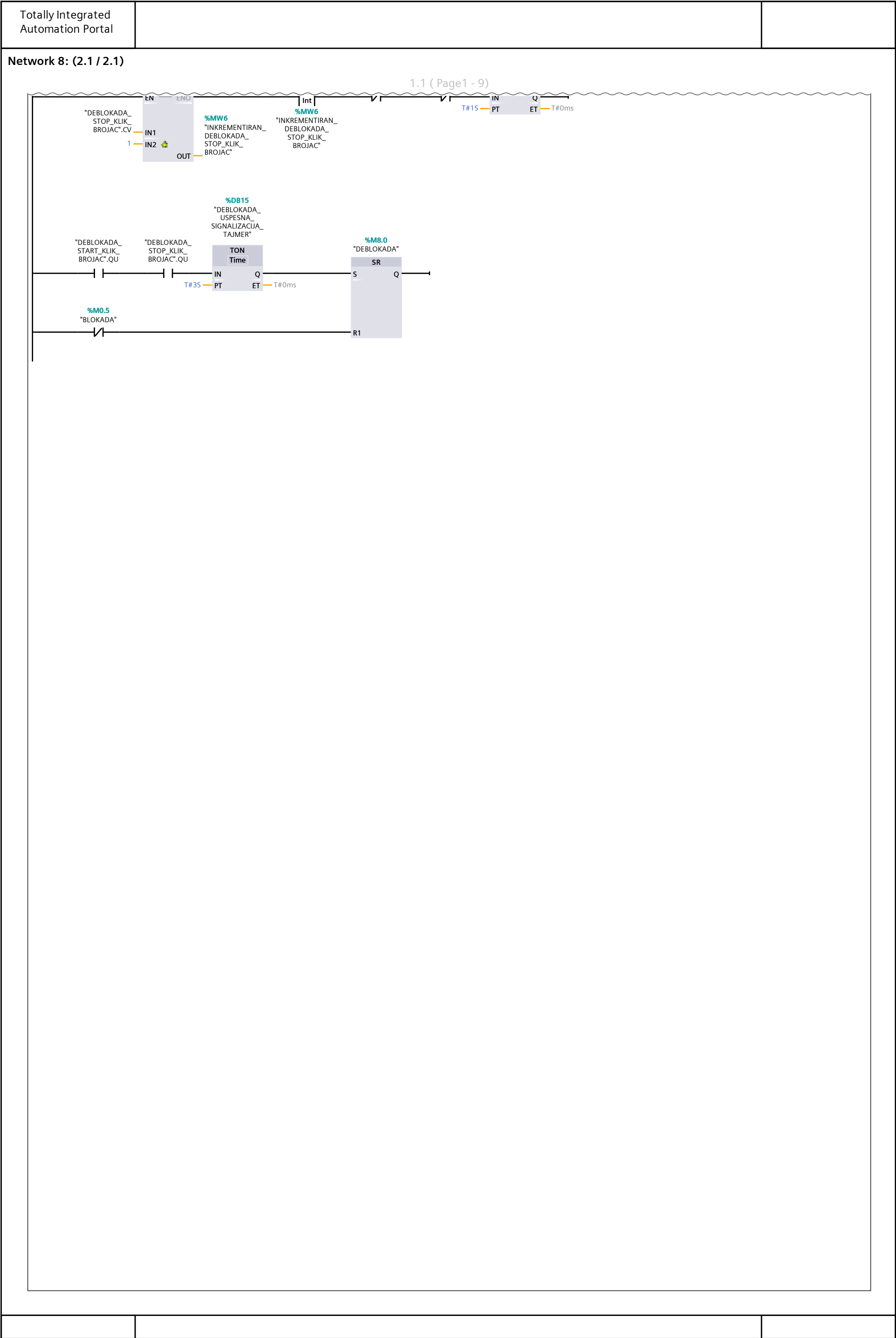
Network 8:

Network 8: (1.1 / 2.1)

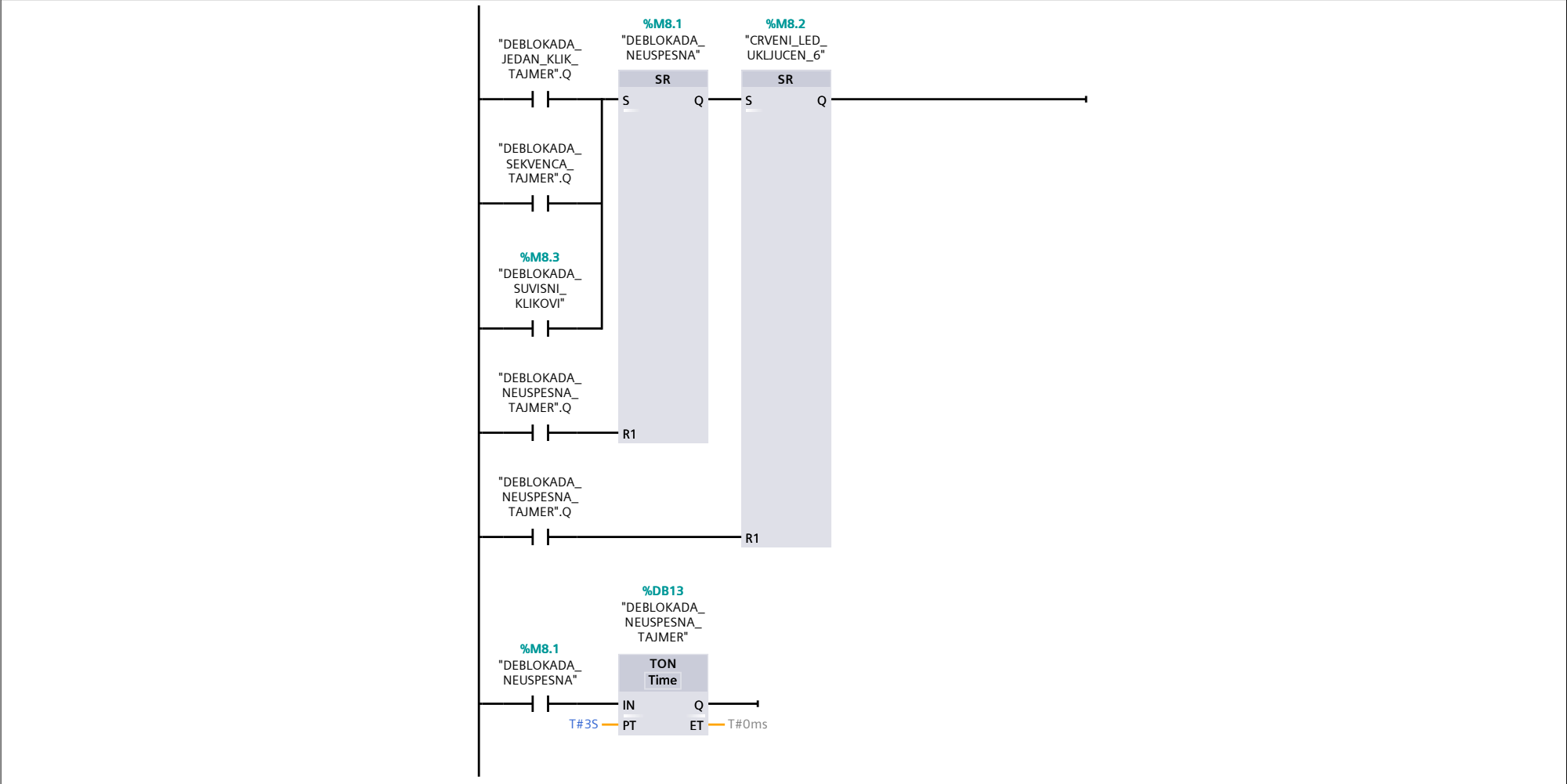
The diagram illustrates the logic for Network 8, which includes several interlocking and timing functions:

- SR Latch (%M5.1):** The set coil (S) is triggered by a series of conditions: `%M8.4` (Blokada uspesna signalizacija gotova), `%M8.1` (Deblokada neuspesna), `%I0.1` (Start no), `%M0.5` (Blokada), `%I0.3` (Automatski rezim no), `%I0.2` (Motor radi no), and `%M8.0` (Deblokada). The reset coil (R) is triggered by `%M8.1`. The output (Q) is connected to the IN of a TON timer (%DB9, "DEBLOKADA_SEKVENCA_TAJMER") with a preset time (PT) of 6 seconds. The timer's ET output is connected to the Q of the SR latch.
- CTU Counter (%DB10):** The count up (CU) coil is triggered by `%M8.4`, `%M8.1`, `%I0.1`, `%M0.5`, and `%M8.0`. The reset (R) coil is triggered by `%M8.0`. The output (Q) is connected to the QV output of the counter, which is set to 0. The PV output is set to 4.
- CTU Counter (%DB11):** The count up (CU) coil is triggered by `%M5.1`, `%M8.1`, `%I0.5` (Stop no), `%M0.5`, and `%M8.0`. The reset (R) coil is triggered by `%M8.0`. The output (Q) is connected to the QV output of the counter, which is set to 0. The PV output is set to 4.
- RS Latch (%M8.3):** The reset (R) coil is triggered by `%M8.1`. The set (S) coil is triggered by `"DEBLOKADA_STOP_KLIK_BROJAC".CV` (with an internal integer comparison) and `"DEBLOKADA_STOP_KLIK_BROJAC".QU` (with a NOT condition and an internal integer comparison). The output (Q) is connected to the QV output of the latch.
- ADD Auto (Int) (%MW6):** The EN (enable) coil is triggered by `"DEBLOKADA_STOP_KLIK_BROJAC".CV`. The IN1 input is set to 1. The IN2 input is set to `"DEBLOKADA_STOP_KLIK_BROJAC".CV`. The OUT output is connected to the QV output of the counter.
- TON Timer (%DB12):** The IN input is triggered by `"DEBLOKADA_STOP_KLIK_BROJAC".CV`. The PT (preset time) is set to 1 second. The ET (elapsed time) output is connected to the QV output of the counter.





Network 9:



Network 10:

