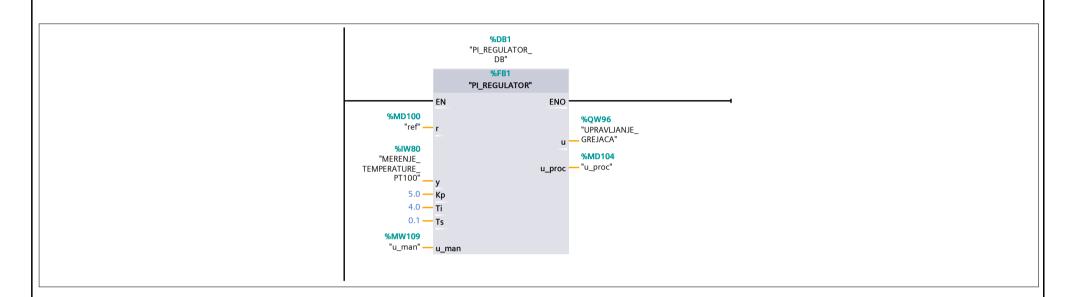
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| Automation Portal  |  |
|                    |  |

## Cyclic interrupt [OB30]

| Cyclic interrupt Properties |                  |                 |    |         |    |          |     |  |
|-----------------------------|------------------|-----------------|----|---------|----|----------|-----|--|
| General                     | General          |                 |    |         |    |          |     |  |
| Name                        | Cyclic interrupt | Number          | 30 | Туре    | OB | Language | LAD |  |
| Numbering                   | Automatic        |                 |    |         |    |          |     |  |
| Information                 |                  |                 |    |         |    |          |     |  |
| Title                       |                  | Author          |    | Comment |    | Family   |     |  |
| Version                     | 0.1              | User-defined ID |    |         |    |          |     |  |

| Name           | Data type | Default value | Comment                 |
|----------------|-----------|---------------|-------------------------|
| <b>▼</b> Input |           |               |                         |
| Initial_Call   | Bool      |               | Initial call of this OB |
| Event_Count    | Int       |               | Events discarded        |
| Temp           |           |               |                         |
| Constant       |           |               |                         |

## Network 1:



| in Properti<br>neral<br>me | Main                          | Number 1        |               | Туре    | ОВ                      | Language           | LAD |
|----------------------------|-------------------------------|-----------------|---------------|---------|-------------------------|--------------------|-----|
| mbering<br>ormation        | Automatic                     |                 |               | , Jr    |                         | <sub> </sub>   55- |     |
| le                         | "Main Program Sweep (Cy-cle)" | Author          |               | Comment |                         | Family             |     |
| rsion                      | 0.1                           | User-defined ID |               |         |                         |                    |     |
| me<br>Input                |                               | Data type       | Default value |         | Comment                 |                    |     |
| Initial_C                  |                               | Bool            |               |         | Initial call of this Ol |                    |     |
| Remane<br>Temp             | nce                           | Bool            |               |         | =True, if remanent      | data are available |     |
| Constant                   |                               |                 |               |         |                         |                    |     |
|                            |                               |                 |               |         |                         |                    |     |
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|---------------------------------|----------------------|----------|----------------------|-----------------------------|---------------|---------|---------------------------------------|-------------|-----|
| Startup [O                      | B100]                |          |                      |                             |               |         |                                       |             | •   |
| Startup Proper                  | ies :                |          |                      |                             |               |         |                                       |             |     |
| General<br>Name<br>Numbering    | Startup<br>Automatic |          | Number               | 100                         |               | Туре    | ОВ                                    | Language    | LAD |
| Information<br>Title<br>Version | "Complete            | Restart" | Author<br>User-defin | ned ID                      |               | Comment |                                       | Family      |     |
| Name                            |                      |          | Data t               | type                        | Default value |         | Comment                               |             |     |
| ▼ Input<br>LostReter            | ntive                |          | Bool                 |                             |               |         | True if retentive da                  | ta are lost |     |
| LostRTC                         | TUVC                 |          | Bool                 |                             |               |         | True if date and tim                  |             |     |
| Temp<br>Constant                |                      |          |                      |                             |               |         |                                       |             |     |
| Network 1:                      |                      |          | '                    |                             |               |         | ,                                     |             |     |
|                                 |                      |          |                      | %M108.1<br>"auto_previous"  |               |         | %M108.0<br>"auto_current"             |             |     |
|                                 |                      |          |                      | ——— <b>(</b> R <b>)</b> ——— |               |         | ————————————————————————————————————— |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
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|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
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|                                 |                      |          |                      |                             |               |         |                                       |             |     |
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|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |
|                                 |                      |          |                      |                             |               |         |                                       |             |     |

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## PI\_REGULATOR [FB1]

| PI_RE | PI_REGULATOR Properties |              |                 |   |         |    |          |     |  |  |
|-------|-------------------------|--------------|-----------------|---|---------|----|----------|-----|--|--|
| Gene  | eral                    |              |                 |   |         |    |          |     |  |  |
| Name  | е                       | PI_REGULATOR | Number          | 1 | Туре    | FB | Language | SCL |  |  |
| Num   | bering                  |              |                 |   |         |    |          |     |  |  |
| Infor | Information             |              |                 |   |         |    |          |     |  |  |
| Title |                         |              | Author          |   | Comment |    | Family   |     |  |  |
| Versi | on                      | 0.1          | User-defined ID |   |         |    |          | '   |  |  |

| Name            | Data type | Default value | Retain     | Accessible<br>from<br>HMI/OPC<br>UA | able | Visible in<br>HMI engi-<br>neering |       | Supervi-<br>sion | Comment |
|-----------------|-----------|---------------|------------|-------------------------------------|------|------------------------------------|-------|------------------|---------|
| <b>r</b> Input  |           |               |            |                                     |      |                                    |       |                  |         |
| r               | Real      | 0.0           | Retain     | True                                | True | True                               | False |                  |         |
| у               | Word      | 16#0          | Retain     | True                                | True | True                               | False |                  |         |
| Кр              | Real      | 0.0           | Retain     | True                                | True | True                               | False |                  |         |
| Ti              | Real      | 0.0           | Retain     | True                                | True | True                               | False |                  |         |
| Ts              | Real      | 0.0           | Retain     | True                                | True | True                               | False |                  |         |
| u_man           | Word      | 16#0          | Retain     | True                                | True | True                               | False |                  |         |
| <b>▼</b> Output |           |               |            |                                     |      |                                    |       |                  |         |
| u               | Word      | 16#0          | Non-retain | True                                | True | True                               | False |                  |         |
| u_proc          | Real      | 0.0           | Non-retain | True                                | True | True                               | False |                  |         |
| InOut           |           |               |            |                                     |      |                                    |       |                  |         |
| <b>▼</b> Static |           |               |            |                                     |      |                                    |       |                  |         |
| е               | Real      | 0.0           | Non-retain | True                                | True | True                               | False |                  |         |
| ир              | Real      | 0.0           | Non-retain | True                                | True | True                               | False |                  |         |
| ui              | Real      | 0.0           | Non-retain | True                                | True | True                               | False |                  |         |
| u_Pl            | Real      | 0.0           | Non-retain | True                                | True | True                               | False |                  |         |
| y_float         | Real      | 0.0           | Non-retain | True                                | True | True                               | False |                  |         |
| u_man_float     | Real      | 0.0           | Non-retain | True                                | True | True                               | False |                  |         |
| Temp            |           |               |            |                                     |      |                                    |       |                  |         |
| Constant        |           |               |            |                                     |      |                                    |       |                  |         |

```
0001 // PI regulator - P (Proportional Gain), I (Integral Gain)
0002
0003 // Na P (Proportional Gain) utice parametar Kp.
0004 // Sto je vece Kp, to sistem brze reaguje na gresku (razliku izmedju trenutne izmerene temperature i zeljene, tj.
0005 // reference \rightarrow e = r - y_float), tj. agresivnije ce da menja temperaturu.
0006 // Tada sistem brze postize zeljenu temperaturu, ali postoji rizik od "overshoot-a", tj da se zeljena temperatura
0007 // premasi, pa da je potrebno da se postepeno vrati temperatura na zeljenu, kada sistem ulazi u stacionarno stanje
0008 // kada temperatura oscilira oko zeljene.
0009 // Takodje, sto je vece Kp, to je veca verovatnoca da budu manje oscilacije nakon sto sistem udje u stacionarno
0010 // stanje, tj. veca je preciznost u stacionarnom stanju.
0011
0012 // Na I (Integral Gain) utice parametar Ti.
0013 // Sto je vece Ti, to sistem blaze i postepenije ispravlja akumulirane greske iz proslosti kako bi se postiglo
0014 // sto preciznije stacionarno stanje, tj. da se odrzava temperatura sto blize zeljenoj.
0015
0016 #y_float := #y / 10.0;
0017 #u_man_float := #u_man / 27700.0 * 100.0;
0018
0019 IF "auto_current" THEN
0020 // Obezbedjivanje Bumpless prelaza izmedju rucnog i automatskog rezima rada.
0021
     IF NOT "auto previous" THEN
0022
       #r := #y_float;
0023
       #ui := #u_man_float;
0024
     END IF;
0025
      #e := #r - #y_float;
0026
      #up := #Kp * #e;
0027
      #ui := #ui + #Kp / #Ti * #Ts * #e;
     #u PI := #up + #ui;
0029
      // Obezbedjivanje Antiwindup mehanizma u okviru automatskog rezima rada.
0030
     IF #ui > 100.0 THEN
       #ui := 100.0;
0031
0032 END IF;
     IF #ui < 0.0 THEN
0033
0034
       #ui := 0.0;
0035
     END IF;
0036
     IF #u PI > 100.0 THEN
0037
       #u PI := 100.0;
0038
0039
     IF \#u PI < 0.0 THEN
       #u PI := 0.0;
0040
     END IF;
0041
0042
     #u proc := #u PI;
0043
      #u := REAL TO INT(#u PI / 100.0 * 27700.0);
0044
      "auto previous" := true;
0045 ELSE
0046 IF #u man > 27700 THEN
0047
         #u := 27700;
```

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```
0048 ELSE
0049
      IF #u_man < 0 THEN</pre>
0050
        #u := 0;
0051
      ELSE
0052
           #u := #u_man;
0053
      END_IF;
0054
      END_IF;
0055 // Obezbedjivanje Bumpless prelaza izmedju rucnog i automatskog rezima rada.
0056 #r := #y_float;
0057 "auto_previous" := false;
0058 END_IF;
```

| Symbol          | Address | Туре | Comment |
|-----------------|---------|------|---------|
| "auto_current"  | %M108.0 | Bool |         |
| "auto_previous" | %M108.1 | Bool |         |
| #e              |         | Real |         |
| #Kp             |         | Real |         |
| #r              |         | Real |         |
| #Ti             |         | Real |         |
| #Ts             |         | Real |         |
| #u              |         | Word |         |
| #u_man          |         | Word |         |
| #u_man_float    |         | Real |         |
| #u_PI           |         | Real |         |
| #u_proc         |         | Real |         |
| #ui             |         | Real |         |
| #up             |         | Real |         |
| #y              |         | Word |         |
| #y_float        |         | Real |         |