

The I-V curve of the diode circuit D. Vs (7) 18d -5 150200 Vs

Slot the diagram of Vont.

A = 200, Ra=100

role. #2. (Redifier):

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Tuyrose the turn-on vottage of all the diodes are Von and the are no inner resistance in them. Find

- (1) Flac (2) Div. (suppose D1, Z2C>>J).
- (3) Discuss could the circuit work of the inner resistance of diodes are comparable with Lior D. (Just a thinking problem and do not require in exam

Probe # 3 (Redifier)

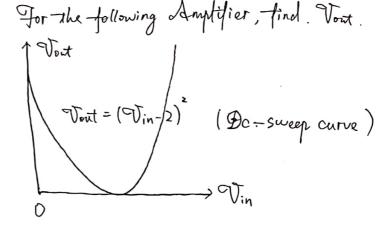
For the clicale in the circuit, Von = 0. Wand To=59, Flot the curve of Vont and Von in the same graph. (900 (2.6 >>7, 7 is the period of Vs)

Mention that you need to show the relationshy between Vort and Von when you plot the diagram.

Prob#4 (General od mplifier).

Tool #4 (General od mplifier).

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(1) Vin = (0.1 sin (120 pt)

(2) Discuss at which point, it's hardest to the most difficult to approximate the admptifier into a small-signor model if the Dc input voltage \overline{U}_{in} externally a constant. (I. $\overline{V}_{in}=1$ I. $\overline{V}_{in}=2$ II $\overline{V}_{in}=3$)

Prob#5: For the Following circuit, Is = 1×10-16, B=100, VAF=100.

Toca Re Tin=Tin+Vin Tout

(1). Suppose Jin = 0.79, Tec=3V, Re=Rf=5000s Find the value of gm, ro, ra

(Hind: Tupuse the node voltage & is Vz).

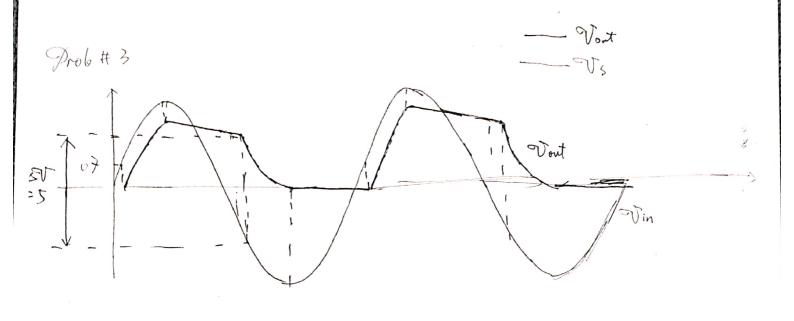
(2) Calculate the small signal gain of = Tout (You do not need to find the value, only need to derive the expression of with gm, ra, ro).

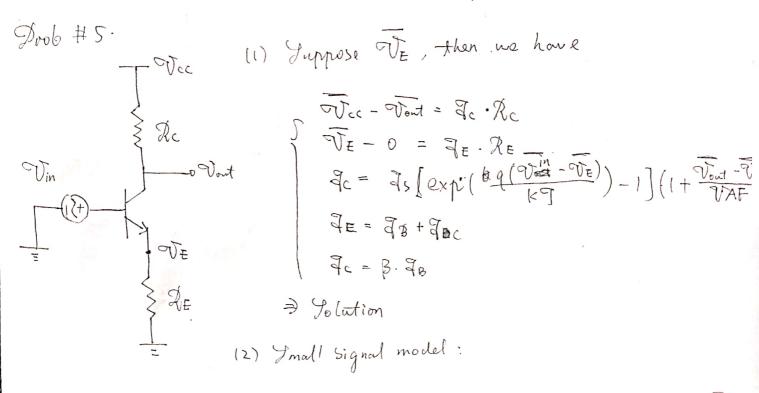
Prob #6: Suppose (\$\beta \&& \text{VA} \beta \text{00}), Find Av Wing Fz, Fo & gm.

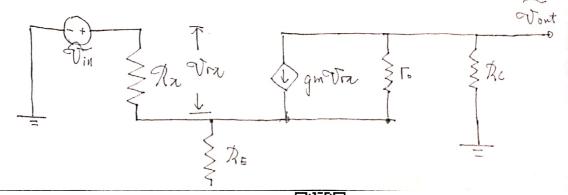
Fin=\text{Tin+Vin}

Face

(Suppose w is high)







Drob #6. Small signal model:

