

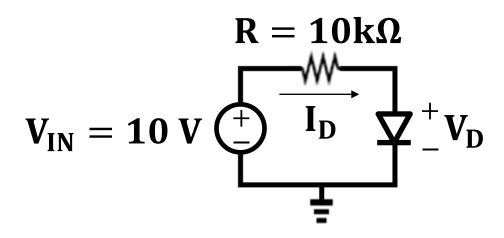
Diode Circuit

VE311 Electronic Circuits (Summer 2019)

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Solving Techniques



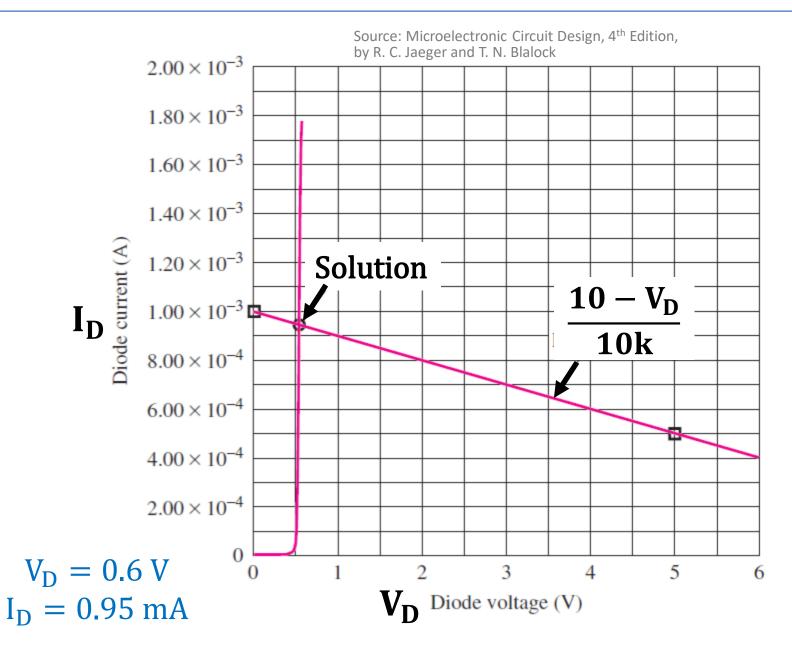
$$V_{IN} = I_D R + V_D$$

$$I_D = ?$$

$$V_D = ?$$

- 1. Graphical analysis
- 2. Mathematical analysis
- **3. Simplified analysis** (ideal diode)
- **4. Simplified analysis** (constant voltage drop)

Graphical Analysis



Mathematical Analysis

$$V_{IN} = I_{D}R + V_{D}$$

$$V_{IN} = \left[I_{S} \left(\frac{qV_{D}}{e^{R}} - 1\right)\right]R + V_{D}$$

$$10 = \left[10^{-13} \left(\frac{V_{D}}{e^{0.0258}} - 1\right)\right]10^{4} + V_{D}$$

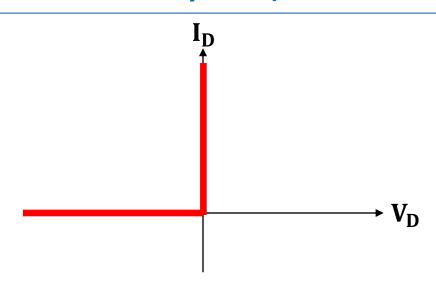
Above is a transcendental equation which does not have a close-form **analytical solution**. So, through trial and error, we seek a **numerical solution**.

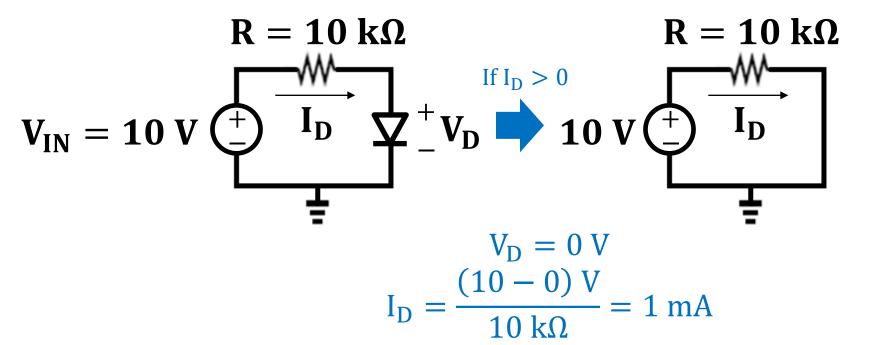
function xd = diode(vd)
xd = 10 - (10⁻⁹) *
$$\left(\exp\left(\frac{vd}{0.0258}\right) - 1\right)$$
 - vd

Use MATLAB to plot xd as a function of vd, and find out a vd that makes xd closest to zero.

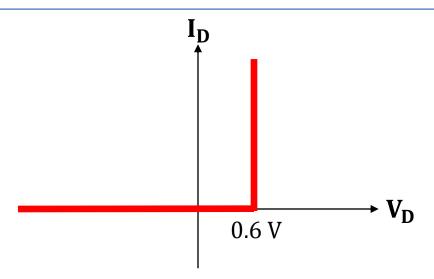
$$V_D = 0.5742 \text{ V}$$
 $I_D = 0.944 \text{ mA}$

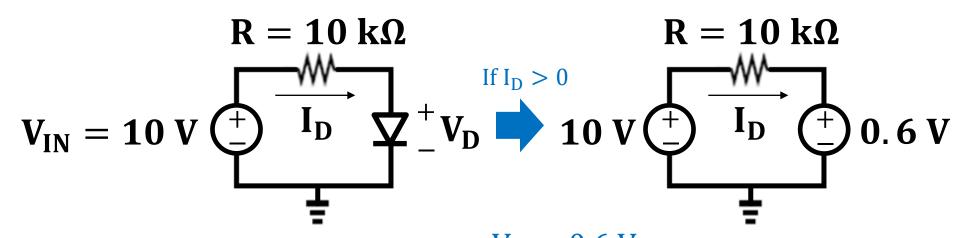
Simplified Analysis (Ideal Diode)





Simplified Analysis (Constant Voltage Drop)





 V_D a power supply? What happens if $V_{IN} = 0.5 \ V$?

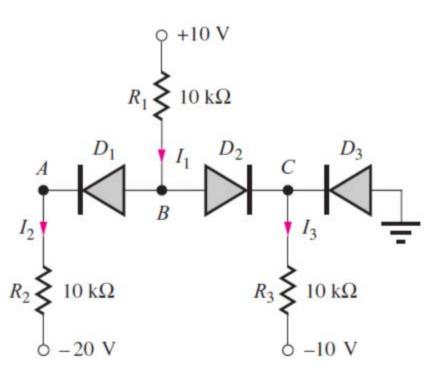
$$V_D = 0.6 \text{ V}$$

$$I_D = \frac{(10 - 0.6) \text{ V}}{10 \text{ k}\Omega} = 0.94 \text{ mA}$$

Comparison

	V_{D}	I_D
Graphical Analysis	0.6 V	0.95 mA
Mathematical Analysis	0.5742 V	0.944 mA
Ideal Diode Model	0 V	1 mA
Constant Voltage Drop Model	0.6 V	0.94 mA

Use constant voltage drop model ($V_{on} = 0.6 \text{ V}$) to calculate V_D and I_D of each diode.



Assume no current flowing through D₃

$$\frac{10-V_B}{10k} = \frac{V_B-0.6+20}{10k} + \frac{V_B-0.6+10}{10k}$$

$$V_B = -6.27 \text{ V}$$

$$V_C = -6.87 \text{ V} \implies D_3 \text{ in forward bias}$$

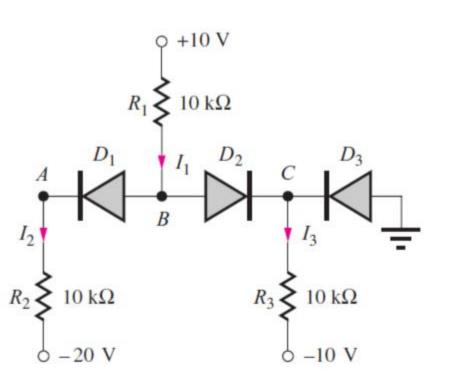
$$Assumption \text{ NOT valid}$$

• Assume no current flowing through D₂

$$\begin{split} \frac{10-V_B}{10k} &= \frac{V_B-0.6+20}{10k}\\ V_B &= -4.7 \text{ V}\\ V_C &= -0.6 \text{ V} \quad \Rightarrow D_2 \text{ indeed in reverse bias}\\ &\qquad \qquad \text{Assumption valid} \end{split}$$

$$\begin{array}{cccc} V_{D1} = 0.6 \ V & V_{D2} = -4.1 \ V & V_{D3} = 0.6 \ V \\ I_{D1} = 1.47 \ mA & I_{D2} = 0 \ mA & I_{D3} = 0.94 \ mA \end{array}$$

Use ideal diode model ($V_{on} = 0 \text{ V}$) to calculate V_D and I_D of each diode.



• Assume D₃ in reverse bias

$$\frac{10-V_B}{10k} = \frac{V_B+20}{10k} + \frac{V_B+10}{10k}$$

$$V_B = -6.67 \text{ V}$$

$$V_C = -6.67 \text{ V} \Rightarrow D_3 \text{ in forward bias}$$

$$Assumption NOT valid$$

• Assume D₂ in reverse bias

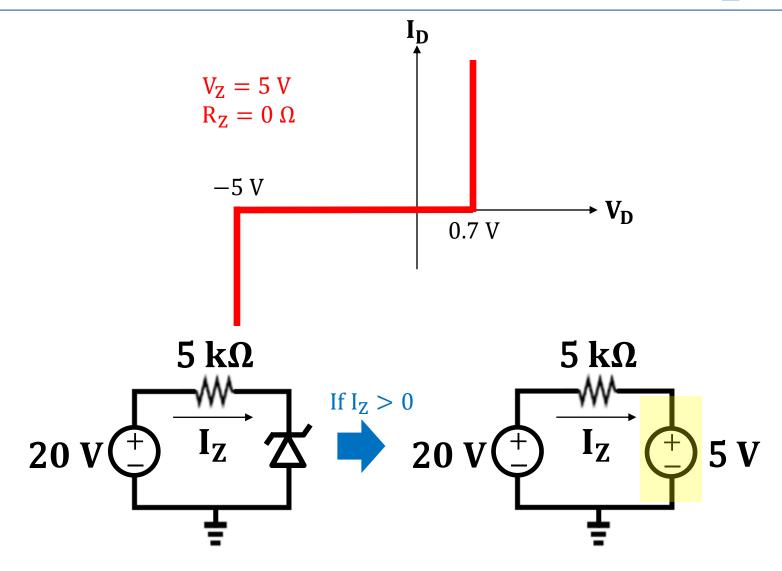
$$\begin{split} \frac{10-V_B}{10k} &= \frac{V_B+20}{10k} \\ V_B &= -5 \text{ V} \\ V_C &= 0 \text{ V} \qquad \Rightarrow \text{ D}_2 \text{ indeed in reverse bias} \end{split}$$

$$V_{D1} = 0 V$$
 $V_{D2} = -5 V$ $V_{D3} = 0 V$ $I_{D1} = 1.5 \text{ mA}$ $I_{D2} = 0 \text{ mA}$ $I_{D3} = 1 \text{ mA}$

Assumption valid

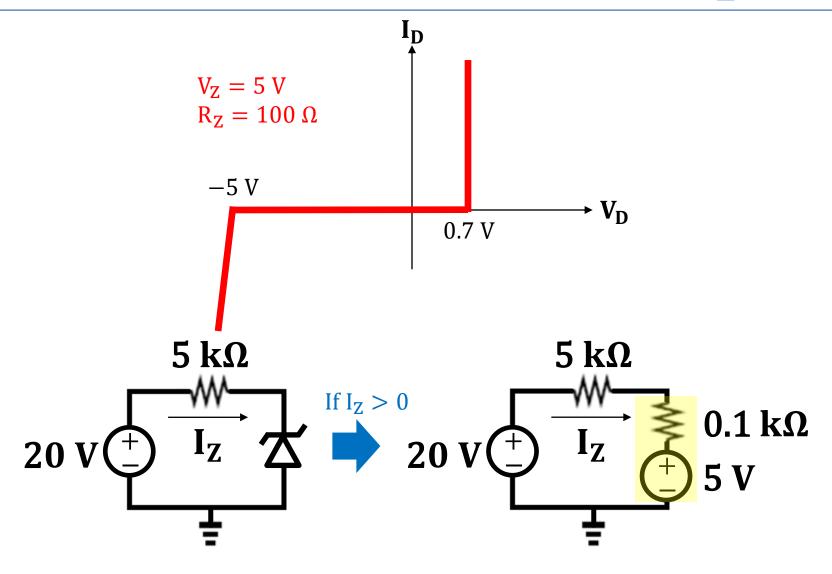
Zener Diode Circuit

Simplified Analysis for Zener Diode ($R_Z = 0$)



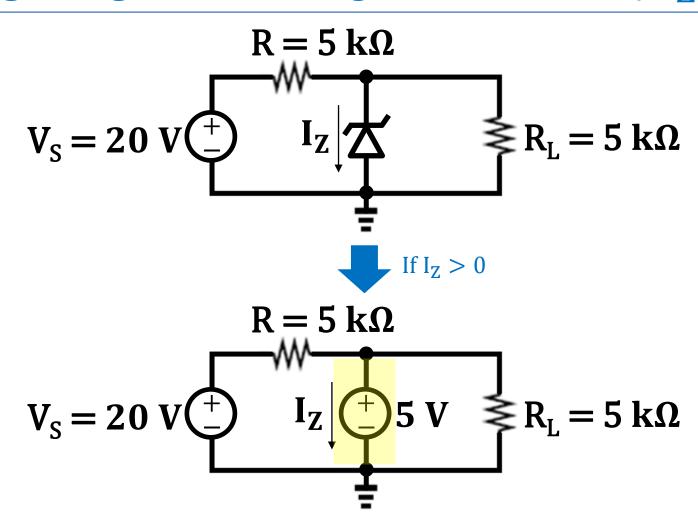
$$I_{\rm Z} = \frac{20 - 5}{5000} = 3 \times 10^{-3} \text{ (A)}$$

Simplified Analysis for Zener Diode ($R_Z \neq 0$)



$$I_Z = \frac{20 - 5}{5100} = 2.94 \times 10^{-3} \text{ (A)}$$

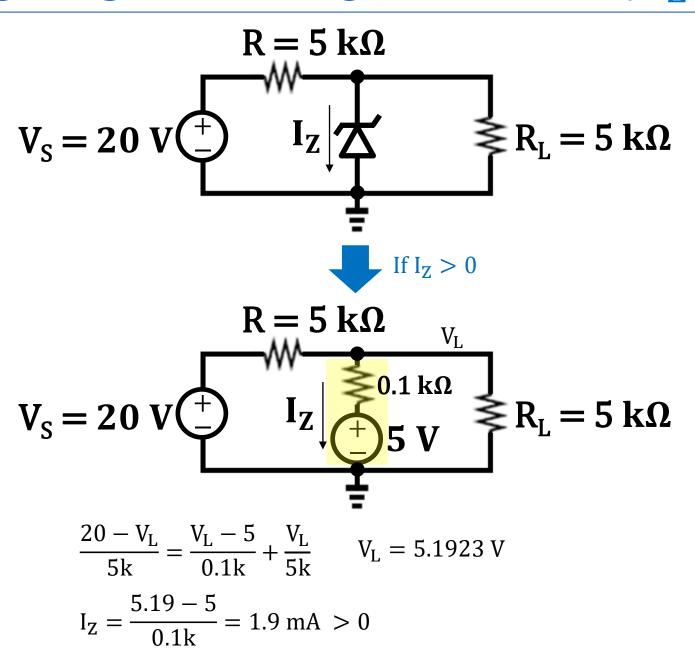
Voltage Regulator Using Zener Diode $(R_Z = 0)^{13}$



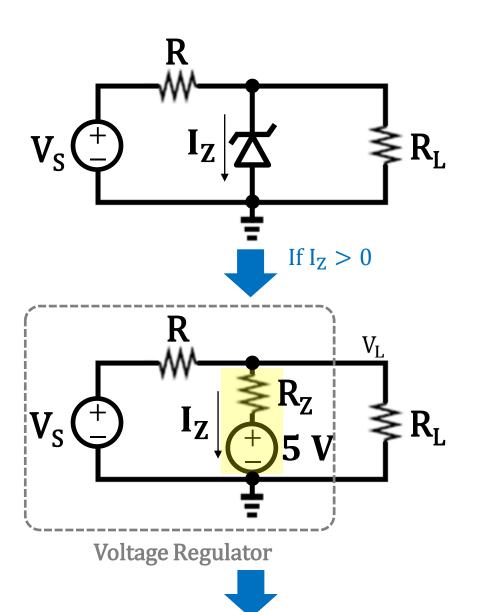
$$I_Z = \frac{20-5}{5k} - \frac{5}{5k} = 2 \text{ mA} > 0$$

As long as the zener diode operates in reverse breakdown region ($I_Z > 0$), a constant voltage (5 V) appears across R_L .

Voltage Regulator Using Zener Diode $(R_Z \neq 0)^{14}$



Line Regulation and Load Regulation



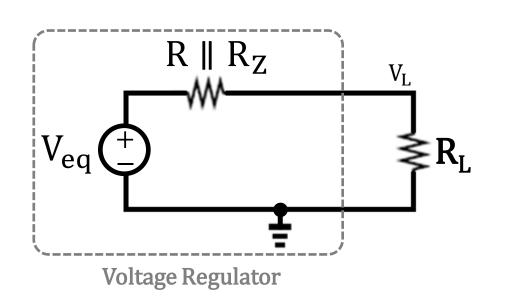
• Line Regulation: how sensitive the output voltage (V_L) is to input voltage (V_S) changes, when $R_L = \infty$.

Line Regulation =
$$\frac{dV_L}{dV_S} = \frac{R_Z}{R + R_Z}$$

 Load Regulation: output impedance of the voltage regulator.

$$Load Regulation = \frac{dV_L}{dI_L} = R \parallel R_Z$$

Thevinen Equivalent Circuit of Voltage Regulator



$$V_{eq} = 5 + \frac{V_{S} - 5}{R + R_{Z}} R_{Z}$$

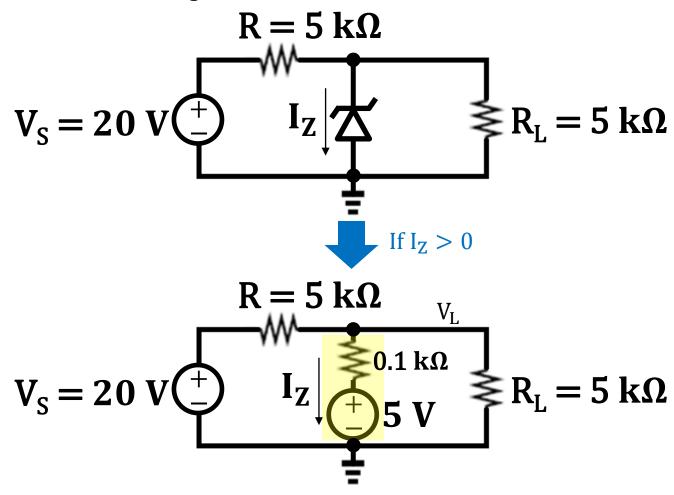
$$V_{L} = V_{eq} \frac{R_{L}}{(R \parallel R_{Z}) + R_{L}}$$

Numerical Test: $V_S=20$ V, R=5 k Ω , $R_Z=0.1$ k Ω , $V_Z=5$ V, $R_L=5$ k Ω

$$V_{eq} = 5 + \frac{20 - 5}{5k + 0.1k} 0.1k = 5.2941 (V)$$

$$V_{L} = 5.2941 \times \frac{5k}{\frac{5k \times 0.1k}{5k + 0.1k} + 5k} = 5.1923 (V)$$
 The result exactly the same as on page 14.

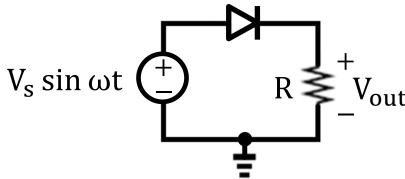
What are the line and load regulations for the circuit below?



Line Regulation =
$$\frac{0.1 k}{5 k + 0.1 k}$$
 = 19.6 mV/V Load Regulation = $5 k \parallel 0.1 k = 98 \Omega$

Half-Wave Rectifier

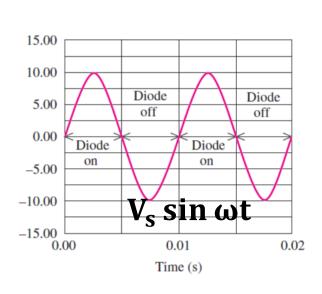
Half-Wave Rectifier with Resistive Load

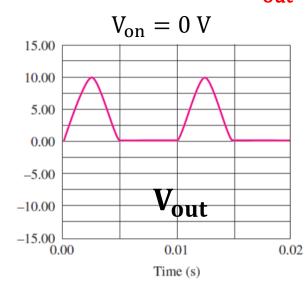


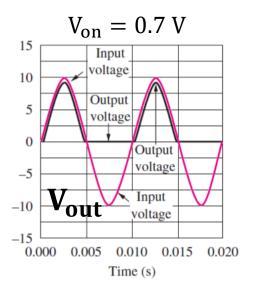
$$V_s = 10 \text{ V}$$

 $\omega = 2\pi f = 2\pi \frac{1}{0.01} = 200\pi \text{ (rad/sec)}$

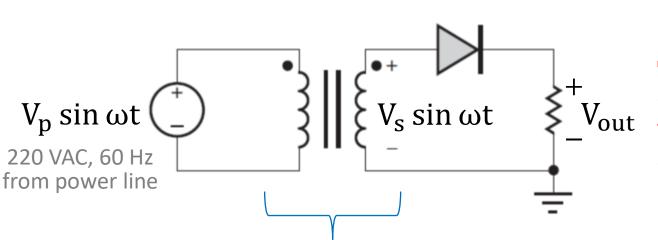
V_{out} is not DC.



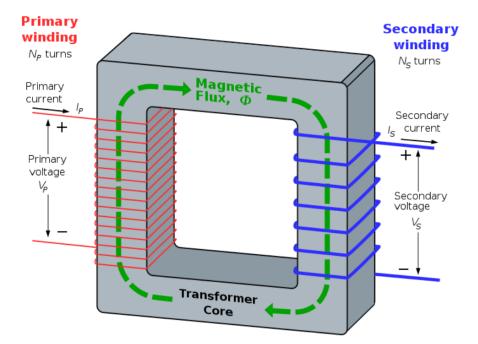




Transformer



The output of an ideal transformer can be represented as an ideal voltage source.



Source: Daniels, A (1976). Introduction to Electrical Machines. Macmillan Publishers.

Working Principle: By Faraday's law of induction

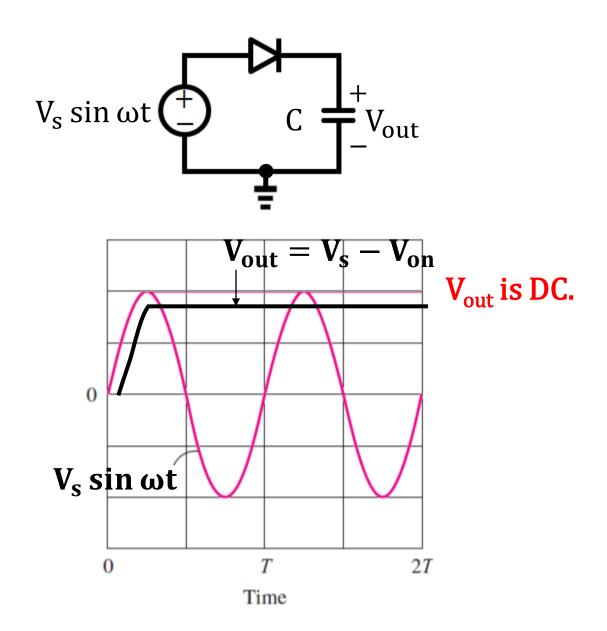
$$V_{s} = -N_{s} \frac{d\emptyset}{dt}$$

$$d\emptyset$$

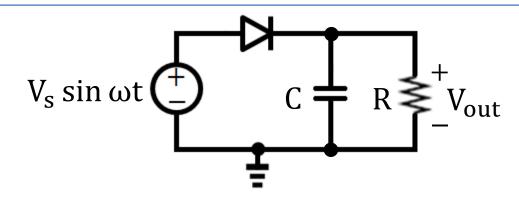
$$V_{p} = -N_{p} \frac{d\emptyset}{dt}$$

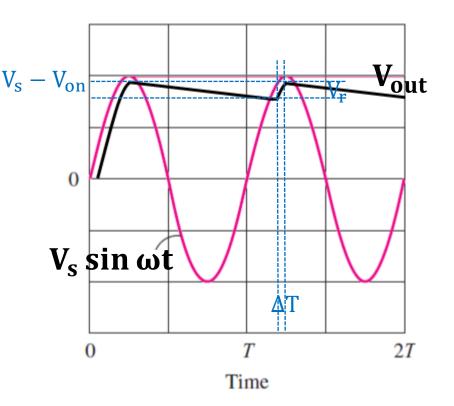
$$\frac{V_s}{V_p} = \frac{N_s}{N_1}$$

Half-Wave Rectifier with Capacitive Load



Half-Wave Rectifier with RC Load (I)





$$V_{dc} = V_{s} - V_{on}$$

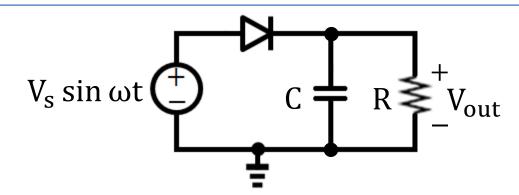
$$I_{dc} = \frac{V_{dc}}{R}$$

ripple voltage
$$V_{r} = (V_{s} - V_{on}) \left(1 - e^{-\frac{T - \Delta T}{RC}}\right)$$

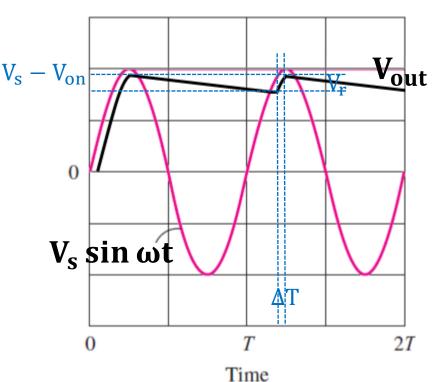
$$\cong (V_{s} - V_{on}) \left(\frac{T - \Delta T}{RC}\right) if(T - \Delta T) \ll RC$$

$$\cong (V_{s} - V_{on}) \left(\frac{T}{RC}\right) if\Delta T \ll T$$

Half-Wave Rectifier with RC Load (II)



conduction angle and interval



$$V_{out} V_{s} \sin \left[\omega \left(\frac{5T}{4} - \Delta T \right) \right] - V_{on} = (V_{s} - V_{on}) - V_{r}$$

$$V_{s} \sin \left(\frac{5\pi}{2} - \theta_{c}\right) - V_{on} = (V_{s} - V_{on}) - V_{r}$$

$$V_s \cos \theta_c = V_s - V_r$$

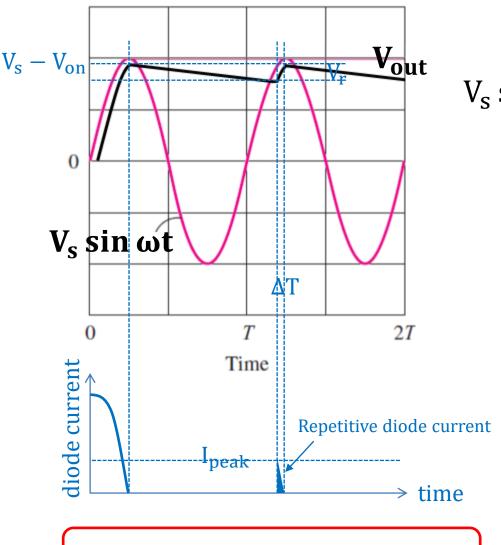
$$\cos \theta_c = \frac{V_s - V_r}{V_c} \cong 1 - \frac{{\theta_c}^2}{2}$$
 if θ_c very small

$$\theta_{\rm c} = \sqrt{\frac{2V_{\rm r}}{V_{\rm s}}}$$
 ΔT

$$\Theta_{c} = \sqrt{\frac{2V_{r}}{V_{s}}}$$

$$\Delta T = \frac{\Theta_{c}}{\omega} = \frac{1}{\omega} \sqrt{\frac{2V_{r}}{V_{s}}}$$

Half-Wave Rectifier with RC Load (III)



$$I_{surge} = C \frac{dV_s \sin \omega t}{dt} = \omega CV_s$$
 at $t = 0$

$$V_s \sin \omega t$$
 $C = R$
 V_{out}

The charge filled on C during ΔT is discharged during $T - \Delta T$.

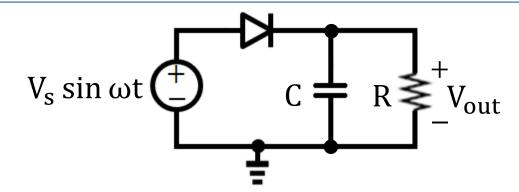
$$Q \cong \frac{I_{\text{peak}}\Delta T}{2} = I_{\text{dc}}(T - \Delta T) \cong I_{\text{dc}}T$$

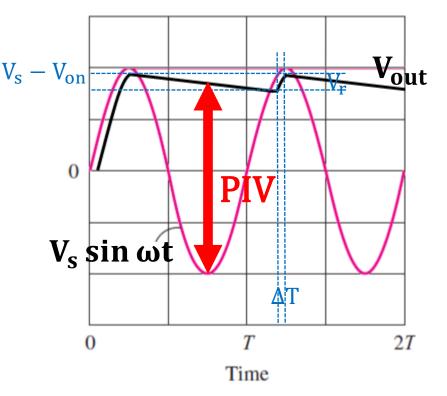
$$I_{peak} = \frac{2I_{dc}T}{\Delta T}$$

During charging period (ΔT), almost all diode current goes to C.

$$\left| \frac{1}{\text{SC}} \right| = \frac{1}{2\pi \frac{1}{T}C} = \frac{T}{2\pi C} \ll R \text{ if } RC \gg T$$

Half-Wave Rectifier with RC Load (IV)





Peak-inverse-voltage (PIV) $\cong 2V_s - V_{on}$

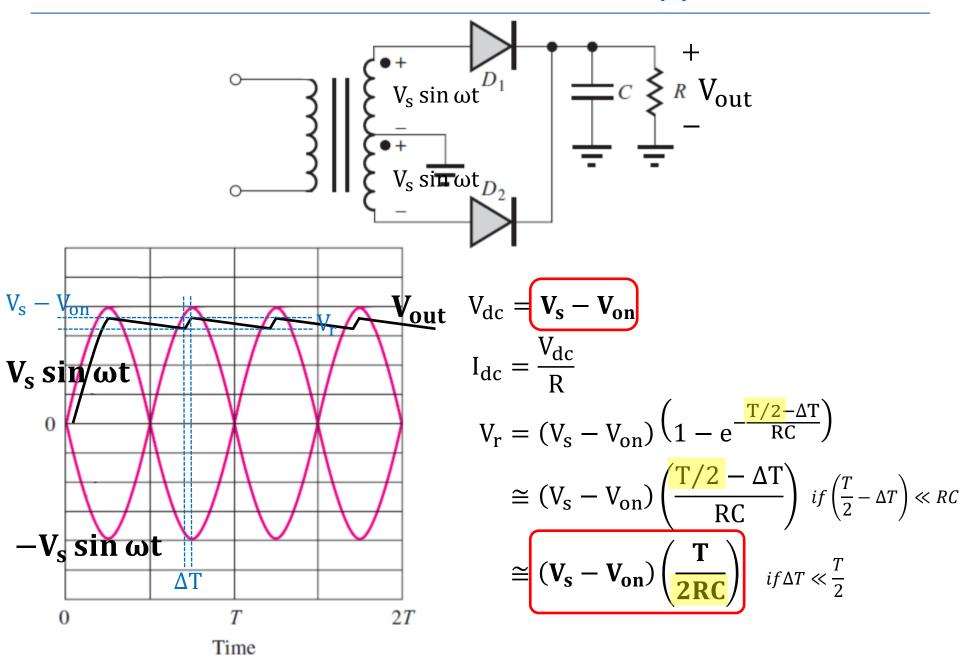
If too large, the diode breaks down.

Find the value of the dc output voltage, dc output current, ripple voltage, conduction interval, conduction angle and diode peak current for a half-wave rectifier driven from a transformer having a secondary voltage of 12.6 V_{rms} (60 Hz) with $R=15~\Omega$ and $C=25,000~\mu F$. Assume $V_{on}=1~V$.

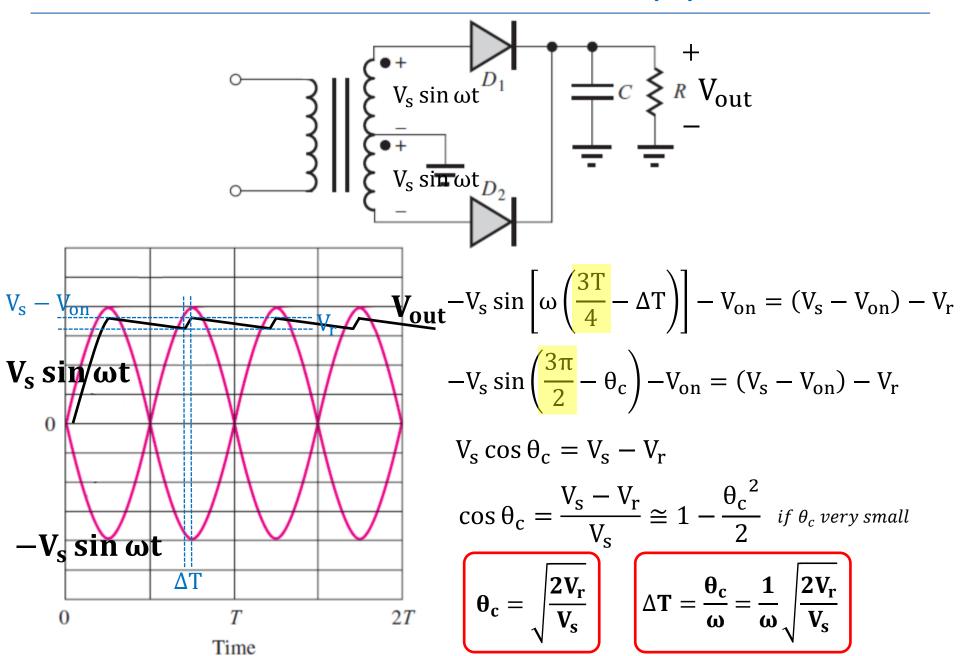
$$\begin{split} &V_{dc} = 12.6\sqrt{2} - 1 = 16.8 \text{ (V)} \\ &I_{dc} = \frac{16.8}{15} = 1.12 \text{ (A)} \\ &V_{r} \cong V_{dc} \frac{T}{RC} = 16.8 \frac{\frac{1}{60}}{15 \times 25000 \times 10^{-6}} = 0.747 \text{ (V)} \\ &\theta_{c} \cong \sqrt{\frac{2V_{r}}{V_{s}}} = \sqrt{\frac{2 \times 0.747}{12.6 \times \sqrt{2}}} = 0.29 \text{ (rad) or } 16.6^{\circ} \\ &\Delta T \cong \frac{\theta_{c}}{\omega} = \frac{0.29}{2\pi \times 60} = 7.69 \times 10^{-4} \text{ (sec)} \\ &I_{peak} = \frac{2 \times 1.12 \times \frac{1}{60}}{7.69 \times 10^{-4}} = 48.6 \text{ (A)} \end{split}$$

- Make sure all assumptions are valid.
- Since R is small (15 Ω), C needs to be large (25,000 μ F) to maintain a low V_r
- The diode must be able to handle these repetitively high peak currents.

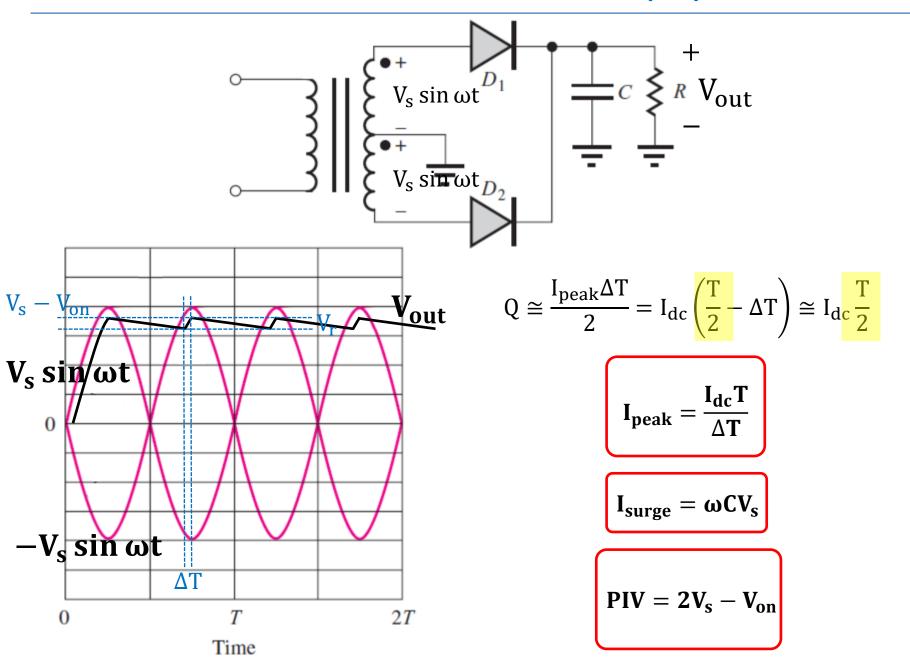
Full-Wave Rectifier (I)



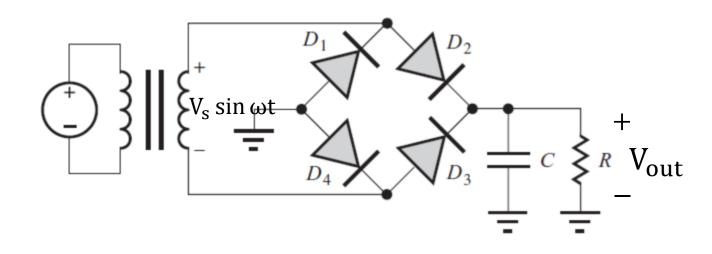
Full-Wave Rectifier (II)

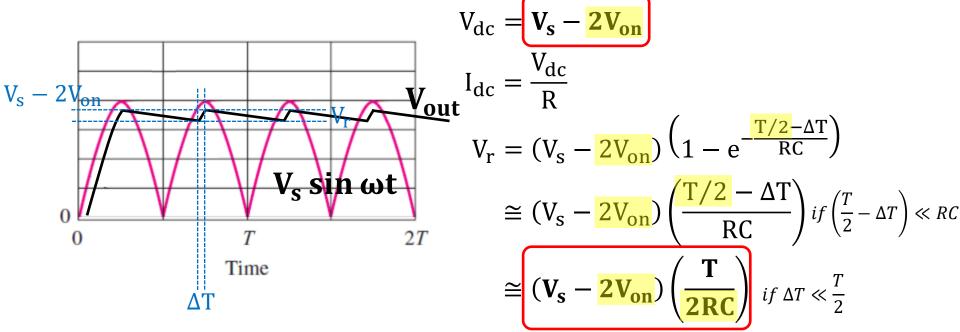


Full-Wave Rectifier (III)

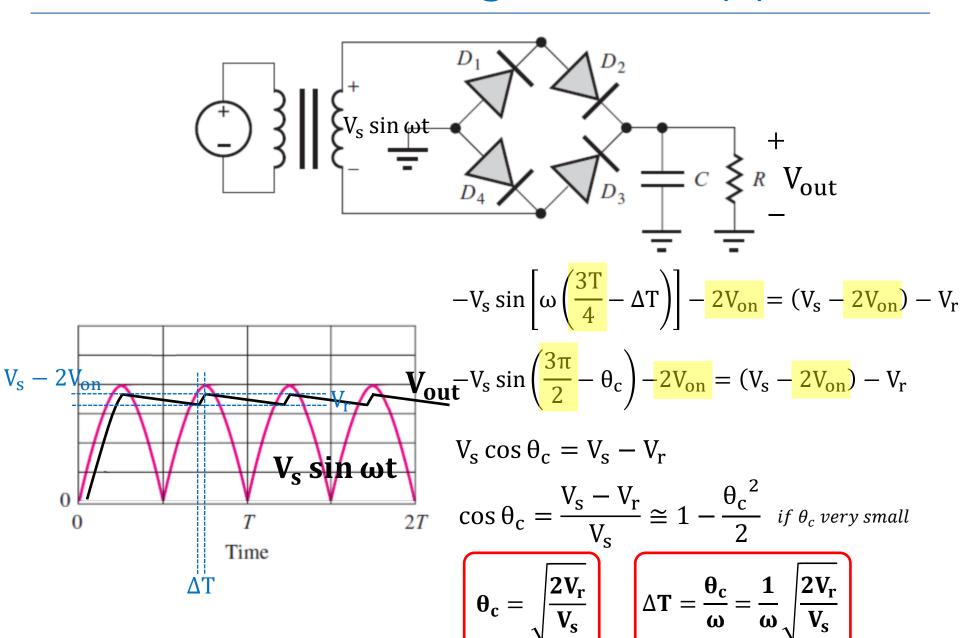


Full-Wave Bridge Rectifier (I)

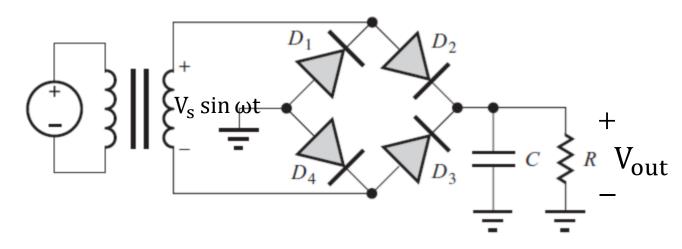


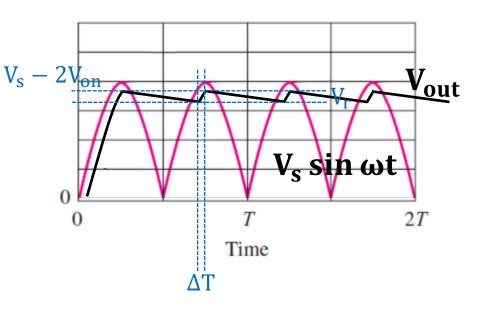


Full-Wave Bridge Rectifier (II)



Full-Wave Bridge Rectifier (III)





$$Q \cong \frac{I_{\text{peak}}\Delta T}{2} = I_{\text{dc}} \left(\frac{T}{2} - \Delta T \right) \cong I_{\text{dc}} \frac{T}{2}$$

$$I_{peak} = \frac{I_{dc}T}{\Delta T}$$

$$I_{\text{surge}} = \omega CV_{\text{s}}$$

$$PIV = \frac{V_s - V_{on}}{}$$

Design a full-wave bridge rectifier to provide a dc output voltage 15 V with no more than 1 percent ripple at a load current of 2A. ($V_{on} = 1 \text{ V}$, T = 1/60 sec)

$$V_{dc} = 15 \text{ (V)}$$

 $V_r < 0.15 \text{ (V)}$
 $I_{dc} = 2 \text{ (A)}$
Load resistance = $15/2 = 7.5 \text{ (}\Omega\text{)}$

The required transformer voltage $V_s=15+2=$ **17** (**V**) or $\frac{17}{\sqrt{2}}$ (**V**_{rms})

$$V_{\rm r} \cong (V_{\rm s} - 2V_{\rm on}) \left(\frac{T}{2RC}\right) = 15 \left(\frac{1}{2 \times 60 \times 7.5 \times C}\right) = 0.15 \Rightarrow \mathbf{C} = \mathbf{0}.\mathbf{111} \ (\mathbf{F})$$

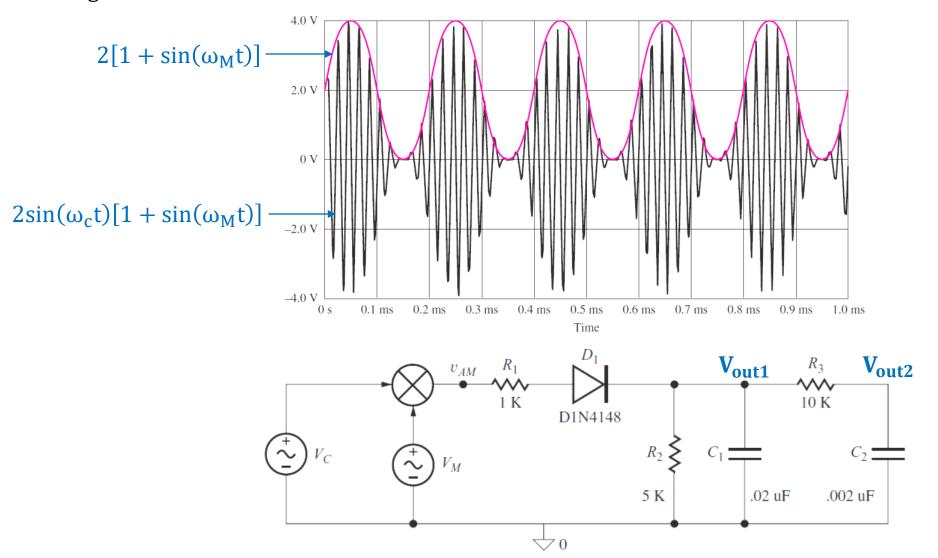
$$\Delta T = \frac{1}{\omega} \sqrt{\frac{2V_r}{V_s}} = \frac{1}{2\pi \times 60} \sqrt{\frac{2 \times 0.15}{17}} = \mathbf{0}.352 \times \mathbf{10}^{-3} \text{ (sec)}$$

$$I_{\text{peak}} = \frac{I_{\text{dc}}T}{\Delta T} = \frac{2 \times \frac{1}{60}}{0.352 \times 10^{-3}} = 94.7 \text{ (A)}$$

$$I_{\text{surge}} = \omega CV_s = 2\pi \times 60 \times 0.111 \times 17 = 711 \text{ (A)}$$

Make sure the diodes can handle these large currents

An amplitude modulated (AM) signal is shown below. The envelope of the AM signal contains the information being transmitted, and the envelope can be recovered using a single half-wave rectifier.



An amplitude modulated (AM) signal is shown below. The envelope of the AM signal contains the information being transmitted, and the envelope can be recovered using a single half-wave rectifier.

