

Setup Information			
DUT ID	DUT001	DPOJET Version	10.0.5.1
Date/Time	2018-09-07 11:44:19	Scope Model	DPO73304SX
Device Type	CEM	Scope Serial Number	B320098
TekExpress Version	PCI Express:10.2.1.4 Framework:4.2.5.7	SPC, FactoryCalibration	PASS;PASS
Test Mode	SigTest Compliance	Scope F/W Version	10.8.5 Build 4
Spec Version	Gen2 - 2.0	Probe1 Model	TCA292D
SigTest Version	3_2_0	Probe1 Serial Number	N/A
SigTest Interface	DLL Mode	Probe2 Model	TCA292D
Slot Number	01	Probe2 Serial Number	N/A
Overall Execution Time	0:01:18	Probe3 Model	TCA292D
Overall Test Result	Pass	Probe3 Serial Number	N/A
DUT COMMENT:		Probe4 Model	TCA292D
		Probe4 Serial Number	N/A
		Probe4 Serial Number	N/A

Test Name Summary Table								
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Unit Interval								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Mean Unit Interval	Lane0	5Gbps	3.5dB	199.999 ps	Pass	L:0.059ps H:1.061ps	199.94	201.06
COMMENTS	Found Amplitude = 655.6,Found Data Rate = 5 PCI Express Base Specification Revision 3.1 Table 4-18: Transmitter Specifications							

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Mask Hits(All Bits)								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Mask Hits	Lane0	5Gbps	3.5dB	0 hits	Pass	N.A	0	0
COMMENTS	Found Amplitude = 655.6,Found Data Rate = 5							

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Composit Eye Height								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Composit Eye Height	Lane0	5Gbps	3.5dB	434.581 mV	Informative	N.A	N.A	N.A
COMMENTS	Found Amplitude = 655.6,Found Data Rate = 5							

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Transition Eye Diagram								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
<a href="#">Min Transition Eye Height</a>	Lane0	5Gbps	3.5dB	434.581 mV	Informative	N.A	N.A	N.A
<a href="#">Min Transition Voltage</a>	Lane0	5Gbps	3.5dB	-312.832 mV	Pass	L:287.168mV	-600	N.A
<a href="#">Max Transition Voltage</a>	Lane0	5Gbps	3.5dB	309.504 mV	Pass	H:290.496mV	N.A	600
<a href="#">Min Transition Top Margin</a>	Lane0	5Gbps	3.5dB	104.858 mV	Pass	L:104.858mV	0	N.A
<a href="#">Min Transition Bottom Margin</a>	Lane0	5Gbps	3.5dB	-104.723 mV	Pass	H:104.723mV	N.A	0
<a href="#">Transition Eye Mask Hits</a>	Lane0	5Gbps	3.5dB	0 hits	Pass	N.A	0	0
COMMENTS	Found Amplitude = 655.6,Found Data Rate = 5 PCI Express Card Electromechanical Specification Revision 3.0 Table 4-17:							

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Non Transition Eye Diagram								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit

<a href="#">Min Non Transition Eye Height</a>	Lane0	5Gbps	3.5dB	497.677 mV	Informative	N.A	N.A	N.A
<a href="#">Min Non Transition Voltage</a>	Lane0	5Gbps	3.5dB	-326.144 mV	Pass	L:273.856mV	-600	N.A
<a href="#">Max Non Transition Voltage</a>	Lane0	5Gbps	3.5dB	326.144 mV	Pass	H:273.856mV	N.A	600
<a href="#">Min Non Transition Top Margin</a>	Lane0	5Gbps	3.5dB	137.100 mV	Pass	L:137.100mV	0	N.A
<a href="#">Min Non Transition Bottom Margin</a>	Lane0	5Gbps	3.5dB	-135.577 mV	Pass	H:135.577mV	N.A	0
<a href="#">Non Transition Eye Mask Hits</a>	Lane0	5Gbps	3.5dB	0 hits	Pass	N.A	0	0
COMMENTS		Found Amplitude = 655.6, Found Data Rate = 5 PCI Express Card Electromechanical Specification Revision 3.0 Table 4-17:						

Min Eye Width								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Eye Width	Lane0	5Gbps	3.5dB	147.588 ps	Informative	N.A	N.A	N.A
COMMENTS		Found Amplitude = 655.6.Found Data Rate = 5						

Min Time Between Crossovers								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Time Between Crossovers	Lane0	5Gbps	3.5dB	186.227 ps	Informative	N.A	N.A	N.A
COMMENTS		Found Amplitude = 655.6, Found Data Rate = 5						

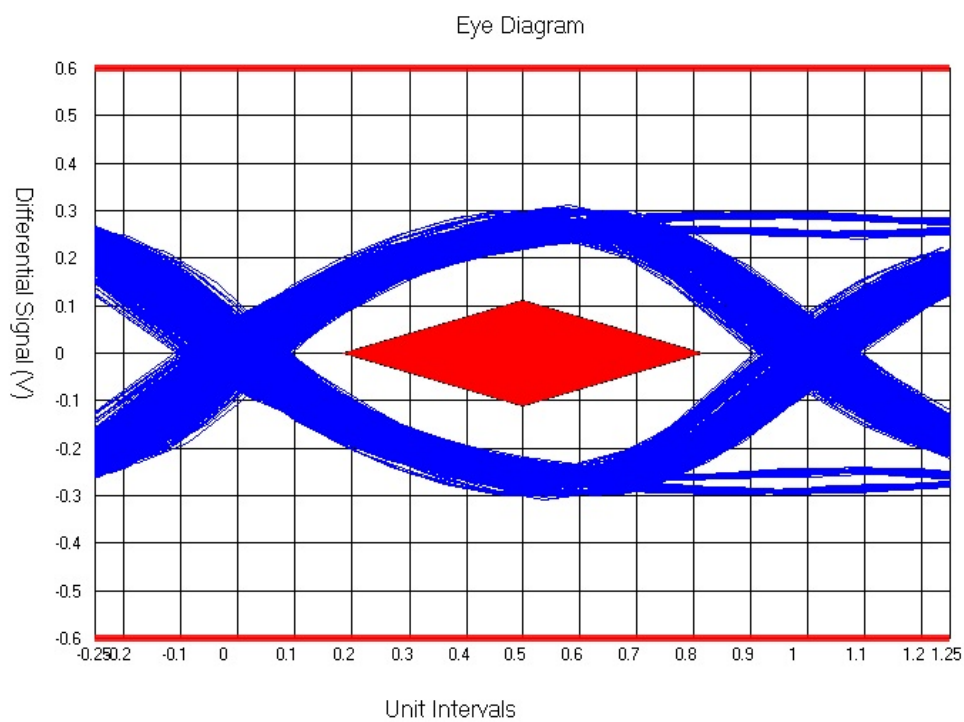
TJ @ E-12								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
TJ@E-12	Lane0	5Gbps	3.5dB	52.412 ps	Pass	H:52.588ps	N.A	105.0
COMMENTS	Found Amplitude = 655.6, Found Data Rate = 5 PCI Express Card Electromechanical Specification Revision 3.0 Table 4-18:							

Dj_dd								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Dj_dd	Lane0	5Gbps	3.5dB	6.894 ps	Pass	H:50.106ps	N.A	57.0
COMMENTS	Found Amplitude = 655.6, Found Data Rate = 5 PCI Express Card Electromechanical Specification Revision 3.0 Table 4-18:							

RJ(RMS)								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
RJ	Lane0	5Gbps	3.5dB	3.237 ps	Informative	N.A	N.A	N.A
COMMENTS	Found Amplitude = 655.6, Found Data Rate = 5 PCI Express Card Electromechanical Specification Revision 3.0 Table 4-18:							

Peak to Peak Jitter								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Max Peak-to-Peak Jitter	Lane0	5Gbps	3.5dB	40.988 ps	Informative	N.A	N.A	N.A
COMMENTS		Found Amplitude = 655.6, Found Data Rate = 5						

Transition Eye Diagram
Transition Eye - Lane0 5Gbps 3.5dB



Non Transition Eye Diagram

Non Transition Eye - Lane0 5Gbps 3.5dB

