Lógica Digital (1001351) Mapas de Karnaugh

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Row number	x_1	x_2	x_3	f
0	0	0	0	1
1	0	0	1	0
2	0	1	0	1
3	0	1	1	0
4	1	0	0	1
4 5 6	1	0	1	1
6	1	1	0	1
7	1	1	1	0

Figure 2.48 The function $f(x_1, x_2, x_3) = \sum m(0, 2, 4, 5, 6)$.

Row number	x_1	x_2	x_3	$f = \overline{x}_3$
0	0	0	0	1
1	0	0	1	0
2	0	1	0 -	→ 1
3	0	1	1	0
4	1	0	0 -	→ 1
5	1	0	1	1
6	1	1	0 -	→ 1
7	1	1	1	0

Figure 2.48 The function $f(x_1, x_2, x_3) = \sum m(0, 2, 4, 5, 6)$.

Row number	x_1	x_2	x_3	f	$+x_1\overline{x}_2$
0	0	0	0	1	
1	0	0	1	0	
2	0	1	0	1	
3	0	1	1	0	
4	1	0 -	0	→ 1	
5	1	0 -	1	→ 1	
6	1	1	0	1	
7	1	1	1	0	

Figure 2.48 The function $f(x_1, x_2, x_3) = \sum m(0, 2, 4, 5, 6)$.

Row number	x_1	x_2	x_3	$f = \overline{x_3} + x_1 \overline{x_2}$
0 1 2 3 4 5	0 0 0 0 1 1	0 0 1 1 0 -	0 - 1 0 - 1	1 0 1 0 1 0
6 7	1 1	1 1	0 - 1	1 0

Figure 2.48 The function $f(x_1, x_2, x_3) = \sum m(0, 2, 4, 5, 6)$.

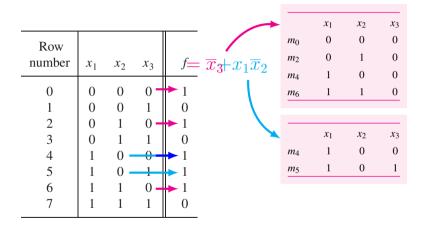


Figure 2.48 The function $f(x_1, x_2, x_3) = \sum m(0, 2, 4, 5, 6)$.

Mapas de Karnaugh: duas variáveis

x_1	x_2	
0	0	m_0
0	1	m_1
1	0	m_2
1	1	m_3
		ı

(a) Truth table

 $\begin{array}{c|cccc}
x_1 & & & & & \\
0 & m_0 & m_2 & & \\
1 & m_1 & m_3 & & & \\
\end{array}$

(b) Karnaugh map

 $m_2 + m_3 = x_1 \overline{x}_2 + x_1 x_2$ $= x_1 (\overline{x}_2 + x_2)$ $= x_1 \cdot 1$ $= x_1$

Figure 2.49 Location of two-variable minterms.

Mapas de Karnaugh: duas variáveis

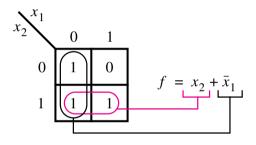


Figure 2.50 The function of Figure 2.19.

Mapas de Karnaugh: três variáveis

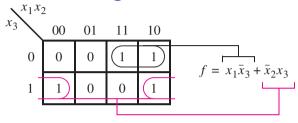
x_1	x_2	x_3	
0	0	0	m_0
0	0	1	m_1
0	1	0	m_2
0	1	1	m_3
1	0	0	m_4
1	0	1	m_5
1	1	0	m_6
1	1	1	m_7
			I

x_1x_2						
x_3	00	01	11	10		
0	m_0	m_2	m_6	m_4		
1	m_1	m_3	m_7	m_5		

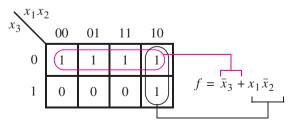
(b) Karnaugh map

(a) Truth table

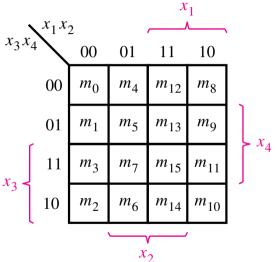
Mapas de Karnaugh: três variáveis



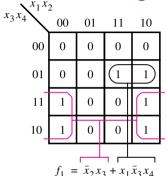
(a) The function of Figure 2.23

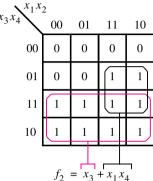


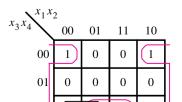
Mapas de Karnaugh: quatro variáveis

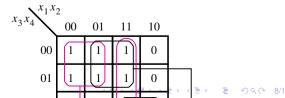


Mapas de Karnaugh: quatro variáveis









Mapas de Karnaugh: cinco variáveis

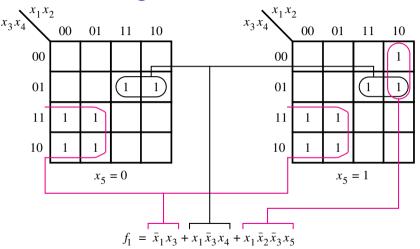


Figure 2.55 A five-variable Karnaugh map.

Bibliografia

▶ Brown, S. & Vranesic, Z. - Fundamentals of Digital Logic with Verilog Design, 3rd Ed., Mc Graw Hill, 2009

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