# Questões

1. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 3 bits

	a 0	b 1	b 0	Como
<u>a_1</u>	a_0			Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a 1	a 0	b 1	b 0	Soma
$\frac{1}{0}$	0	0	0	15 5 11 101
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

### Questões

1. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
0	0			
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_2$	$a_1$	$a_0$	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

### Questões

1. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_1$	$a_0$	b_1	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

### Questões

1. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
0	0			
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits

	_ 1	- 0	1- Ω	C
_a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 3 bits

a 0	b 2	b 1	b 0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	$a_0$	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a 1	a 0	b 1	b 0	Soma
$\frac{-1}{0}$	0	0	0	15 5 11 101
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
0	0			
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

#### Questões

1. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_1$	$a_0$	$b_1$	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
0	0			
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
				Doma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

### Questões

1. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_2$	$a_1$	$a_0$	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
0	0			
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_1$	$a_0$	b_1	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
0	0			
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_1$	$a_0$	b_1	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 3 bits

				~
$a_{-}0$	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_2$	$a_1$	$a_0$	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
0	0			
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_1$	$a_0$	b_1	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
				Боша
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_2$	$a_1$	$a_0$	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

#### Questões

1. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 3 bits

$a_0$	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_1$	$a_0$	b_1	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 3 bits

$a_0$	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_1$	$a_0$	$b_1$	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 3 bits

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_2$	$a_1$	$a_0$	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits

a 2	a 1	a 0	b 0	Soma
0				
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_2$	$a_1$	$a_0$	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
				Doma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a 1	a 0	b 1	b 0	Soma
$\frac{-1}{0}$	0	0	0	15 5 11 101
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits

a 2	a 1	a 0	b 0	Soma
0				
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	$a_0$	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_1$	$a_0$	b_1	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a 1	a 0	b 1	b 0	Soma
$\frac{-1}{0}$	0	0	0	15 5 11 101
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a_1	$a_0$	b_1	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
				Doma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

#### Questões

1. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 3 bits

				~
$a_{-}0$	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	$a_0$	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
				Doma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 3 bits

a 0	b 2	b 1	b 0	Soma
0				
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_2$	$a_1$	$a_0$	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

## Questões

1. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 3 bits

$a_{-}0$	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_1$	$a_0$	b_1	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

## Questões

1. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a 1	a 0	b 1	b 0	Soma
$\frac{-1}{0}$	0	0	0	15 5 11 101
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

## Questões

1. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
0	0			
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_2$	$a_1$	$a_0$	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

## Questões

1. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	$a_0$	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

## Questões

1. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 3 bits

_a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_2$	$a_1$	$a_0$	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

## Questões

1. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
0	0			
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

$a_2$	$a_1$	$a_0$	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

## Questões

1. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
0	0			
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

## Questões

1. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a_1	$a_0$	b_1	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

# Questões

1. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits

$a_2$	a_1	$a_0$	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	$a_0$	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

## Questões

1. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
				Боша
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

## Questões

1. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a_1	$a_0$	b_1	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

## Questões

1. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits

$a_2$	a_1	$a_0$	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a 1	a 0	b 1	b 0	Soma
$\frac{-1}{0}$	0	0	0	15 5 11 101
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

## Questões

1. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits

$a_2$	$a_1$	$a_0$	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	$a_0$	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a 1	a 0	b 1	b 0	Soma
$\frac{-1}{0}$	0	0	0	15 5 11 101
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
0	0			
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a 1	a 0	b 1	b 0	Soma
$\frac{-1}{0}$	0	0	0	15 5 11 101
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

## Questões

1. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits

$a_2$	$a_1$	$a_0$	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits

a 2	a 1	a 0	b 0	Soma
	0			
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

2. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 4 bits

a_0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
0	0			
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

2. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits

$a_2$	$a_1$	$a_0$	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a 1	a 0	b 1	b 0	Soma
0	0			
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

2. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 3 bits

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

2. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo A 1 bits B 3 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

0	b_2	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

1. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits

	_ 1	- 0	1- Ω	C
_a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

2. Supondo Compl2 A 2 bits Compl2 B 2 bits, preencha a tabela verdade soma S com 4 bits

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo Compl2 A 2 bits B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

## 60 Nome e Matricula:

# Questões

1. Supondo A 2 bits Compl 2 B 2 bits, preencha a tabela verdade soma S com 3 bits

$a_1$	$a_0$	b_1	$b_0$	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

2. Supondo A 3 bits B 1 bits, preencha a tabela verdade soma S com 4 bits

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

- 3. Supondo Compl 2 A 3 bits B 1 bits, preencha a tabela verdade soma S com 3 bits
- 4. Supondo A 2 bits B 2 bits, preencha a tabela verdade soma S com 4 bits
- 5. Implemente os circuitos com módulos FA e HA no DigitalJs e verifique o funcionamento.
- 6. Enumere os mintermos das funções  $s_i$  e verifique o funcionamento.

a_2	a_1	a_0	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

a_1	a_0	b_1	b_0	Soma
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	