

	A	B	C	D	E	F	G	H
1	LQFP144	Pin name		Type	I/O Level	Main	Default	Remap
2	1	PE2	1	I/O	FT	PE2	TRACECK/FSMC_A23	
3	2	PE3	1	I/O	FT	PE3	TRACED0/FSMC_A19	
4	3	PE4	1	I/O	FT	PE4	TRACED1/FSMC_A20	
5	4	PE5	1	I/O	FT	PE5	TRACED2/FSMC_A21	
6	5	PE6	1	I/O	FT	PE6	TRACED3/FSMC_A22	
7	6	VBAT		S		VBAT		
8	7	PC13-TAMPERRTC(4)		I/O		PC13(5)	TAMPER-RTC	
9	8	PC14-OSC32_IN(4)		I/O		PC14(5)	OSC32_IN	
10	9	PC15-OSC32_OUT(4)		I/O		PC15(5)	OSC32_OUT	
11	10	PF0	1	I/O	FT	PF0	FSMC_A0	
12	11	PF1	1	I/O	FT	PF1	FSMC_A1	
13	12	PF2	1	I/O	FT	PF2	FSMC_A2	
14	13	PF3	1	I/O	FT	PF3	FSMC_A3	
15	14	PF4	1	I/O	FT	PF4	FSMC_A4	
16	15	PF5	1	I/O	FT	PF5	FSMC_A5	
17	16	VSS_5		S		VSS_5		
18	17	VDD_5		S		VDD_5		
19	18	PF6	1	I/O		PF6	ADC3_IN4/FSMC_NIORD	
20	19	PF7	1	I/O		PF7	ADC3_IN5/FSMC_NREG	
21	20	PF8	1	I/O		PF8	ADC3_IN6/FSMC_NIOWR	
22	21	PF9	1	I/O		PF9	ADC3_IN7/FSMC_CD	
23	22	PF10	1	I/O		PF10	ADC3_IN8/FSMC_INTR	
24	23	OSC_IN		I		OSC_IN		
25	24	OSC_OUT		O		OSC_OUT		
26	25	NRST		I/O		NRST		
27	26	PC0	1	I/O		PC0	ADC123_IN10	
28	27	PC1	1	I/O		PC1	ADC123_IN11	
29	28	PC2	1	I/O		PC2	ADC123_IN12	
30	29	PC3	1	I/O		PC3	ADC123_IN13	
31	30	VSSA		S		VSSA		
32	31	VREF-		S		VREF-		
33	32	VREF+		S		VREF+		
34	33	VDDA		S		VDDA		
35	34	PA0-WKUP	1	I/O		PA0	WKUP/USART2_CTS(7)/ADC123_IN0/TIM2_CH1_ETR/TIM5_CH1/TIM8_ETR	
36	35	PA1	1	I/O		PA1	USART2_RTS(7)/ADC123_IN1/TIM5_CH2/TIM2_CH2(7)	
37	36	PA2	1	I/O		PA2	USART2_TX(7)/TIM5_CH3/ADC123_IN2/TIM2_CH3(7)	
38	37	PA3	1	I/O		PA3	USART2_RX(7)/TIM5_CH4/ADC123_IN3/TIM2_CH4(7)	
39	38	VSS_4		S		VSS_4		
40	39	VDD_4		S		VDD_4		
41	40	PA4	1	I/O		PA4	SPI1_NSS(7)/USART2_CK(7)/DAC_OUT1/ADC12_IN4	
42	41	PA5	1	I/O		PA5	SPI1_SCK(7)/DAC_OUT2/ADC12_IN5	
43	42	PA6	1	I/O		PA6	SPI1_MISO(7)/TIM8_BKIN/ADC12_IN6/TIM3_CH1(7)	TIM1_BKIN
44	43	PA7	1	I/O		PA7	SPI1_MOSI(7)/TIM8_CH1N/ADC12_IN7/TIM3_CH2(7)	TIM1_CH1N
45	44	PC4	1	I/O		PC4	ADC12_IN14	
46	45	PC5	1	I/O		PC5	ADC12_IN15	
47	46	PB0	1	I/O		PB0	ADC12_IN8/TIM3_CH3/TIM8_CH2N	TIM1_CH2N
48	47	PB1	1	I/O		PB1	ADC12_IN9/TIM3_CH4(7)/TIM8_CH3N	TIM1_CH3N
49	48	PB2	1	I/O	FT	PB2/BOOT1		
50	49	PF11	1	I/O	FT	PF11	FSMC_NIOS16	
51	50	PF12	1	I/O	FT	PF12	FSMC_A6	
52	51	VSS_6		S		VSS_6		
53	52	VDD_6		S		VDD_6		
54	53	PF13	1	I/O	FT	PF13	FSMC_A7	
55	54	PF14	1	I/O	FT	PF14	FSMC_A8	
56	55	PF15	1	I/O	FT	PF15	FSMC_A9	
57	56	PG0	1	I/O	FT	PG0	FSMC_A10	
58	57	PG1	1	I/O	FT	PG1	FSMC_A11	
59	58	PE7	1	I/O	FT	PE7	FSMC_D4	TIM1_ETR
60	59	PE8	1	I/O	FT	PE8	FSMC_D5	TIM1_CH1N
61	60	PE9	1	I/O	FT	PE9	FSMC_D6	TIM1_CH1
62	61	VSS_7		S		VSS_7		
63	62	VDD_7		S		VDD_7		
64	63	PE10	1	I/O	FT	PE10	FSMC_D7	TIM1_CH2N
65	64	PE11	1	I/O	FT	PE11	FSMC_D8	TIM1_CH2
66	65	PE12	1	I/O	FT	PE12	FSMC_D9	TIM1_CH3N
67	66	PE13	1	I/O	FT	PE13	FSMC_D10	TIM1_CH3

	A	B	C	D	E	F	G	H
68	67	PE14	1	I/O	FT	PE14	FSMC_D11	TIM1_CH4
69	68	PE15	1	I/O	FT	PE15	FSMC_D12	TIM1_BKIN
70	69	PB10	1	I/O	FT	PB10	I2C2_SCL/USART3_TX(7)	TIM2_CH3
71	70	PB11	1	I/O	FT	PB11	I2C2_SDA/USART3_RX(7)	TIM2_CH4
72	71	VSS_1		S		VSS_1		
73	72	VDD_1		S		VDD_1		
74	73	PB12	1	I/O	FT	PB12	SPI2_NSS/I2S2_WS/I2C2_S MBA/USART3_CK(7)/TIM1_B KIN(7)	
75	74	PB13	1	I/O	FT	PB13	SPI2_SCK/I2S2_CK/USART3 _CTS(7)/TIM1_CH1N	
76	75	PB14	1	I/O	FT	PB14	SPI2_MISO/TIM1_CH2N/USA RT3_RTS(7)	
77	76	PB15	1	I/O	FT	PB15	SPI2_MOSI/I2S2_SD/TIM1_ CH3N(7)	
78	77	PD8	1	I/O	FT	PD8	FSMC_D13	USART3_TX
79	78	PD9	1	I/O	FT	PD9	FSMC_D14	USART3_RX
80	79	PD10	1	I/O	FT	PD10	FSMC_D15	USART3_CK
81	80	PD11	1	I/O	FT	PD11	FSMC_A16	USART3_CTS
82	81	PD12	1	I/O	FT	PD12	FSMC_A17	TIM4_CH1/U
83	82	PD13	1	I/O	FT	PD13	FSMC_A18	TIM4_CH2
84	83	VSS_8		S		VSS_8		
85	84	VDD_8		S		VDD_8		
86	85	PD14	1	I/O	FT	PD14	FSMC_D0	TIM4_CH3
87	86	PD15	1	I/O	FT	PD15	FSMC_D1	TIM4_CH4
88	87	PG2	1	I/O	FT	PG2	FSMC_A12	
89	88	PG3	1	I/O	FT	PG3	FSMC_A13	
90	89	PG4	1	I/O	FT	PG4	FSMC_A14	
91	90	PG5	1	I/O	FT	PG5	FSMC_A15	
92	91	PG6	1	I/O	FT	PG6	FSMC_INT2	
93	92	PG7	1	I/O	FT	PG7	FSMC_INT3	
94	93	PG8	1	I/O	FT	PG8		
95	94	VSS_9		S		VSS_9		
96	95	VDD_9		S		VDD_9		
97	96	PC6	1	I/O	FT	PC6	I2S2_MCK/TIM8_CH1/SDIO_ D6	TIM3_CH1
98	97	PC7	1	I/O	FT	PC7	I2S3_MCK/TIM8_CH2/SDIO_ D7	TIM3_CH2
99	98	PC8	1	I/O	FT	PC8	TIM8_CH3/SDIO_D0	TIM3_CH3
100	99	PC9	1	I/O	FT	PC9	TIM8_CH4/SDIO_D1	TIM3_CH4
101	100	PA8	1	I/O	FT	PA8	USART1_CK/TIM1_CH1(7)/M CO	
102	101	PA9	0	I/O	FT	PA9	USART1_TX(7)/TIM1_CH2(7)	
103	102	PA10	0	I/O	FT	PA10	USART1_RX(7)/TIM1_CH3(7)	
104	103	PA11	1	I/O	FT	PA11	USART1_CTS/USBDM/CAN_RX (7)/TIM1_CH4(7)	
105	104	PA12	1	I/O	FT	PA12	USART1_RTS/USBDP/CAN_TX (7)/TIM1_ETR(7)	
106	105	PA13	1	I/O	FT	JTMSSWDIO		PA13
107	106	Not connected						
108	107	VSS_2		S		VSS_2		
109	108	VDD_2		S		VDD_2		
110	109	PA14	1	I/O	FT	JTCKSWCLK		PA14
111	110	PA15	1	I/O	FT	JTDI	SPI3_NSS/I2S3_WS	TIM2_CH1_E
112	111	PC10	1	I/O	FT	PC10	UART4_TX/SDIO_D2	USART3_TX
113	112	PC11	1	I/O	FT	PC11	UART4_RX/SDIO_D3	USART3_RX
114	113	PC12	1	I/O	FT	PC12	UART5_TX/SDIO_CK	USART3_CK
115	114	PD0	1	I/O	FT	OSC_IN(8)	FSMC_D2(9)	CAN_RX
116	115	PD1	1	I/O	FT	OSC_OUT(8)	FSMC_D3(9)	CAN_TX
117	116	PD2	1	I/O	FT	PD2	TIM3_ETR/UART5_RX/SDIO_ CMD	
118	117	PD3	1	I/O	FT	PD3	FSMC_CLK	USART2_CTS
119	118	PD4	1	I/O	FT	PD4	FSMC_NOE	USART2_RTS
120	119	PD5	1	I/O	FT	PD5	FSMC_NWE	USART2_TX
121	120	VSS_10		S		VSS_10		
122	121	VDD_10		S		VDD_10		
123	122	PD6	1	I/O	FT	PD6	FSMC_NWAIT	USART2_RX
124	123	PD7	1	I/O	FT	PD7	FSMC_NE1/FSMC_NCE2	USART2_CK
125	124	PG9	1	I/O	FT	PG9	FSMC_NE2/FSMC_NCE3	
126	125	PG10	1	I/O	FT	PG10	FSMC_NCE4_1/FSMC_NE3	
127	126	PG11	1	I/O	FT	PG11	FSMC_NCE4_2	
128	127	PG12	1	I/O	FT	PG12	FSMC_NE4	
129	128	PG13	1	I/O	FT	PG13	FSMC_A24	
130	129	PG14	1	I/O	FT	PG14	FSMC_A25	
131	130	VSS_11		S		VSS_11		
132	131	VDD_11		S		VDD_11		

	A	B	C	D	E	F	G	H
133	132	PG15	1	I/O	FT	PG15		
134	133	PB3/	1	I/O	FT	JTDO	SPI3_SCK/I2S3_CK/	PB3/TRACES
135	134	PB4	1	I/O	FT	NJTRST	SPI3_MISO	PB4/TIM3_C
136	135	PB5	1	I/O		PB5	I2C1_SMBA/SPI3_MOSI/I2S3_SD	TIM3_CH2/SPI1_MOSI
137	136	PB6	1	I/O	FT	PB6	I2C1_SCL(7)/TIM4_CH1(7)	USART1_TX
138	137	PB7	1	I/O	FT	PB7	I2C1_SDA(7)/FSMC_NADV/TIM4_CH2(7)	USART1_RX
139	138	BOOT0		I		BOOT0		
140	139	PB8	1	I/O	FT	PB8	TIM4_CH3(7)/SDIO_D4	I2C1_SCL/C
141	140	PB9	1	I/O	FT	PB9	TIM4_CH4(7)/SDIO_D5	I2C1_SDA/C
142	141	PE0	1	I/O	FT	PE0	TIM4_ETR/FSMC_NBL0	
143	142	PE1	1	I/O	FT	PE1	FSMC_NBL1	
144	143	VSS_3		S	VSS_3			
145	144	VDD_3		S	VDD_3			