CSE 244 Project

•Design VSCPU

Design VSCPU with all instructions. Instruction set of VSCPU can be found in lab 5 doc README.md file. You can use Add only VSCPU design as an example which can be found in YULEARN.

Your design module name, inputs and outputs should be named like below. Designs which are not following this get 0 from this.

```
module VerySimpleCPU(clk, rst, data_fromRAM, wrEn, addr_toRAM, data_toRAM);
input clk , rst ;
input wire [31:0] data_fromRAM;
output reg wrEn;
output reg[SIZE-1:0] addr_toRAM; output reg[31:0] data_toRAM;
```

•VSCPU ASSEMBLY

Write a VSCPU assembly code to find the biggest number in a ten sized array. Assume values in the array start at memory address 500.
 EXAMPLE:

0: /// your VSCPU ASSEMBLY SHOULD START FROM HERE////
1:
2:
......
///start of array///
500: 10
501: 23
502: 7
503: 244
504: 10
505: 23
506: 7
507: 244
508: 10
509: 23
510: 7
////end of array///