

# PEIYUAN QI

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## EDUCATION

**Shanghai Jiao Tong University**, Shanghai, China. GPA: 3.55/4.0

University of Michigan – Shanghai Jiao Tong University Joint Institute (UM-SJTU JI),  
*Bachelor of Engineering in Electrical and Computer Engineering*, Sep 2014 – Aug 2018

**Case Western Reserve University**, Cleveland, Ohio, USA. GPA: 4.0/4.0  
*Exchange Program*. Jan 2017 – May 2017

## ACADEMIC AWARDS

- 2017 UM-SJTU JI Winter Design Expo Gold Award. Dec 2017
- 2017 Interdisciplinary Contest in Modeling Honorable Mention. Feb 2017
- 2015 Mathematical Contest in Modeling Honorable Mention. Feb 2015
- UM-SJTU JI Dean's List 2014 - 2017. 2014 – 2017

## WORK EXPERIENCE

**NVIDIA Semiconductor Technology (Shanghai) Co., Ltd.**, Shanghai, China.

- Infrastructure Internship Aug 2017 – Present
  - Designed and developed a Perl Module to post more than 140,000 GPU simulation data per day to Elasticsearch, and visualized posted data using Kibana;
  - Implemented an auto-detecting and reporting commands error function module of GPU simulation software;
  - Applied doc2vec and Word Mover's Distance algorithms to perform the software Bug Triage tasks to improve team debugging efficiency.

## RESEARCH EXPERIENCE

**Intelligent Internet of Things Research Center**, Shanghai, China.

- Undergraduate Research Assistant Jun 2017 – Present
  - Optimized the database system query scheme to double the speed of concurrent access for Acemap, an academic search database;
  - Developed an Interdisciplinary Mentor-Student Recommendation System as a team and collected more than 100,000 data for experiment with Scrapy.

**Undergraduate Innovation Practice Program**, Shanghai, China.

- Undergraduate Research Student Nov 2014 – Nov 2015
  - Designed and implemented a social experiment on university education evaluation with 260 volunteers.

## SELECTED PROJECTS

**TouchMore iOS and macOS Applications**

Apr 2017 – May 2017

- Developed an iOS application to provide virtual keyboard and shortcut buttons;
- Developed a macOS application to perform received key events.

**Interpreter Based on Scheme**

Mar 2017 – May 2017

- Designed and implemented an interpreter to interpret a language supporting function, class, inheritance and polymorphism features.

**Network Application Layer Protocol Implementation**

Apr 2017

- Designed an application layer protocol for server and clients to communicate;
- Implemented the new protocol with socket programming.

**Pipeline CPU in Verilog**

Nov 2016 – Dec 2016

- Implemented a pipeline CPU in Verilog with data and control hazard solutions.

**Linux Shell in C**

Sep 2016

- Developed a linux shell supporting commands, pipeline and redirection features.

## SKILLS

C, C++, Perl, Python, Swift, Scheme, Haskell, Bash, PHP, SQL, Verilog, HTML/CSS/JS, Mathematica, L<sup>A</sup>T<sub>E</sub>X, MATLAB, Git, Perforce.

## CAMPUS ACTIVITIES

**UM-SJTU JI Teaching Assistant**, Shanghai, China.

- Teaching Assistant of Introduction to Logic Design May 2017 – Aug 2017
  - Held recitation classes and laboratory sessions for total 80 students each week;
  - Graded 200 students' homework and exam papers.
- Teaching Assistant of Calculus IV Sep 2016 – Dec 2016
  - Held Recitation Class and Office Hour each week;
  - Graded 25 students' homework, worksheets and exam papers.
- Assignment Reader of Honor Calculus IV Sep 2016 – Dec 2016
  - Graded 200 students' homework.
- Teaching Assistant of Technical Communication May 2016 – Aug 2016
  - Held Office Hour each week;
  - Graded 60 students' homework and exam papers.

## EXTRA-CURRICULAR ACTIVITIES

**UM-SJTU JI Honor Council**, Shanghai, China.

- Member Sep 2016 – Aug 2017
  - Investigated violation cases of Honor Code and Participated in hearing.

**UM-SJTU JI International Program Office**, Shanghai, China.

- Student Assistant Mar 2015 – Dec 2016
  - Led a 10 assistants team;
  - Designed and built International Programs On-Line Application System.

## LANGUAGES

Chinese: Native, English: Proficient, French: Intermediate, German: Elementary.