
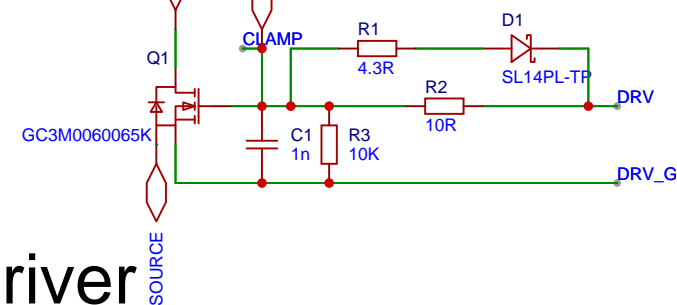
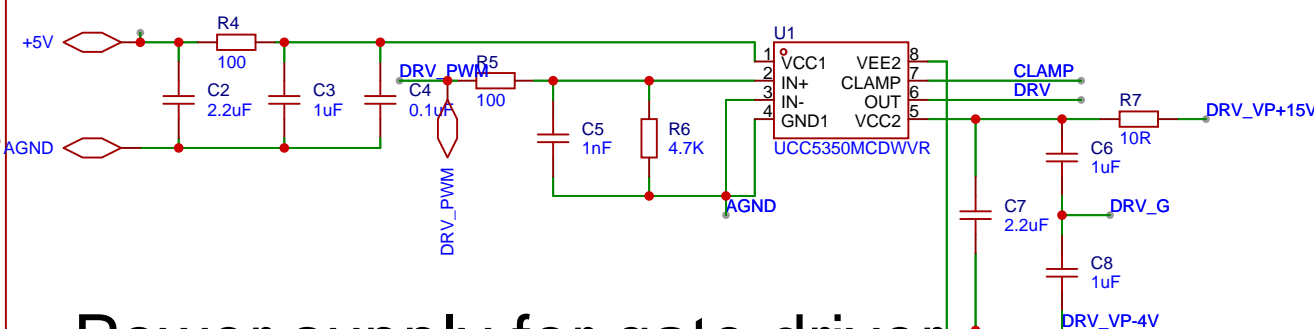


原理图	Schematic2			更新日期	2024-10-28
				创建日期	2024-10-21
图页	Final Quadrupler TPC			物料编码	
绘制		All in one TPC Project_2024-10-21			
审阅					
		版本	尺寸	页	1 共 1
		V1.0	A4	嘉立创EDA	

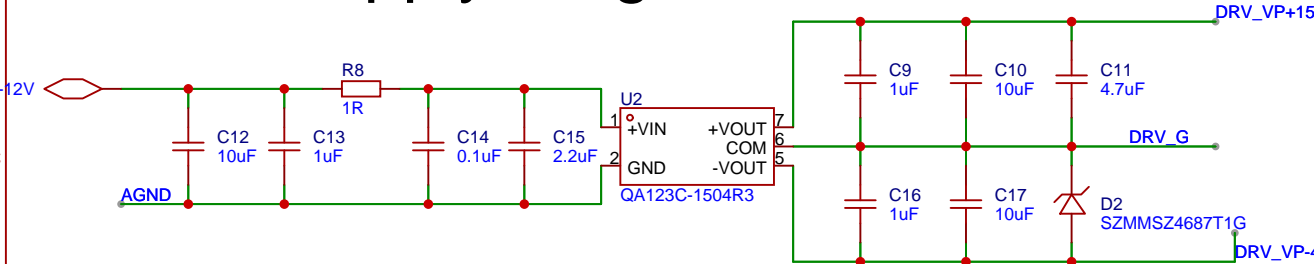
Main Power Circuit



Gate driver

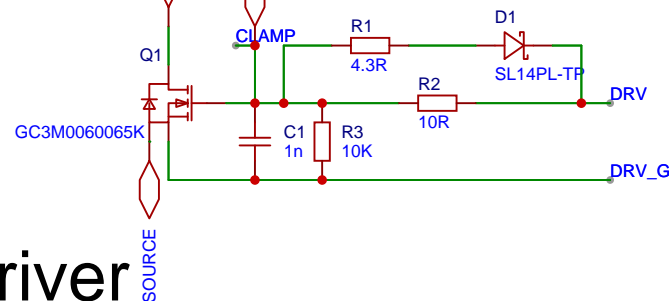


Power supply for gate driver

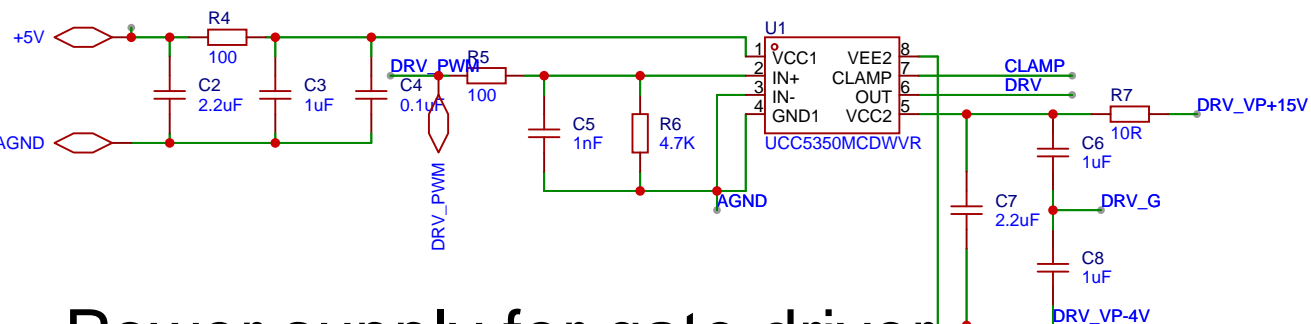


原理图	Switch_DRV_1		更新日期	2024-10-27
			创建日期	2024-10-22
图页	P1		物料编码	
绘制		All in one TPC Project_2024-10-21		
审阅				
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嘉立创EDA		V1.0	A4	嘉立创EDA

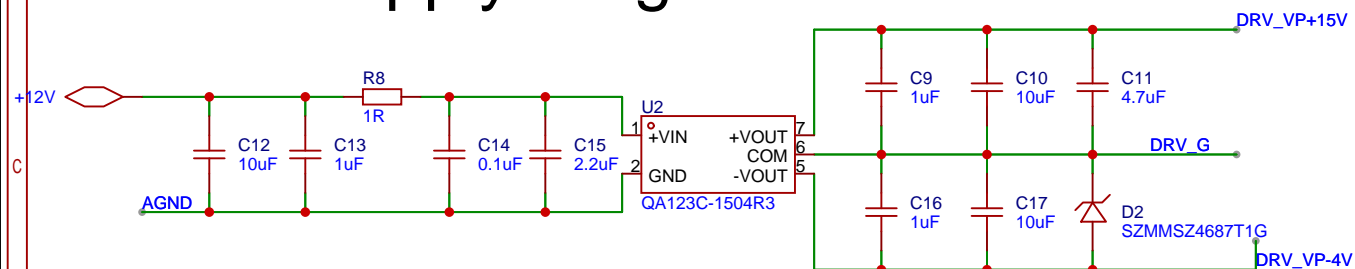
Main Power Circuit



Gate driver

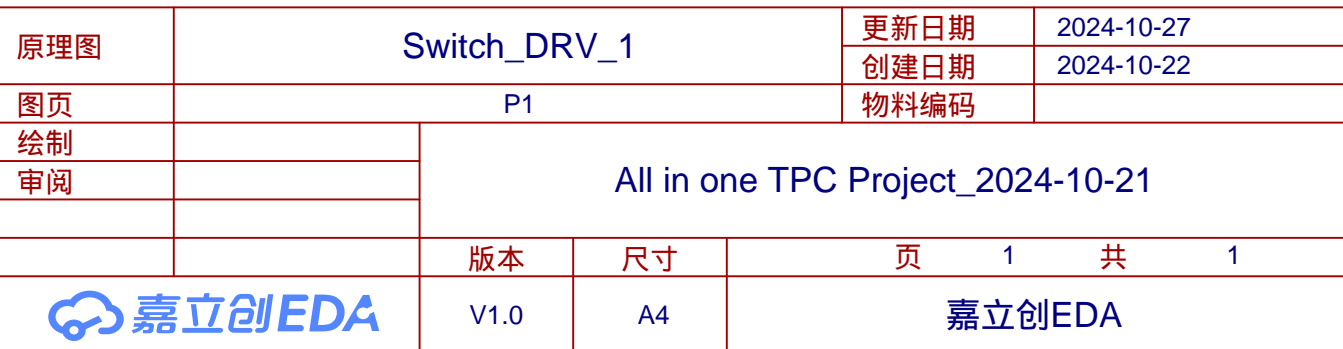


Power supply for gate driver



原理图	Switch_DRV_1		更新日期	2024-10-27
			创建日期	2024-10-22
图页	P1		物料编码	
绘制		All in one TPC Project_2024-10-21		
审阅				
		版本	尺寸	页 1 共 1
嘉立创EDA		V1.0	A4	嘉立创EDA

Gate driver



Gate driver

