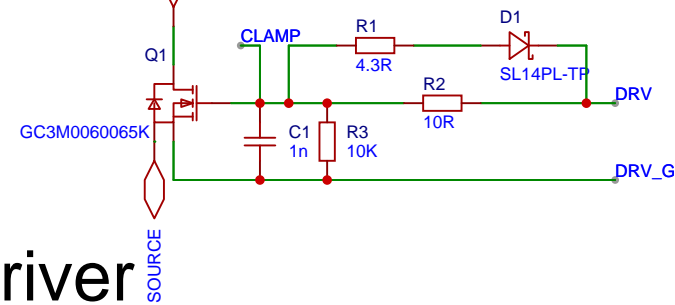
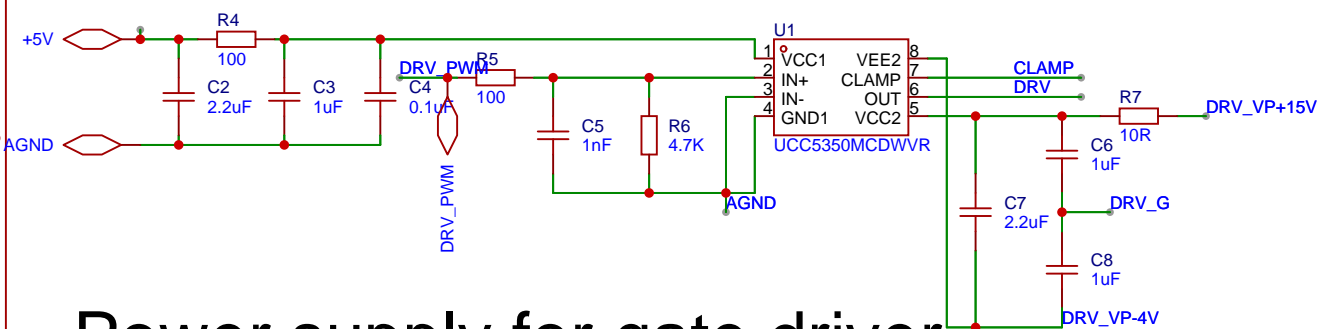


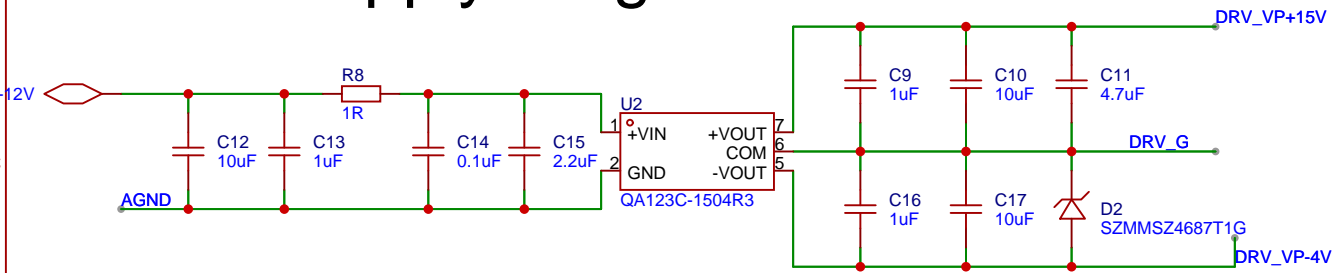
Main Power Circuit



Gate driver



Power supply for gate driver



原理图	Switch_DRV		更新日期	2024-10-22
			创建日期	2024-10-22
图页	P1		物料编码	
绘制		All in one TPC Project_2024-10-21		
审阅				
		版本	尺寸	页 1 共 1
嘉立创EDA		V1.0	A4	嘉立创EDA