## ChibiOS/HAL 4.0.9

## Reference Manual

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## **Chapter 1**

## ChibiOS/HAL

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### 1.2 Introduction

This document is the Reference Manual for the ChibiOS/HAL hardware abstraction layer.

### 1.3 Related Documents

· ChibiOS/HAL General Architecture

2 ChibiOS/HAL

## **Chapter 2**

# **Deprecated List**

Global sdGetWouldBlock (SerialDriver \*sdp)

Global sdPutWouldBlock (SerialDriver \*sdp)

4 Deprecated List

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|-----------|---------------|---------|----|------|--|------|------|------|------|--|------|------|------|--|--|
| Configura | ation         |         |    | <br> |  | <br> | <br> |      | <br> |  |      | <br> |      |  |  |
| Normal E  |               |         |    | <br> |  | <br> | <br> |      | <br> |  |      | <br> |      |  |  |
| ADC       | Driver        |         |    | <br> |  | <br> | <br> |      |      |  |      | <br> | <br> |  |  |
| _         | -             |         |    |      |  |      |      |      |      |  |      |      |      |  |  |
| DAC       | Driver        |         |    | <br> |  | <br> | <br> |      | <br> |  |      | <br> |      |  |  |
| EXT       | Oriver        |         |    | <br> |  | <br> | <br> |      | <br> |  |      | <br> | <br> |  |  |
| GPT       | Driver        |         |    | <br> |  | <br> | <br> |      | <br> |  | <br> | <br> |      |  |  |
| HAL       | Oriver        |         |    | <br> |  | <br> | <br> |      | <br> |  |      | <br> |      |  |  |
| I2C D     | river         |         |    | <br> |  | <br> | <br> |      | <br> |  |      | <br> |      |  |  |
| I2S D     | river         |         |    | <br> |  | <br> | <br> |      | <br> |  | <br> | <br> |      |  |  |
| ICU [     | Oriver        |         |    | <br> |  | <br> | <br> |      | <br> |  |      | <br> |      |  |  |
| MAC       | Driver        |         |    | <br> |  |      | <br> |      | <br> |  | <br> | <br> |      |  |  |
| PAL I     | Oriver        |         |    | <br> |  |      | <br> |      | <br> |  |      | <br> |      |  |  |
| PWM       | Driver        |         |    | <br> |  |      | <br> |      | <br> |  |      | <br> |      |  |  |
| RTC       | Driver        |         |    | <br> |  | <br> | <br> |      | <br> |  |      |      |      |  |  |
| SDC       | Driver        |         |    | <br> |  | <br> | <br> |      | <br> |  |      |      |      |  |  |
| Seria     | Driver        |         |    | <br> |  |      | <br> |      | <br> |  |      |      |      |  |  |
| SPI D     | river         |         |    | <br> |  |      | <br> |      | <br> |  |      |      |      |  |  |
| ST D      | iver          |         |    | <br> |  |      | <br> |      | <br> |  |      |      |      |  |  |
| UAR       | Driver        |         |    | <br> |  |      | <br> |      | <br> |  |      |      |      |  |  |
|           | Driver        |         |    |      |  |      |      |      |      |  |      |      |      |  |  |
| WDG       | Driver        |         |    | <br> |  |      | <br> |      | <br> |  |      |      |      |  |  |
| Complex   | Drivers       |         |    | <br> |  | <br> | <br> |      | <br> |  |      | <br> | <br> |  |  |
| MMC       | over SPI Di   | river . |    | <br> |  |      | <br> |      | <br> |  |      |      |      |  |  |
| Seria     | l over USB [  | Driver  |    | <br> |  |      | <br> |      | <br> |  |      |      |      |  |  |
| Interface | S             |         |    | <br> |  | <br> | <br> |      | <br> |  |      | <br> | <br> |  |  |
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| Abstr     | act Files .   |         |    | <br> |  | <br> | <br> |      | <br> |  |      | <br> |      |  |  |
| Abstr     | act I/O Blocl | k Devic | е. | <br> |  |      | <br> |      | <br> |  |      | <br> |      |  |  |
| Abstr     | act Streams   |         |    | <br> |  | <br> | <br> |      | <br> |  |      | <br> |      |  |  |
| Inner Co  | de            |         |    | <br> |  | <br> | <br> |      | <br> |  |      | <br> |      |  |  |
| I/O B     | uffers Queu   | es      |    | <br> |  | <br> | <br> |      | <br> |  | <br> | <br> |      |  |  |
| I/O B     | ytes Queues   | s       |    | <br> |  | <br> | <br> |      | <br> |  | <br> | <br> |      |  |  |
| MMC       | /SD Block D   | evice)  |    | <br> |  | <br> | <br> |      | <br> |  |      |      |      |  |  |
| Support   | Code          |         |    | <br> |  | <br> | <br> |      | <br> |  |      | <br> |      |  |  |
| MII/R     | MII Header    |         |    | <br> |  | <br> | <br> |      | <br> |  |      | <br> |      |  |  |

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# Chapter 4

## **Hierarchical Index**

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| MMCDriver  | 22 |
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| SerialDriver   | 43 |
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| BaseSequentialStreamVMT  | 83 |
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| SerialUSBDriverVMT   | 49 |
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| CANTxFrame   |    |
| cdc_linecoding_t   |    |
| DACConfig  |    |
| DACConversionGroup   |    |
| DACDriver  |    |
| EXTConfig  |    |
| EXTDriver  |    |
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| I2CDriver             | 5 |
| I2SConfig             | 6 |
| I2SDriver             | 8 |
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| io_queue              | 4 |
| IOBus                 | 5 |
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| MACDriver             | 7 |
| MACReceiveDescriptor  | 9 |
| MACTransmitDescriptor | 0 |
| MMCConfig             | 0 |
| PALConfig             | 9 |
| PWMChannelConfig      | 9 |
| PWMConfig             | 1 |
| PWMDriver             | 2 |
| RTCAlarm              | 4 |
| RTCDateTime           | 4 |
| RTCDriver             | 6 |
| SDCConfig             | 8 |
| SDCDriver             | 9 |
| SerialConfig          | 2 |
| SerialUSBConfig       | 6 |
| SPIConfig             | 1 |
| SPIDriver             | 2 |
| UARTConfig            | 3 |
| UARTDriver            | 4 |
| unpacked_mmc_cid_t    | 6 |
| unpacked_mmc_csd_t    | 7 |
| unpacked_sdc_cid_t    | 8 |
| unpacked_sdc_csd_10_t | 8 |
| unpacked_sdc_csd_20_t | 9 |
| USBConfig             | 0 |
| USBDescriptor         | 2 |
| USBDriver             | 3 |
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| USBInEndpointState    | 9 |
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| WDGConfig             | 1 |
| WDGDriver             | 2 |
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## **Chapter 5**

# **Data Structure Index**

## 5.1 Data Structures

Here are the data structures with brief descriptions:

| ADCConfig                                     |
|---|
| Driver configuration structure                |
| ADCConversionGroup                            |
| Conversion group configuration structure      |
| ADCDriver                                     |
| Structure representing an ADC driver          |
| BaseAsynchronousChannel                       |
| Base asynchronous channel class               |
| BaseAsynchronousChannelVMT                    |
| BaseAsynchronousChannel virtual methods table |
| BaseBlockDevice                               |
| Base block device class                       |
| BaseBlockDeviceVMT                            |
| BaseBlockDevice virtual methods table         |
| BaseChannel                                   |
| Base channel class                            |
| BaseChannelVMT                                |
| BaseChannel virtual methods table             |
| BaseSequentialStream                          |
| Base stream class                             |
| BaseSequentialStreamVMT                       |
| BaseSequentialStream virtual methods table    |
| BlockDeviceInfo Block device info             |
| CANConfig                                     |
| Driver configuration structure                |
| CANDriver                                     |
| Structure representing an CAN driver          |
| CANRxFrame                                    |
| CAN received frame                            |
| CANTxFrame                                    |
| CAN transmission frame                        |
| cdc linecoding t                              |
| Type of Line Coding structure                 |
| DACConfig                                     |
| Driver configuration structure                |
| DACConversionGroup                            |
| DAC Conversion group structure                |
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| EXTChannelConfig   |       |
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| EXTDriver  | +90   |
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| FileStream   |       |
| Base file stream class   | 500   |
| FileStreamVMT  |       |
|  | 501   |
| GPTConfig  Driver configuration structure  | 502   |
| GPTDriver  | JUZ   |
|  | 504   |
| I2CConfig  |       |
| Type of I2C driver configuration structure   | 504   |
| I2CDriver  |       |
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| I2SConfig  Driver configuration structure  | 506   |
| I2SDriver  | 000   |
|  | 508   |
| ICUConfig  |       |
| 5  | 509   |
| ICUDriver  |       |
|  | 510   |
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| io queue   | ,,,   |
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| IOBus  |       |
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| MACConfig  |       |
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| MMCConfig  |       |
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| MMCSDBlockDevice   |       |
|  | 525   |
| MMCSDBlockDeviceVMT  | -07   |
|  | 527   |
| PALConfig  Generic I/O ports static initializer  | 529   |
| PWMChannelConfig   | 0     |
|  | 529   |
| PWMConfig  |       |
| Type of a PWM driver configuration structure   | 531   |

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| RTCDateTime  | E0.4 |
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| Structure representing an RTC driver                         | 536  |
| RTCDriverVMT   | 500  |
| RTCDriver virtual methods table                              | 537  |
| SDCConfig  |      |
| Driver configuration structure                               | 538  |
| SDCDriver  |      |
| Structure representing an SDC driver                         | 539  |
| SDCDriverVMT   |      |
| SDCDriver virtual methods table                              | 541  |
| SerialConfig  PLATFORM Serial Driver configuration structure | E 40 |
| SerialDriver   | 542  |
| Full duplex serial driver class                              | 543  |
| Serial Driver VMT  | J+0  |
| SerialDriver virtual methods table                           | 544  |
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| SerialDriver virtual methods table                           | 549  |
| SPIConfig  |      |
| Driver configuration structure                               | 551  |
| SPIDriver  Characteristic and SPI delivers                   | FFC  |
| Structure representing an SPI driver                         | 552  |
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| UARTDriver   | 000  |
| Structure representing an UART driver                        | 554  |
| unpacked_mmc_cid_t   |      |
| Unpacked CID register from MMC                               | 556  |
| unpacked_mmc_csd_t   |      |
| 1  | 557  |
| unpacked_sdc_cid_t   |      |
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| unpacked_sdc_csd_10_t  |      |
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| unpacked_sdc_csd_20_t Unpacked CSD v2.0 register from SDC    | 559  |
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| USBDescriptor  | 000  |
| ·  | 562  |
| USBDriver  |      |
| Structure representing an USB driver                         | 563  |
| USBEndpointConfig  |      |
| Type of an USB endpoint configuration structure              | 566  |
| USBInEndpointState   |      |
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|--------------------------------------|-----|
| Driver configuration structure       | 571 |
| WDGDriver                            |     |
| Structure representing an WDG driver | 572 |

# **Chapter 6**

## File Index

## 6.1 File List

Here is a list of all documented files with brief descriptions:

| adc.c     |  |     |
|-----------|--|-----|
|           | ADC Driver code                                | 575 |
| adc.h     | ADC Driver macros and structures               | 576 |
| adc_lld.c |  | 577 |
| adc_lld.h |  | 577 |
| can.c     |  |     |
| can.h     | CAN Driver code                                | 579 |
| can Ild.c |  | 579 |
| _         | PLATFORM CAN subsystem low level driver source | 580 |
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## **Chapter 7**

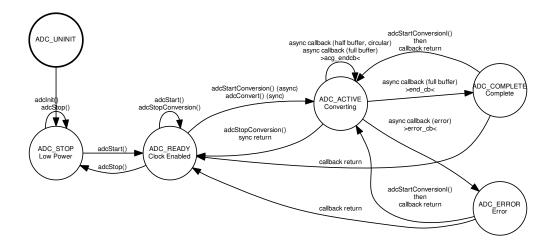
## **Module Documentation**

| 7.1 ADC Driver   |
|--|
| Generic ADC Driver.  |
|  |
|  |
| 7.1.1 Detailed Description   |
| Generic ADC Driver.  |
| This module implements a generic ADC (Analog to Digital Converter) driver supporting a variety of buffer and conversion modes. |
|  |
| Precondition   |
| In order to use the ADC driver the <code>HAL_USE_ADC</code> option must be enabled in <code>halconf.h</code> .                 |

### 7.1.2 Driver State Machine

The driver implements a state machine internally, not all the driver functionalities can be used in any moment, any transition not explicitly shown in the following diagram has to be considered an error and shall be captured by an assertion (if enabled).

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### 7.1.3 ADC Operations

The ADC driver is quite complex, an explanation of the terminology and of the operational details follows.

#### 7.1.3.1 ADC Conversion Groups

The ADCConversionGroup is the objects that specifies a physical conversion operation. This structure contains some standard fields and several implementation-dependent fields.

The standard fields define the CG mode, the number of channels belonging to the CG and the optional callbacks. The implementation-dependent fields specify the physical ADC operation mode, the analog channels belonging to the group and any other implementation-specific setting. Usually the extra fields just mirror the physical ADC registers, please refer to the vendor's MCU Reference Manual for details about the available settings. Details are also available into the documentation of the ADC low level drivers and in the various sample applications.

### 7.1.3.2 ADC Conversion Modes

The driver supports several conversion modes:

- One Shot, the driver performs a single group conversion then stops.
- Linear Buffer, the driver performs a series of group conversions then stops. This mode is like a one shot conversion repeated N times, the buffer pointer increases after each conversion. The buffer is organized as an S(CG)\*N samples matrix, when S(CG) is the conversion group size (number of channels) and N is the buffer depth (number of repeated conversions).
- Circular Buffer, much like the linear mode but the operation does not stop when the buffer is filled, it is automatically restarted with the buffer pointer wrapping back to the buffer base.

### 7.1.3.3 ADC Callbacks

The driver is able to invoke callbacks during the conversion process. A callback is invoked when the operation has been completed or, in circular mode, when the buffer has been filled and the operation is restarted. In circular mode a callback is also invoked when the buffer is half filled.

The "half filled" and "filled" callbacks in circular mode allow to implement "streaming processing" of the sampled

7.1 ADC Driver

data, while the driver is busy filling one half of the buffer the application can process the other half, this allows for continuous interleaved operations.

The driver is not thread safe for performance reasons, if you need to access the ADC bus from multiple threads then use the adcAcquireBus () and adcReleaseBus () APIs in order to gain exclusive access.

### **ADC** configuration options

• #define ADC USE WAIT TRUE

Enables synchronous APIs.

• #define ADC\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the adcAcquireBus () and adcReleaseBus () APIs.

### Low level driver helper macros

#define \_adc\_reset\_i(adcp) osalThreadResumeI(&(adcp)->thread, MSG\_RESET)

Resumes a thread waiting for a conversion completion.

#define \_adc\_reset\_s(adcp) osalThreadResumeS(&(adcp)->thread, MSG\_RESET)

Resumes a thread waiting for a conversion completion.

#define \_adc\_wakeup\_isr(adcp)

Wakes up the waiting thread.

#define \_adc\_timeout\_isr(adcp)

Wakes up the waiting thread with a timeout message.

#define \_adc\_isr\_half\_code(adcp)

Common ISR code, half buffer event.

#define \_adc\_isr\_full\_code(adcp)

Common ISR code, full buffer event.

#define \_adc\_isr\_error\_code(adcp, err)

Common ISR code, error event.

### **PLATFORM configuration options**

• #define PLATFORM\_ADC\_USE\_ADC1 FALSE

ADC1 driver enable switch.

### **Typedefs**

typedef uint16\_t adcsample\_t

ADC sample data type.

typedef uint16\_t adc\_channels\_num\_t

Channels number in a conversion group.

typedef struct ADCDriver ADCDriver

Type of a structure representing an ADC driver.

• typedef void(\* adccallback\_t) (ADCDriver \*adcp, adcsample\_t \*buffer, size\_t n)

ADC notification callback type.

typedef void(\* adcerrorcallback\_t) (ADCDriver \*adcp, adcerror\_t err)

ADC error callback type.

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### **Data Structures**

struct ADCConversionGroup

Conversion group configuration structure.

· struct ADCConfig

Driver configuration structure.

struct ADCDriver

Structure representing an ADC driver.

### **Functions**

void adcInit (void)

ADC Driver initialization.

void adcObjectInit (ADCDriver \*adcp)

Initializes the standard part of a ADCDriver structure.

void adcStart (ADCDriver \*adcp, const ADCConfig \*config)

Configures and activates the ADC peripheral.

void adcStop (ADCDriver \*adcp)

Deactivates the ADC peripheral.

void adcStartConversion (ADCDriver \*adcp, const ADCConversionGroup \*grpp, adcsample\_t \*samples, size\_t depth)

Starts an ADC conversion.

void adcStartConversionI (ADCDriver \*adcp, const ADCConversionGroup \*grpp, adcsample\_t \*samples, size\_t depth)

Starts an ADC conversion.

void adcStopConversion (ADCDriver \*adcp)

Stops an ongoing conversion.

void adcStopConversionI (ADCDriver \*adcp)

Stops an ongoing conversion.

msg\_t adcConvert (ADCDriver \*adcp, const ADCConversionGroup \*grpp, adcsample\_t \*samples, size\_t depth)

Performs an ADC conversion.

void adcAcquireBus (ADCDriver \*adcp)

Gains exclusive access to the ADC peripheral.

void adcReleaseBus (ADCDriver \*adcp)

Releases exclusive access to the ADC peripheral.

void adc Ild init (void)

Low level ADC driver initialization.

void adc\_lld\_start (ADCDriver \*adcp)

Configures and activates the ADC peripheral.

void adc\_lld\_stop (ADCDriver \*adcp)

Deactivates the ADC peripheral.

void adc\_lld\_start\_conversion (ADCDriver \*adcp)

Starts an ADC conversion.

void adc\_lld\_stop\_conversion (ADCDriver \*adcp)

Stops an ongoing conversion.

### **Enumerations**

### **Variables**

ADCDriver ADCD1

ADC1 driver identifier.

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## 7.1.4 Macro Definition Documentation

## 7.1.4.1 #define ADC\_USE\_WAIT TRUE

Enables synchronous APIs.

Note

Disabling this option saves both code and data space.

## 7.1.4.2 #define ADC\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the adcAcquireBus () and adcReleaseBus () APIs.

Note

Disabling this option saves both code and data space.

7.1.4.3 #define \_adc\_reset\_i( adcp ) osalThreadResumel(&(adcp)->thread, MSG\_RESET)

Resumes a thread waiting for a conversion completion.

#### **Parameters**

```
in adcp pointer to the ADCDriver object
```

### **Function Class:**

Not an API, this function is for internal use only.

 $7.1.4.4 \quad \texttt{\#define\_adc\_reset\_s(} \quad \textit{adcp} \ \texttt{)} \ osalThreadResumeS(\&(adcp)->thread, MSG\_RESET)$ 

Resumes a thread waiting for a conversion completion.

#### **Parameters**

```
in adcp pointer to the ADCDriver object
```

### **Function Class:**

Not an API, this function is for internal use only.

## 7.1.4.5 #define \_adc\_wakeup\_isr( adcp )

### Value:

```
{
  osalSysLockFromISR();
  osalThreadResumeI(&(adcp)->thread, MSG_OK);
  osalSysUnlockFromISR();
}
```

Wakes up the waiting thread.

### **Parameters**

|--|

### **Function Class:**

Not an API, this function is for internal use only.

## 7.1.4.6 #define \_adc\_timeout\_isr( adcp )

### Value:

Wakes up the waiting thread with a timeout message.

# **Parameters**

```
in | adcp | pointer to the ADCDriver object
```

### **Function Class:**

Not an API, this function is for internal use only.

## 7.1.4.7 #define \_adc\_isr\_half\_code( adcp )

## Value:

```
{
  if ((adcp)->grpp->end_cb != NULL) {
    (adcp)->grpp->end_cb(adcp, (adcp)->samples, (adcp)->depth / 2);
  }
}
```

Common ISR code, half buffer event.

This code handles the portable part of the ISR code:

· Callback invocation.

## Note

This macro is meant to be used in the low level drivers implementation only.

#### **Parameters**

| in | adcp | pointer to the ADCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

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```
7.1.4.8 #define _adc_isr_full_code( adcp )
```

Common ISR code, full buffer event.

This code handles the portable part of the ISR code:

- · Callback invocation.
- · Waiting thread wakeup, if any.
- · Driver state transitions.

#### Note

This macro is meant to be used in the low level drivers implementation only.

#### **Parameters**

```
in adcp pointer to the ADCDriver object
```

### **Function Class:**

Not an API, this function is for internal use only.

#### 7.1.4.9 #define \_adc\_isr\_error\_code( adcp, err )

### Value:

```
adc_lld_stop_conversion(adcp);
if ((adcp)->grpp->error_cb != NULL) {
   (adcp)->state = ADC_ERROR;
   (adcp)->state == ADC_ERROR)
   (adcp)->state == ADC_ERROR)
   (adcp)->state = ADC_READY;
   (adcp)->grpp = NULL;
}
else {
   (adcp)->state = ADC_READY;
   (adcp)->grpp = NULL;
}
adc_timeout_isr(adcp);
```

Common ISR code, error event.

This code handles the portable part of the ISR code:

- · Callback invocation.
- · Waiting thread timeout signaling, if any.
- · Driver state transitions.

## Note

This macro is meant to be used in the low level drivers implementation only.

| in | adcp | pointer to the ADCDriver object |
|----|------|---------------------------------|
| in | err  | platform dependent error code   |

## **Function Class:**

Not an API, this function is for internal use only.

## 7.1.4.10 #define PLATFORM\_ADC\_USE\_ADC1 FALSE

ADC1 driver enable switch.

If set to  $\ensuremath{\mathtt{TRUE}}$  the support for ADC1 is included.

### Note

The default is FALSE.

# 7.1.5 Typedef Documentation

### 7.1.5.1 typedef uint16\_t adcsample\_t

ADC sample data type.

## 7.1.5.2 typedef uint16\_t adc\_channels\_num\_t

Channels number in a conversion group.

## 7.1.5.3 typedef struct ADCDriver ADCDriver

Type of a structure representing an ADC driver.

# 7.1.5.4 typedef void(\* adccallback\_t) (ADCDriver \*adcp, adcsample\_t \*buffer, size\_t n)

ADC notification callback type.

### **Parameters**

| in | adcp   | pointer to the ADCDriver object triggering the callback |
|----|--------|---|
| in | buffer | pointer to the most recent samples data                 |
| in | n      | number of buffer rows available starting from buffer    |

# 7.1.5.5 typedef void(\* adcerrorcallback\_t) (ADCDriver \*adcp, adcerror\_t err)

ADC error callback type.

### **Parameters**

| in | adcp | pointer to the ADCDriver object triggering the callback |
|----|------|---|
| in | err  | ADC error code  |

# 7.1.6 Enumeration Type Documentation

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#### 7.1.6.1 enum adcstate\_t

Driver state machine possible states.

#### **Enumerator**

ADC\_UNINIT Not initialized.

ADC\_STOP Stopped.

ADC\_READY Ready.

ADC\_ACTIVE Converting.

**ADC\_COMPLETE** Conversion complete.

ADC\_ERROR Conversion error.

### 7.1.6.2 enum adcerror\_t

Possible ADC failure causes.

Note

Error codes are architecture dependent and should not relied upon.

#### **Enumerator**

**ADC\_ERR\_DMAFAILURE** DMA operations failure.

ADC\_ERR\_OVERFLOW ADC overflow condition.

ADC\_ERR\_AWD Analog watchdog triggered.

## 7.1.7 Function Documentation

7.1.7.1 void adclnit (void)

ADC Driver initialization.

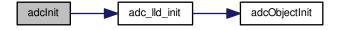
Note

This function is implicitly invoked by hallnit (), there is no need to explicitly initialize the driver.

# **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



## 7.1.7.2 void adcObjectInit ( ADCDriver \* adcp )

Initializes the standard part of a ADCDriver structure.

### **Parameters**

| out | adcp | pointer to the ADCDriver object |
|-----|------|---------------------------------|
|-----|------|---------------------------------|

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

7.1.7.3 void adcStart ( ADCDriver \* adcp, const ADCConfig \* config )

Configures and activates the ADC peripheral.

#### **Parameters**

| in | adcp   | pointer to the ADCDriver object  |
|----|--------|--|
| in | config | pointer to the ${\tt ADCConfig}$ object. Depending on the implementation the value can be ${\tt NULL}$ . |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.1.7.4 void adcStop ( ADCDriver \* adcp )

Deactivates the ADC peripheral.

#### **Parameters**

| in | adcp | pointer to the ADCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

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Here is the call graph for this function:



7.1.7.5 void adcStartConversion ( ADCDriver \* adcp, const ADCConversionGroup \* grpp, adcsample\_t \* samples, size\_t depth )

Starts an ADC conversion.

Starts an asynchronous conversion operation.

Note

The buffer is organized as a matrix of M\*N elements where M is the channels number configured into the conversion group and N is the buffer depth. The samples are sequentially written into the buffer with no gaps.

### **Parameters**

| in  | adcp    | pointer to the ADCDriver object        |  |
|-----|---------|--|--|
| in  | grpp    | pointer to a ADCConversionGroup object |  |
|     |         | pointer to the samples buffer          |  |
| out | samples | pointer to the samples buffer          |  |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.1.7.6 void adcStartConversionI ( ADCDriver \* adcp, const ADCConversionGroup \* grpp, adcsample\_t \* samples, size\_t depth )

Starts an ADC conversion.

Starts an asynchronous conversion operation.

### Postcondition

The callbacks associated to the conversion group will be invoked on buffer fill and error events.

### Note

The buffer is organized as a matrix of M\*N elements where M is the channels number configured into the conversion group and N is the buffer depth. The samples are sequentially written into the buffer with no gaps.

### **Parameters**

| in  | adcp    | pointer to the ADCDriver object  |
|-----|---------|--|
| in  | grpp    | pointer to a ADCConversionGroup object   |
| out | samples | pointer to the samples buffer  |
| in  | depth   | buffer depth (matrix rows number). The buffer depth must be one or an even number. |

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



## 7.1.7.7 void adcStopConversion ( ADCDriver \* adcp )

Stops an ongoing conversion.

This function stops the currently ongoing conversion and returns the driver in the ADC\_READY state. If there was no conversion being processed then the function does nothing.

### **Parameters**

| in | adcp | pointer to the ADCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

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Here is the call graph for this function:



# 7.1.7.8 void adcStopConversionI ( ADCDriver \* adcp )

Stops an ongoing conversion.

This function stops the currently ongoing conversion and returns the driver in the ADC\_READY state. If there was no conversion being processed then the function does nothing.

### **Parameters**

| in | adcp | pointer to the ADCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



7.1.7.9 msg\_t adcConvert ( ADCDriver \* adcp, const ADCConversionGroup \* grpp, adcsample\_t \* samples, size\_t depth )

Performs an ADC conversion.

Performs a synchronous conversion operation.

Note

The buffer is organized as a matrix of M\*N elements where M is the channels number configured into the conversion group and N is the buffer depth. The samples are sequentially written into the buffer with no gaps.

#### **Parameters**

| in  | adcp    | pointer to the ADCDriver object  |
|-----|---------|--|
| in  | grpp    | pointer to a ADCConversionGroup object   |
| out | samples | pointer to the samples buffer  |
| in  | depth   | buffer depth (matrix rows number). The buffer depth must be one or an even number. |

## Returns

The operation result.

## Return values

| MSG_OK      | Conversion finished.  |
|-------------|---|
| MSG_RESET   | The conversion has been stopped using acdStopConversion() or        |
|             | acdStopConversionI(), the result buffer may contain incorrect data. |
| MSG_TIMEOUT | The conversion has been stopped because an hardware error.          |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.1.7.10 void adcAcquireBus ( ADCDriver \* adcp )

Gains exclusive access to the ADC peripheral.

This function tries to gain ownership to the ADC bus, if the bus is already being used then the invoking thread is queued.

### Precondition

In order to use this function the option  ${\tt ADC\_USE\_MUTUAL\_EXCLUSION}$  must be enabled.

### **Parameters**

| in | adcp | pointer to the ADCDriver object |
|----|------|---------------------------------|

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.1 ADC Driver

### 7.1.7.11 void adcReleaseBus ( ADCDriver \* adcp )

Releases exclusive access to the ADC peripheral.

## Precondition

In order to use this function the option  ${\tt ADC\_USE\_MUTUAL\_EXCLUSION}$  must be enabled.

#### **Parameters**

```
in | adcp | pointer to the ADCDriver object
```

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.1.7.12 void adc\_lld\_init (void )

Low level ADC driver initialization.

#### **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.1.7.13 void adc\_lld\_start ( ADCDriver \* adcp )

Configures and activates the ADC peripheral.

### **Parameters**

| in | adcp | pointer to the ADCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

# **Function Class:**

Not an API, this function is for internal use only.

7.1.7.14 void adc\_lld\_stop ( ADCDriver \* adcp )

Deactivates the ADC peripheral.

## **Parameters**

| in | adcp | pointer to the ADCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### **Function Class:**

Not an API, this function is for internal use only.

7.1.7.15 void adc\_lld\_start\_conversion ( ADCDriver \* adcp )

Starts an ADC conversion.

### **Parameters**

| in | adcp | pointer to the ADCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

7.1.7.16 void adc\_lld\_stop\_conversion ( ADCDriver \* adcp )

Stops an ongoing conversion.

## **Parameters**

| in | adcp | pointer to the ADCDriver object |
|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

# 7.1.8 Variable Documentation

# 7.1.8.1 ADCDriver ADCD1

ADC1 driver identifier.

7.2 CAN Driver

## 7.2 CAN Driver

Generic CAN Driver.

# 7.2.1 Detailed Description

Generic CAN Driver.

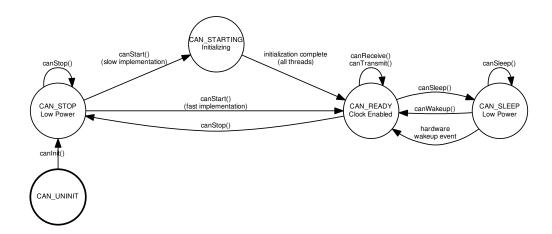
This module implements a generic CAN (Controller Area Network) driver allowing the exchange of information at frame level.

### Precondition

In order to use the CAN driver the HAL\_USE\_CAN option must be enabled in halconf.h.

#### 7.2.2 Driver State Machine

The driver implements a state machine internally, not all the driver functionalities can be used in any moment, any transition not explicitly shown in the following diagram has to be considered an error and shall be captured by an assertion (if enabled).



## **Macros**

- #define CAN\_ANY\_MAILBOX 0
  - Special mailbox identifier.
- #define CAN\_TX\_MAILBOXES 1

Number of transmit mailboxes.

• #define CAN RX MAILBOXES 1

Number of receive mailboxes.

# **CAN status flags**

• #define CAN\_LIMIT\_WARNING 1U

Errors rate warning.

• #define CAN\_LIMIT\_ERROR 2U

Errors rate error.

• #define CAN\_BUS\_OFF\_ERROR 4U

Bus off condition reached.

• #define CAN\_FRAMING\_ERROR 8U

Framing error of some kind on the CAN bus.

• #define CAN\_OVERFLOW\_ERROR 16U

Overflow in receive queue.

# **CAN** configuration options

• #define CAN USE SLEEP MODE TRUE

Sleep mode related APIs inclusion switch.

## **Macro Functions**

#define CAN\_MAILBOX\_TO\_MASK(mbx) (1U << ((mbx) - 1U))</li>

Converts a mailbox index to a bit mask.

# **PLATFORM** configuration options

• #define PLATFORM CAN USE CAN1 FALSE

CAN1 driver enable switch.

# **Typedefs**

typedef uint32\_t canmbx\_t

Type of a transmission mailbox index.

### **Data Structures**

struct CANTxFrame

CAN transmission frame.

struct CANRxFrame

CAN received frame.

struct CANConfig

Driver configuration structure.

struct CANDriver

Structure representing an CAN driver.

### **Functions**

void canInit (void)

CAN Driver initialization.

void canObjectInit (CANDriver \*canp)

 ${\it Initializes the standard part of a CANDriver structure}.$ 

void canStart (CANDriver \*canp, const CANConfig \*config)

Configures and activates the CAN peripheral.

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void canStop (CANDriver \*canp)

Deactivates the CAN peripheral.

bool canTryTransmitI (CANDriver \*canp, canmbx t mailbox, const CANTxFrame \*ctfp)

Can frame transmission attempt.

bool canTryReceivel (CANDriver \*canp, canmbx\_t mailbox, CANRxFrame \*crfp)

Can frame receive attempt.

msg\_t canTransmit (CANDriver \*canp, canmbx\_t mailbox, const CANTxFrame \*ctfp, systime\_t timeout)
 Can frame transmission.

• msg\_t canReceive (CANDriver \*canp, canmbx\_t mailbox, CANRxFrame \*crfp, systime\_t timeout)

Can frame receive.

void canSleep (CANDriver \*canp)

Enters the sleep mode.

void canWakeup (CANDriver \*canp)

Enforces leaving the sleep mode.

void can\_lld\_init (void)

Low level CAN driver initialization.

void can\_lld\_start (CANDriver \*canp)

Configures and activates the CAN peripheral.

void can\_lld\_stop (CANDriver \*canp)

Deactivates the CAN peripheral.

bool can\_lld\_is\_tx\_empty (CANDriver \*canp, canmbx\_t mailbox)

Determines whether a frame can be transmitted.

• void can\_lld\_transmit (CANDriver \*canp, canmbx\_t mailbox, const CANTxFrame \*ctfp)

Inserts a frame into the transmit queue.

bool can\_lld\_is\_rx\_nonempty (CANDriver \*canp, canmbx\_t mailbox)

Determines whether a frame has been received.

void can\_lld\_receive (CANDriver \*canp, canmbx\_t mailbox, CANRxFrame \*crfp)

Receives a frame from the input queue.

void can\_lld\_sleep (CANDriver \*canp)

Enters the sleep mode.

void can\_Ild\_wakeup (CANDriver \*canp)

Enforces leaving the sleep mode.

### **Enumerations**

### **Variables**

CANDriver CAND1

CAN1 driver identifier.

# 7.2.3 Macro Definition Documentation

7.2.3.1 #define CAN\_LIMIT\_WARNING 1U

Errors rate warning.

7.2.3.2 #define CAN\_LIMIT\_ERROR 2U

Errors rate error.

7.2.3.3 #define CAN\_BUS\_OFF\_ERROR 4U

Bus off condition reached.

7.2.3.4 #define CAN\_FRAMING\_ERROR 8U

Framing error of some kind on the CAN bus.

7.2.3.5 #define CAN\_OVERFLOW\_ERROR 16U

Overflow in receive queue.

7.2.3.6 #define CAN\_ANY\_MAILBOX 0

Special mailbox identifier.

7.2.3.7 #define CAN\_USE\_SLEEP\_MODE TRUE

Sleep mode related APIs inclusion switch.

This option can only be enabled if the CAN implementation supports the sleep mode, see the macro CAN\_SUPP ← ORTS\_SLEEP exported by the underlying implementation.

7.2.3.8 #define CAN\_MAILBOX\_TO\_MASK( mbx ) (1U << ((mbx) - 1U))

Converts a mailbox index to a bit mask.

7.2.3.9 #define CAN\_TX\_MAILBOXES 1

Number of transmit mailboxes.

7.2.3.10 #define CAN\_RX\_MAILBOXES 1

Number of receive mailboxes.

7.2.3.11 #define PLATFORM\_CAN\_USE\_CAN1 FALSE

CAN1 driver enable switch.

If set to  $\ensuremath{\mathtt{TRUE}}$  the support for CAN1 is included.

Note

The default is FALSE.

7.2.4 Typedef Documentation

7.2.4.1 typedef uint32\_t canmbx\_t

Type of a transmission mailbox index.

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# 7.2.5 Enumeration Type Documentation

# 7.2.5.1 enum canstate\_t

Driver state machine possible states.

#### Enumerator

CAN\_UNINIT Not initialized.

CAN\_STOP Stopped.

CAN\_STARTING Starting.

CAN\_READY Ready.

CAN\_SLEEP Sleep state.

# 7.2.6 Function Documentation

7.2.6.1 void canInit (void)

CAN Driver initialization.

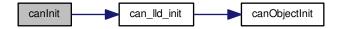
Note

This function is implicitly invoked by hallnit (), there is no need to explicitly initialize the driver.

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



# 7.2.6.2 void canObjectInit ( CANDriver \* canp )

Initializes the standard part of a CANDriver structure.

#### **Parameters**

| out | canp | pointer to the CANDriver object |
|-----|------|---------------------------------|

## **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

7.2.6.3 void canStart ( CANDriver \* canp, const CANConfig \* config )

Configures and activates the CAN peripheral.

Note

Activating the CAN bus can be a slow operation.

Unlike other drivers it is not possible to restart the CAN driver without first stopping it using canStop().

### **Parameters**

| in | canp   | pointer to the CANDriver object  |
|----|--------|--|
| in | config | pointer to the ${\tt CANConfig}$ object. Depending on the implementation the value can be ${\tt NULL}$ . |

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.2.6.4 void canStop ( CANDriver \* canp )

Deactivates the CAN peripheral.

### **Parameters**

| in | canp | pointer to the CANDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



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7.2.6.5 bool canTryTransmitl ( CANDriver \* canp, canmbx\_t mailbox, const CANTxFrame \* ctfp )

Can frame transmission attempt.

The specified frame is queued for transmission, if the hardware queue is full then the function fails.

#### **Parameters**

| in | canp    | pointer to the CANDriver object                 |
|----|---------|---|
| in | mailbox | mailbox number, CAN_ANY_MAILBOX for any mailbox |
| in | ctfp    | pointer to the CAN frame to be transmitted      |

### Returns

The operation result.

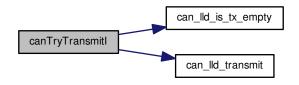
### Return values

| false | Frame transmitted. |
|-------|--------------------|
| true  | Mailbox full.      |

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



7.2.6.6 bool canTryReceivel ( CANDriver \* canp, canmbx\_t mailbox, CANRxFrame \* crfp )

Can frame receive attempt.

The function tries to fetch a frame from a mailbox.

| in  | canp    | pointer to the CANDriver object                     |
|-----|---------|---|
| in  | mailbox | mailbox number, CAN_ANY_MAILBOX for any mailbox     |
| out | crfp    | pointer to the buffer where the CAN frame is copied |

### Returns

The operation result.

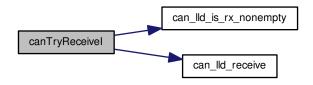
### Return values

| false | Frame fetched. |
|-------|----------------|
| true  | Mailbox empty. |

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



7.2.6.7 msg\_t canTransmit ( CANDriver \* canp, canmbx\_t mailbox, const CANTxFrame \* ctfp, systime\_t timeout )

Can frame transmission.

The specified frame is queued for transmission, if the hardware queue is full then the invoking thread is queued.

## Note

Trying to transmit while in sleep mode simply enqueues the thread.

# Parameters

| canp    | pointer to the CANDriver object  |
|---------|--|
| mailbox | mailbox number, CAN_ANY_MAILBOX for any mailbox  |
| ctfp    | pointer to the CAN frame to be transmitted   |
| timeout | the number of ticks before the operation timeouts, the following special values are allowed: |
|         | TIME_IMMEDIATE immediate timeout.  |
|         | TIME_INFINITE no timeout.  |
|         | mailbox<br>ctfp  |

## Returns

The operation result.

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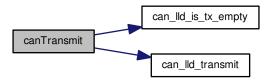
## Return values

| MSG_OK      | the frame has been queued for transmission. |
|-------------|---|
| MSG_TIMEOUT | The operation has timed out.                |
| MSG_RESET   | The driver has been stopped while waiting.  |

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.2.6.8 msg\_t canReceive ( CANDriver \* canp, canmbx\_t mailbox, CANRxFrame \* crfp, systime\_t timeout )

Can frame receive.

The function waits until a frame is received.

Note

Trying to receive while in sleep mode simply enqueues the thread.

# Parameters

| in  | canp    | pointer to the CANDriver object   |
|-----|---------|---|
| in  | mailbox | mailbox number, CAN_ANY_MAILBOX for any mailbox   |
| out | crfp    | pointer to the buffer where the CAN frame is copied   |
| in  | timeout | the number of ticks before the operation timeouts, the following special values are allowed:  |
|     |         | <ul> <li>TIME_IMMEDIATE immediate timeout (useful in an event driven scenario where a thread never blocks for I/O).</li> <li>TIME_INFINITE no timeout.</li> </ul> |

### Returns

The operation result.

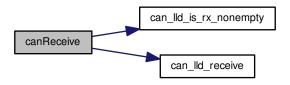
### Return values

| MSG_OK      | a frame has been received and placed in the buffer. |
|-------------|---|
| MSG_TIMEOUT | The operation has timed out.                        |
| MSG_RESET   | The driver has been stopped while waiting.          |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.2.6.9 void canSleep ( CANDriver \* canp )

Enters the sleep mode.

This function puts the CAN driver in sleep mode and broadcasts the <code>sleep\_event</code> event source.

## Precondition

In order to use this function the option CAN\_USE\_SLEEP\_MODE must be enabled and the CAN\_SUPPOR  $\leftarrow$  TS\_SLEEP mode must be supported by the low level driver.

#### **Parameters**

| in | canp | pointer to the CANDriver object |
|----|------|---------------------------------|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



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7.2.6.10 void canWakeup ( CANDriver \* canp )

Enforces leaving the sleep mode.

Note

The sleep mode is supposed to be usually exited automatically by an hardware event.

### **Parameters**

```
in canp pointer to the CANDriver object
```

Here is the call graph for this function:



7.2.6.11 void can\_lld\_init (void)

Low level CAN driver initialization.

**Function Class:** 

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.2.6.12 void can\_lld\_start ( CANDriver \* canp )

Configures and activates the CAN peripheral.

| in | canp | pointer to the CANDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

7.2.6.13 void can\_lld\_stop ( CANDriver \* canp )

Deactivates the CAN peripheral.

#### **Parameters**

| in | canp | pointer to the CANDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

7.2.6.14 bool can\_lld\_is\_tx\_empty ( CANDriver \* canp, canmbx\_t mailbox )

Determines whether a frame can be transmitted.

#### **Parameters**

| in | canp    | pointer to the CANDriver object                 |
|----|---------|---|
| in | mailbox | mailbox number, CAN_ANY_MAILBOX for any mailbox |

## Returns

The queue space availability.

# Return values

| FALSE | no space in the transmit queue. |
|-------|---------------------------------|
| TRUE  | transmit slot available.        |

## **Function Class:**

Not an API, this function is for internal use only.

7.2.6.15 void can\_lld\_transmit ( CANDriver \* canp, canmbx\_t mailbox, const CANTxFrame \* ctfp )

Inserts a frame into the transmit queue.

| in | canp    | pointer to the CANDriver object                 |
|----|---------|---|
| in | ctfp    | pointer to the CAN frame to be transmitted      |
| in | mailbox | mailbox number, CAN_ANY_MAILBOX for any mailbox |

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## **Function Class:**

Not an API, this function is for internal use only.

7.2.6.16 bool can\_lld\_is\_rx\_nonempty ( CANDriver \* canp, canmbx\_t mailbox )

Determines whether a frame has been received.

## **Parameters**

|   | in | canp    | pointer to the CANDriver object                 |
|---|----|---------|---|
| ſ | in | mailbox | mailbox number, CAN_ANY_MAILBOX for any mailbox |

## Returns

The queue space availability.

### Return values

| FALSE | no space in the transmit queue. |
|-------|---------------------------------|
| TRUE  | transmit slot available.        |

## **Function Class:**

Not an API, this function is for internal use only.

7.2.6.17 void can\_lld\_receive ( CANDriver \* canp, canmbx\_t mailbox, CANRxFrame \* crfp )

Receives a frame from the input queue.

# Parameters

| in  | canp    | pointer to the CANDriver object                     |
|-----|---------|---|
| in  | mailbox | mailbox number, CAN_ANY_MAILBOX for any mailbox     |
| out | crfp    | pointer to the buffer where the CAN frame is copied |

### **Function Class:**

Not an API, this function is for internal use only.

7.2.6.18 void can\_lld\_sleep ( CANDriver \* canp )

Enters the sleep mode.

| in | canp | pointer to the CANDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

# **Function Class:**

Not an API, this function is for internal use only.

7.2.6.19 void can\_lld\_wakeup ( CANDriver \* canp )

Enforces leaving the sleep mode.

### **Parameters**

| in | canp | pointer to the CANDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

# 7.2.7 Variable Documentation

# 7.2.7.1 CANDriver CAND1

CAN1 driver identifier.

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## 7.3 DAC Driver

Generic DAC Driver.

## 7.3.1 Detailed Description

Generic DAC Driver.

This module implements a generic DAC (Digital to Analog Converter) driver.

#### Precondition

In order to use the MAC driver the HAL\_USE\_DAC option must be enabled in halconf.h.

### **Macros**

• #define DAC\_MAX\_CHANNELS 2

Maximum number of DAC channels per unit.

# **DAC** configuration options

• #define DAC\_USE\_WAIT TRUE

Enables synchronous APIs.

#define DAC USE MUTUAL EXCLUSION TRUE

Enables the dacAcquireBus() and dacReleaseBus() APIs.

## Low level driver helper macros

• #define \_dac\_wait\_s(dacp) osalThreadSuspendS(&(dacp)->thread)

Waits for operation completion.

• #define \_dac\_reset\_i(dacp) osalThreadResumeI(&(dacp)->thread, MSG\_RESET)

Resumes a thread waiting for a conversion completion.

• #define \_dac\_reset\_s(dacp) osalThreadResumeS(&(dacp)->thread, MSG\_RESET)

Resumes a thread waiting for a conversion completion.

#define \_dac\_wakeup\_isr(dacp)

Wakes up the waiting thread.

#define \_dac\_timeout\_isr(dacp)

Wakes up the waiting thread with a timeout message.

• #define \_dac\_isr\_half\_code(dacp)

Common ISR code, half buffer event.

• #define \_dac\_isr\_full\_code(dacp)

Common ISR code, full buffer event.

#define \_dac\_isr\_error\_code(dacp, err)

Common ISR code, error event.

# **Configuration options**

#define PLATFORM\_DAC\_USE\_DAC1 FALSE

DAC1 CH1 driver enable switch.

# **Typedefs**

typedef uint32 t dacchannel t

Type of a DAC channel index.

typedef struct DACDriver DACDriver

Type of a structure representing an DAC driver.

typedef uint16\_t dacsample\_t

Type representing a DAC sample.

typedef void(\* daccallback\_t) (DACDriver \*dacp, dacsample\_t \*buffer, size\_t n)

DAC notification callback type.

typedef void(\* dacerrorcallback\_t) (DACDriver \*dacp, dacerror\_t err)

ADC error callback type.

### **Data Structures**

struct DACConversionGroup

DAC Conversion group structure.

struct DACConfig

Driver configuration structure.

struct DACDriver

Structure representing a DAC driver.

## **Functions**

· void dacInit (void)

DAC Driver initialization.

void dacObjectInit (DACDriver \*dacp)

 ${\it Initializes the standard part of a {\it DACDriver structure}}.$ 

void dacStart (DACDriver \*dacp, const DACConfig \*config)

Configures and activates the DAC peripheral.

void dacStop (DACDriver \*dacp)

Deactivates the DAC peripheral.

• void dacPutChannelX (DACDriver \*dacp, dacchannel\_t channel, dacsample\_t sample)

Outputs a value directly on a DAC channel.

void dacStartConversion (DACDriver \*dacp, const DACConversionGroup \*grpp, dacsample\_t \*samples, size\_t depth)

Starts a DAC conversion.

void dacStartConversionI (DACDriver \*dacp, const DACConversionGroup \*grpp, dacsample\_t \*samples, size\_t depth)

Starts a DAC conversion.

void dacStopConversion (DACDriver \*dacp)

Stops an ongoing conversion.

void dacStopConversionI (DACDriver \*dacp)

Stops an ongoing conversion.

msg\_t dacConvert (DACDriver \*dacp, const DACConversionGroup \*grpp, dacsample\_t \*samples, size\_t depth)

Performs a DAC conversion.

• void dacAcquireBus (DACDriver \*dacp)

Gains exclusive access to the DAC bus.

void dacReleaseBus (DACDriver \*dacp)

Releases exclusive access to the DAC bus.

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void dac\_lld\_init (void)

Low level DAC driver initialization.

void dac\_lld\_start (DACDriver \*dacp)

Configures and activates the DAC peripheral.

void dac\_lld\_stop (DACDriver \*dacp)

Deactivates the DAC peripheral.

• void dac\_lld\_put\_channel (DACDriver \*dacp, dacchannel\_t channel, dacsample\_t sample)

Outputs a value directly on a DAC channel.

void dac\_lld\_start\_conversion (DACDriver \*dacp)

Starts a DAC conversion.

void dac\_lld\_stop\_conversion (DACDriver \*dacp)

Stops an ongoing conversion.

### **Enumerations**

### **Variables**

DACDriver DACD1

DAC1 driver identifier.

### 7.3.2 Macro Definition Documentation

#### 7.3.2.1 #define DAC\_USE\_WAIT TRUE

Enables synchronous APIs.

Note

Disabling this option saves both code and data space.

## 7.3.2.2 #define DAC\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the dacAcquireBus () and dacReleaseBus () APIs.

Note

Disabling this option saves both code and data space.

7.3.2.3 #define \_dac\_wait\_s( dacp ) osalThreadSuspendS(&(dacp)->thread)

Waits for operation completion.

This function waits for the driver to complete the current operation.

### Precondition

An operation must be running while the function is invoked.

Note

No more than one thread can wait on a DAC driver using this function.

### **Parameters**

| in | dacp | pointer to the DACDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### **Function Class:**

Not an API, this function is for internal use only.

7.3.2.4 #define \_dac\_reset\_i( dacp ) osalThreadResumel(&(dacp)->thread, MSG\_RESET)

Resumes a thread waiting for a conversion completion.

#### **Parameters**

```
in dacp pointer to the DACDriver object
```

### **Function Class:**

Not an API, this function is for internal use only.

7.3.2.5  $\#define\_dac\_reset\_s(\ \textit{dacp}\ )\ osalThreadResumeS(\&(dacp)->thread,\ MSG\_RESET)$ 

Resumes a thread waiting for a conversion completion.

#### **Parameters**

|  | in | dacp | pointer to the DACDriver object |
|--|----|------|---------------------------------|
|--|----|------|---------------------------------|

# **Function Class:**

Not an API, this function is for internal use only.

7.3.2.6 #define \_dac\_wakeup\_isr( dacp )

# Value:

```
{
  osalSysLockFromISR();
  osalThreadResumeI(&(dacp)->thread, MSG_OK);
  osalSysUnlockFromISR();
  \
}
```

Wakes up the waiting thread.

## **Parameters**

| in | dacp | pointer to the DACDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

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### 7.3.2.7 #define \_dac\_timeout\_isr( dacp )

### Value:

```
{
  osalSysLockFromISR();
  osalThreadResumeI(&(dacp)->thread, MSG_TIMEOUT);
  osalSysUnlockFromISR();
}
```

Wakes up the waiting thread with a timeout message.

#### **Parameters**

```
in dacp pointer to the DACDriver object
```

#### **Function Class:**

Not an API, this function is for internal use only.

```
7.3.2.8 #define _dac_isr_half_code( dacp )
```

## Value:

```
{
  if ((dacp)->grpp->end_cb != NULL) {
    (dacp)->grpp->end_cb(dacp, (dacp)->samples, (dacp)->depth / 2);
     }
}
```

Common ISR code, half buffer event.

This code handles the portable part of the ISR code:

· Callback invocation.

## Note

This macro is meant to be used in the low level drivers implementation only.

#### **Parameters**

```
in dacp pointer to the DACDriver object
```

### **Function Class:**

Not an API, this function is for internal use only.

```
7.3.2.9 #define _dac_isr_full_code( dacp )
```

## Value:

```
if ((dacp)->grpp->end_cb != NULL) {
  if ((dacp)->depth > 1) {
    /* Invokes the callback passing the 2nd half of the buffer.*/
    size_t half = (dacp)->depth / 2;
}
```

```
size_t half_index = half * (dacp)->grpp->num_channels;
   (dacp)->grpp->end_cb(dacp, (dacp)->samples + half_index, half);
}
else {
   /* Invokes the callback passing the whole buffer.*/
   (dacp)->grpp->end_cb(dacp, (dacp)->samples, (dacp)->depth);
}
}
```

Common ISR code, full buffer event.

This code handles the portable part of the ISR code:

- · Callback invocation.
- · Waiting thread wakeup, if any.
- · Driver state transitions.

#### Note

This macro is meant to be used in the low level drivers implementation only.

### **Parameters**

```
in dacp pointer to the DACDriver object
```

#### **Function Class:**

Not an API, this function is for internal use only.

```
7.3.2.10 #define _dac_isr_error_code( dacp, err )
```

## Value:

```
{
  dac_lld_stop_conversion(dacp);
  if ((dacp)->grpp->error_cb != NULL) {
    (dacp)->state = DAC_ERROR;
    (dacp)->state == DAC_ERROR)
        if ((dacp)->state == DAC_ERROR)
            (dacp)->state = DAC_ERROR)
        (dacp)->state = DAC_READY;
}
(dacp)->grpp = NULL;
_dac_timeout_isr(dacp);
}
```

Common ISR code, error event.

This code handles the portable part of the ISR code:

- · Callback invocation.
- · Waiting thread timeout signaling, if any.
- · Driver state transitions.

### Note

This macro is meant to be used in the low level drivers implementation only.

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#### **Parameters**

| in | dacp | pointer to the DACDriver object |
|----|------|---------------------------------|
| in | err  | platform dependent error code   |

### **Function Class:**

Not an API, this function is for internal use only.

7.3.2.11 #define DAC\_MAX\_CHANNELS 2

Maximum number of DAC channels per unit.

7.3.2.12 #define PLATFORM\_DAC\_USE\_DAC1 FALSE

DAC1 CH1 driver enable switch.

If set to TRUE the support for DAC1 channel 1 is included.

Note

The default is FALSE.

# 7.3.3 Typedef Documentation

7.3.3.1 typedef uint32\_t dacchannel\_t

Type of a DAC channel index.

7.3.3.2 typedef struct DACDriver DACDriver

Type of a structure representing an DAC driver.

7.3.3.3 typedef uint16\_t dacsample\_t

Type representing a DAC sample.

7.3.3.4 typedef void(\* daccallback\_t) (DACDriver \*dacp, dacsample\_t \*buffer, size\_t n)

DAC notification callback type.

### **Parameters**

| i | in | dacp   | pointer to the DACDriver object triggering the                |
|---|----|--------|---|
| i | in | buffer | pointer to the next semi-buffer to be filled                  |
| i | in | n      | number of buffer rows available starting from buffer callback |

7.3.3.5 typedef void(\* dacerrorcallback\_t) (DACDriver \*dacp, dacerror\_t err)

ADC error callback type.

#### **Parameters**

| in | dacp | pointer to the DACDriver object triggering the callback |
|----|------|---|
| in | err  | ADC error code  |

# 7.3.4 Enumeration Type Documentation

## 7.3.4.1 enum dacstate\_t

Driver state machine possible states.

#### Enumerator

DAC\_UNINIT Not initialized.

DAC\_STOP Stopped.

DAC\_READY Ready.

**DAC\_ACTIVE** Exchanging data.

**DAC\_COMPLETE** Asynchronous operation complete.

DAC\_ERROR Error.

## 7.3.4.2 enum dacerror\_t

Possible DAC failure causes.

Note

Error codes are architecture dependent and should not relied upon.

### Enumerator

DAC\_ERR\_DMAFAILURE DMA operations failure.

DAC\_ERR\_UNDERFLOW DAC overflow condition.

### 7.3.5 Function Documentation

7.3.5.1 void dacInit (void)

DAC Driver initialization.

Note

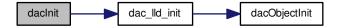
This function is implicitly invoked by halinit (), there is no need to explicitly initialize the driver.

## **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

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Here is the call graph for this function:



# 7.3.5.2 void dacObjectInit ( DACDriver \* dacp )

Initializes the standard part of a DACDriver structure.

### **Parameters**

|  | out | dacp | pointer to the DACDriver object |  |
|--|-----|------|---------------------------------|--|
|--|-----|------|---------------------------------|--|

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

## 7.3.5.3 void dacStart ( DACDriver \* dacp, const DACConfig \* config )

Configures and activates the DAC peripheral.

## **Parameters**

| in | dacp   | pointer to the DACDriver object  |  |
|----|--------|--|--|
| in | config | pointer to the DACConfig object, it can be NULL if the low level driver implementation |  |
|    |        | supports a default configuration   |  |

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.3.5.4 void dacStop ( DACDriver \* dacp )

Deactivates the DAC peripheral.

Note

Deactivating the peripheral also enforces a release of the slave select line.

### **Parameters**

|  | in | dacp | pointer to the DACDriver object | ] |
|--|----|------|---------------------------------|---|
|--|----|------|---------------------------------|---|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.3.5.5 void dacPutChannelX ( DACDriver \* dacp, dacchannel\_t channel, dacsample\_t sample )

Outputs a value directly on a DAC channel.

## **Parameters**

| in | dacp    | pointer to the DACDriver object |
|----|---------|---------------------------------|
| in | channel | DAC channel number              |
| in | sample  | value to be output              |

## **Function Class:**

This is an X-Class API, this function can be invoked from any context.

Here is the call graph for this function:



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7.3.5.6 void dacStartConversion ( DACDriver \* dacp, const DACConversionGroup \* grpp, dacsample\_t \* samples, size\_t depth )

Starts a DAC conversion.

Starts an asynchronous conversion operation.

#### Note

The buffer is organized as a matrix of M\*N elements where M is the channels number configured into the conversion group and N is the buffer depth. The samples are sequentially written into the buffer with no gaps.

#### **Parameters**

| in | dacp    | pointer to the DACDriver object  |  |
|----|---------|--|--|
| in | grpp    | pointer to a DACConversionGroup object   |  |
| in | samples | pointer to the samples buffer  |  |
| in | depth   | puffer depth (matrix rows number). The buffer depth must be one or an even number. |  |

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.3.5.7 void dacStartConversionI ( DACDriver \* dacp, const DACConversionGroup \* grpp, dacsample\_t \* samples, size\_t depth )

Starts a DAC conversion.

Starts an asynchronous conversion operation.

### Postcondition

The callbacks associated to the conversion group will be invoked on buffer fill and error events.

## Note

The buffer is organized as a matrix of M\*N elements where M is the channels number configured into the conversion group and N is the buffer depth. The samples are sequentially written into the buffer with no gaps.

### **Parameters**

| in | dacp    | pointer to the DACDriver object  |  |
|----|---------|--|--|
| in | grpp    | pointer to a DACConversionGroup object   |  |
| in | samples | ointer to the samples buffer   |  |
| in | depth   | ouffer depth (matrix rows number). The buffer depth must be one or an even number. |  |

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## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



## 7.3.5.8 void dacStopConversion ( DACDriver \* dacp )

Stops an ongoing conversion.

This function stops the currently ongoing conversion and returns the driver in the DAC\_READY state. If there was no conversion being processed then the function does nothing.

#### **Parameters**

|  | in | dacp | pointer to the DACDriver object |  |
|--|----|------|---------------------------------|--|
|--|----|------|---------------------------------|--|

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.3.5.9 void dacStopConversionI ( DACDriver \* dacp )

Stops an ongoing conversion.

This function stops the currently ongoing conversion and returns the driver in the DAC\_READY state. If there was no conversion being processed then the function does nothing.

#### **Parameters**

| in | dacp | pointer to the DACDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

7.3 DAC Driver 59

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



7.3.5.10 msg\_t dacConvert ( DACDriver \* dacp, const DACConversionGroup \* grpp, dacsample\_t \* samples, size\_t depth )

Performs a DAC conversion.

Performs a synchronous conversion operation.

#### Note

The buffer is organized as a matrix of M\*N elements where M is the channels number configured into the conversion group and N is the buffer depth. The samples are sequentially written into the buffer with no gaps.

#### **Parameters**

| in  | dacp    | pointer to the DACDriver object  |
|-----|---------|--|
| in  | grpp    | pointer to a DACConversionGroup object   |
| out | samples | pointer to the samples buffer  |
| in  | depth   | buffer depth (matrix rows number). The buffer depth must be one or an even number. |

#### Returns

The operation result.

### Return values

| MSG_OK      | Conversion finished.  |  |
|-------------|---|--|
| MSG_RESET   | The conversion has been stopped using acdStopConversion() or        |  |
|             | acdStopConversionI(), the result buffer may contain incorrect data. |  |
| MSG_TIMEOUT | DUT The conversion has been stopped because an hardware error.      |  |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



#### 7.3.5.11 void dacAcquireBus ( DACDriver \* dacp )

Gains exclusive access to the DAC bus.

This function tries to gain ownership to the DAC bus, if the bus is already being used then the invoking thread is queued.

#### Precondition

In order to use this function the option DAC\_USE\_MUTUAL\_EXCLUSION must be enabled.

#### **Parameters**

| in | dacp | pointer to the DACDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

#### 7.3.5.12 void dacReleaseBus ( DACDriver \* dacp )

Releases exclusive access to the DAC bus.

# Precondition

In order to use this function the option DAC\_USE\_MUTUAL\_EXCLUSION must be enabled.

#### **Parameters**

| i | 1 | dacp | pointer to the DACDriver object |
|---|---|------|---------------------------------|
|---|---|------|---------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

## 7.3.5.13 void dac\_lld\_init (void)

Low level DAC driver initialization.

## **Function Class:**

Not an API, this function is for internal use only.

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Here is the call graph for this function:



7.3.5.14 void dac\_lld\_start ( DACDriver \* dacp )

Configures and activates the DAC peripheral.

#### **Parameters**

| i | .n | dacp | pointer to the DACDriver object |
|---|----|------|---------------------------------|
|---|----|------|---------------------------------|

#### **Function Class:**

Not an API, this function is for internal use only.

7.3.5.15 void dac\_lld\_stop ( DACDriver \* dacp )

Deactivates the DAC peripheral.

# **Parameters**

| in | dacp | pointer to the DACDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

7.3.5.16 void dac\_lld\_put\_channel ( DACDriver \* dacp, dacchannel\_t channel, dacsample\_t sample )

Outputs a value directly on a DAC channel.

#### **Parameters**

| in | dacp    | pointer to the DACDriver object |
|----|---------|---------------------------------|
| in | channel | DAC channel number              |
| in | sample  | value to be output              |

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.3.5.17 void dac\_lld\_start\_conversion ( DACDriver \* dacp )

Starts a DAC conversion.

Starts an asynchronous conversion operation.

#### Note

In DAC\_DHRM\_8BIT\_RIGHT mode the parameters passed to the callback are wrong because two samples are packed in a single dacsample\_t element. This will not be corrected, do not rely on those parameters. In DAC\_DHRM\_8BIT\_RIGHT\_DUAL mode two samples are treated as a single 16 bits sample and packed into a single dacsample\_t element. The num\_channels must be set to one in the group conversion configuration structure.

#### **Parameters**

| in | dacp | pointer to the DACDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### **Function Class:**

Not an API, this function is for internal use only.

7.3.5.18 void dac\_lld\_stop\_conversion ( DACDriver \* dacp )

Stops an ongoing conversion.

This function stops the currently ongoing conversion and returns the driver in the DAC\_READY state. If there was no conversion being processed then the function does nothing.

### **Parameters**

| in | dacp | pointer to the DACDriver object |
|----|------|---------------------------------|

# **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

#### 7.3.6 Variable Documentation

## 7.3.6.1 DACDriver DACD1

DAC1 driver identifier.

7.4 EXT Driver 63

## 7.4 EXT Driver

Generic EXT Driver.

# 7.4.1 Detailed Description

Generic EXT Driver.

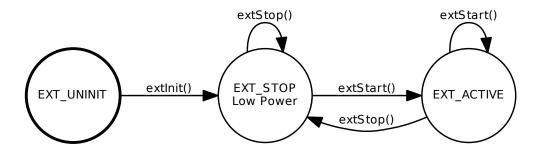
This module implements a generic EXT (EXTernal) driver.

#### Precondition

In order to use the EXT driver the HAL\_USE\_EXT option must be enabled in halconf.h.

#### 7.4.2 Driver State Machine

The driver implements a state machine internally, not all the driver functionalities can be used in any moment, any transition not explicitly shown in the following diagram has to be considered an error and shall be captured by an assertion (if enabled).



## 7.4.3 EXT Operations.

This driver abstracts generic external interrupt sources, a callback is invoked when a programmable transition is detected on one of the configured channels. Several channel modes are possible.

- EXT\_CH\_MODE\_DISABLED, channel not used.
- EXT\_CH\_MODE\_RISING\_EDGE, callback on a rising edge.
- EXT\_CH\_MODE\_FALLING\_EDGE, callback on a falling edge.
- EXT\_CH\_MODE\_BOTH\_EDGES, callback on a both edges.

### **Macros**

• #define EXT\_MAX\_CHANNELS 20

Available number of EXT channels.

#### **EXT** channel modes

• #define EXT\_CH\_MODE\_EDGES\_MASK 3U

Mask of edges field.

• #define EXT\_CH\_MODE\_DISABLED 0U

Channel disabled.

• #define EXT\_CH\_MODE\_RISING\_EDGE 1U

Rising edge callback.

• #define EXT CH MODE FALLING EDGE 2U

Falling edge callback.

• #define EXT\_CH\_MODE\_BOTH\_EDGES 3U

Both edges callback.

• #define EXT\_CH\_MODE\_AUTOSTART 4U

Channel started automatically on driver start.

## **Macro Functions**

• #define extChannelEnableI(extp, channel) ext\_lld\_channel\_enable(extp, channel)

Enables an EXT channel.

#define extChannelDisableI(extp, channel) ext\_lld\_channel\_disable(extp, channel)

Disables an EXT channel.

• #define extSetChannelMode(extp, channel, extcp)

Changes the operation mode of a channel.

## **PLATFORM configuration options**

• #define PLATFORM\_EXT\_USE\_EXT1 FALSE

EXT driver enable switch.

## **Typedefs**

typedef struct EXTDriver EXTDriver

Type of a structure representing a EXT driver.

• typedef uint32\_t expchannel\_t

EXT channel identifier.

• typedef void(\* extcallback\_t) (EXTDriver \*extp, expchannel\_t channel)

Type of an EXT generic notification callback.

#### **Data Structures**

· struct EXTChannelConfig

Channel configuration structure.

struct EXTConfig

Driver configuration structure.

struct EXTDriver

Structure representing an EXT driver.

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#### **Functions**

· void extInit (void)

EXT Driver initialization.

void extObjectInit (EXTDriver \*extp)

Initializes the standard part of a EXTDriver structure.

void extStart (EXTDriver \*extp, const EXTConfig \*config)

Configures and activates the EXT peripheral.

void extStop (EXTDriver \*extp)

Deactivates the EXT peripheral.

void extChannelEnable (EXTDriver \*extp, expchannel\_t channel)

Enables an EXT channel.

void extChannelDisable (EXTDriver \*extp, expchannel\_t channel)

Disables an EXT channel.

void extSetChannelModel (EXTDriver \*extp, expchannel t channel, const EXTChannelConfig \*extcp)

Changes the operation mode of a channel.

void ext\_lld\_init (void)

Low level EXT driver initialization.

void ext\_lld\_start (EXTDriver \*extp)

Configures and activates the EXT peripheral.

void ext Ild stop (EXTDriver \*extp)

Deactivates the EXT peripheral.

void ext\_lld\_channel\_enable (EXTDriver \*extp, expchannel\_t channel)

Enables an EXT channel.

• void ext\_lld\_channel\_disable (EXTDriver \*extp, expchannel\_t channel)

Disables an EXT channel.

## **Enumerations**

#### **Variables**

• EXTDriver EXTD1

EXT1 driver identifier.

#### 7.4.4 Macro Definition Documentation

7.4.4.1 #define EXT\_CH\_MODE\_EDGES\_MASK 3U

Mask of edges field.

7.4.4.2 #define EXT\_CH\_MODE\_DISABLED 0U

Channel disabled.

7.4.4.3 #define EXT\_CH\_MODE\_RISING\_EDGE 1U

Rising edge callback.

7.4.4.4 #define EXT\_CH\_MODE\_FALLING\_EDGE 2U

Falling edge callback.

7.4.4.5 #define EXT\_CH\_MODE\_BOTH\_EDGES 3U

Both edges callback.

7.4.4.6 #define EXT\_CH\_MODE\_AUTOSTART 4U

Channel started automatically on driver start.

7.4.4.7 #define extChannelEnablel( extp, channel ) ext IId channel enable(extp, channel)

Enables an EXT channel.

#### **Parameters**

| in | extp    | pointer to the EXTDriver object |
|----|---------|---------------------------------|
| in | channel | channel to be enabled           |

#### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.4.4.8 #define extChannelDisablel( extp, channel ) ext\_lld\_channel\_disable(extp, channel)

Disables an EXT channel.

#### **Parameters**

| in | extp    | pointer to the EXTDriver object |
|----|---------|---------------------------------|
| in | channel | channel to be disabled          |

# **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.4.4.9 #define extSetChannelMode( extp, channel, extcp )

### Value:

```
{
  osalSysLock();
  extSetChannelModeI(extp, channel, extcp);
  osalSysUnlock();
}
```

Changes the operation mode of a channel.

## Note

This function attempts to write over the current configuration structure that must have been not declared constant. This violates the const qualifier in extStart() but it is intentional. This function cannot be used if the configuration structure is declared const.

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#### **Parameters**

| in | extp    | pointer to the EXTDriver object   |
|----|---------|-----------------------------------|
| in | channel | channel to be changed             |
| in | extcp   | new configuration for the channel |

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.4.4.10 #define EXT\_MAX\_CHANNELS 20

Available number of EXT channels.

7.4.4.11 #define PLATFORM\_EXT\_USE\_EXT1 FALSE

EXT driver enable switch.

If set to TRUE the support for EXT1 is included.

Note

The default is FALSE.

# 7.4.5 Typedef Documentation

# 7.4.5.1 typedef struct EXTDriver EXTDriver

Type of a structure representing a EXT driver.

7.4.5.2 typedef uint32\_t expchannel\_t

EXT channel identifier.

7.4.5.3 typedef void(\* extcallback\_t) (EXTDriver \*extp, expchannel\_t channel)

Type of an EXT generic notification callback.

#### **Parameters**

| in | extp | pointer to the EXPDriver object triggering the callback |
|----|------|---|
|----|------|---|

# 7.4.6 Enumeration Type Documentation

7.4.6.1 enum extstate\_t

Driver state machine possible states.

Enumerator

EXT\_UNINIT Not initialized.

**EXT\_STOP** Stopped. **EXT\_ACTIVE** Active.

## 7.4.7 Function Documentation

7.4.7.1 void extlnit (void)

EXT Driver initialization.

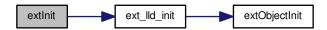
Note

This function is implicitly invoked by halInit(), there is no need to explicitly initialize the driver.

#### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



## 7.4.7.2 void extObjectInit ( EXTDriver \* extp )

Initializes the standard part of a EXTDriver structure.

## **Parameters**

| out | extp | pointer to the EXTDriver object |
|-----|------|---------------------------------|
|-----|------|---------------------------------|

# **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

7.4.7.3 void extStart ( EXTDriver \* extp, const EXTConfig \* config )

Configures and activates the EXT peripheral.

### Postcondition

After activation all EXT channels are in the disabled state, use extChannelEnable () in order to activate them.

#### **Parameters**

| in | extp   | pointer to the EXTDriver object |
|----|--------|---------------------------------|
| in | config | pointer to the EXTConfig object |

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## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.4.7.4 void extStop ( EXTDriver \* extp )

Deactivates the EXT peripheral.

## **Parameters**

|  | in | extp | pointer to the EXTDriver object |  |
|--|----|------|---------------------------------|--|
|--|----|------|---------------------------------|--|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.4.7.5 void extChannelEnable ( EXTDriver \* extp, expchannel\_t channel )

Enables an EXT channel.

# Precondition

The channel must not be in EXT\_CH\_MODE\_DISABLED mode.

### **Parameters**

| in | extp    | pointer to the EXTDriver object |
|----|---------|---------------------------------|
| in | channel | channel to be enabled           |

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.4.7.6 void extChannelDisable ( EXTDriver \* extp, expchannel\_t channel )

Disables an EXT channel.

#### Precondition

The channel must not be in EXT\_CH\_MODE\_DISABLED mode.

#### **Parameters**

| in | extp    | pointer to the EXTDriver object |
|----|---------|---------------------------------|
| in | channel | channel to be disabled          |

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.4.7.7 void extSetChannelModel ( EXTDriver \* extp, expchannel\_t channel, const EXTChannelConfig \* extcp )

Changes the operation mode of a channel.

### Note

This function attempts to write over the current configuration structure that must have been not declared constant. This violates the const qualifier in extStart () but it is intentional.

This function cannot be used if the configuration structure is declared const.

The effect of this function on constant configuration structures is not defined.

### **Parameters**

| in | extp    | pointer to the EXTDriver object   |
|----|---------|-----------------------------------|
| in | channel | channel to be changed             |
| in | extcp   | new configuration for the channel |

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



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# 7.4.7.8 void ext\_lld\_init (void)

Low level EXT driver initialization.

## **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



# 7.4.7.9 void ext\_lld\_start ( EXTDriver \* extp )

Configures and activates the EXT peripheral.

#### **Parameters**

| in | extp | pointer to the EXTDriver object | 1 |
|----|------|---------------------------------|---|
|----|------|---------------------------------|---|

# **Function Class:**

Not an API, this function is for internal use only.

# 7.4.7.10 void ext\_lld\_stop ( EXTDriver \* extp )

Deactivates the EXT peripheral.

### **Parameters**

| in | extp | pointer to the EXTDriver object |
|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

# 7.4.7.11 void ext\_lld\_channel\_enable ( EXTDriver \* extp, expchannel\_t channel )

Enables an EXT channel.

## **Parameters**

| in | extp    | pointer to the EXTDriver object |
|----|---------|---------------------------------|
| in | channel | channel to be enabled           |

# **Function Class:**

Not an API, this function is for internal use only.

7.4.7.12 void ext\_lld\_channel\_disable ( EXTDriver \* extp, expchannel\_t channel )

Disables an EXT channel.

# **Parameters**

| in | extp    | pointer to the EXTDriver object |
|----|---------|---------------------------------|
| in | channel | channel to be disabled          |

## **Function Class:**

Not an API, this function is for internal use only.

# 7.4.8 Variable Documentation

# 7.4.8.1 EXTDriver EXTD1

EXT1 driver identifier.

7.5 GPT Driver 73

## 7.5 GPT Driver

Generic GPT Driver.

## 7.5.1 Detailed Description

#### Generic GPT Driver.

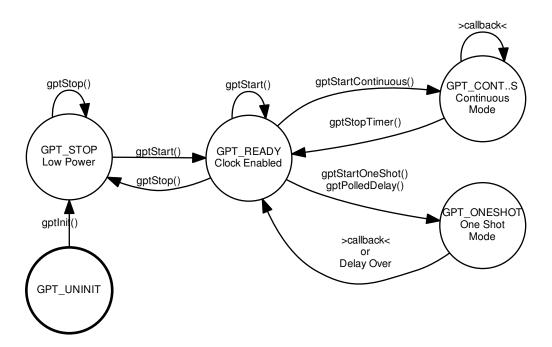
This module implements a generic GPT (General Purpose Timer) driver. The timer can be programmed in order to trigger callbacks after a specified time period or continuously with a specified interval.

#### Precondition

In order to use the GPT driver the HAL\_USE\_GPT option must be enabled in halconf.h.

#### 7.5.2 Driver State Machine

The driver implements a state machine internally, not all the driver functionalities can be used in any moment, any transition not explicitly shown in the following diagram has to be considered an error and shall be captured by an assertion (if enabled).



# 7.5.3 GPT Operations.

This driver abstracts a generic timer composed of:

- · A clock prescaler.
- A main up counter.

• A comparator register that resets the main counter to zero when the limit is reached. A callback is invoked when this happens.

The timer can operate in three different modes:

- · Continuous Mode, a periodic callback is invoked until the driver is explicitly stopped.
- One Shot Mode, a callback is invoked after the programmed period and then the timer automatically stops.
- **Delay Mode**, the timer is used for inserting a brief delay into the execution flow, no callback is invoked in this mode.

## **Macros**

• #define gptChangeIntervall(gptp, interval)

Changes the interval of GPT peripheral.

• #define gptGetIntervalX(gptp) gpt\_lld\_get\_interval(gptp)

Returns the interval of GPT peripheral.

#define gptGetCounterX(gptp) gpt\_lld\_get\_counter(gptp)

Returns the counter value of GPT peripheral.

• #define gpt\_lld\_change\_interval(gptp, interval)

Changes the interval of GPT peripheral.

## **PLATFORM configuration options**

• #define PLATFORM\_GPT\_USE\_GPT1 FALSE

GPTD1 driver enable switch.

## **Typedefs**

· typedef struct GPTDriver GPTDriver

Type of a structure representing a GPT driver.

typedef void(\* gptcallback\_t) (GPTDriver \*gptp)

GPT notification callback type.

• typedef uint32\_t gptfreq\_t

GPT frequency type.

• typedef uint16\_t gptcnt\_t

GPT counter type.

### **Data Structures**

• struct GPTConfig

Driver configuration structure.

struct GPTDriver

Structure representing a GPT driver.

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#### **Functions**

void gptInit (void)

GPT Driver initialization.

void gptObjectInit (GPTDriver \*gptp)

Initializes the standard part of a GPTDriver structure.

void gptStart (GPTDriver \*gptp, const GPTConfig \*config)

Configures and activates the GPT peripheral.

void gptStop (GPTDriver \*gptp)

Deactivates the GPT peripheral.

void gptChangeInterval (GPTDriver \*gptp, gptcnt\_t interval)

Changes the interval of GPT peripheral.

void gptStartContinuous (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in continuous mode.

void gptStartContinuousI (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in continuous mode.

void gptStartOneShot (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in one shot mode.

void gptStartOneShotI (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in one shot mode.

void gptStopTimer (GPTDriver \*gptp)

Stops the timer.

void gptStopTimerI (GPTDriver \*gptp)

Stops the timer.

void gptPolledDelay (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in one shot mode and waits for completion.

void gpt\_lld\_init (void)

Low level GPT driver initialization.

void gpt\_lld\_start (GPTDriver \*gptp)

Configures and activates the GPT peripheral.

void gpt\_Ild\_stop (GPTDriver \*gptp)

Deactivates the GPT peripheral.

void gpt\_lld\_start\_timer (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in continuous mode.

void gpt\_lld\_stop\_timer (GPTDriver \*gptp)

Stops the timer.

void gpt\_lld\_polled\_delay (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in one shot mode and waits for completion.

## **Enumerations**

#### **Variables**

• GPTDriver GPTD1

GPTD1 driver identifier.

## 7.5.4 Macro Definition Documentation

## 7.5.4.1 #define gptChangeIntervall( gptp, interval )

## Value:

```
{
   gpt_lld_change_interval(gptp, interval);
}
```

Changes the interval of GPT peripheral.

This function changes the interval of a running GPT unit.

#### Precondition

The GPT unit must be running in continuous mode.

## Postcondition

The GPT unit interval is changed to the new value.

#### **Parameters**

|   | in | gptp     | pointer to a GPTDriver object |
|---|----|----------|-------------------------------|
| ĺ | in | interval | new cycle time in timer ticks |

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

## 7.5.4.2 #define gptGetIntervalX( gptp ) gpt\_lld\_get\_interval(gptp)

Returns the interval of GPT peripheral.

# Precondition

The GPT unit must be running in continuous mode.

### **Parameters**

| in | gptp | pointer to a GPTDriver object |
|----|------|-------------------------------|
|----|------|-------------------------------|

#### Returns

The current interval.

#### **Function Class:**

This is an X-Class API, this function can be invoked from any context.

## 7.5.4.3 #define gptGetCounterX( gptp ) gpt\_lld\_get\_counter(gptp)

Returns the counter value of GPT peripheral.

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#### Precondition

The GPT unit must be running in continuous mode.

#### Note

The nature of the counter is not defined, it may count upward or downward, it could be continuously running or not

#### **Parameters**

```
in gptp pointer to a GPTDriver object
```

#### Returns

The current counter value.

## **Function Class:**

This is an X-Class API, this function can be invoked from any context.

## 7.5.4.4 #define PLATFORM\_GPT\_USE\_GPT1 FALSE

GPTD1 driver enable switch.

If set to TRUE the support for GPTD1 is included.

Note

The default is FALSE.

# 7.5.4.5 #define gpt\_lld\_change\_interval( gptp, interval)

# Value:

```
{
  (void) gptp;
  (void) interval;
}
```

Changes the interval of GPT peripheral.

This function changes the interval of a running GPT unit.

## Precondition

```
The GPT unit must have been activated using gptStart(). The GPT unit must have been running in continuous mode using gptStartContinuous().
```

# Postcondition

The GPT unit interval is changed to the new value.

# Note

The function has effect at the next cycle start.

#### **Parameters**

| in | gptp     | pointer to a GPTDriver object |
|----|----------|-------------------------------|
| in | interval | new cycle time in timer ticks |

#### **Function Class:**

Not an API, this function is for internal use only.

# 7.5.5 Typedef Documentation

7.5.5.1 typedef struct GPTDriver GPTDriver

Type of a structure representing a GPT driver.

7.5.5.2 typedef void(\* gptcallback\_t) (GPTDriver \*gptp)

GPT notification callback type.

#### **Parameters**

|  | in | gptp | pointer to a GPTDriver object |
|--|----|------|-------------------------------|
|--|----|------|-------------------------------|

7.5.5.3 typedef uint32\_t gptfreq\_t

GPT frequency type.

7.5.5.4 typedef uint16\_t gptcnt\_t

GPT counter type.

# 7.5.6 Enumeration Type Documentation

7.5.6.1 enum gptstate\_t

Driver state machine possible states.

#### **Enumerator**

GPT\_UNINIT Not initialized.

GPT\_STOP Stopped.

GPT\_READY Ready.

GPT\_CONTINUOUS Active in continuous mode.

GPT\_ONESHOT Active in one shot mode.

## 7.5.7 Function Documentation

7.5.7.1 void gptlnit (void)

GPT Driver initialization.

7.5 GPT Driver

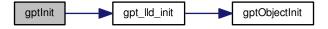
#### Note

This function is implicitly invoked by hallnit (), there is no need to explicitly initialize the driver.

#### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



## 7.5.7.2 void gptObjectInit ( GPTDriver \* gptp )

Initializes the standard part of a  ${\tt GPTDriver}$  structure.

#### **Parameters**

| ſ | out | gptp | pointer to the GPTDriver object |
|---|-----|------|---------------------------------|
|---|-----|------|---------------------------------|

## **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

# 7.5.7.3 void gptStart ( GPTDriver \* gptp, const GPTConfig \* config )

Configures and activates the GPT peripheral.

## **Parameters**

| in | gptp   | pointer to the GPTDriver object |
|----|--------|---------------------------------|
| in | config | pointer to the GPTConfig object |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.5.7.4 void gptStop ( GPTDriver \* gptp )

Deactivates the GPT peripheral.

#### **Parameters**

| in | gptp | pointer to the GPTDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.5.7.5 void gptChangeInterval ( GPTDriver \* gptp, gptcnt\_t interval )

Changes the interval of GPT peripheral.

This function changes the interval of a running GPT unit.

## Precondition

The GPT unit must be running in continuous mode.

### Postcondition

The GPT unit interval is changed to the new value.

#### **Parameters**

| in | gptp     | pointer to a GPTDriver object |
|----|----------|-------------------------------|
| in | interval | new cycle time in timer ticks |

7.5 GPT Driver

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

# 7.5.7.6 void gptStartContinuous ( GPTDriver \* gptp, gptcnt\_t interval )

Starts the timer in continuous mode.

#### **Parameters**

| in | gptp     | pointer to the GPTDriver object |
|----|----------|---------------------------------|
| in | interval | period in ticks                 |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.5.7.7 void gptStartContinuousI ( GPTDriver \* gptp, gptcnt\_t interval )

Starts the timer in continuous mode.

#### **Parameters**

| in | gptp     | pointer to the GPTDriver object |
|----|----------|---------------------------------|
| in | interval | period in ticks                 |

#### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



## 7.5.7.8 void gptStartOneShot ( GPTDriver \* gptp, gptcnt\_t interval )

Starts the timer in one shot mode.

#### **Parameters**

| in | gptp     | pointer to the GPTDriver object |
|----|----------|---------------------------------|
| in | interval | time interval in ticks          |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



## 7.5.7.9 void gptStartOneShotl ( GPTDriver \* gptp, gptcnt\_t interval )

Starts the timer in one shot mode.

### **Parameters**

| in | gptp     | pointer to the GPTDriver object |
|----|----------|---------------------------------|
| in | interval | time interval in ticks          |

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.5.7.10 void gptStopTimer ( GPTDriver \* gptp )

Stops the timer.

7.5 GPT Driver

#### **Parameters**

| in | gptp | pointer to the GPTDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



## 7.5.7.11 void gptStopTimerl ( GPTDriver \* gptp )

Stops the timer.

#### **Parameters**

|  | in | gptp | pointer to the GPTDriver object |
|--|----|------|---------------------------------|
|--|----|------|---------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



### 7.5.7.12 void gptPolledDelay ( GPTDriver \* gptp, gptcnt\_t interval )

Starts the timer in one shot mode and waits for completion.

This function specifically polls the timer waiting for completion in order to not have extra delays caused by interrupt servicing, this function is only recommended for short delays.

Note

The configured callback is not invoked when using this function.

## **Parameters**

| in | gptp     | pointer to the GPTDriver object |
|----|----------|---------------------------------|
| in | interval | time interval in ticks          |

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.5.7.13 void gpt\_lld\_init ( void )

Low level GPT driver initialization.

#### **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.5.7.14 void gpt\_lld\_start ( GPTDriver \* gptp )

Configures and activates the GPT peripheral.

### **Parameters**

|  | in | gptp | pointer to the GPTDriver object |
|--|----|------|---------------------------------|
|--|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

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# 7.5.7.15 void gpt\_lld\_stop ( GPTDriver \* gptp )

Deactivates the GPT peripheral.

#### **Parameters**

| in | gptp | pointer to the GPTDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### **Function Class:**

Not an API, this function is for internal use only.

```
7.5.7.16 void gpt_lld_start_timer ( GPTDriver * gptp, gptcnt_t interval )
```

Starts the timer in continuous mode.

#### **Parameters**

| in | gptp     | pointer to the GPTDriver object |
|----|----------|---------------------------------|
| in | interval | period in ticks                 |

# **Function Class:**

Not an API, this function is for internal use only.

```
7.5.7.17 void gpt_lld_stop_timer ( GPTDriver * gptp )
```

Stops the timer.

## **Parameters**

| in | gptp | pointer to the GPTDriver object |
|----|------|---------------------------------|

### **Function Class:**

Not an API, this function is for internal use only.

```
7.5.7.18 void gpt_lld_polled_delay ( GPTDriver * gptp, gptcnt_t interval )
```

Starts the timer in one shot mode and waits for completion.

This function specifically polls the timer waiting for completion in order to not have extra delays caused by interrupt servicing, this function is only recommended for short delays.

### **Parameters**

| in | gptp     | pointer to the GPTDriver object |
|----|----------|---------------------------------|
| in | interval | time interval in ticks          |

# **Function Class:**

Not an API, this function is for internal use only.

# 7.5.8 Variable Documentation

# 7.5.8.1 **GPTDriver GPTD1**

GPTD1 driver identifier.

7.6 HAL Driver

## 7.6 HAL Driver

Hardware Abstraction Layer.

## 7.6.1 Detailed Description

Hardware Abstraction Layer.

The HAL (Hardware Abstraction Layer) driver performs the system initialization and includes the platform support code shared by the other drivers. This driver does contain any API function except for a general initialization function <code>halInit()</code> that must be invoked before any HAL service can be used, usually the HAL initialization should be performed immediately before the kernel initialization.

Some HAL driver implementations also offer a custom early clock setup function that can be invoked before the C runtime initialization in order to accelerate the startup time.

#### **Macros**

```
    #define _CHIBIOS_HAL_
    ChibiOS/HAL identification macro.
```

#define CH\_HAL\_STABLE 1

Stable release flag.

#### ChibiOS/HAL version identification

• #define HAL\_VERSION "4.0.9"

HAL version string.

• #define CH HAL MAJOR 4

HAL version major number.

• #define CH HAL MINOR 0

HAL version minor number.

• #define CH\_HAL\_PATCH 9

HAL version patch number.

# Return codes

- #define HAL\_SUCCESS false
- #define HAL\_FAILED true

### Platform identification macros

#define PLATFORM\_NAME "templates"

# **Functions**

void hallnit (void)

HAL initialization.

void hal Ild init (void)

Low level HAL driver initialization.

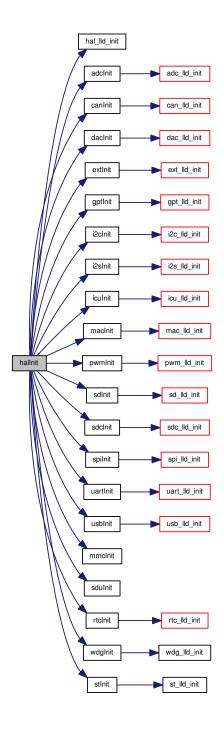
| 7.6.2   | Macro Definition Documentation   |
|---------|--|
| 7.6.2.1 | #define _CHIBIOS_HAL_  |
| ChibiO  | S/HAL identification macro.  |
|         |  |
| 7.6.2.2 | #define CH_HAL_STABLE 1  |
| Stable  | release flag.  |
|         |  |
| 7.6.2.3 | #define HAL_VERSION "4.0.9"  |
| HAL ve  | rsion string.  |
|         |  |
| 7.6.2.4 | #define CH_HAL_MAJOR 4   |
| HAL ve  | rsion major number.  |
|         |  |
|         | #define CH_HAL_MINOR 0   |
| HAL ve  | rsion minor number.  |
| 7606    | #define CIL HAL DATCH O  |
|         | #define CH_HAL_PATCH 9 rsion patch number.   |
| HAL VE  | rsion paten number.  |
| 7.6.3   | Function Documentation   |
| 7.6.3.1 | void hallnit ( void )  |
|         | tialization.   |
| This fu | nction invokes the low level initialization code then initializes all the drivers enabled in the HAL. Finally the specific initialization is performed by invoking boardInit() (usually defined in board.c). |
|         |  |

**Function Class:** 

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

7.6 HAL Driver

Here is the call graph for this function:



7.6.3.2 void hal\_lld\_init ( void )

Low level HAL driver initialization.

**Function Class:** 

Not an API, this function is for internal use only.

### 7.7 I/O Buffers Queues

### 7.7.1 Detailed Description

Buffers Queues are used when there is the need to exchange fixed-length data buffers between ISRs and threads. On the ISR side data can be exchanged only using buffers, on the thread side data can be exchanged both using buffers and/or using an emulation of regular byte queues. There are several kind of buffers queues:

- Input queue, unidirectional queue where the writer is the ISR side and the reader is the thread side.
- Output queue, unidirectional queue where the writer is the ISR side and the reader is the thread side.
- Full duplex queue, bidirectional queue. Full duplex queues are implemented by pairing an input queue and an output queue together.

#### **Macros**

#define BQ\_BUFFER\_SIZE(n, size) (((size\_t)(size) + sizeof (size\_t)) \* (size\_t)(n))
 Computes the size of a buffers queue buffer size.

#### **Macro Functions**

#define bqSizeX(bqp) ((bqp)->bn)

Returns the queue's number of buffers.

#define bqSpaceI(bqp) ((bqp)->bcounter)

Return the ready buffers number.

#define bqGetLinkX(bqp) ((bqp)->link)

Returns the queue application-defined link.

#define ibqlsEmptyl(ibqp) ((bool)(bqSpacel(ibqp) == 0U))

Evaluates to TRUE if the specified input buffers queue is empty.

• #define ibglsFullI(ibqp)

Evaluates to TRUE if the specified input buffers queue is full.

#define obqlsEmptyl(obqp)

Evaluates to true if the specified output buffers queue is empty.

#define obqlsFullI(obqp) ((bool)(bqSpaceI(obqp) == 0U))

Evaluates to true if the specified output buffers queue is full.

## **Typedefs**

typedef struct io\_buffers\_queue io\_buffers\_queue\_t

Type of a generic queue of buffers.

typedef void(\* bqnotify\_t) (io\_buffers\_queue\_t \*bqp)

Double buffer notification callback type.

• typedef io\_buffers\_queue\_t input\_buffers\_queue\_t

Type of an input buffers queue.

• typedef io\_buffers\_queue\_t output\_buffers\_queue\_t

Type of an output buffers queue.

#### **Data Structures**

· struct io buffers queue

Structure of a generic buffers queue.

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#### **Functions**

• void ibqObjectInit (input\_buffers\_queue\_t \*ibqp, uint8\_t \*bp, size\_t size, size\_t n, bqnotify\_t infy, void \*link)

Initializes an input buffers queue object.

void ibqResetl (input\_buffers\_queue\_t \*ibqp)

Resets an input buffers queue.

uint8\_t \* ibqGetEmptyBufferI (input\_buffers\_queue\_t \*ibqp)

Gets the next empty buffer from the queue.

void ibqPostFullBufferI (input\_buffers\_queue\_t \*ibqp, size\_t size)

Posts a new filled buffer to the queue.

msg\_t ibqGetFullBufferTimeout (input\_buffers\_queue\_t \*ibqp, systime\_t timeout)

Gets the next filled buffer from the queue.

msg\_t ibqGetFullBufferTimeoutS (input\_buffers queue\_t \*ibqp, systime\_t timeout)

Gets the next filled buffer from the queue.

void ibqReleaseEmptyBuffer (input\_buffers\_queue\_t \*ibqp)

Releases the buffer back in the queue.

void ibqReleaseEmptyBufferS (input\_buffers\_queue\_t \*ibqp)

Releases the buffer back in the queue.

• msg\_t ibqGetTimeout (input\_buffers\_queue\_t \*ibqp, systime\_t timeout)

Input queue read with timeout.

• size\_t ibqReadTimeout (input\_buffers\_queue\_t \*ibqp, uint8\_t \*bp, size\_t n, systime\_t timeout)

Input queue read with timeout.

 void obqObjectInit (output\_buffers\_queue\_t \*obqp, uint8\_t \*bp, size\_t size, size\_t n, bqnotify\_t onfy, void \*link)

Initializes an output buffers queue object.

void obqResetl (output\_buffers\_queue\_t \*obqp)

Resets an output buffers queue.

• uint8 t \* obgGetFullBufferI (output buffers queue t \*obgp, size t \*sizep)

Gets the next filled buffer from the queue.

void obqReleaseEmptyBufferI (output\_buffers\_queue\_t \*obqp)

Releases the next filled buffer back in the queue.

msq\_t obgGetEmptyBufferTimeout (output\_buffers queue\_t \*obgp, systime\_t timeout)

Gets the next empty buffer from the queue.

msg\_t obqGetEmptyBufferTimeoutS (output\_buffers\_queue\_t \*obqp, systime\_t timeout)

Gets the next empty buffer from the queue.

void obqPostFullBuffer (output\_buffers\_queue\_t \*obqp, size\_t size)

Posts a new filled buffer to the queue.

void obgPostFullBufferS (output buffers queue t \*obgp, size t size)

Posts a new filled buffer to the queue.

msg\_t obqPutTimeout (output\_buffers\_queue\_t \*obqp, uint8\_t b, systime\_t timeout)

Output queue write with timeout.

• size\_t obqWriteTimeout (output\_buffers\_queue\_t \*obqp, const uint8\_t \*bp, size\_t n, systime\_t timeout)

Output queue write with timeout.

bool obqTryFlushl (output\_buffers\_queue\_t \*obqp)

Flushes the current, partially filled, buffer to the queue.

void obqFlush (output\_buffers\_queue\_t \*obqp)

Flushes the current, partially filled, buffer to the queue.

### 7.7.2 Macro Definition Documentation

7.7.2.1 #define BQ\_BUFFER\_SIZE( n, size ) (((size\_t)(size) + sizeof (size\_t)) \* (size\_t)(n))

Computes the size of a buffers queue buffer size.

#### **Parameters**

| in | n    | number of buffers in the queue |
|----|------|--------------------------------|
| in | size | size of the buffers            |

## 7.7.2.2 #define bqSizeX( bqp ) ((bqp)->bn)

Returns the queue's number of buffers.

## **Parameters**

| in | bqp | pointer to an io_ | _buffers_ | _queue_ | _t structure |
|----|-----|-------------------|-----------|---------|--------------|
|----|-----|-------------------|-----------|---------|--------------|

## Returns

The number of buffers.

#### **Function Class:**

This is an X-Class API, this function can be invoked from any context.

## 7.7.2.3 #define bqSpacel( bqp ) ((bqp)->bcounter)

Return the ready buffers number.

Returns the number of filled buffers if used on an input queue or the number of empty buffers if used on an output queue.

#### **Parameters**

|  | in | bqp | pointer to an io_buffers_queue_t structu | е |
|--|----|-----|--|---|
|--|----|-----|--|---|

## Returns

The number of ready buffers.

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

# 7.7.2.4 #define bqGetLinkX( bqp ) ((bqp)->link)

Returns the queue application-defined link.

## **Parameters**

| in  | han | pointer to an io_ | huffere   | M10110  | + structure  |
|-----|-----|-------------------|-----------|---------|--------------|
| T11 | υчр | pointer to an ±0_ | _bullers_ | _queue_ | _c Siruciure |

## Returns

The application-defined link.

## **Function Class:**

Special function, this function has special requirements see the notes.

7.7.2.5 #define ibqlsEmptyl( ibqp ) ((bool)(bqSpacel(ibqp) == 0U))

Evaluates to TRUE if the specified input buffers queue is empty.

#### **Parameters**

| in | ibqp | pointer to an input_ | buffers_ | _queue_ | _t structure |
|----|------|----------------------|----------|---------|--------------|
|----|------|----------------------|----------|---------|--------------|

### Returns

The queue status.

#### Return values

| false | if the queue is not empty. |
|-------|----------------------------|
| true  | if the queue is empty.     |

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

# 7.7.2.6 #define ibqlsFulll( ibqp )

# Value:

```
/*lint -save -e9007 [13.5] No side effects, a pointer is passed.*/
  ((bool)(((ibqp)->bwrptr == (ibqp)->brdptr) && ((ibqp)->bcounter != 0U))) \
  /*lint -restore*/
```

Evaluates to  ${\tt TRUE}$  if the specified input buffers queue is full.

# **Parameters**

| in   ibqp   pointer to an input_buffers_queue_t structure |
|---|
|---|

### Returns

The queue status.

# Return values

| false | if the queue is not full. |
|-------|---------------------------|
| true  | if the queue is full.     |

# **Function Class:**

This is an I-Class API, this function can be invoked from within a system lock zone by both threads and interrupt

handlers.

### 7.7.2.7 #define obqlsEmptyl( obqp )

### Value:

```
/*lint -save -e9007 [13.5] No side effects, a pointer is passed.*/
  ((bool)(((obqp)->bwrptr == (obqp)->brdptr) && ((obqp)->bcounter != 0U))) \
  /*lint -restore*/
```

Evaluates to true if the specified output buffers queue is empty.

## **Parameters**

| in | obqp | <pre>pointer to an output_buffers_queue_t structure</pre> |
|----|------|---|
|----|------|---|

## Returns

The queue status.

#### Return values

| false | if the queue is not empty. |
|-------|----------------------------|
| true  | if the queue is empty.     |

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.7.2.8 #define obqlsFulll( obqp) ((bool)(bqSpacel(obqp) == 0U))

Evaluates to true if the specified output buffers queue is full.

#### **Parameters**

| i | n | obqp | <pre>pointer to an output_buffers_queue_t structure</pre> |
|---|---|------|---|
|---|---|------|---|

## Returns

The queue status.

### Return values

| false | if the queue is not full. |
|-------|---------------------------|
| true  | if the queue is full.     |

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

# 7.7.3 Typedef Documentation

7.7.3.1 typedef struct io\_buffers\_queue io\_buffers\_queue\_t

Type of a generic queue of buffers.

7.7.3.2 typedef void(\* bqnotify\_t) (io\_buffers\_queue\_t \*bqp)

Double buffer notification callback type.

#### **Parameters**

|  | in | iodbp | the buffers queue pointer |
|--|----|-------|---------------------------|
|--|----|-------|---------------------------|

7.7.3.3 typedef io\_buffers\_queue\_t input\_buffers\_queue\_t

Type of an input buffers queue.

7.7.3.4 typedef io\_buffers\_queue\_t output\_buffers\_queue\_t

Type of an output buffers queue.

### 7.7.4 Function Documentation

7.7.4.1 void ibqObjectInit ( input\_buffers\_queue\_t \* ibqp, uint8\_t \* bp, size\_t size, size\_t n, bqnotify\_t infy, void \* link )

Initializes an input buffers queue object.

# Parameters

| out | ibqp | <pre>pointer to the input_buffers_queue_t object</pre> |  |
|-----|------|--|--|
| in  | bp   | pointer to a memory area allocated for buffers         |  |
| in  | size | buffers size   |  |
| in  | n    | number of buffers                                      |  |
| in  | infy | callback called when a buffer is returned to the queue |  |
| in  | link | application defined pointer                            |  |

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

7.7.4.2 void ibqResetl ( input\_buffers\_queue\_t \*ibqp )

Resets an input buffers queue.

All the data in the input buffers queue is erased and lost, any waiting thread is resumed with status MSG\_RESET.

# Note

A reset operation can be used by a low level driver in order to obtain immediate attention from the high level layers.

### **Parameters**

| in | ibqp | <pre>pointer to the input_buffers_queue_t object</pre> | 1 |
|----|------|--|---|
|----|------|--|---|

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers

7.7.4.3 uint8\_t \* ibqGetEmptyBufferI ( input\_buffers\_queue\_t \* ibqp )

Gets the next empty buffer from the queue.

Note

The function always returns the same buffer if called repeatedly.

#### **Parameters**

|  | in | ibqp | <pre>pointer to the input_buffers_queue_t object</pre> |  |
|--|----|------|--|--|
|--|----|------|--|--|

### Returns

A pointer to the next buffer to be filled.

## Return values

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.7.4.4 void ibqPostFullBufferl ( input\_buffers\_queue\_t \* ibqp, size\_t size )

Posts a new filled buffer to the queue.

# **Parameters**

| in | ibqp | <pre>pointer to the input_buffers_queue_t object</pre> |  |
|----|------|--|--|
| in | size | used size of the buffer, cannot be zero                |  |

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.7.4.5 msg\_t ibqGetFullBufferTimeout ( input\_buffers\_queue\_t \* ibqp, systime\_t timeout )

Gets the next filled buffer from the queue.

## Note

The function always acquires the same buffer if called repeatedly.

## Postcondition

After calling the function the fields ptr and top are set at beginning and end of the buffer data or NULL if the queue is empty.

### **Parameters**

| in | ibqp    | pointer to the input_buffers_queue_t object  |
|----|---------|--|
| in | timeout | the number of ticks before the operation timeouts, the following special values are allowed: |
|    |         | TIME_IMMEDIATE immediate timeout.  |
|    |         | TIME_INFINITE no timeout.  |

### Returns

The operation status.

#### **Return values**

| MSG_OK      | if a buffer has been acquired. |
|-------------|--------------------------------|
| MSG_TIMEOUT | if the specified time expired. |
| MSG_RESET   | if the queue has been reset.   |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.7.4.6 msg\_t ibqGetFullBufferTimeoutS ( input\_buffers\_queue\_t \*ibqp, systime\_t timeout )

Gets the next filled buffer from the queue.

### Note

The function always acquires the same buffer if called repeatedly.

## Postcondition

After calling the function the fields ptr and top are set at beginning and end of the buffer data or NULL if the queue is empty.

### **Parameters**

| in | ibqp    | pointer to the input_buffers_queue_t object  |
|----|---------|--|
| in | timeout | the number of ticks before the operation timeouts, the following special values are allowed: |
|    |         | TIME_IMMEDIATE immediate timeout.  |
|    |         | TIME_INFINITE no timeout.  |
|    |         |  |

## Returns

The operation status.

### Return values

| MSG_OK      | if a buffer has been acquired. |
|-------------|--------------------------------|
| MSG_TIMEOUT | if the specified time expired. |
| MSG_RESET   | if the queue has been reset.   |

# **Function Class:**

This is an **S-Class** API, this function can be invoked from within a system lock zone by threads only.

7.7.4.7 void ibqReleaseEmptyBuffer ( input\_buffers\_queue\_t \* ibqp )

Releases the buffer back in the queue.

Note

The object callback is called after releasing the buffer.

# **Parameters**

| i | n | ibqp | <pre>pointer to the input_buffers_queue_t object</pre> |  |
|---|---|------|--|--|
|---|---|------|--|--|

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.7.4.8 void ibqReleaseEmptyBufferS ( input\_buffers\_queue\_t \*ibqp )

Releases the buffer back in the queue.

Note

The object callback is called after releasing the buffer.

### **Parameters**

|  | in | ibqp | <pre>pointer to the input_buffers_queue_t object</pre> |  |
|--|----|------|--|--|
|--|----|------|--|--|

### **Function Class:**

This is an S-Class API, this function can be invoked from within a system lock zone by threads only.

## 7.7.4.9 msg\_t ibqGetTimeout ( input\_buffers\_queue\_t \* ibqp, systime\_t timeout )

Input queue read with timeout.

This function reads a byte value from an input queue. If the queue is empty then the calling thread is suspended until a new buffer arrives in the queue or a timeout occurs.

#### **Parameters**

| in | ibqp    | <pre>pointer to the input_buffers_queue_t object</pre>                                       |  |
|----|---------|--|--|
| in | timeout | the number of ticks before the operation timeouts, the following special values are allowed: |  |
|    |         | TIME_IMMEDIATE immediate timeout.  |  |
|    |         | TIME_INFINITE no timeout.  |  |

# Returns

A byte value from the queue.

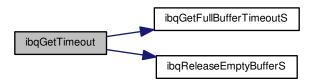
#### Return values

| MSG_TIMEOUT | if the specified time expired. |
|-------------|--------------------------------|
| MSG_RESET   | if the queue has been reset.   |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.7.4.10 size\_t ibqReadTimeout ( input\_buffers\_queue\_t \* ibqp, uint8\_t \* bp, size\_t n, systime\_t timeout )

Input queue read with timeout.

The function reads data from an input queue into a buffer. The operation completes when the specified amount of data has been transferred or after the specified timeout or if the queue has been reset.

#### **Parameters**

| in  | ibqp    | pointer to the input_buffers_queue_t object  |  |
|-----|---------|--|--|
| out | bp      | pointer to the data buffer   |  |
| in  | n       | the maximum amount of data to be transferred, the value 0 is reserved                        |  |
| in  | timeout | the number of ticks before the operation timeouts, the following special values are allowed: |  |
|     |         | <ul> <li>TIME_IMMEDIATE immediate timeout.</li> <li>TIME_INFINITE no timeout.</li> </ul>     |  |

## Returns

The number of bytes effectively transferred.

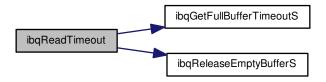
#### Return values

| Ω | if a timeout occurred. |
|---|------------------------|
|   |                        |

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.7.4.11 void obqObjectInit ( output\_buffers\_queue\_t \* obqp, uint8\_t \* bp, size\_t size, size\_t n, bqnotify\_t onfy, void \* link )

Initializes an output buffers queue object.

#### **Parameters**

| out | obqp | pointer to the output_buffers_queue_t object         |
|-----|------|--|
| in  | bp   | pointer to a memory area allocated for buffers       |
| in  | size | buffers size   |
| in  | n    | number of buffers                                    |
| in  | onfy | callback called when a buffer is posted in the queue |
| in  | link | application defined pointer                          |

## **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

7.7.4.12 void obqResetl ( output\_buffers\_queue\_t \* obqp )

Resets an output buffers queue.

All the data in the output buffers queue is erased and lost, any waiting thread is resumed with status MSG\_RESET.

Note

A reset operation can be used by a low level driver in order to obtain immediate attention from the high level layers.

### **Parameters**

|  | in | obqp | pointer to the output_buffers_queue_t object |
|--|----|------|--|
|--|----|------|--|

# **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.7.4.13 uint8\_t \* obqGetFullBufferl ( output\_buffers\_queue\_t \* obqp, size\_t \* sizep )

Gets the next filled buffer from the queue.

Note

The function always returns the same buffer if called repeatedly.

#### **Parameters**

| in  | obqp  | <pre>pointer to the output_buffers_queue_t object</pre> |
|-----|-------|---|
| out | sizep | pointer to the filled buffer size                       |

## Returns

A pointer to the filled buffer.

#### Return values

| NULL if the queue is empty. |  |
|-----------------------------|--|
|-----------------------------|--|

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.7.4.14 void obqReleaseEmptyBufferI ( output\_buffers\_queue\_t \* obqp )

Releases the next filled buffer back in the queue.

## **Parameters**

| in | obqp | <pre>pointer to the output_buffers_queue_t object</pre> |
|----|------|---|
|----|------|---|

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.7.4.15 msg\_t obqGetEmptyBufferTimeout ( output buffers queue t \* obqp, systime\_t timeout )

Gets the next empty buffer from the queue.

Note

The function always acquires the same buffer if called repeatedly.

#### Postcondition

After calling the function the fields ptr and top are set at beginning and end of the buffer data or NULL if the queue is empty.

### **Parameters**

| in | obqp    | pointer to the output_buffers_queue_t object   |
|----|---------|--|
| in | timeout | the number of ticks before the operation timeouts, the following special values are allowed: |
|    |         | TIME_IMMEDIATE immediate timeout.  |
|    |         | • TIME_INFINITE no timeout.  |
|    |         |  |

## Returns

The operation status.

### Return values

| MSG_OK      | if a buffer has been acquired. |
|-------------|--------------------------------|
| MSG_TIMEOUT | if the specified time expired. |
| MSG_RESET   | if the queue has been reset.   |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.7.4.16 msg\_t obqGetEmptyBufferTimeoutS ( output\_buffers\_queue\_t \* obqp, systime\_t timeout )

Gets the next empty buffer from the queue.

## Note

The function always acquires the same buffer if called repeatedly.

# Postcondition

After calling the function the fields ptr and top are set at beginning and end of the buffer data or NULL if the queue is empty.

#### **Parameters**

| in | obqp    | pointer to the output_buffers_queue_t object   |       |
|----|---------|--|-------|
| in | timeout | the number of ticks before the operation timeouts, the following special values are allowed: |       |
|    |         | TIME_IMMEDIATE immediate timeout.  |       |
|    |         | TIME_INFINITE no timeout.     Chibios  | S/HAL |

### Returns

The operation status.

### **Return values**

| MSG_OK      | if a buffer has been acquired. |
|-------------|--------------------------------|
| MSG_TIMEOUT | if the specified time expired. |
| MSG_RESET   | if the queue has been reset.   |

### **Function Class:**

This is an **S-Class** API, this function can be invoked from within a system lock zone by threads only.

7.7.4.17 void obqPostFullBuffer ( output\_buffers\_queue\_t \* obqp, size\_t size )

Posts a new filled buffer to the queue.

Note

The object callback is called after releasing the buffer.

### **Parameters**

| in | obqp | <pre>pointer to the output_buffers_queue_t object</pre> |
|----|------|---|
| in | size | used size of the buffer, cannot be zero                 |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.7.4.18 void obqPostFullBufferS ( output\_buffers\_queue\_t \* obqp, size\_t size )

Posts a new filled buffer to the queue.

Note

The object callback is called after releasing the buffer.

#### **Parameters**

| iı | obqp | pointer to the output_buffers_queue_t object |
|----|------|--|
| iı | size | used size of the buffer, cannot be zero      |

### **Function Class:**

This is an S-Class API, this function can be invoked from within a system lock zone by threads only.

7.7.4.19 msg\_t obqPutTimeout ( output\_buffers\_queue\_t \* obqp, uint8\_t b, systime\_t timeout )

Output queue write with timeout.

This function writes a byte value to an output queue. If the queue is full then the calling thread is suspended until a new buffer is freed in the queue or a timeout occurs.

#### **Parameters**

| in | obqp    | pointer to the output_buffers_queue_t object   |
|----|---------|--|
| in | b       | byte value to be transferred   |
| in | timeout | the number of ticks before the operation timeouts, the following special values are allowed: |
|    |         | TIME_IMMEDIATE immediate timeout.  |
|    |         | TIME_INFINITE no timeout.  |
|    |         |  |

# Returns

A byte value from the queue.

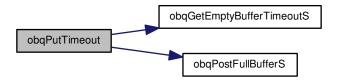
### **Return values**

| MSG_TIMEOUT | if the specified time expired. |
|-------------|--------------------------------|
| MSG_RESET   | if the queue has been reset.   |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.7.4.20 size\_t obqWriteTimeout ( output\_buffers\_queue\_t \* obqp, const uint8\_t \* bp, size\_t n, systime\_t timeout )

Output queue write with timeout.

The function writes data from a buffer to an output queue. The operation completes when the specified amount of data has been transferred or after the specified timeout or if the queue has been reset.

#### **Parameters**

| in | obqp    | pointer to the output_buffers_queue_t object   |
|----|---------|--|
| in | bp      | pointer to the data buffer   |
| in | n       | the maximum amount of data to be transferred, the value 0 is reserved                        |
| in | timeout | the number of ticks before the operation timeouts, the following special values are allowed: |
|    |         | <ul> <li>TIME_IMMEDIATE immediate timeout.</li> <li>TIME_INFINITE no timeout.</li> </ul>     |

### Returns

The number of bytes effectively transferred.

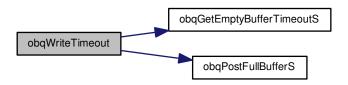
#### Return values

| 0 if a timeout occur | red. |
|----------------------|------|
|----------------------|------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.7.4.21 bool obqTryFlushI ( output\_buffers\_queue\_t \* obqp )

Flushes the current, partially filled, buffer to the queue.

# Note

The notification callback is not invoked because the function is meant to be called from ISR context. An operation status is returned instead.

### **Parameters**

| in | obqp | <pre>pointer to the output_buffers_queue_t object</pre> |
|----|------|---|
|----|------|---|

### Returns

The operation status.

## Return values

| false | if no new filled buffer has been posted to the queue. |
|-------|---|
| true  | if a new filled buffer has been posted to the queue.  |

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.7.4.22 void obqFlush (  $output\_buffers\_queue\_t * obqp$  )

Flushes the current, partially filled, buffer to the queue.

### **Parameters**

| in | obqp | <pre>pointer to the output_buffers_queue_t object</pre> |
|----|------|---|
|----|------|---|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



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### 7.8 Abstract I/O Channel

# 7.8.1 Detailed Description

This module defines an abstract interface for I/O channels by extending the BaseSequentialStream interface. Note that no code is present, I/O channels are just abstract interface like structures, you should look at the systems as to a set of abstract C++ classes (even if written in C). Specific device drivers can use/extend the interface and implement them.

This system has the advantage to make the access to channels independent from the implementation logic.

#### **Macros**

· #define base channel methods

BaseChannel specific methods.

• #define \_base\_channel\_data \_base\_sequential\_stream\_data

BaseChannel specific data.

• #define \_base\_asynchronous\_channel\_methods \_base\_channel\_methods \

BaseAsynchronousChannel specific methods.

• #define \_base\_asynchronous\_channel\_data

BaseAsynchronousChannel specific data.

### Macro Functions (BaseChannel)

• #define chnPutTimeout(ip, b, time) ((ip)->vmt->putt(ip, b, time))

Channel blocking byte write with timeout.

• #define chnGetTimeout(ip, time) ((ip)->vmt->gett(ip, time))

Channel blocking byte read with timeout.

• #define chnWrite(ip, bp, n) streamWrite(ip, bp, n)

Channel blocking write.

• #define chnWriteTimeout(ip, bp, n, time) ((ip)->vmt->writet(ip, bp, n, time))

Channel blocking write with timeout.

• #define chnRead(ip, bp, n) streamRead(ip, bp, n)

Channel blocking read.

#define chnReadTimeout(ip, bp, n, time) ((ip)->vmt->readt(ip, bp, n, time))

Channel blocking read with timeout.

# I/O status flags added to the event listener

#define CHN\_NO\_ERROR (eventflags\_t)0

No pending conditions.

• #define CHN\_CONNECTED (eventflags\_t)1

Connection happened.

• #define CHN DISCONNECTED (eventflags t)2

Disconnection happened.

• #define CHN\_INPUT\_AVAILABLE (eventflags\_t)4

Data available in the input queue.

• #define CHN\_OUTPUT\_EMPTY (eventflags\_t)8

Output queue empty.

• #define CHN\_TRANSMISSION\_END (eventflags\_t)16

Transmission end.

# Macro Functions (BaseAsynchronousChannel)

#define chnGetEventSource(ip) (&((ip)->event))

Returns the I/O condition event source.

#define chnAddFlagsI(ip, flags)

Adds status flags to the listeners's flags mask.

### **Data Structures**

struct BaseChannelVMT

BaseChannel virtual methods table.

struct BaseChannel

Base channel class.

struct BaseAsynchronousChannelVMT

BaseAsynchronousChannel virtual methods table.

· struct BaseAsynchronousChannel

Base asynchronous channel class.

### 7.8.2 Macro Definition Documentation

## 7.8.2.1 #define \_base\_channel\_methods

#### Value:

```
_base_sequential_stream_methods

/* Channel put method with timeout specification.*/

msg_t (*put) (void *instance, uint8_t b, systime_t time);

/* Channel get method with timeout specification.*/

msg_t (*gett) (void *instance, systime_t time);

/* Channel write method with timeout specification.*/

size_t (*writet) (void *instance, const uint8_t *bp,

size_t n, systime_t time);

/* Channel read method with timeout specification.*/

size_t (*readt) (void *instance, uint8_t *bp, size_t n, systime_t time);
```

BaseChannel specific methods.

7.8.2.2 #define \_base\_channel\_data \_base\_sequential\_stream\_data

BaseChannel specific data.

Note

It is empty because BaseChannel is only an interface without implementation.

7.8.2.3 #define chnPutTimeout( ip, b, time ) ((ip)->vmt->putt(ip, b, time))

Channel blocking byte write with timeout.

This function writes a byte value to a channel. If the channel is not ready to accept data then the calling thread is suspended.

#### **Parameters**

| in | ip | pointer to a BaseChannel or derived class   |  |
|----|----|---|--|
| in | b  | the byte value to be written to the channel |  |

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### **Parameters**

| in | time | the number of ticks before the operation timeouts, the following special values are allowed: |
|----|------|--|
|    |      | TIME_IMMEDIATE immediate timeout.  |
|    |      | TIME_INFINITE no timeout.  |

#### Returns

The operation status.

#### Return values

| STM_OK      | if the operation succeeded.                         |
|-------------|---|
| STM_TIMEOUT | if the specified time expired.                      |
| STM_RESET   | if the channel associated queue (if any) was reset. |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

# 7.8.2.4 #define chnGetTimeout( ip, time ) ((ip)->vmt->gett(ip, time))

Channel blocking byte read with timeout.

This function reads a byte value from a channel. If the data is not available then the calling thread is suspended.

# **Parameters**

| in time the number of ticks before the operation timeouts, the following special values a | are allowed: |
|---|--------------|
|   |              |
| TIME_IMMEDIATE immediate timeout.   |              |
| • TIME_INFINITE no timeout.   |              |

# Returns

A byte value from the queue.

## Return values

| STM_TIMEOUT | if the specified time expired.                           |
|-------------|--|
| STM_RESET   | if the channel associated queue (if any) has been reset. |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.8.2.5 #define chnWrite( ip, bp, n ) streamWrite(ip, bp, n)

Channel blocking write.

The function writes data from a buffer to a channel. If the channel is not ready to accept data then the calling thread is suspended.

#### **Parameters**

| in  | ip | pointer to a BaseChannel or derived class    |
|-----|----|--|
| out | bp | pointer to the data buffer                   |
| in  | n  | the maximum amount of data to be transferred |

### Returns

The number of bytes transferred.

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.8.2.6 #define chnWriteTimeout( ip, bp, n, time ) ((ip)->vmt->writet(ip, bp, n, time))

Channel blocking write with timeout.

The function writes data from a buffer to a channel. If the channel is not ready to accept data then the calling thread is suspended.

## **Parameters**

| in  | ip   | pointer to a BaseChannel or derived class  |
|-----|------|--|
| out | bp   | pointer to the data buffer   |
| in  | n    | the maximum amount of data to be transferred   |
| in  | time | the number of ticks before the operation timeouts, the following special values are allowed: |
|     |      | <ul> <li>TIME_IMMEDIATE immediate timeout.</li> <li>TIME_INFINITE no timeout.</li> </ul>     |
|     |      | THINE_IT WITH THE UNICOUN.   |

# Returns

The number of bytes transferred.

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.8.2.7 #define chnRead( ip, bp, n ) streamRead(ip, bp, n)

Channel blocking read.

The function reads data from a channel into a buffer. If the data is not available then the calling thread is suspended.

#### **Parameters**

| in | ip | pointer to a BaseChannel or derived class |
|----|----|---|
|----|----|---|

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#### **Parameters**

| in | bp | pointer to the data buffer                   |
|----|----|--|
| in | n  | the maximum amount of data to be transferred |

#### Returns

The number of bytes transferred.

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.8.2.8 #define chnReadTimeout( ip, bp, n, time ) ((ip)->vmt->readt(ip, bp, n, time))

Channel blocking read with timeout.

The function reads data from a channel into a buffer. If the data is not available then the calling thread is suspended.

### **Parameters**

| in | ip   | pointer to a BaseChannel or derived class  |
|----|------|--|
| in | bp   | pointer to the data buffer   |
| in | n    | the maximum amount of data to be transferred   |
| in | time | the number of ticks before the operation timeouts, the following special values are allowed: |
|    |      | <ul> <li>TIME_IMMEDIATE immediate timeout.</li> <li>TIME_INFINITE no timeout.</li> </ul>     |

# Returns

The number of bytes transferred.

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.8.2.9 #define CHN\_NO\_ERROR (eventflags\_t)0

No pending conditions.

7.8.2.10 #define CHN\_CONNECTED (eventflags\_t)1

Connection happened.

7.8.2.11 #define CHN\_DISCONNECTED (eventflags\_t)2

Disconnection happened.

7.8.2.12 #define CHN\_INPUT\_AVAILABLE (eventflags\_t)4

Data available in the input queue.

7.8.2.13 #define CHN\_OUTPUT\_EMPTY (eventflags\_t)8

Output queue empty.

7.8.2.14 #define CHN\_TRANSMISSION\_END (eventflags\_t)16

Transmission end.

7.8.2.15 #define \_base\_asynchronous\_channel\_methods base\_channel\_methods \

BaseAsynchronousChannel specific methods.

7.8.2.16 #define \_base\_asynchronous\_channel\_data

#### Value:

BaseAsynchronousChannel specific data.

7.8.2.17 #define chnGetEventSource( ip ) (&((ip)->event))

Returns the I/O condition event source.

The event source is broadcasted when an I/O condition happens.

# Parameters

| in | ip | pointer to a BaseAsynchronousChannel or derived class |
|----|----|---|

### Returns

A pointer to an EventSource object.

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.8.2.18 #define chnAddFlagsl( ip, flags )

## Value:

```
{
  osalEventBroadcastFlagsI(&(ip)->event, flags);
  \
}
```

Adds status flags to the listeners's flags mask.

This function is usually called from the I/O ISRs in order to notify I/O conditions such as data events, errors, signal changes etc.

## Parameters

| in | ip   | pointer to a BaseAsynchronousChannel or derived class  |
|----|------|--|
|    | £1   |  |
| ın | nags | condition flags to be added to the listener flags mask |

7.8 Abstract I/O Channel



This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

## 7.9 Abstract Files

# 7.9.1 Detailed Description

This module define an abstract interface for generic data files by extending the <code>BaseSequentialStream</code> interface. Note that no code is present, data files are just abstract interface-like structures, you should look at the systems as to a set of abstract C++ classes (even if written in C). This system has the advantage to make the access to streams independent from the implementation logic.

The data files interface can be used as base class for high level object types such as an API for a File System implementation.

#### **Macros**

• #define file stream methods

FileStream specific methods.

• #define \_file\_stream\_data \_base\_sequential\_stream\_data

FileStream specific data.

### Files return codes

• #define FILE\_OK STM\_OK

No error return code.

• #define FILE ERROR STM TIMEOUT

Error code from the file stream methods.

• #define FILE\_EOF STM\_RESET

End-of-file condition for file get/put methods.

# **Macro Functions (FileStream)**

• #define fileStreamWrite(ip, bp, n) streamWrite(ip, bp, n)

File stream write.

• #define fileStreamRead(ip, bp, n) streamRead(ip, bp, n)

File stream read.

#define fileStreamPut(ip, b) streamPut(ip, b)

File stream blocking byte write.

#define fileStreamGet(ip) streamGet(ip)

File stream blocking byte read.

• #define fileStreamClose(ip) ((ip)->vmt->close(ip))

File Stream close.

#define fileStreamGetError(ip) ((ip)->vmt->geterror(ip))

Returns an implementation dependent error code.

#define fileStreamGetSize(ip) ((ip)->vmt->getsize(ip))

Returns the current file size.

#define fileStreamGetPosition(ip) ((ip)->vmt->getposition(ip))

Returns the current file pointer position.

• #define fileStreamSeek(ip, offset) ((ip)->vmt->lseek(ip, offset))

Moves the file current pointer to an absolute position.

### **Typedefs**

· typedef uint32 t fileoffset t

File offset type.

7.9 Abstract Files

### **Data Structures**

struct FileStreamVMT

FileStream virtual methods table.

struct FileStream

Base file stream class.

### 7.9.2 Macro Definition Documentation

7.9.2.1 #define FILE\_OK STM\_OK

No error return code.

7.9.2.2 #define FILE\_ERROR STM\_TIMEOUT

Error code from the file stream methods.

7.9.2.3 #define FILE\_EOF STM\_RESET

End-of-file condition for file get/put methods.

7.9.2.4 #define \_file\_stream\_methods

### Value:

```
_base_sequential_stream_methods

/* File close method.*/

msg_t (*close)(void *instance);

/* Get last error code method.*/

msg_t (*geterror)(void *instance);

/* File get size method.*/

msg_t (*getsize)(void *instance);

/* File get current position method.*/

msg_t (*getposition)(void *instance);

/* File seek method.*/

msg_t (*lseek)(void *instance);

/* File seek method.*/
```

FileStream specific methods.

7.9.2.5 #define\_file\_stream\_data\_base\_sequential\_stream\_data

FileStream specific data.

Note

It is empty because FileStream is only an interface without implementation.

7.9.2.6 #define fileStreamWrite( ip, bp, n ) streamWrite(ip, bp, n)

File stream write.

The function writes data from a buffer to a file stream.

## **Parameters**

| in | ip | pointer to a FileStream or derived class     |
|----|----|--|
| in | bp | pointer to the data buffer                   |
| in | n  | the maximum amount of data to be transferred |

ChibiOS/HAL

#### Returns

The number of bytes transferred. The return value can be less than the specified number of bytes if an end-of-file condition has been met.

#### Return values

| FILE_ERROR | operation failed. |
|------------|-------------------|
|------------|-------------------|

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.9.2.7 #define fileStreamRead( ip, bp, n ) streamRead(ip, bp, n)

File stream read.

The function reads data from a file stream into a buffer.

### **Parameters**

| in  | ip | pointer to a FileStream or derived class     |
|-----|----|--|
| out | bp | pointer to the data buffer                   |
| in  | n  | the maximum amount of data to be transferred |

### Returns

The number of bytes transferred. The return value can be less than the specified number of bytes if an end-of-file condition has been met.

## Return values

| FILE_ERROR | operation failed. |
|------------|-------------------|

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.9.2.8 #define fileStreamPut( ip, b ) streamPut(ip, b)

File stream blocking byte write.

This function writes a byte value to a channel. If the channel is not ready to accept data then the calling thread is suspended.

## **Parameters**

|    |   | pointer to a FileStream or derived class    |
|----|---|---|
| in | b | the byte value to be written to the channel |

7.9 Abstract Files

#### Returns

The operation status.

### Return values

| FILE_OK    | if the operation succeeded.               |
|------------|---|
| FILE_ERROR | operation failed.                         |
| FILE_EOF   | if an end-of-file condition has been met. |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

# 7.9.2.9 #define fileStreamGet( ip ) streamGet(ip)

File stream blocking byte read.

This function reads a byte value from a channel. If the data is not available then the calling thread is suspended.

# **Parameters**

| i | .n | ip | pointer to a FileStream or derived class | 1 |
|---|----|----|--|---|
|---|----|----|--|---|

### Returns

A byte value from the queue.

# Return values

| FILE_ERROR | operation failed.                         |
|------------|---|
| FILE_EOF   | if an end-of-file condition has been met. |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.9.2.10 #define fileStreamClose( ip ) ((ip)->vmt->close(ip))

File Stream close.

The function closes a file stream.

### **Parameters**

| in ip pointer to a FileStream | am or derived class |
|-------------------------------|---------------------|
|-------------------------------|---------------------|

## Returns

The operation status.

### Return values

| FILE_OK    | no error.         |
|------------|-------------------|
| FILE_ERROR | operation failed. |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.9.2.11 #define fileStreamGetError( ip ) ((ip)->vmt->geterror(ip))

Returns an implementation dependent error code.

## Precondition

The previously called function must have returned FILE\_ERROR.

### **Parameters**

|  | in | ip | pointer to a FileStream or derived class |
|--|----|----|--|
|--|----|----|--|

### Returns

Implementation dependent error code.

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.9.2.12 #define fileStreamGetSize( ip ) ((ip)->vmt->getsize(ip))

Returns the current file size.

### **Parameters**

|--|

# Returns

The file size.

## Return values

| FILE_ERROR | operation failed. |
|------------|-------------------|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.9 Abstract Files

# 7.9.2.13 #define fileStreamGetPosition( $\it ip$ ) ((ip)->vmt->getposition(ip))

Returns the current file pointer position.

### **Parameters**

|  | in | ip | pointer to a FileStream or derived class | 1 |
|--|----|----|--|---|
|--|----|----|--|---|

## Returns

The current position inside the file.

### **Return values**

| FILE_ERROR | operation failed. |
|------------|-------------------|
|------------|-------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

# 7.9.2.14 #define fileStreamSeek( ip, offset ) ((ip)->vmt->lseek(ip, offset))

Moves the file current pointer to an absolute position.

### **Parameters**

| in | ip     | pointer to a FileStream or derived class |
|----|--------|--|
| in | offset | new absolute position                    |

## Returns

The operation status.

# Return values

| FILE_OK    | no error.         |
|------------|-------------------|
| FILE_ERROR | operation failed. |

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

# 7.9.3 Typedef Documentation

## 7.9.3.1 typedef uint32\_t fileoffset\_t

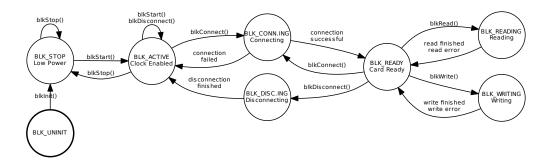
File offset type.

### 7.10 Abstract I/O Block Device

## 7.10.1 Detailed Description

#### 7.10.2 Driver State Machine

The drivers implementing this interface shall implement the following state machine internally. Not all the driver functionalities can be used in any moment, any transition not explicitly shown in the following diagram has to be considered an error and shall be captured by an assertion (if enabled).



This module defines an abstract interface for accessing generic block devices.

Note that no code is present, just abstract interfaces-like structures, you should look at the system as to a set of abstract C++ classes (even if written in C). This system has then advantage to make the access to block devices independent from the implementation logic.

## **Macros**

• #define \_base\_block\_device\_methods

BaseBlockDevice specific methods.

• #define \_base\_block\_device\_data

BaseBlockDevice specific data.

## Macro Functions (BaseBlockDevice)

#define blkGetDriverState(ip) ((ip)->state)

Returns the driver state.

#define blklsTransferring(ip)

Determines if the device is transferring data.

• #define blklsInserted(ip) ((ip)->vmt->is\_inserted(ip))

Returns the media insertion status.

#define blklsWriteProtected(ip) ((ip)->vmt->is\_protected(ip))

Returns the media write protection status.

#define blkConnect(ip) ((ip)->vmt->connect(ip))

Performs the initialization procedure on the block device.

#define blkDisconnect(ip) ((ip)->vmt->disconnect(ip))

Terminates operations on the block device.

• #define blkRead(ip, startblk, buf, n) ((ip)->vmt->read(ip, startblk, buf, n))

Reads one or more blocks.

• #define blkWrite(ip, startblk, buf, n) ((ip)->vmt->write(ip, startblk, buf, n))

Writes one or more blocks.

#define blkSync(ip) ((ip)->vmt->sync(ip))

Ensures write synchronization.

#define blkGetInfo(ip, bdip) ((ip)->vmt->get\_info(ip, bdip))

Returns a media information structure.

### **Data Structures**

struct BlockDeviceInfo

Block device info.

struct BaseBlockDeviceVMT

BaseBlockDevice virtual methods table.

• struct BaseBlockDevice

Base block device class.

#### **Enumerations**

### 7.10.3 Macro Definition Documentation

7.10.3.1 #define \_base\_block\_device\_methods

## Value:

BaseBlockDevice specific methods.

7.10.3.2 #define \_base\_block\_device\_data

### Value:

```
/* Driver state.*/
blkstate_t state;
```

BaseBlockDevice specific data.

# 7.10.3.3 #define blkGetDriverState( ip ) ((ip)->state)

Returns the driver state.

Note

Can be called in ISR context.

### **Parameters**

| in | ip | pointer to a BaseBlockDevice or derived class |
|----|----|---|
|----|----|---|

#### Returns

The driver state.

## **Function Class:**

Special function, this function has special requirements see the notes.

# 7.10.3.4 #define blklsTransferring( ip )

### Value:

Determines if the device is transferring data.

Note

Can be called in ISR context.

## **Parameters**

| in | ip | pointer to a BaseBlockDevice or derived class |
|----|----|---|
|----|----|---|

## Returns

The driver state.

### Return values

| FALSE | the device is not transferring data. |
|-------|--------------------------------------|
| TRUE  | the device not transferring data.    |

# **Function Class:**

Special function, this function has special requirements see the notes.

7.10.3.5 #define blklsInserted( ip ) ((ip)->vmt->is\_inserted(ip))

Returns the media insertion status.

Note

On some implementations this function can only be called if the device is not transferring data. The function blkIsTransferring() should be used before calling this function.

#### **Parameters**

|  | in | ip | pointer to a BaseBlockDevice or derived class | ] |
|--|----|----|---|---|
|--|----|----|---|---|

#### Returns

The media state.

## Return values

| FALSE | media not inserted. |
|-------|---------------------|
| TRUE  | media inserted.     |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.10.3.6 #define blklsWriteProtected( ip ) ((ip)->vmt->is\_protected(ip))

Returns the media write protection status.

# **Parameters**

| in | ip | pointer to a BaseBlockDevice or derived class |
|----|----|---|

## Returns

The media state.

#### **Return values**

| FALSE | writable media.     |
|-------|---------------------|
| TRUE  | non writable media. |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.10.3.7 #define blkConnect( ip ) ((ip)->vmt->connect(ip))

Performs the initialization procedure on the block device.

This function should be performed before I/O operations can be attempted on the block device and after insertion has been confirmed using blklsInserted().

#### **Parameters**

| in | ip | pointer to a BaseBlockDevice or derived class |
|----|----|---|
|----|----|---|

#### Returns

The operation status.

#### Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.10.3.8 #define blkDisconnect( ip ) ((ip)->vmt->disconnect(ip))

Terminates operations on the block device.

This operation safely terminates operations on the block device.

## **Parameters**

| in | ip | pointer to a BaseBlockDevice or derived class |
|----|----|---|
|----|----|---|

## Returns

The operation status.

#### Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.10.3.9 #define blkRead( ip, startblk, buf, n) ((ip)->vmt->read(ip, startblk, buf, n))

Reads one or more blocks.

## **Parameters**

| in | ip       | pointer to a BaseBlockDevice or derived class |
|----|----------|---|
| in | startblk | first block to read                           |

## **Parameters**

| out | buf | pointer to the read buffer |  |
|-----|-----|----------------------------|--|
| in  | n   | number of blocks to read   |  |

#### Returns

The operation status.

### Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.10.3.10 #define blkWrite( ip, startblk, buf, n) ((ip)->vmt->write(ip, startblk, buf, n))

Writes one or more blocks.

### **Parameters**

| in  | ip       | pointer to a BaseBlockDevice or derived class |
|-----|----------|---|
| in  | startblk | first block to write                          |
| out | buf      | pointer to the write buffer                   |
| in  | n        | number of blocks to write                     |

## Returns

The operation status.

## Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.10.3.11 #define blkSync( ip ) ((ip)->vmt->sync(ip))

Ensures write synchronization.

## **Parameters**

| in | ip | pointer to a BaseBlockDevice or derived class |
|----|----|---|
|----|----|---|

#### Returns

The operation status.

### **Return values**

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.10.3.12 #define blkGetInfo( ip, bdip ) ((ip)->vmt->get\_info(ip, bdip))

Returns a media information structure.

### **Parameters**

| in  | ip   | pointer to a BaseBlockDevice or derived class |
|-----|------|---|
| out | bdip | pointer to a BlockDeviceInfo structure        |

## Returns

The operation status.

# Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

# 7.10.4 Enumeration Type Documentation

7.10.4.1 enum blkstate t

Driver state machine possible states.

## **Enumerator**

**BLK\_UNINIT** Not initialized.

BLK\_STOP Stopped.

BLK\_ACTIVE Interface active.

**BLK\_CONNECTING** Connection in progress.

BLK\_DISCONNECTING Disconnection in progress.

BLK\_READY Device ready.

**BLK\_READING** Read operation in progress.

**BLK\_WRITING** Write operation in progress.

BLK\_SYNCING Sync. operation in progress.

# 7.11 I/O Bytes Queues

## 7.11.1 Detailed Description

Queues are mostly used in serial-like device drivers. Serial device drivers are usually designed to have a lower side (lower driver, it is usually an interrupt service routine) and an upper side (upper driver, accessed by the application threads).

There are several kind of gueues:

- Input queue, unidirectional queue where the writer is the lower side and the reader is the upper side.
- Output queue, unidirectional queue where the writer is the upper side and the reader is the lower side.
- Full duplex queue, bidirectional queue. Full duplex queues are implemented by pairing an input queue and an output queue together.

#### Queue functions returned status value

```
• #define Q OK MSG OK
```

Operation successful.

• #define Q\_TIMEOUT MSG\_TIMEOUT

Timeout condition.

#define Q RESET MSG RESET

Queue has been reset.

#define Q\_EMPTY (msg\_t)-3

Queue empty.

• #define Q\_FULL (msg\_t)-4

Queue full,.

### **Macro Functions**

#define qSizeX(qp)

Returns the queue's buffer size.

#define qSpaceI(qp) ((qp)->q\_counter)

Queue space.

#define qGetLink(qp) ((qp)->q\_link)

Returns the queue application-defined link.

#define iqGetFullI(iqp) qSpaceI(iqp)

Returns the filled space into an input queue.

#define iqGetEmptyl(iqp) (qSizeX(iqp) - qSpacel(iqp))

Returns the empty space into an input queue.

#define iqlsEmptyl(iqp) ((bool)(qSpacel(iqp) == 0U))

Evaluates to true if the specified input queue is empty.

#define iqlsFull(iqp)

Evaluates to true if the specified input queue is full.

#define iqGet(iqp) iqGetTimeout(iqp, TIME\_INFINITE)

Input queue read.

#define oqGetFullI(oqp) (qSizeX(oqp) - qSpaceI(oqp))

Returns the filled space into an output queue.

#define oqGetEmptyl(oqp) qSpacel(oqp)

Returns the empty space into an output queue.

```
    #define oqlsEmptyl(oqp)
```

Evaluates to true if the specified output queue is empty.

#define oqlsFullI(oqp) ((bool)(qSpaceI(oqp) == 0U))

Evaluates to true if the specified output queue is full.

#define oqPut(oqp, b) oqPutTimeout(oqp, b, TIME\_INFINITE)

Output queue write.

# **Typedefs**

• typedef struct io\_queue io\_queue\_t

Type of a generic I/O queue structure.

typedef void(\* qnotify\_t) (io\_queue\_t \*qp)

Queue notification callback type.

• typedef io\_queue\_t input\_queue\_t

Type of an input queue structure.

• typedef io\_queue\_t output\_queue\_t

Type of an output queue structure.

### **Data Structures**

· struct io queue

Generic I/O queue structure.

### **Functions**

```
    void iqObjectInit (input_queue_t *iqp, uint8_t *bp, size_t size, qnotify_t infy, void *link)
    Initializes an input queue.
```

void iqResetl (input\_queue\_t \*iqp)

Resets an input queue.

• msg\_t iqPutl (input\_queue\_t \*iqp, uint8\_t b)

Input queue write.

• msg\_t iqGetTimeout (input\_queue\_t \*iqp, systime\_t timeout)

Input queue read with timeout.

• size\_t iqReadTimeout (input\_queue\_t \*iqp, uint8\_t \*bp, size\_t n, systime\_t timeout)

Input queue read with timeout.

• void oqObjectInit (output\_queue\_t \*oqp, uint8\_t \*bp, size\_t size, qnotify\_t onfy, void \*link)

Initializes an output queue.

void ogResetl (output queue t \*oqp)

Resets an output queue.

• msg\_t oqPutTimeout (output\_queue\_t \*oqp, uint8\_t b, systime\_t timeout)

Output queue write with timeout.

msg\_t oqGetI (output\_queue\_t \*oqp)

Output queue read.

size\_t oqWriteTimeout (output\_queue\_t \*oqp, const uint8\_t \*bp, size\_t n, systime\_t timeout)

Output queue write with timeout.

#### 7.11.2 Macro Definition Documentation

7.11.2.1 #define Q\_OK MSG\_OK

Operation successful.

7.11.2.2 #define Q\_TIMEOUT MSG\_TIMEOUT

Timeout condition.

7.11.2.3 #define Q\_RESET MSG\_RESET

Queue has been reset.

7.11.2.4 #define Q\_EMPTY (msg\_t)-3

Queue empty.

7.11.2.5 #define Q\_FULL (msg\_t)-4

Queue full,.

7.11.2.6 #define qSizeX( qp )

### Value:

```
/*lint -save -e9033 [10.8] The cast is safe.*/
((size_t)((qp)->q_top - (qp)->q_buffer))

/*lint -restore*/
```

Returns the queue's buffer size.

## **Parameters**

```
in | qp | pointer to a io_queue_t structure
```

Returns

The buffer size.

**Function Class:** 

This is an X-Class API, this function can be invoked from any context.

```
7.11.2.7 #define qSpacel( qp ) ((qp)->q_counter)
```

Queue space.

Returns the used space if used on an input queue or the empty space if used on an output queue.

## **Parameters**

```
in | qp | pointer to a io_queue_t structure
```

### Returns

The buffer space.

#### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

```
7.11.2.8 #define qGetLink( qp ) ((qp)->q_link)
```

Returns the queue application-defined link.

Note

This function can be called in any context.

## **Parameters**

```
in | qp | pointer to a io_queue_t structure
```

### Returns

The application-defined link.

## **Function Class:**

Special function, this function has special requirements see the notes.

# 7.11.2.9 #define iqGetFullI( iqp ) qSpaceI(iqp)

Returns the filled space into an input queue.

## **Parameters**

| in | iqp | pointer to an input_queue_t structure |
|----|-----|---------------------------------------|

#### Returns

The number of full bytes in the queue.

## Return values

```
0 if the queue is empty.
```

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

# 7.11.2.10 #define iqGetEmptyl( iqp ) (qSizeX(iqp) - qSpaceI(iqp))

Returns the empty space into an input queue.

### **Parameters**

| in | iqp | pointer to an input_queue_t structure |
|----|-----|---------------------------------------|
|----|-----|---------------------------------------|

## Returns

The number of empty bytes in the queue.

#### Return values

```
0 if the queue is full.
```

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

```
7.11.2.11 #define iqlsEmptyl( iqp ) ((bool)(qSpacel(iqp) == 0U))
```

Evaluates to true if the specified input queue is empty.

#### **Parameters**

|  | in | iqp | pointer to an input_queue_t structure |  |
|--|----|-----|---------------------------------------|--|
|--|----|-----|---------------------------------------|--|

## Returns

The queue status.

#### Return values

| false | if the queue is not empty. |
|-------|----------------------------|
| true  | if the queue is empty.     |

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

## 7.11.2.12 #define iqlsFullI( iqp )

# Value:

```
/*lint -save -e9007 [13.5] No side effects, a pointer is passed.*/
  ((bool)(((iqp)->q_wrptr == (iqp)->q_rdptr) && ((iqp)->q_counter != 0U))) \
  /*lint -restore*/
```

Evaluates to true if the specified input queue is full.

#### **Parameters**

| in | iqp | pointer to an input_queue_t structure | ] |
|----|-----|---------------------------------------|---|
|----|-----|---------------------------------------|---|

#### Returns

The queue status.

### Return values

| false | if the queue is not full. |
|-------|---------------------------|
| true  | if the queue is full.     |

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.11.2.13 #define iqGet( iqp ) iqGetTimeout(iqp, TIME\_INFINITE)

Input queue read.

This function reads a byte value from an input queue. If the queue is empty then the calling thread is suspended until a byte arrives in the queue.

## **Parameters**

| in | iqp | pointer to an input_queue_t structure |
|----|-----|---------------------------------------|

## Returns

A byte value from the queue.

### Return values

| Q_RESET | if the queue has been reset. |
|---------|------------------------------|
|         |                              |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.11.2.14 #define oqGetFullI( oqp ) (qSizeX(oqp) - qSpaceI(oqp))

Returns the filled space into an output queue.

### **Parameters**

|  | in | oqp | pointer to an output_queue_t structure |
|--|----|-----|--|
|--|----|-----|--|

### Returns

The number of full bytes in the queue.

### **Return values**

```
0 if the queue is empty.
```

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

```
7.11.2.15 #define oqGetEmptyl( oqp ) qSpacel(oqp)
```

Returns the empty space into an output queue.

#### **Parameters**

| in | oqp | pointer to an output_queue_t structure |
|----|-----|--|
|----|-----|--|

### Returns

The number of empty bytes in the queue.

### Return values

```
0 if the queue is full.
```

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

# 7.11.2.16 #define oqlsEmptyl( oqp )

#### Value:

Evaluates to true if the specified output queue is empty.

# **Parameters**

|   | in | oap | pointer to an output_queue_t structure |
|---|----|-----|--|
| ı |    | 990 |  |

### Returns

The queue status.

#### Return values

| false | if the queue is not empty. |
|-------|----------------------------|
| true  | if the queue is empty.     |

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.11.2.17 #define oqlsFullI( oqp) ((bool)(qSpaceI(oqp) == 0U))

Evaluates to true if the specified output queue is full.

#### **Parameters**

|  | in | oqp | pointer to an output_ | _queue_ | _t structure |  |
|--|----|-----|-----------------------|---------|--------------|--|
|--|----|-----|-----------------------|---------|--------------|--|

#### Returns

The queue status.

### Return values

| false | if the queue is not full. |  |
|-------|---------------------------|--|
| true  | if the queue is full.     |  |

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.11.2.18 #define oqPut( oqp, b ) oqPutTimeout(oqp, b, TIME\_INFINITE)

Output queue write.

This function writes a byte value to an output queue. If the queue is full then the calling thread is suspended until there is space in the queue.

## **Parameters**

| in | oqp | pointer to an output_queue_t structure    |
|----|-----|---|
| in | b   | the byte value to be written in the queue |

### Returns

The operation status.

## Return values

| Q_OK | if the operation succeeded. |
|------|-----------------------------|

#### Return values

| Q_RESET if the queue has been reset. |
|--------------------------------------|
|--------------------------------------|

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

### 7.11.3 Typedef Documentation

7.11.3.1 typedef struct io\_queue io\_queue\_t

Type of a generic I/O queue structure.

7.11.3.2 typedef void(\* qnotify\_t) (io\_queue\_t \*qp)

Queue notification callback type.

### **Parameters**

| in | qp | the queue pointer |
|----|----|-------------------|
|----|----|-------------------|

### 7.11.3.3 typedef io\_queue\_t input\_queue\_t

Type of an input queue structure.

This structure represents a generic asymmetrical input queue. Writing to the queue is non-blocking and can be performed from interrupt handlers or from within a kernel lock zone. Reading the queue can be a blocking operation and is supposed to be performed by a system thread.

7.11.3.4 typedef io\_queue\_t output\_queue\_t

Type of an output queue structure.

This structure represents a generic asymmetrical output queue. Reading from the queue is non-blocking and can be performed from interrupt handlers or from within a kernel lock zone. Writing the queue can be a blocking operation and is supposed to be performed by a system thread.

## 7.11.4 Function Documentation

7.11.4.1 void iqObjectInit (input queue t \* iqp, uint8\_t \* bp, size\_t size, qnotify t infy, void \* link)

Initializes an input queue.

A Semaphore is internally initialized and works as a counter of the bytes contained in the queue.

Note

The callback is invoked from within the S-Locked system state.

#### **Parameters**

| out | iqp | pointer to an input_queue_t structure |
|-----|-----|---------------------------------------|
|-----|-----|---------------------------------------|

### **Parameters**

| in | bp   | pointer to a memory area allocated as queue buffer  |  |
|----|------|---|--|
| in | size | size size of the queue buffer   |  |
| in | infy | pointer to a callback function that is invoked when data is read from the queue. The value can be |  |
|    |      | NULL.   |  |
| in | link | application defined pointer   |  |

## **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

7.11.4.2 void iqResetl ( input\_queue\_t \* iqp )

Resets an input queue.

All the data in the input queue is erased and lost, any waiting thread is resumed with status Q\_RESET.

Note

A reset operation can be used by a low level driver in order to obtain immediate attention from the high level layers.

### **Parameters**

|  | in | iqp | pointer to an input_queue_t structure |
|--|----|-----|---------------------------------------|
|--|----|-----|---------------------------------------|

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.11.4.3 msg\_t iqPutl ( input\_queue\_t \* iqp, uint8\_t b )

Input queue write.

A byte value is written into the low end of an input queue.

## **Parameters**

| in | iqp | pointer to an input_queue_t structure     |
|----|-----|---|
| in | b   | the byte value to be written in the queue |

# Returns

The operation status.

#### Return values

| Q_OK   | if the operation has been completed with success.           |
|--------|---|
| Q FULL | if the queue is full and the operation cannot be completed. |

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.11.4.4 msg\_t iqGetTimeout ( input\_queue\_t \* iqp, systime\_t timeout )

Input queue read with timeout.

This function reads a byte value from an input queue. If the queue is empty then the calling thread is suspended until a byte arrives in the queue or a timeout occurs.

#### Note

The callback is invoked before reading the character from the buffer or before entering the state THD\_STA← TE\_WTQUEUE.

#### **Parameters**

| in | iqp     | pointer to an input_queue_t structure   |  |
|----|---------|---|--|
| in | timeout | the number of ticks before the operation timeouts, the following special values are allowed |  |
|    |         | TIME_IMMEDIATE immediate timeout.   |  |
|    |         | TIME_INFINITE no timeout.   |  |
|    |         |   |  |

### Returns

A byte value from the queue.

#### **Return values**

| Q_TIMEOUT | if the specified time expired. |
|-----------|--------------------------------|
| Q_RESET   | if the queue has been reset.   |

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.11.4.5 size\_t iqReadTimeout ( input\_queue\_t \* iqp, uint8\_t \* bp, size\_t n, systime\_t timeout )

Input queue read with timeout.

The function reads data from an input queue into a buffer. The operation completes when the specified amount of data has been transferred or after the specified timeout or if the queue has been reset.

## Note

The function is not atomic, if you need atomicity it is suggested to use a semaphore or a mutex for mutual exclusion.

The callback is invoked before reading each character from the buffer or before entering the state  $THD\_ST \leftarrow ATE\_WTQUEUE$ .

#### **Parameters**

| in  | iqp     | pointer to an input_queue_t structure  |
|-----|---------|--|
| out | bp      | pointer to the data buffer   |
| in  | n       | the maximum amount of data to be transferred, the value 0 is reserved                        |
| in  | timeout | the number of ticks before the operation timeouts, the following special values are allowed: |
|     |         | TIME_IMMEDIATE immediate timeout.  TIME_INFINITE no timeout.                                 |

### Returns

The number of bytes effectively transferred.

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.11.4.6 void oqObjectInit ( output\_queue\_t \* oqp, uint8\_t \* bp, size\_t size, qnotify\_t onfy, void \* link )

Initializes an output queue.

A Semaphore is internally initialized and works as a counter of the free bytes in the queue.

#### Note

The callback is invoked from within the S-Locked system state.

## Parameters

| out | oqp  | pointer to an output_queue_t structure   |  |
|-----|------|--|--|
| in  | bp   | pointer to a memory area allocated as queue buffer   |  |
| in  | size | size of the queue buffer   |  |
| in  | onfy | pointer to a callback function that is invoked when data is written to the queue. The value can be |  |
|     |      | NULL.  |  |
| in  | link | application defined pointer  |  |

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

7.11.4.7 void oqResetl ( output\_queue\_t \* oqp )

Resets an output queue.

All the data in the output queue is erased and lost, any waiting thread is resumed with status Q\_RESET.

### Note

A reset operation can be used by a low level driver in order to obtain immediate attention from the high level layers.

#### **Parameters**

|  | n <i>oqp</i> | pointer to an output_queue_t structure | ] |
|--|--------------|--|---|
|--|--------------|--|---|

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers

7.11.4.8 msg\_t oqPutTimeout ( output\_queue\_t \* oqp, uint8\_t b, systime\_t timeout )

Output queue write with timeout.

This function writes a byte value to an output queue. If the queue is full then the calling thread is suspended until there is space in the queue or a timeout occurs.

#### Note

The callback is invoked after writing the character into the buffer.

## **Parameters**

| in | oqp     | <pre>pointer to an output_queue_t structure</pre>  |  |
|----|---------|--|--|
| in | b       | the byte value to be written in the queue  |  |
| in | timeout | the number of ticks before the operation timeouts, the following special values are allowed: |  |
|    |         | TIME_IMMEDIATE immediate timeout.  TIME_IMMEDIATE immediate timeout.                         |  |
|    |         | TIME_INFINITE no timeout.  |  |

## Returns

The operation status.

## Return values

| Q_OK      | if the operation succeeded.    |
|-----------|--------------------------------|
| Q_TIMEOUT | if the specified time expired. |
| Q_RESET   | if the queue has been reset.   |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.11.4.9 msg\_t oqGetl ( output\_queue\_t \* oqp )

Output queue read.

A byte value is read from the low end of an output queue.

## **Parameters**

| in | oqp | pointer to an output_queue_t structure |
|----|-----|--|
|----|-----|--|

#### Returns

The byte value from the queue.

### **Return values**

| <i>Q_EMPTY</i> if the queue is empty. |
|---------------------------------------|
|---------------------------------------|

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.11.4.10 size\_t oqWriteTimeout ( output\_queue\_t \* oqp, const uint8\_t \* bp, size\_t n, systime\_t timeout )

Output queue write with timeout.

The function writes data from a buffer to an output queue. The operation completes when the specified amount of data has been transferred or after the specified timeout or if the queue has been reset.

#### Note

The function is not atomic, if you need atomicity it is suggested to use a semaphore or a mutex for mutual exclusion.

The callback is invoked after writing each character into the buffer.

#### **Parameters**

| in | oqp     | pointer to an output_queue_t structure   |  |
|----|---------|--|--|
| in | bp      | pointer to the data buffer   |  |
| in | n       | the maximum amount of data to be transferred, the value 0 is reserved                        |  |
| in | timeout | the number of ticks before the operation timeouts, the following special values are allowed: |  |
|    |         | <ul> <li>TIME_IMMEDIATE immediate timeout.</li> <li>TIME_INFINITE no timeout.</li> </ul>     |  |

### Returns

The number of bytes effectively transferred.

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

### 7.12 Abstract Streams

## 7.12.1 Detailed Description

This module define an abstract interface for generic data streams. Note that no code is present, just abstract interfaces-like structures, you should look at the system as to a set of abstract C++ classes (even if written in C). This system has then advantage to make the access to data streams independent from the implementation logic. The stream interface can be used as base class for high level object types such as files, sockets, serial ports, pipes etc.

### **Macros**

• #define \_base\_sequential\_stream\_methods

BaseSequentialStream specific methods.

• #define \_base\_sequential\_stream\_data

BaseSequentialStream specific data.

### Streams return codes

- #define STM\_OK MSG\_OK
- #define STM\_TIMEOUT MSG\_TIMEOUT
- #define STM\_RESET MSG\_RESET

## Macro Functions (BaseSequentialStream)

```
• #define streamWrite(ip, bp, n) ((ip)->vmt->write(ip, bp, n))
```

Sequential Stream write.

#define streamRead(ip, bp, n) ((ip)->vmt->read(ip, bp, n))

Sequential Stream read.

#define streamPut(ip, b) ((ip)->vmt->put(ip, b))

Sequential Stream blocking byte write.

#define streamGet(ip) ((ip)->vmt->get(ip))

Sequential Stream blocking byte read.

### **Data Structures**

struct BaseSequentialStreamVMT

BaseSequentialStream virtual methods table.

• struct BaseSequentialStream

Base stream class.

#### 7.12.2 Macro Definition Documentation

## 7.12.2.1 #define \_base\_sequential\_stream\_methods

#### Value:

```
/* Stream write buffer method.*/
size_t (*write) (void *instance, const uint8_t *bp, size_t n);
/* Stream read buffer method.*/
size_t (*read) (void *instance, uint8_t *bp, size_t n);
/* Channel put method, blocking.*/
msg_t (*put) (void *instance, uint8_t b);
/* Channel get method, blocking.*/
msg_t (*get) (void *instance);
```

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BaseSequentialStream specific methods.

7.12.2.2 #define \_base\_sequential\_stream\_data

BaseSequentialStream specific data.

Note

It is empty because <code>BaseSequentialStream</code> is only an interface without implementation.

7.12.2.3 #define streamWrite( ip, bp, n) ((ip)->vmt->write(ip, bp, n))

Sequential Stream write.

The function writes data from a buffer to a stream.

#### **Parameters**

| in | ip | pointer to a BaseSequentialStream or derived class |
|----|----|--|
| in | bp | pointer to the data buffer                         |
| in | n  | the maximum amount of data to be transferred       |

### Returns

The number of bytes transferred. The return value can be less than the specified number of bytes if an end-of-file condition has been met.

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.12.2.4 #define streamRead( ip, bp, n) ((ip)->vmt->read(ip, bp, n))

Sequential Stream read.

The function reads data from a stream into a buffer.

#### **Parameters**

| in  | ip | pointer to a BaseSequentialStream or derived class |
|-----|----|--|
| out | bp | pointer to the data buffer                         |
| in  | n  | the maximum amount of data to be transferred       |

### Returns

The number of bytes transferred. The return value can be less than the specified number of bytes if an end-of-file condition has been met.

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.12.2.5 #define streamPut( ip, b) ((ip)->vmt->put(ip, b))

Sequential Stream blocking byte write.

This function writes a byte value to a channel. If the channel is not ready to accept data then the calling thread is suspended.

#### **Parameters**

| in | ip | pointer to a BaseChannel or derived class   |
|----|----|---|
| in | b  | the byte value to be written to the channel |

### Returns

The operation status.

#### Return values

| STM_OK    | if the operation succeeded.               |
|-----------|---|
| STM_RESET | if an end-of-file condition has been met. |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

# 7.12.2.6 #define streamGet( ip ) ((ip)->vmt->get(ip))

Sequential Stream blocking byte read.

This function reads a byte value from a channel. If the data is not available then the calling thread is suspended.

# **Parameters**

| Ī | in | ip | pointer to a BaseChannel or derived class |
|---|----|----|---|
|---|----|----|---|

### Returns

A byte value from the queue.

## Return values

| STM_RESET | if an end-of-file condition has been met. |
|-----------|---|
|-----------|---|

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

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## **7.13 I2C** Driver

Generic I2C Driver.

## 7.13.1 Detailed Description

Generic I2C Driver.

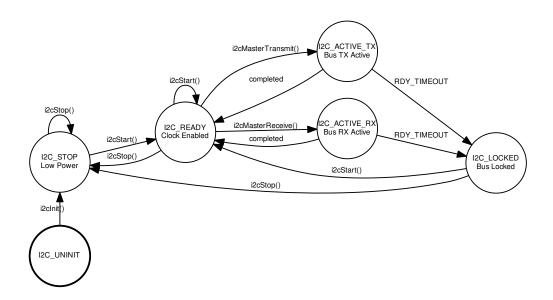
This module implements a generic I2C (Inter-Integrated Circuit) driver.

#### Precondition

In order to use the I2C driver the <code>HAL\_USE\_I2C</code> option must be enabled in <code>halconf.h</code>.

### 7.13.2 Driver State Machine

The driver implements a state machine internally, not all the driver functionalities can be used in any moment, any transition not explicitly shown in the following diagram has to be considered an error and shall be captured by an assertion (if enabled).



The driver is not thread safe for performance reasons, if you need to access the I2C bus from multiple threads then use the i2cAcquireBus() and i2cReleaseBus() APIs in order to gain exclusive access.

#### **Macros**

- #define I2C\_USE\_MUTUAL\_EXCLUSION TRUE
  - Enables the mutual exclusion APIs on the I2C bus.
- #define i2c wakeup isr(i2cp)

Wakes up the waiting thread notifying no errors.

• #define \_i2c\_wakeup\_error\_isr(i2cp)

Wakes up the waiting thread notifying errors.

#define i2cMasterTransmit(i2cp, addr, txbuf, txbytes, rxbuf, rxbytes)

Wrap i2cMasterTransmitTimeout function with TIME\_INFINITE timeout.

 #define i2cMasterReceive(i2cp, addr, rxbuf, rxbytes) (i2cMasterReceiveTimeout(i2cp, addr, rxbuf, rxbytes, TIME\_INFINITE))

Wrap i2cMasterReceiveTimeout function with TIME\_INFINITE timeout.

• #define i2c\_lld\_get\_errors(i2cp) ((i2cp)->errors)

Get errors from I2C driver.

## I2C bus error conditions

• #define I2C NO ERROR 0x00

No error.

#define I2C\_BUS\_ERROR 0x01

Bus Error.

#define I2C\_ARBITRATION\_LOST 0x02

Arbitration Lost.

• #define I2C ACK FAILURE 0x04

Acknowledge Failure.

• #define I2C\_OVERRUN 0x08

Overrun/Underrun.

• #define I2C\_PEC\_ERROR 0x10

PEC Error in reception.

• #define I2C\_TIMEOUT 0x20

Hardware timeout.

• #define I2C\_SMB\_ALERT 0x40

SMBus Alert.

## **PLATFORM configuration options**

• #define PLATFORM\_I2C\_USE\_I2C1 FALSE

I2C1 driver enable switch.

# **Typedefs**

• typedef uint16\_t i2caddr\_t

Type representing an I2C address.

· typedef uint32\_t i2cflags\_t

Type of I2C Driver condition flags.

typedef struct I2CDriver I2CDriver

Type of a structure representing an I2C driver.

### **Data Structures**

struct I2CConfig

Type of I2C driver configuration structure.

• struct I2CDriver

Structure representing an I2C driver.

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### **Functions**

void i2clnit (void)

I2C Driver initialization.

void i2cObjectInit (I2CDriver \*i2cp)

Initializes the standard part of a I2CDriver structure.

void i2cStart (I2CDriver \*i2cp, const I2CConfig \*config)

Configures and activates the I2C peripheral.

void i2cStop (I2CDriver \*i2cp)

Deactivates the I2C peripheral.

i2cflags\_t i2cGetErrors (I2CDriver \*i2cp)

Returns the errors mask associated to the previous operation.

 msg\_t i2cMasterTransmitTimeout (I2CDriver \*i2cp, i2caddr\_t addr, const uint8\_t \*txbuf, size\_t txbytes, uint8\_t \*rxbuf, size\_t rxbytes, systime\_t timeout)

Sends data via the I2C bus.

msg\_t i2cMasterReceiveTimeout (I2CDriver \*i2cp, i2caddr\_t addr, uint8\_t \*rxbuf, size\_t rxbytes, systime\_t timeout)

Receives data from the I2C bus.

void i2cAcquireBus (I2CDriver \*i2cp)

Gains exclusive access to the I2C bus.

void i2cReleaseBus (I2CDriver \*i2cp)

Releases exclusive access to the I2C bus.

• void i2c lld init (void)

Low level I2C driver initialization.

void i2c\_lld\_start (l2CDriver \*i2cp)

Configures and activates the I2C peripheral.

void i2c\_lld\_stop (I2CDriver \*i2cp)

Deactivates the I2C peripheral.

msg\_t i2c\_lld\_master\_receive\_timeout (I2CDriver \*i2cp, i2caddr\_t addr, uint8\_t \*rxbuf, size\_t rxbytes, systime\_t timeout)

Receives data via the I2C bus as master.

 msg\_t i2c\_lld\_master\_transmit\_timeout (l2CDriver \*i2cp, i2caddr\_t addr, const uint8\_t \*txbuf, size\_t txbytes, uint8\_t \*rxbuf, size\_t rxbytes, systime\_t timeout)

Transmits data via the I2C bus as master.

### **Enumerations**

#### **Variables**

I2CDriver I2CD1

I2C1 driver identifier.

## 7.13.3 Macro Definition Documentation

7.13.3.1 #define I2C\_NO\_ERROR 0x00

No error.

7.13.3.2 #define I2C\_BUS\_ERROR 0x01

Bus Error.

7.13.3.3 #define I2C\_ARBITRATION\_LOST 0x02

Arbitration Lost.

7.13.3.4 #define I2C\_ACK\_FAILURE 0x04

Acknowledge Failure.

7.13.3.5 #define I2C\_OVERRUN 0x08

Overrun/Underrun.

7.13.3.6 #define I2C\_PEC\_ERROR 0x10

PEC Error in reception.

7.13.3.7 #define I2C\_TIMEOUT 0x20

Hardware timeout.

7.13.3.8 #define I2C\_SMB\_ALERT 0x40

SMBus Alert.

7.13.3.9 #define I2C\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the mutual exclusion APIs on the I2C bus.

7.13.3.10 #define \_i2c\_wakeup\_isr( i2cp )

## Value:

```
do {
   osalSysLockFromISR();
   osalThreadResumeI(&(i2cp)->thread, MSG_OK);
   osalSysUnlockFromISR();
} while(0)
```

Wakes up the waiting thread notifying no errors.

### **Parameters**

```
in | i2cp | pointer to the I2CDriver object
```

## **Function Class:**

Not an API, this function is for internal use only.

7.13.3.11 #define \_i2c\_wakeup\_error\_isr( i2cp )

# Value:

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```
do {
   osalSysLockFromISR();
   osalThreadResumeI(&(i2cp)->thread, MSG_RESET);
   osalSysUnlockFromISR();
} while(0)
```

Wakes up the waiting thread notifying errors.

#### **Parameters**

```
in i2cp pointer to the I2CDriver object
```

## **Function Class:**

Not an API, this function is for internal use only.

7.13.3.12 #define i2cMasterTransmit( i2cp, addr, txbuf, txbytes, rxbuf, rxbytes)

#### Value:

Wrap i2cMasterTransmitTimeout function with TIME\_INFINITE timeout.

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.13.3.13 #define i2cMasterReceive( *i2cp, addr, rxbuf, rxbytes* ) (i2cMasterReceiveTimeout(i2cp, addr, rxbuf, rxbytes, TIME\_INFINITE))

Wrap i2cMasterReceiveTimeout function with TIME\_INFINITE timeout.

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.13.3.14 #define PLATFORM\_I2C\_USE\_I2C1 FALSE

I2C1 driver enable switch.

If set to TRUE the support for I2C1 is included.

Note

The default is FALSE.

7.13.3.15 #define i2c\_lld\_get\_errors( i2cp ) ((i2cp)->errors)

Get errors from I2C driver.

#### **Parameters**

| in | і2ср | pointer to the I2CDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### **Function Class:**

Not an API, this function is for internal use only.

# 7.13.4 Typedef Documentation

7.13.4.1 typedef uint16\_t i2caddr\_t

Type representing an I2C address.

7.13.4.2 typedef uint32\_t i2cflags\_t

Type of I2C Driver condition flags.

## 7.13.4.3 typedef struct I2CDriver I2CDriver

Type of a structure representing an I2C driver.

# 7.13.5 Enumeration Type Documentation

### 7.13.5.1 enum i2cstate\_t

Driver state machine possible states.

#### Enumerator

I2C\_UNINIT Not initialized.

I2C\_STOP Stopped.

I2C\_READY Ready.

*I2C\_ACTIVE\_TX* Transmitting.

I2C\_ACTIVE\_RX Receiving.

# 7.13.6 Function Documentation

7.13.6.1 void i2clnit ( void )

I2C Driver initialization.

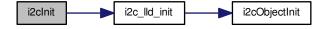
Note

This function is implicitly invoked by halinit (), there is no need to explicitly initialize the driver.

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



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# 7.13.6.2 void i2cObjectInit ( I2CDriver \* i2cp )

Initializes the standard part of a I2CDriver structure.

## **Parameters**

|  | out | і2ср | pointer to the I2CDriver object | pointer to the |
|--|-----|------|---------------------------------|----------------|
|--|-----|------|---------------------------------|----------------|

## **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

# 7.13.6.3 void i2cStart ( I2CDriver \* i2cp, const I2CConfig \* config )

Configures and activates the I2C peripheral.

### **Parameters**

| in | і2ср   | pointer to the I2CDriver object |
|----|--------|---------------------------------|
| in | config | pointer to the I2CConfig object |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.13.6.4 void i2cStop ( I2CDriver \* i2cp )

Deactivates the I2C peripheral.

## Parameters

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.13.6.5 i2cflags\_t i2cGetErrors ( I2CDriver \* i2cp )

Returns the errors mask associated to the previous operation.

#### **Parameters**

| in | і2ср | pointer to the I2CDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### Returns

The errors mask.

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.13.6.6 msg\_t i2cMasterTransmitTimeout ( I2CDriver \* i2cp, i2caddr\_t addr, const uint8\_t \* txbuf, size\_t txbytes, uint8\_t \* rxbuf, size\_t rxbytes, systime\_t timeout )

Sends data via the I2C bus.

Function designed to realize "read-through-write" transfer paradigm. If you want transmit data without any further read, than set **rxbytes** field to 0.

#### **Parameters**

| in  | i2cp    | pointer to the I2CDriver object  |
|-----|---------|--|
| in  | addr    | slave device address (7 bits) without R/W bit  |
| in  | txbuf   | pointer to transmit buffer   |
| in  | txbytes | number of bytes to be transmitted  |
| out | rxbuf   | pointer to receive buffer  |
| in  | rxbytes | number of bytes to be received, set it to 0 if you want transmit only                        |
| in  | timeout | the number of ticks before the operation timeouts, the following special values are allowed: |
|     |         | TIME_INFINITE no timeout.  |

# Returns

The operation status.

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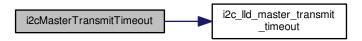
## Return values

| MSG_OK      | if the function succeeded.  |
|-------------|---|
| MSG_RESET   | if one or more I2C errors occurred, the errors can be retrieved using i2cGetErrors(). |
| MSG_TIMEOUT | if a timeout occurred before operation end.   |

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.13.6.7 msg\_t i2cMasterReceiveTimeout ( I2CDriver \* i2cp, i2caddr\_t addr, uint8\_t \* rxbuf, size\_t rxbytes, systime\_t timeout )

Receives data from the I2C bus.

## **Parameters**

| in  | і2ср    | pointer to the I2CDriver object  |
|-----|---------|--|
| in  | addr    | slave device address (7 bits) without R/W bit  |
| out | rxbuf   | pointer to receive buffer  |
| in  | rxbytes | number of bytes to be received   |
| in  | timeout | the number of ticks before the operation timeouts, the following special values are allowed: |
|     |         | TIME_INFINITE no timeout.  |

## Returns

The operation status.

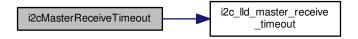
# Return values

| MSG_OK      | if the function succeeded.   |
|-------------|--|
| MSG_RESET   | if one or more I2C errors occurred, the errors can be retrieved using $i2cGetErrors()$ . |
| MSG_TIMEOUT | if a timeout occurred before operation end.  |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.13.6.8 void i2cAcquireBus ( I2CDriver \* i2cp )

Gains exclusive access to the I2C bus.

This function tries to gain ownership to the I2C bus, if the bus is already being used then the invoking thread is queued.

### Precondition

In order to use this function the option <code>I2C\_USE\_MUTUAL\_EXCLUSION</code> must be enabled.

#### **Parameters**

|  | in | і2ср | pointer to the I2CDriver object |  |
|--|----|------|---------------------------------|--|
|--|----|------|---------------------------------|--|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.13.6.9 void i2cReleaseBus ( I2CDriver \* i2cp )

Releases exclusive access to the I2C bus.

## Precondition

In order to use this function the option <code>I2C\_USE\_MUTUAL\_EXCLUSION</code> must be enabled.

## **Parameters**

| in | i2cp | pointer to the I2CDriver object |
|----|------|---------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.13.6.10 void i2c\_lld\_init (void)

Low level I2C driver initialization.

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## **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.13.6.11 void i2c\_lld\_start ( I2CDriver \* i2cp )

Configures and activates the I2C peripheral.

## **Parameters**

| ſ | in | і2ср | pointer to the I2CDriver object |  |
|---|----|------|---------------------------------|--|
|---|----|------|---------------------------------|--|

## **Function Class:**

Not an API, this function is for internal use only.

7.13.6.12 void i2c\_lld\_stop ( I2CDriver \* i2cp )

Deactivates the I2C peripheral.

## **Parameters**

| in | і2ср | pointer to the I2CDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

# **Function Class:**

Not an API, this function is for internal use only.

7.13.6.13 msg\_t i2c\_lld\_master\_receive\_timeout ( I2CDriver \* i2cp, i2caddr\_t addr, uint8\_t \* rxbuf, size\_t rxbytes, systime\_t timeout )

Receives data via the I2C bus as master.

### **Parameters**

| in  | і2ср    | pointer to the I2CDriver object |
|-----|---------|---------------------------------|
| in  | addr    | slave device address            |
| out | rxbuf   | pointer to the receive buffer   |
| in  | rxbytes | number of bytes to be received  |

## **Parameters**

| in | timeout | the number of ticks before the operation timeouts, the following special values are allowed: |
|----|---------|--|
|    |         | TIME_INFINITE no timeout.  |

### Returns

The operation status.

## Return values

| MSG_OK      | if the function succeeded.   |
|-------------|--|
| MSG_RESET   | if one or more I2C errors occurred, the errors can be retrieved using i2cGetErrors().  |
| MSG_TIMEOUT | if a timeout occurred before operation end. After a timeout the driver must be stopped |
|             | and restarted because the bus is in an uncertain state.                                |

# **Function Class:**

Not an API, this function is for internal use only.

7.13.6.14 msg\_t i2c\_lld\_master\_transmit\_timeout ( I2CDriver \* i2cp, i2caddr\_t addr, const uint8\_t \* txbuf, size\_t txbytes, uint8\_t \* rxbuf, size\_t rxbytes, systime\_t timeout )

Transmits data via the I2C bus as master.

### **Parameters**

| in  | і2ср    | pointer to the I2CDriver object  |
|-----|---------|--|
| in  | addr    | slave device address   |
| in  | txbuf   | pointer to the transmit buffer   |
| in  | txbytes | number of bytes to be transmitted  |
| out | rxbuf   | pointer to the receive buffer  |
| in  | rxbytes | number of bytes to be received   |
| in  | timeout | the number of ticks before the operation timeouts, the following special values are allowed: |
|     |         | TIME_INFINITE no timeout.  |

# Returns

The operation status.

## Return values

| MSG_OK      | if the function succeeded.   |
|-------------|--|
| MSG_RESET   | if one or more I2C errors occurred, the errors can be retrieved using i2cGetErrors().  |
| MSG_TIMEOUT | if a timeout occurred before operation end. After a timeout the driver must be stopped |
|             | and restarted because the bus is in an uncertain state.                                |

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# **Function Class:**

Not an API, this function is for internal use only.

# 7.13.7 Variable Documentation

## 7.13.7.1 I2CDriver I2CD1

I2C1 driver identifier.

## **7.14 I2S** Driver

Generic I2S Driver.

# 7.14.1 Detailed Description

Generic I2S Driver.

This module implements a generic I2S driver.

### Precondition

In order to use the I2S driver the HAL\_USE\_I2S option must be enabled in halconf.h.

## 7.14.2 Driver State Machine

## I2S modes

- #define I2S MODE SLAVE 0
- #define I2S\_MODE\_MASTER 1

#### **Macro Functions**

• #define i2sStartExchangel(i2sp)

Starts a I2S data exchange.

#define i2sStopExchangel(i2sp)

Stops the ongoing data exchange.

• #define \_i2s\_isr\_half\_code(i2sp)

Common ISR code, half buffer event.

• #define \_i2s\_isr\_full\_code(i2sp)

Common ISR code.

# **PLATFORM** configuration options

#define PLATFORM\_I2S\_USE\_I2S1 FALSE
 I2SD1 driver enable switch.

# **Typedefs**

• typedef struct I2SDriver I2SDriver

Type of a structure representing an I2S driver.

• typedef void(\* i2scallback\_t) (I2SDriver \*i2sp, size\_t offset, size\_t n)

I2S notification callback type.

## **Data Structures**

struct I2SConfig

Driver configuration structure.

• struct I2SDriver

Structure representing an I2S driver.

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## **Functions**

void i2sInit (void)

12S Driver initialization.

void i2sObjectInit (I2SDriver \*i2sp)

Initializes the standard part of a I2SDriver structure.

void i2sStart (I2SDriver \*i2sp, const I2SConfig \*config)

Configures and activates the I2S peripheral.

void i2sStop (I2SDriver \*i2sp)

Deactivates the I2S peripheral.

void i2sStartExchange (I2SDriver \*i2sp)

Starts a I2S data exchange.

void i2sStopExchange (I2SDriver \*i2sp)

Stops the ongoing data exchange.

void i2s\_lld\_init (void)

Low level I2S driver initialization.

• void i2s\_lld\_start (I2SDriver \*i2sp)

Configures and activates the I2S peripheral.

## **Enumerations**

### **Variables**

I2SDriver I2SD1

12S2 driver identifier.

### 7.14.3 Macro Definition Documentation

## 7.14.3.1 #define i2sStartExchangel( i2sp )

#### Value:

```
{
  i2s_lld_start_exchange(i2sp);
  (l2sp)->state = I2S_ACTIVE;
}
```

Starts a I2S data exchange.

#### **Parameters**

```
in i2sp pointer to the I2SDriver object
```

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

# 7.14.3.2 #define i2sStopExchangel( i2sp )

#### Value:

```
{
```

```
i2s_lld_stop_exchange(i2sp);
  (i2sp)->state = I2S_READY;
}
```

Stops the ongoing data exchange.

The ongoing data exchange, if any, is stopped, if the driver was not active the function does nothing.

#### **Parameters**

```
in i2sp pointer to the I2SDriver object
```

#### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

```
7.14.3.3 #define _i2s_isr_half_code( i2sp )
```

## Value:

```
{
  if ((i2sp)->config->end_cb != NULL) {
    (i2sp)->config->end_cb(i2sp, 0, (i2sp)->config->size / 2);
  }
}
```

Common ISR code, half buffer event.

This code handles the portable part of the ISR code:

· Callback invocation.

Note

This macro is meant to be used in the low level drivers implementation only.

## Parameters

```
in | i2sp | pointer to the I2CDriver object
```

## **Function Class:**

Not an API, this function is for internal use only.

## 7.14.3.4 #define \_i2s\_isr\_full\_code( i2sp )

### Value:

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```
else
   (i2sp)->state = I2S_READY;
}
```

Common ISR code.

This code handles the portable part of the ISR code:

- · Callback invocation.
- · Driver state transitions.

Note

This macro is meant to be used in the low level drivers implementation only.

#### **Parameters**

|  | in | i2sp | pointer to the I2CDriver object |
|--|----|------|---------------------------------|
|--|----|------|---------------------------------|

### **Function Class:**

Not an API, this function is for internal use only.

# 7.14.3.5 #define PLATFORM\_I2S\_USE\_I2S1 FALSE

I2SD1 driver enable switch.

If set to TRUE the support for I2S1 is included.

Note

The default is FALSE.

# 7.14.4 Typedef Documentation

## 7.14.4.1 typedef struct I2SDriver I2SDriver

Type of a structure representing an I2S driver.

7.14.4.2 typedef void(\* i2scallback\_t) (I2SDriver \*i2sp, size\_t offset, size\_t n)

I2S notification callback type.

## **Parameters**

| in | i2sp   | pointer to the I2SDriver object             |
|----|--------|---|
| in | offset | offset in buffers of the data to read/write |
| in | n      | number of samples to read/write             |

# 7.14.5 Enumeration Type Documentation

## 7.14.5.1 enum i2sstate\_t

Driver state machine possible states.

## Enumerator

I2S\_UNINIT Not initialized.

I2S\_STOP Stopped.

I2S\_READY Ready.

I2S\_ACTIVE Active.

I2S\_COMPLETE Transmission complete.

## 7.14.6 Function Documentation

```
7.14.6.1 void i2sInit ( void )
```

I2S Driver initialization.

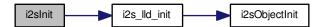
Note

This function is implicitly invoked by halInit(), there is no need to explicitly initialize the driver.

#### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



### 7.14.6.2 void i2sObjectInit ( I2SDriver \* i2sp )

Initializes the standard part of a I2SDriver structure.

### **Parameters**

|  | out | i2sp | pointer to the I2SDriver object | pointer to the |
|--|-----|------|---------------------------------|----------------|
|--|-----|------|---------------------------------|----------------|

# **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

## 7.14.6.3 void i2sStart ( I2SDriver \* i2sp, const I2SConfig \* config )

Configures and activates the I2S peripheral.

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#### **Parameters**

| in | i2sp   | pointer to the I2SDriver object |
|----|--------|---------------------------------|
| in | config | pointer to the I2SConfig object |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.14.6.4 void i2sStop ( I2SDriver \* i2sp )

Deactivates the I2S peripheral.

#### **Parameters**

| in | i2sp | pointer to the I2SDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.14.6.5 void i2sStartExchange ( I2SDriver \* i2sp )

Starts a I2S data exchange.

### **Parameters**

| in | i2sp | pointer to the I2SDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.14.6.6 void i2sStopExchange ( I2SDriver \*i2sp )

Stops the ongoing data exchange.

The ongoing data exchange, if any, is stopped, if the driver was not active the function does nothing.

### **Parameters**

| in | i2sp | pointer to the I2SDriver object |  |
|----|------|---------------------------------|--|
|----|------|---------------------------------|--|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.14.6.7 void i2s\_Ild\_init (void)

Low level I2S driver initialization.

#### **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.14.6.8 void i2s\_lld\_start ( I2SDriver \* i2sp )

Configures and activates the I2S peripheral.

#### **Parameters**

| in | i2sp | pointer to the I2SDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

Deactivates the I2S peripheral.

### **Parameters**

| in   i2sp   pointer to the I2SDriver obje | ect |
|---|-----|
|---|-----|

### **Function Class:**

Not an API, this function is for internal use only.

Starts a I2S data exchange.

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## **Parameters**

| in | i2sp | pointer to the I2SDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

Stops the ongoing data exchange.

The ongoing data exchange, if any, is stopped, if the driver was not active the function does nothing.

### **Parameters**

| in | i2sp | pointer to the I2SDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

# 7.14.7 Variable Documentation

# 7.14.7.1 I2SDriver I2SD1

I2S2 driver identifier.

## 7.15 ICU Driver

Generic ICU Driver.

# 7.15.1 Detailed Description

Generic ICU Driver.

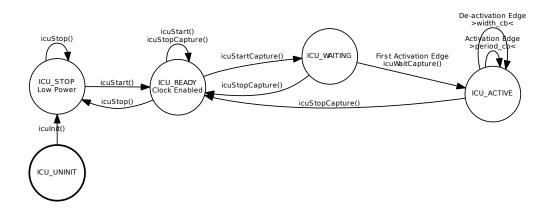
This module implements a generic ICU (Input Capture Unit) driver. The purpose of the driver is to measure period and duty cycle of an input digital signal (PWM input).

### Precondition

In order to use the ICU driver the <code>HAL\_USE\_ICU</code> option must be enabled in <code>halconf.h</code>.

### 7.15.2 Driver State Machine

The driver implements a state machine internally, not all the driver functionalities can be used in any moment, any transition not explicitly shown in the following diagram has to be considered an error and shall be captured by an assertion (if enabled).



## 7.15.3 ICU Operations.

This driver abstracts a generic Input Capture Unit composed of:

- A clock prescaler.
- · A main up counter.
- Two capture registers triggered by the rising and falling edges on the sampled input.

The ICU unit can be programmed to synchronize on the rising or falling edge of the sample input:

• ICU\_INPUT\_ACTIVE\_HIGH, a rising edge is the start signal.

• ICU\_INPUT\_ACTIVE\_LOW, a falling edge is the start signal.

Callbacks are optionally invoked when:

- · On the PWM de-activation edge.
- On the PWM activation edge, measurements for the previous cycle are available from this callback and can be retrieved using icuGetPeriodX() and icuGetWidthX().

### **Macros**

• #define icu\_lld\_get\_width(icup) 0

Returns the width of the latest pulse.

#define icu\_lld\_get\_period(icup) 0

Returns the width of the latest cycle.

• #define icu\_lld\_are\_notifications\_enabled(icup) false

Check on notifications status.

#### **Macro Functions**

#define icuStartCaptureI(icup)

Starts the input capture.

• #define icuStopCaptureI(icup)

Stops the input capture.

• #define icuEnableNotificationsI(icup) icu\_lld\_enable\_notifications(icup)

Enables notifications.

#define icuDisableNotificationsl(icup) icu\_lld\_disable\_notifications(icup)

Disables notifications.

• #define icuAreNotificationsEnabledX(icup) icu\_lld\_are\_notifications\_enabled(icup)

Check on notifications status.

#define icuGetWidthX(icup) icu\_lld\_get\_width(icup)

Returns the width of the latest pulse.

#define icuGetPeriodX(icup) icu\_lld\_get\_period(icup)

Returns the width of the latest cycle.

### Low level driver helper macros

• #define \_icu\_isr\_invoke\_width\_cb(icup)

Common ISR code, ICU width event.

#define \_icu\_isr\_invoke\_period\_cb(icup)

Common ISR code, ICU period event.

• #define \_icu\_isr\_invoke\_overflow\_cb(icup)

Common ISR code, ICU timer overflow event.

# **PLATFORM** configuration options

• #define PLATFORM\_ICU\_USE\_ICU1 FALSE

ICUD1 driver enable switch.

# **Typedefs**

typedef struct ICUDriver ICUDriver

Type of a structure representing an ICU driver.

typedef void(\* icucallback t) (ICUDriver \*icup)

ICU notification callback type.

• typedef uint32\_t icufreq\_t

ICU frequency type.

• typedef uint32 t icucnt t

ICU counter type.

### **Data Structures**

· struct ICUConfig

Driver configuration structure.

struct ICUDriver

Structure representing an ICU driver.

## **Functions**

· void iculnit (void)

ICU Driver initialization.

• void icuObjectInit (ICUDriver \*icup)

Initializes the standard part of a ICUDriver structure.

void icuStart (ICUDriver \*icup, const ICUConfig \*config)

Configures and activates the ICU peripheral.

void icuStop (ICUDriver \*icup)

Deactivates the ICU peripheral.

void icuStartCapture (ICUDriver \*icup)

Starts the input capture.

bool icuWaitCapture (ICUDriver \*icup)

Waits for a completed capture.

void icuStopCapture (ICUDriver \*icup)

Stops the input capture.

void icuEnableNotifications (ICUDriver \*icup)

Enables notifications.

• void icuDisableNotifications (ICUDriver \*icup)

Disables notifications.

void icu\_lld\_init (void)

Low level ICU driver initialization.

void icu\_Ild\_start (ICUDriver \*icup)

Configures and activates the ICU peripheral.

void icu\_lld\_stop (ICUDriver \*icup)

Deactivates the ICU peripheral.

• void icu\_lld\_start\_capture (ICUDriver \*icup)

Starts the input capture.

bool icu\_lld\_wait\_capture (ICUDriver \*icup)

Waits for a completed capture.

• void icu\_lld\_stop\_capture (ICUDriver \*icup)

Stops the input capture.

• void icu\_lld\_enable\_notifications (ICUDriver \*icup)

Enables notifications.

• void icu\_lld\_disable\_notifications (ICUDriver \*icup)

Disables notifications.

### **Enumerations**

## **Variables**

ICUDriver ICUD1

ICUD1 driver identifier.

## 7.15.4 Macro Definition Documentation

## 7.15.4.1 #define icuStartCapturel( icup )

#### Value:

```
do {
  icu_lld_start_capture(icup);
  (icup)->state = ICU_WAITING;
} while (false)
```

Starts the input capture.

#### **Parameters**

```
in icup pointer to the ICUDriver object
```

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

## 7.15.4.2 #define icuStopCapturel( icup )

## Value:

```
do {
  icu_lld_stop_capture(icup);
  (icup)->state = ICU_READY;
} while (false)
```

Stops the input capture.

### **Parameters**

| in | icup | pointer to the ICUDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.15.4.3 #define icuEnableNotificationsl( icup ) icu\_IId\_enable\_notifications(icup)

Enables notifications.

### Precondition

The ICU unit must have been activated using icuStart().

Note

If the notification is already enabled then the call has no effect.

#### **Parameters**

|  | in | icup | pointer to the ICUDriver object |
|--|----|------|---------------------------------|
|--|----|------|---------------------------------|

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

 $7.15.4.4 \quad \texttt{\#define\ icuDisableNotifications(icup\ )\ icu\_IId\_disable\_notifications(icup)}$ 

Disables notifications.

## Precondition

The ICU unit must have been activated using icuStart().

Note

If the notification is already disabled then the call has no effect.

## **Parameters**

|  | in | icup | pointer to the ICUDriver object |
|--|----|------|---------------------------------|
|--|----|------|---------------------------------|

#### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.15.4.5 #define icuAreNotificationsEnabledX( icup ) icu\_Ild\_are\_notifications\_enabled(icup)

Check on notifications status.

#### **Parameters**

| in | icup | pointer to the ICUDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### Returns

The notifications status.

#### **Return values**

| false | if notifications are not enabled. |
|-------|-----------------------------------|
| true  | if notifications are enabled.     |

### **Function Class:**

Not an API, this function is for internal use only.

7.15.4.6 #define icuGetWidthX( icup ) icu\_Ild\_get\_width(icup)

Returns the width of the latest pulse.

The pulse width is defined as number of ticks between the start edge and the stop edge.

Note

This function is meant to be invoked from the width capture callback.

#### **Parameters**

| in | icup | pointer to the ICUDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## Returns

The number of ticks.

## **Function Class:**

This is an X-Class API, this function can be invoked from any context.

7.15.4.7 #define icuGetPeriodX( icup ) icu\_IId\_get\_period(icup)

Returns the width of the latest cycle.

The cycle width is defined as number of ticks between a start edge and the next start edge.

Note

This function is meant to be invoked from the width capture callback.

# **Parameters**

| in | icup | pointer to the ICUDriver object |
|----|------|---------------------------------|
|    | 1-   |                                 |

#### Returns

The number of ticks.

### **Function Class:**

This is an X-Class API, this function can be invoked from any context.

```
7.15.4.8 #define _icu_isr_invoke_width_cb( icup )
```

### Value:

Common ISR code, ICU width event.

#### **Parameters**

```
in | icup | pointer to the ICUDriver object
```

#### **Function Class:**

Not an API, this function is for internal use only.

```
7.15.4.9 #define _icu_isr_invoke_period_cb( icup )
```

### Value:

```
do {
   if (((icup)->state == ICU_ACTIVE) &&
        ((icup)->config->period_cb != NULL))
        (icup)->config->period_cb(icup);
        (icup)->state = ICU_ACTIVE;
} while (0)
```

Common ISR code, ICU period event.

# Note

A period event brings the driver into the <code>ICU\_ACTIVE</code> state.

### **Parameters**

```
in icup pointer to the ICUDriver object
```

### **Function Class:**

Not an API, this function is for internal use only.

7.15.4.10 #define \_icu\_isr\_invoke\_overflow\_cb( icup )

## Value:

```
do {
    (icup) ->config->overflow_cb(icup);
    (icup) ->state = ICU_WAITING;
} while (0)
```

Common ISR code, ICU timer overflow event.

Note

An overflow always brings the driver back to the ICU\_WAITING state.

#### **Parameters**

| in | icup | pointer to the ICUDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### **Function Class:**

Not an API, this function is for internal use only.

7.15.4.11 #define PLATFORM\_ICU\_USE\_ICU1 FALSE

ICUD1 driver enable switch.

If set to TRUE the support for ICUD1 is included.

Note

The default is FALSE.

7.15.4.12 #define icu\_lld\_get\_width( icup ) 0

Returns the width of the latest pulse.

The pulse width is defined as number of ticks between the start edge and the stop edge.

### **Parameters**

| in | icup | pointer to the ICUDriver object |
|----|------|---------------------------------|

#### Returns

The number of ticks.

**Function Class:** 

Not an API, this function is for internal use only.

7.15.4.13 #define icu\_lld\_get\_period( icup ) 0

Returns the width of the latest cycle.

The cycle width is defined as number of ticks between a start edge and the next start edge.

### **Parameters**

|  | in | icup | pointer to the ICUDriver object |  |
|--|----|------|---------------------------------|--|
|--|----|------|---------------------------------|--|

### Returns

The number of ticks.

### **Function Class:**

Not an API, this function is for internal use only.

7.15.4.14 #define icu\_lld\_are\_notifications\_enabled( icup ) false

Check on notifications status.

#### **Parameters**

| in | icup | pointer to the ICUDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### Returns

The notifications status.

### **Return values**

| false | if notifications are not enabled. |
|-------|-----------------------------------|
| true  | if notifications are enabled.     |

### **Function Class:**

Not an API, this function is for internal use only.

# 7.15.5 Typedef Documentation

7.15.5.1 typedef struct ICUDriver ICUDriver

Type of a structure representing an ICU driver.

7.15.5.2 typedef void(\* icucallback\_t) (ICUDriver \*icup)

ICU notification callback type.

# **Parameters**

| in | icup | pointer to a ICUDriver object |
|----|------|-------------------------------|

7.15.5.3 typedef uint32\_t icufreq\_t

ICU frequency type.

7.15.5.4 typedef uint32\_t icucnt\_t

ICU counter type.

# 7.15.6 Enumeration Type Documentation

## 7.15.6.1 enum icustate\_t

Driver state machine possible states.

#### **Enumerator**

ICU\_UNINIT Not initialized.

ICU\_STOP Stopped.

ICU\_READY Ready.

ICU\_WAITING Waiting for first front.

ICU\_ACTIVE First front detected.

## 7.15.6.2 enum icumode\_t

ICU driver mode.

### Enumerator

ICU\_INPUT\_ACTIVE\_HIGH Trigger on rising edge.ICU\_INPUT\_ACTIVE\_LOW Trigger on falling edge.

### 7.15.7 Function Documentation

7.15.7.1 void iculnit (void)

ICU Driver initialization.

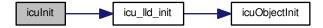
Note

This function is implicitly invoked by  ${\tt halInit}$  (), there is no need to explicitly initialize the driver.

## **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



# 7.15.7.2 void icuObjectInit ( ICUDriver \*icup )

Initializes the standard part of a ICUDriver structure.

### **Parameters**

| ou | icup | pointer to the ICUDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

7.15.7.3 void icuStart ( ICUDriver \* icup, const ICUConfig \* config )

Configures and activates the ICU peripheral.

#### **Parameters**

| in | icup   | pointer to the ICUDriver object |
|----|--------|---------------------------------|
| in | config | pointer to the ICUConfig object |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.15.7.4 void icuStop ( ICUDriver \* icup )

Deactivates the ICU peripheral.

#### **Parameters**

|  | in | icup | pointer to the ICUDriver object |  |
|--|----|------|---------------------------------|--|
|--|----|------|---------------------------------|--|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.15.7.5 void icuStartCapture ( ICUDriver \* icup )

Starts the input capture.

#### **Parameters**

| in | icup | pointer to the ICUDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

## 7.15.7.6 bool icuWaitCapture ( ICUDriver \* icup )

Waits for a completed capture.

### Note

The operation could be performed in polled mode depending on. In order to use this function notifications must be disabled.

### Precondition

The driver must be in ICU\_WAITING or ICU\_ACTIVE states.

### Postcondition

After the capture is available the driver is in  $ICU\_ACTIVE$  state. If a capture fails then the driver is in  $ICU \leftarrow \_WAITING$  state.

#### **Parameters**

| in | icup | pointer to the ICUDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## Returns

The capture status.

### Return values

| false | if the capture is successful. |
|-------|-------------------------------|
| true  | if a timer overflow occurred. |

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



### 7.15.7.7 void icuStopCapture ( ICUDriver \* icup )

Stops the input capture.

#### **Parameters**

| iı | 1 | icup | pointer to the ICUDriver object |
|----|---|------|---------------------------------|
|----|---|------|---------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

# 7.15.7.8 void icuEnableNotifications ( ICUDriver \* icup )

Enables notifications.

# Precondition

The ICU unit must have been activated using icuStart().

### Note

If the notification is already enabled then the call has no effect.

### **Parameters**

| in | icup | pointer to the ICUDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.15.7.9 void icuDisableNotifications ( ICUDriver \* icup )

Disables notifications.

## Precondition

The ICU unit must have been activated using icuStart().

#### Note

If the notification is already disabled then the call has no effect.

### **Parameters**

|  | in | icup | pointer to the ICUDriver object |
|--|----|------|---------------------------------|
|--|----|------|---------------------------------|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.15.7.10 void icu\_Ild\_init (void)

Low level ICU driver initialization.

## **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.15.7.11 void icu\_lld\_start ( ICUDriver \* icup )

Configures and activates the ICU peripheral.

# **Parameters**

| in   icup   pointer to the ICUDriver object | in | icup | pointer to the ICUDriver object |
|---|----|------|---------------------------------|
|---|----|------|---------------------------------|

### **Function Class:**

Not an API, this function is for internal use only.

7.15.7.12 void icu\_lld\_stop ( ICUDriver \* icup )

Deactivates the ICU peripheral.

## **Parameters**

|  | in | icup | pointer to the ICUDriver object |
|--|----|------|---------------------------------|
|--|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

7.15.7.13 void icu\_lld\_start\_capture ( ICUDriver \* icup )

Starts the input capture.

### **Parameters**

| in | icup | pointer to the ICUDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### **Function Class:**

Not an API, this function is for internal use only.

7.15.7.14 bool icu\_lld\_wait\_capture ( ICUDriver \* icup )

Waits for a completed capture.

Note

The operation is performed in polled mode. In order to use this function notifications must be disabled.

#### **Parameters**

| j | n | icup | pointer to the ICUDriver object |
|---|---|------|---------------------------------|
|---|---|------|---------------------------------|

## Returns

The capture status.

#### Return values

| false | if the capture is successful. |
|-------|-------------------------------|
| true  | if a timer overflow occurred. |

### **Function Class:**

Not an API, this function is for internal use only.

7.15.7.15 void icu\_lld\_stop\_capture ( ICUDriver \* icup )

Stops the input capture.

### **Parameters**

| in | icup | pointer to the ICUDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

7.15.7.16 void icu\_lld\_enable\_notifications ( ICUDriver \* icup )

Enables notifications.

#### Precondition

The ICU unit must have been activated using icuStart().

Note

If the notification is already enabled then the call has no effect.

#### **Parameters**

| in | icup | pointer to the ICUDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.15.7.17 void icu\_lld\_disable\_notifications ( ICUDriver \* icup )

Disables notifications.

## Precondition

The ICU unit must have been activated using icuStart ().

Note

If the notification is already disabled then the call has no effect.

### **Parameters**

| in | icup | pointer to the ICUDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

# 7.15.8 Variable Documentation

# 7.15.8.1 ICUDriver ICUD1

ICUD1 driver identifier.

Note

The driver ICUD1 allocates the complex timer TIM1 when enabled.

### 7.16 MAC Driver

Generic MAC Driver.

## 7.16.1 Detailed Description

Generic MAC Driver.

This module implements a generic MAC (Media Access Control) driver for Ethernet controllers.

### Precondition

In order to use the MAC driver the HAL\_USE\_MAC option must be enabled in halconf.h.

### **Macros**

• #define MAC SUPPORTS ZERO COPY TRUE

This implementation supports the zero-copy mode API.

## **MAC** configuration options

#define MAC\_USE\_ZERO\_COPY FALSE

Enables an event sources for incoming packets.

• #define MAC USE EVENTS TRUE

Enables an event sources for incoming packets.

### **Macro Functions**

• #define macGetReceiveEventSource(macp) (&(macp)->rdevent)

Returns the received frames event source.

- #define macWriteTransmitDescriptor(tdp, buf, size) mac\_lld\_write\_transmit\_descriptor(tdp, buf, size) Writes to a transmit descriptor's stream.
- #define macReadReceiveDescriptor(rdp, buf, size) mac\_lld\_read\_receive\_descriptor(rdp, buf, size)

  Reads from a receive descriptor's stream.
- #define macGetNextTransmitBuffer(tdp, size, sizep) mac\_lld\_get\_next\_transmit\_buffer(tdp, size, sizep)

  Returns a pointer to the next transmit buffer in the descriptor chain.
- #define macGetNextReceiveBuffer(rdp, sizep) mac\_lld\_get\_next\_receive\_buffer(rdp, sizep)

  Returns a pointer to the next receive buffer in the descriptor chain.

# **PLATFORM** configuration options

• #define PLATFORM\_MAC\_USE\_MAC1 FALSE

# **Typedefs**

typedef struct MACDriver MACDriver

MAC driver enable switch.

Type of a structure representing a MAC driver.

### **Data Structures**

struct MACConfig

Driver configuration structure.

struct MACDriver

Structure representing a MAC driver.

struct MACTransmitDescriptor

Structure representing a transmit descriptor.

· struct MACReceiveDescriptor

Structure representing a receive descriptor.

### **Functions**

· void macInit (void)

MAC Driver initialization.

void macObjectInit (MACDriver \*macp)

Initialize the standard part of a MACDriver structure.

void macStart (MACDriver \*macp, const MACConfig \*config)

Configures and activates the MAC peripheral.

void macStop (MACDriver \*macp)

Deactivates the MAC peripheral.

• msg\_t macWaitTransmitDescriptor (MACDriver \*macp, MACTransmitDescriptor \*tdp, systime\_t timeout)

Allocates a transmission descriptor.

void macReleaseTransmitDescriptor (MACTransmitDescriptor \*tdp)

Releases a transmit descriptor and starts the transmission of the enqueued data as a single frame.

msg\_t macWaitReceiveDescriptor (MACDriver \*macp, MACReceiveDescriptor \*rdp, systime\_t timeout)

Waits for a received frame.

void macReleaseReceiveDescriptor (MACReceiveDescriptor \*rdp)

Releases a receive descriptor.

bool macPollLinkStatus (MACDriver \*macp)

Updates and returns the link status.

void mac\_lld\_init (void)

Low level MAC initialization.

void mac Ild start (MACDriver \*macp)

Configures and activates the MAC peripheral.

void mac\_lld\_stop (MACDriver \*macp)

Deactivates the MAC peripheral.

msg t mac lld get transmit descriptor (MACDriver \*macp, MACTransmitDescriptor \*tdp)

Returns a transmission descriptor.

void mac\_lld\_release\_transmit\_descriptor (MACTransmitDescriptor \*tdp)

Releases a transmit descriptor and starts the transmission of the enqueued data as a single frame.

msg t mac lld get receive descriptor (MACDriver \*macp, MACReceiveDescriptor \*rdp)

Returns a receive descriptor.

void mac\_lld\_release\_receive\_descriptor (MACReceiveDescriptor \*rdp)

Releases a receive descriptor.

bool mac\_lld\_poll\_link\_status (MACDriver \*macp)

Updates and returns the link status.

size t mac IId write transmit descriptor (MACTransmitDescriptor \*tdp, uint8 t \*buf, size t size)

Writes to a transmit descriptor's stream.

• size t mac Ild read receive descriptor (MACReceiveDescriptor \*rdp, uint8 t \*buf, size t size)

Reads from a receive descriptor's stream.

- uint8\_t \* mac\_lld\_get\_next\_transmit\_buffer (MACTransmitDescriptor \*tdp, size\_t size, size\_t \*sizep)

  Returns a pointer to the next transmit buffer in the descriptor chain.
- const uint8\_t \* mac\_lld\_get\_next\_receive\_buffer (MACReceiveDescriptor \*rdp, size\_t \*sizep)

  Returns a pointer to the next receive buffer in the descriptor chain.

#### **Enumerations**

## Variables

MACDriver ETHD1

MAC1 driver identifier.

## 7.16.2 Macro Definition Documentation

7.16.2.1 #define MAC\_USE\_ZERO\_COPY FALSE

Enables an event sources for incoming packets.

7.16.2.2 #define MAC\_USE\_EVENTS TRUE

Enables an event sources for incoming packets.

7.16.2.3 #define macGetReceiveEventSource( macp ) (&(macp)->rdevent)

Returns the received frames event source.

#### **Parameters**

| in | таср | pointer to the MACDriver object |
|----|------|---------------------------------|

#### Returns

The pointer to the EventSource structure.

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.16.2.4 #define macWriteTransmitDescriptor( tdp, buf, size ) mac\_IId\_write\_transmit\_descriptor(tdp, buf, size)

Writes to a transmit descriptor's stream.

### **Parameters**

| in | tdp  | pointer to a MACTransmitDescriptor structure            |
|----|------|---|
| in | buf  | pointer to the buffer containing the data to be written |
| in | size | number of bytes to be written                           |

#### Returns

The number of bytes written into the descriptor's stream, this value can be less than the amount specified in the parameter size if the maximum frame size is reached.

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.16.2.5 #define macReadReceiveDescriptor( rdp, buf, size ) mac\_IId\_read\_receive\_descriptor(rdp, buf, size)

Reads from a receive descriptor's stream.

#### **Parameters**

| in | rdp  | pointer to a MACReceiveDescriptor structure           |
|----|------|---|
| in | buf  | pointer to the buffer that will receive the read data |
| in | size | number of bytes to be read                            |

#### Returns

The number of bytes read from the descriptor's stream, this value can be less than the amount specified in the parameter size if there are no more bytes to read.

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.16.2.6 #define macGetNextTransmitBuffer( tdp, size, sizep ) mac\_IId\_get\_next\_transmit\_buffer(tdp, size, sizep)

Returns a pointer to the next transmit buffer in the descriptor chain.

### Note

The API guarantees that enough buffers can be requested to fill a whole frame.

#### **Parameters**

| in  | tdp   | pointer to a MACTransmitDescriptor structure   |
|-----|-------|--|
| in  | size  | size of the requested buffer. Specify the frame size on the first call then scale the value down |
|     |       | subtracting the amount of data already copied into the previous buffers.                         |
| out | sizep | pointer to variable receiving the real buffer size. The returned value can be less than the      |
|     |       | amount requested, this means that more buffers must be requested in order to fill the frame      |
|     |       | data entirely.   |

## Returns

Pointer to the returned buffer.

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.16.2.7 #define macGetNextReceiveBuffer( rdp, sizep ) mac\_IId\_get\_next\_receive\_buffer(rdp, sizep)

Returns a pointer to the next receive buffer in the descriptor chain.

Note

The API guarantees that the descriptor chain contains a whole frame.

#### **Parameters**

| in  | rdp   | pointer to a MACReceiveDescriptor structure   |
|-----|-------|---|
| out | sizep | pointer to variable receiving the buffer size, it is zero when the last buffer has already been |
|     |       | returned.   |

#### Returns

Pointer to the returned buffer.

#### Return values

| NULL if the buffer chain has been entirely scanned | d. |
|--|----|
|--|----|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.16.2.8 #define MAC\_SUPPORTS\_ZERO\_COPY TRUE

This implementation supports the zero-copy mode API.

7.16.2.9 #define PLATFORM\_MAC\_USE\_MAC1 FALSE

MAC driver enable switch.

If set to TRUE the support for MAC1 is included.

Note

The default is FALSE.

# 7.16.3 Typedef Documentation

7.16.3.1 typedef struct MACDriver MACDriver

Type of a structure representing a MAC driver.

## 7.16.4 Enumeration Type Documentation

7.16.4.1 enum macstate\_t

Driver state machine possible states.

#### Enumerator

MAC\_UNINIT Not initialized.

MAC\_STOP Stopped.

MAC\_ACTIVE Active.

## 7.16.5 Function Documentation

7.16.5.1 void macInit (void)

MAC Driver initialization.

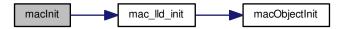
Note

This function is implicitly invoked by halinit (), there is no need to explicitly initialize the driver.

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



## 7.16.5.2 void macObjectInit ( MACDriver \* macp )

Initialize the standard part of a MACDriver structure.

## **Parameters**

|  | out | таср | pointer to the MACDriver object | l |
|--|-----|------|---------------------------------|---|
|--|-----|------|---------------------------------|---|

## **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

# 7.16.5.3 void macStart ( MACDriver \* macp, const MACConfig \* config )

Configures and activates the MAC peripheral.

### **Parameters**

| in | таср   | pointer to the MACDriver object |
|----|--------|---------------------------------|
| in | config | pointer to the MACConfig object |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.16.5.4 void macStop ( MACDriver \* macp )

Deactivates the MAC peripheral.

### **Parameters**

| in | таср | pointer to the MACDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.16.5.5 msg\_t macWaitTransmitDescriptor ( MACDriver \* macp, MACTransmitDescriptor \* tdp, systime\_t timeout )

Allocates a transmission descriptor.

One of the available transmission descriptors is locked and returned. If a descriptor is not currently available then the invoking thread is queued until one is freed.

## **Parameters**

| in  | таср    | pointer to the MACDriver object  |
|-----|---------|--|
| out | tdp     | pointer to a MACTransmitDescriptor structure   |
| in  | timeout | the number of ticks before the operation timeouts, the following special values are allowed: |
|     |         | TIME_IMMEDIATE immediate timeout.  |
|     |         | TIME_INFINITE no timeout.  |
|     |         |  |

ChibiOS/HAL

#### Returns

The operation status.

### Return values

| MSG_OK      | the descriptor was obtained.                         |
|-------------|--|
| MSG_TIMEOUT | the operation timed out, descriptor not initialized. |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.16.5.6 void macReleaseTransmitDescriptor ( MACTransmitDescriptor \*tdp )

Releases a transmit descriptor and starts the transmission of the enqueued data as a single frame.

### **Parameters**

| ir | tdp | the pointer to the MACTransmitDescriptor structure |
|----|-----|--|
|----|-----|--|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.16.5.7 msg\_t macWaitReceiveDescriptor ( MACDriver \* macp, MACReceiveDescriptor \* rdp, systime\_t timeout )

Waits for a received frame.

Stops until a frame is received and buffered. If a frame is not immediately available then the invoking thread is queued until one is received.

### **Parameters**

| in  | таср    | pointer to the MACDriver object  |
|-----|---------|--|
| out | rdp     | pointer to a MACReceiveDescriptor structure  |
| in  | timeout | the number of ticks before the operation timeouts, the following special values are allowed: |
|     |         | TIME_IMMEDIATE immediate timeout.  TIME_INFINITE no timeout.                                 |

#### Returns

The operation status.

#### Return values

| MSG_OK      | the descriptor was obtained.                         |
|-------------|--|
| MSG_TIMEOUT | the operation timed out, descriptor not initialized. |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.16.5.8 void macReleaseReceiveDescriptor ( MACReceiveDescriptor \* rdp )

Releases a receive descriptor.

The descriptor and its buffer are made available for more incoming frames.

## **Parameters**

| in   rdp   the pointer to the MACReceiveDescriptor structu |
|--|
|--|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.16.5.9 bool macPollLinkStatus ( MACDriver \* macp )

Updates and returns the link status.

## **Parameters**

| in | таср | pointer to the MACDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### Returns

The link status.

### Return values

| true  | if the link is active. |
|-------|------------------------|
| false | if the link is down.   |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.16.5.10 void mac\_lld\_init ( void )

Low level MAC initialization.

## **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.16.5.11 void mac\_lld\_start ( MACDriver \* macp )

Configures and activates the MAC peripheral.

### **Parameters**

| in | таср | pointer to the MACDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### **Function Class:**

Not an API, this function is for internal use only.

7.16.5.12 void mac\_lld\_stop ( MACDriver \* macp )

Deactivates the MAC peripheral.

### **Parameters**

| in | таср | pointer to the MACDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

7.16.5.13  $msg_t mac_lld_get_transmit_descriptor ( MACDriver * macp, MACTransmitDescriptor * tdp )$ 

Returns a transmission descriptor.

One of the available transmission descriptors is locked and returned.

### **Parameters**

| in  | таср | pointer to the MACDriver object              |
|-----|------|--|
| out | tdp  | pointer to a MACTransmitDescriptor structure |

## Returns

The operation status.

### Return values

| MSG_OK      | the descriptor has been obtained. |
|-------------|-----------------------------------|
| MSG_TIMEOUT | descriptor not available.         |

### **Function Class:**

Not an API, this function is for internal use only.

7.16.5.14 void mac\_lld\_release\_transmit\_descriptor ( MACTransmitDescriptor \* tdp )

Releases a transmit descriptor and starts the transmission of the enqueued data as a single frame.

#### **Parameters**

|  | in | tdp | the pointer to the MACTransmitDescriptor structure |
|--|----|-----|--|
|--|----|-----|--|

### **Function Class:**

Not an API, this function is for internal use only.

7.16.5.15 msg\_t mac\_lld\_get\_receive\_descriptor ( MACDriver \* macp, MACReceiveDescriptor \* rdp )

Returns a receive descriptor.

#### **Parameters**

| in  | таср | pointer to the MACDriver object             |
|-----|------|---|
| out | rdp  | pointer to a MACReceiveDescriptor structure |

## Returns

The operation status.

### Return values

| MSG_OK      | the descriptor has been obtained. |
|-------------|-----------------------------------|
| MSG_TIMEOUT | descriptor not available.         |

### **Function Class:**

Not an API, this function is for internal use only.

7.16.5.16 void mac\_lld\_release\_receive\_descriptor ( MACReceiveDescriptor \* rdp )

Releases a receive descriptor.

The descriptor and its buffer are made available for more incoming frames.

### **Parameters**

|  | in | rdp | the pointer to the MACReceiveDescriptor structure |  |
|--|----|-----|---|--|
|--|----|-----|---|--|

### **Function Class:**

Not an API, this function is for internal use only.

7.16.5.17 bool mac\_lld\_poll\_link\_status ( MACDriver \* macp )

Updates and returns the link status.

#### **Parameters**

| ACDriver object | pointer to the | таср | in |
|-----------------|----------------|------|----|
|-----------------|----------------|------|----|

### Returns

The link status.

#### Return values

| true  | if the link is active. |
|-------|------------------------|
| false | if the link is down.   |

## **Function Class:**

Not an API, this function is for internal use only.

7.16.5.18 size\_t mac\_lld\_write\_transmit\_descriptor ( MACTransmitDescriptor \* tdp, uint8\_t \* buf, size\_t size )

Writes to a transmit descriptor's stream.

#### **Parameters**

| in | tdp  | pointer to a MACTransmitDescriptor structure            |
|----|------|---|
| in | buf  | pointer to the buffer containing the data to be written |
| in | size | number of bytes to be written                           |

### Returns

The number of bytes written into the descriptor's stream, this value can be less than the amount specified in the parameter size if the maximum frame size is reached.

## **Function Class:**

Not an API, this function is for internal use only.

7.16.5.19 size\_t mac\_lld\_read\_receive\_descriptor ( MACReceiveDescriptor \* rdp, uint8\_t \* buf, size\_t size )

Reads from a receive descriptor's stream.

#### **Parameters**

| in | rdp  | pointer to a MACReceiveDescriptor structure           |
|----|------|---|
| in | buf  | pointer to the buffer that will receive the read data |
| in | size | number of bytes to be read                            |

# Returns

The number of bytes read from the descriptor's stream, this value can be less than the amount specified in the parameter size if there are no more bytes to read.

### **Function Class:**

Not an API, this function is for internal use only.

7.16.5.20 uint8\_t \* mac\_lld\_get\_next\_transmit\_buffer ( MACTransmitDescriptor \* tdp, size\_t \* size\_t \* size\_t )

Returns a pointer to the next transmit buffer in the descriptor chain.

## Note

The API guarantees that enough buffers can be requested to fill a whole frame.

#### **Parameters**

| in  | tdp   | pointer to a MACTransmitDescriptor structure   |
|-----|-------|--|
| in  | size  | size of the requested buffer. Specify the frame size on the first call then scale the value down |
|     |       | subtracting the amount of data already copied into the previous buffers.                         |
| out | sizep | pointer to variable receiving the buffer size, it is zero when the last buffer has already been  |
|     |       | returned. Note that a returned size lower than the amount requested means that more buffers      |
|     |       | must be requested in order to fill the frame data entirely.                                      |

### Returns

Pointer to the returned buffer.

## Return values

| NULL if the buffer chain has been entirely s | scanned. |
|--|----------|
|--|----------|

### **Function Class:**

Not an API, this function is for internal use only.

 $7.16.5.21 \quad const \ uint 8\_t * mac\_lld\_get\_next\_receive\_buffer (\ \ \textbf{MACReceiveDescriptor} * \textit{rdp}, \ size\_t * \textit{sizep} \ )$ 

Returns a pointer to the next receive buffer in the descriptor chain.

## Note

The API guarantees that the descriptor chain contains a whole frame.

# **Parameters**

| in  | rdp   | pointer to a MACReceiveDescriptor structure   |
|-----|-------|---|
| out | sizep | pointer to variable receiving the buffer size, it is zero when the last buffer has already been |
|     |       | returned.   |

## Returns

Pointer to the returned buffer.

# Return values

| NULL | if the buffer chain has been entirely scanned. |
|------|--|
|------|--|

# **Function Class:**

Not an API, this function is for internal use only.

# 7.16.6 Variable Documentation

## 7.16.6.1 MACDriver ETHD1

MAC1 driver identifier.

### 7.17 HAL

Hardware Abstraction Layer.

### 7.17.1 Detailed Description

Hardware Abstraction Layer.

Under ChibiOS the set of the various device driver interfaces is called the HAL subsystem: Hardware Abstraction Layer. The HAL is the abstract interface between ChibiOS applications and hardware.

### 7.17.2 HAL Device Drivers Architecture

The HAL contains several kind of modules:

- · Normal Device Drivers
- · Complex Device Drivers
- · Interfaces
- · Inner Code

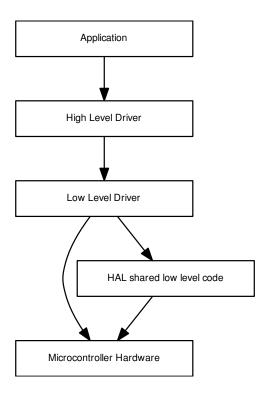
## 7.17.3 HAL Normal Device Drivers

Normal device are meant to interface the application to the underlying hardware through an high level API. Normal Device Drivers are split in two layers:

- High Level Device Driver (HLD). This layer contains the definitions of the driver's APIs and the platform independent part of the driver.
  - An HLD is composed by two files:
    - <driver>.c, the HLD implementation file. This file must be included in the Makefile in order to use the driver.
    - <driver>.h, the HLD header file. This file is implicitly included by the HAL header file hal.h.
- Low Level Device Driver (LLD). This layer contains the platform dependent part of the driver.
   A LLD is composed by two files:
  - <driver>\_lld.c, the LLD implementation file. This file must be included in the Makefile in order to use the driver.
  - <driver>\_1ld.h, the LLD header file. This file is implicitly included by the HLD header file.

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#### 7.17.3.1 Diagram



## 7.17.4 HAL Complex Device Drivers

It is a class of device drivers that offer an high level API but do not use the hardware directly. Complex device drivers use other drivers for accessing the machine resources.

# 7.17.5 HAL Interfaces

An interface is a binary structure allowing the access to a service using virtual functions. This allows to create drivers that can be accessed using a common interface. The concept of interface is commonly found in object-oriented languages like Java or C++, their meaning in ChibiOS/HAL is exactly the same.

# 7.17.6 HAL Inner Code

Some modules are shared among multiple device drivers and are not necessarily meant to be used by the application layer.

## **Modules**

- Configuration
  - HAL Configuration.
- Normal Drivers

HAL Normal Drivers.

• Complex Drivers

HAL Complex Drivers.

• Interfaces

HAL Interfaces.

• Inner Code

HAL Inner Code.

• Support Code

HAL Support Code.

• OSAL

Operating System Abstraction Layer.

7.18 Configuration 203

# 7.18 Configuration

HAL Configuration.

## 7.18.1 Detailed Description

#### HAL Configuration.

The file halconf.h contains the high level settings for all the drivers supported by the HAL. The low level, platform dependent, settings are contained in the mcuconf.h file instead and are describe in the various platforms reference manuals.

#### **Drivers enable switches**

• #define HAL\_USE\_PAL TRUE

Enables the PAL subsystem.

• #define HAL\_USE\_ADC TRUE

Enables the ADC subsystem.

• #define HAL\_USE\_CAN TRUE

Enables the CAN subsystem.

• #define HAL\_USE\_DAC FALSE

Enables the DAC subsystem.

• #define HAL\_USE\_EXT TRUE

Enables the EXT subsystem.

#define HAL\_USE\_GPT TRUE

Enables the GPT subsystem.

• #define HAL\_USE\_I2C TRUE

Enables the I2C subsystem.

• #define HAL\_USE\_I2S TRUE

Enables the I2S subsystem.

• #define HAL\_USE\_ICU TRUE

Enables the ICU subsystem.

#define HAL\_USE\_MAC TRUE

Enables the MAC subsystem.

• #define HAL\_USE\_MMC\_SPI TRUE

Enables the MMC\_SPI subsystem.

#define HAL\_USE\_PWM TRUE

Enables the PWM subsystem.

• #define HAL USE RTC TRUE

Enables the RTC subsystem.

#define HAL\_USE\_SDC TRUE

Enables the SDC subsystem.

#define HAL\_USE\_SERIAL TRUE

Enables the SERIAL subsystem.

• #define HAL USE SERIAL USB TRUE

Enables the SERIAL over USB subsystem.

#define HAL\_USE\_SPI TRUE

Enables the SPI subsystem.

• #define HAL\_USE\_UART TRUE

Enables the UART subsystem.

#define HAL\_USE\_USB TRUE

Enables the USB subsystem.

• #define HAL\_USE\_WDG TRUE

Enables the WDG subsystem.

# ADC driver related setting

• #define ADC USE WAIT TRUE

Enables synchronous APIs.

• #define ADC USE MUTUAL EXCLUSION TRUE

Enables the adcAcquireBus() and adcReleaseBus() APIs.

## **CAN** driver related setting

• #define CAN\_USE\_SLEEP\_MODE TRUE

Sleep mode related APIs inclusion switch.

# I2C driver related setting

• #define I2C USE MUTUAL EXCLUSION TRUE

Enables the mutual exclusion APIs on the I2C bus.

## MAC driver related setting

#define MAC\_USE\_ZERO\_COPY TRUE

Enables an event sources for incoming packets.

• #define MAC USE EVENTS TRUE

Enables an event sources for incoming packets.

# MMC\_SPI driver related setting

• #define MMC\_NICE\_WAITING TRUE

Delays insertions.

# SDC driver related setting

• #define SDC\_INIT\_RETRY 100

Number of initialization attempts before rejecting the card.

• #define SDC\_MMC\_SUPPORT TRUE

Include support for MMC cards.

• #define SDC\_NICE\_WAITING TRUE

Delays insertions.

## SERIAL driver related setting

#define SERIAL\_DEFAULT\_BITRATE 38400

Default bit rate.

• #define SERIAL\_BUFFERS\_SIZE 16

Serial buffers size.

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## SERIAL\_USB driver related setting

• #define SERIAL\_USB\_BUFFERS\_SIZE 256

Serial over USB buffers size.

#define SERIAL\_USB\_BUFFERS\_NUMBER 2

Serial over USB number of buffers.

# SPI driver related setting

• #define SPI\_USE\_WAIT TRUE

Enables synchronous APIs.

• #define SPI\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the spiAcquireBus() and spiReleaseBus() APIs.

## **UART** driver related setting

• #define UART USE WAIT TRUE

Enables synchronous APIs.

• #define UART\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the uartAcquireBus () and uartReleaseBus () APIs.

# **USB** driver related setting

#define USB\_USE\_WAIT TRUE

Enables synchronous APIs.

# 7.18.2 Macro Definition Documentation

7.18.2.1 #define HAL\_USE\_PAL TRUE

Enables the PAL subsystem.

7.18.2.2 #define HAL\_USE\_ADC TRUE

Enables the ADC subsystem.

7.18.2.3 #define HAL\_USE\_CAN TRUE

Enables the CAN subsystem.

7.18.2.4 #define HAL\_USE\_DAC FALSE

Enables the DAC subsystem.

7.18.2.5 #define HAL\_USE\_EXT TRUE

Enables the EXT subsystem.

7.18.2.6 #define HAL\_USE\_GPT TRUE

Enables the GPT subsystem.

7.18.2.7 #define HAL\_USE\_I2C TRUE

Enables the I2C subsystem.

7.18.2.8 #define HAL\_USE\_I2S TRUE

Enables the I2S subsystem.

7.18.2.9 #define HAL\_USE\_ICU TRUE

Enables the ICU subsystem.

7.18.2.10 #define HAL\_USE\_MAC TRUE

Enables the MAC subsystem.

7.18.2.11 #define HAL\_USE\_MMC\_SPI TRUE

Enables the MMC\_SPI subsystem.

7.18.2.12 #define HAL\_USE\_PWM TRUE

Enables the PWM subsystem.

7.18.2.13 #define HAL\_USE\_RTC TRUE

Enables the RTC subsystem.

7.18.2.14 #define HAL\_USE\_SDC TRUE

Enables the SDC subsystem.

7.18.2.15 #define HAL\_USE\_SERIAL TRUE

Enables the SERIAL subsystem.

7.18.2.16 #define HAL\_USE\_SERIAL\_USB TRUE

Enables the SERIAL over USB subsystem.

7.18.2.17 #define HAL\_USE\_SPI TRUE

Enables the SPI subsystem.

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7.18.2.18 #define HAL\_USE\_UART TRUE

Enables the UART subsystem.

7.18.2.19 #define HAL\_USE\_USB TRUE

Enables the USB subsystem.

7.18.2.20 #define HAL\_USE\_WDG TRUE

Enables the WDG subsystem.

7.18.2.21 #define ADC\_USE\_WAIT TRUE

Enables synchronous APIs.

Note

Disabling this option saves both code and data space.

7.18.2.22 #define ADC\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the adcAcquireBus () and adcReleaseBus () APIs.

Note

Disabling this option saves both code and data space.

7.18.2.23 #define CAN\_USE\_SLEEP\_MODE TRUE

Sleep mode related APIs inclusion switch.

7.18.2.24 #define I2C\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the mutual exclusion APIs on the I2C bus.

7.18.2.25 #define MAC\_USE\_ZERO\_COPY TRUE

Enables an event sources for incoming packets.

7.18.2.26 #define MAC\_USE\_EVENTS TRUE

Enables an event sources for incoming packets.

7.18.2.27 #define MMC\_NICE\_WAITING TRUE

Delays insertions.

If enabled this options inserts delays into the MMC waiting routines releasing some extra CPU time for the threads with lower priority, this may slow down the driver a bit however. This option is recommended also if the SPI driver does not use a DMA channel and heavily loads the CPU.

7.18.2.28 #define SDC\_INIT\_RETRY 100

Number of initialization attempts before rejecting the card.

Note

Attempts are performed at 10mS intervals.

7.18.2.29 #define SDC\_MMC\_SUPPORT TRUE

Include support for MMC cards.

Note

MMC support is not yet implemented so this option must be kept at FALSE.

7.18.2.30 #define SDC\_NICE\_WAITING TRUE

Delays insertions.

If enabled this options inserts delays into the MMC waiting routines releasing some extra CPU time for the threads with lower priority, this may slow down the driver a bit however.

7.18.2.31 #define SERIAL DEFAULT BITRATE 38400

Default bit rate.

Configuration parameter, this is the baud rate selected for the default configuration.

7.18.2.32 #define SERIAL\_BUFFERS\_SIZE 16

Serial buffers size.

Configuration parameter, you can change the depth of the queue buffers depending on the requirements of your application.

Note

The default is 16 bytes for both the transmission and receive buffers.

7.18.2.33 #define SERIAL\_USB\_BUFFERS\_SIZE 256

Serial over USB buffers size.

Configuration parameter, the buffer size must be a multiple of the USB data endpoint maximum packet size.

Note

The default is 256 bytes for both the transmission and receive buffers.

7.18.2.34 #define SERIAL\_USB\_BUFFERS\_NUMBER 2

Serial over USB number of buffers.

Note

The default is 2 buffers.

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7.18.2.35 #define SPI\_USE\_WAIT TRUE

Enables synchronous APIs.

Note

Disabling this option saves both code and data space.

7.18.2.36 #define SPI\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the spiAcquireBus () and spiReleaseBus () APIs.

Note

Disabling this option saves both code and data space.

7.18.2.37 #define UART\_USE\_WAIT TRUE

Enables synchronous APIs.

Note

Disabling this option saves both code and data space.

7.18.2.38 #define UART\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the uartAcquireBus() and uartReleaseBus() APIs.

Note

Disabling this option saves both code and data space.

7.18.2.39 #define USB\_USE\_WAIT TRUE

Enables synchronous APIs.

Note

Disabling this option saves both code and data space.

# 7.19 Normal Drivers

HAL Normal Drivers.

# 7.19.1 Detailed Description

HAL Normal Drivers.

#### Modules

ADC Driver

Generic ADC Driver.

CAN Driver

Generic CAN Driver.

• DAC Driver

Generic DAC Driver.

EXT Driver

Generic EXT Driver.

• GPT Driver

Generic GPT Driver.

· HAL Driver

Hardware Abstraction Layer.

• I2C Driver

Generic I2C Driver.

• I2S Driver

Generic I2S Driver.

ICU Driver

Generic ICU Driver.

• MAC Driver

Generic MAC Driver.

• PAL Driver

I/O Ports Abstraction Layer.

• PWM Driver

Generic PWM Driver.

• RTC Driver

Generic RTC Driver.

SDC Driver

Generic SD Card Driver.

Serial Driver

Generic Serial Driver.

· SPI Driver

Generic SPI Driver.

• ST Driver

Generic System Tick Driver.

UART Driver

Generic UART Driver.

• USB Driver

Generic USB Driver.

· WDG Driver

Generic WDG Driver.

7.20 Complex Drivers 211

# 7.20 Complex Drivers

HAL Complex Drivers.

# 7.20.1 Detailed Description

HAL Complex Drivers.

# Modules

• MMC over SPI Driver

Generic MMC driver.

• Serial over USB Driver

Serial over USB Driver.

# 7.21 Interfaces

HAL Interfaces.

# 7.21.1 Detailed Description

HAL Interfaces.

# Modules

- Abstract I/O Channel
- Abstract Files
- Abstract I/O Block Device
- Abstract Streams

7.22 Inner Code 213

# 7.22 Inner Code

HAL Inner Code.

# 7.22.1 Detailed Description

HAL Inner Code.

# **Modules**

- I/O Buffers Queues
- I/O Bytes Queues
- MMC/SD Block Device

# 7.23 Support Code

HAL Support Code.

# 7.23.1 Detailed Description

HAL Support Code.

# Modules

• MII/RMII Header

MII/RMII Support Header.

• USB CDC Header

USB CDC Support Header.

7.24 OSAL 215

## 7.24 **OSAL**

Operating System Abstraction Layer.

Operating System Abstraction Layer.

#### The OSAL

The OSAL is the link between ChibiOS/HAL and services provided by operating systems like:

- · Critical Zones handling.
- · Interrupts handling.
- · Runtime Errors management.
- · Inter-task synchronization.
- · Task-ISR synchronization.
- · Time management.
- · Events.

ChibiOS/HAL is designed to tightly integrate with the underlying RTOS in order to provide the best experience to developers and minimize integration issues.

This section describes the API that OSALs are expected to expose to the HAL.

#### **RTOS Requirements**

The OSAL API closely resembles the ChibiOS/RT API, for obvious reasons, however an OSAL module can be implemented for any reasonably complete RTOS or even a RTOS-less bare metal machine, if required. In order to be able to support an HAL an RTOS should support the following minimal set of features:

- · Task-level critical zones API.
- ISR-level critical zones API, only required on those CPU architectures supporting preemptable ISRs like Cortex-Mx cores.
- Ability to invoke API functions from inside a task critical zone. Functions that are required to support this feature are marked with an "I" or "S" letter at the end of the name.
- Ability to invoke API functions from inside an ISR critical zone. Functions that are required to support this feature are marked with an "I" letter at the end of the name.
- Tasks Queues or Counting Semaphores with Timeout capability.
- Ability to suspend a task and wakeup it from ISR with Timeout capability.
- Event flags, the mechanism can be simulated using callbacks in case the RTOS does not support it.
- · Mutual Exclusion mechanism like Semaphores or Mutexes.

All the above requirements can be satisfied even on naked HW with a very think SW layer. In case that the HAL is required to work without an RTOS.

# **Supported RTOSes**

The RTOSes supported out of the box are:

- ChibiOS/RT
- ChibiOS/NIL

Implementations have also been successfully created on RTOSes not belonging to the ChibiOS products family but are not supported as a core feature of ChibiOS/HAL.

7.25 MII/RMII Header 217

## 7.25 MII/RMII Header

MII/RMII Support Header.

## 7.25.1 Detailed Description

MII/RMII Support Header.

This header contains definitions and types related to MII/RMII.

# **Generic MII registers**

- #define MII BMCR 0x00
- #define MII\_BMSR 0x01
- #define MII\_PHYSID1 0x02
- #define MII\_PHYSID2 0x03
- #define MII\_ADVERTISE 0x04
- #define MII\_LPA 0x05
- #define MII EXPANSION 0x06
- #define MII\_ANNPTR 0x07
- #define MII\_CTRL1000 0x09
- #define MII STAT1000 0x0a
- #define MII ESTATUS 0x0f
- #define MII PHYSTS 0x10
- #define MII\_MICR 0x11
- #define MII DCOUNTER 0x12
- #define MII\_FCSCOUNTER 0x13
- #define MII\_NWAYTEST 0x14
- #define MII RERRCOUNTER 0x15
- #define MII\_SREVISION 0x16
- #define MII\_RESV1 0x17
- #define MII\_LBRERROR 0x18
- #define MII\_PHYADDR 0x19
- #define MII\_RESV2 0x1a
- #define MII\_TPISTATUS 0x1b
- #define MII\_NCONFIG 0x1c

## Basic mode control register

- #define BMCR\_RESV 0x007f
- #define BMCR\_CTST 0x0080
- #define BMCR\_FULLDPLX 0x0100
- #define BMCR\_ANRESTART 0x0200
- #define BMCR\_ISOLATE 0x0400
- #define BMCR\_PDOWN 0x0800
- #define BMCR\_ANENABLE 0x1000
- #define BMCR\_SPEED100 0x2000
- #define BMCR\_LOOPBACK 0x4000
- #define BMCR\_RESET 0x8000

## Basic mode status register

- #define BMSR\_ERCAP 0x0001
- #define BMSR\_JCD 0x0002
- #define BMSR LSTATUS 0x0004
- #define BMSR ANEGCAPABLE 0x0008
- #define BMSR RFAULT 0x0010
- #define BMSR ANEGCOMPLETE 0x0020
- #define BMSR MFPRESUPPCAP 0x0040
- #define BMSR RESV 0x0780
- #define BMSR\_10HALF 0x0800
- #define BMSR 10FULL 0x1000
- #define BMSR 100HALF 0x2000
- #define BMSR 100FULL 0x4000
- #define BMSR 100BASE4 0x8000

#### Advertisement control register

- #define ADVERTISE\_SLCT 0x001f
- #define ADVERTISE\_CSMA 0x0001
- #define ADVERTISE 10HALF 0x0020
- #define ADVERTISE\_10FULL 0x0040
- #define ADVERTISE 100HALF 0x0080
- #define ADVERTISE 100FULL 0x0100
- #define ADVERTISE\_100BASE4 0x0200
- #define ADVERTISE\_PAUSE\_CAP 0x0400
- #define ADVERTISE\_PAUSE\_ASYM 0x0800
- #define ADVERTISE RESV 0x1000
- #define ADVERTISE\_RFAULT 0x2000
- #define ADVERTISE\_LPACK 0x4000
- #define ADVERTISE\_NPAGE 0x8000
- #define ADVERTISE\_FULL
- #define ADVERTISE\_ALL

## Link partner ability register

- #define LPA SLCT 0x001f
- #define LPA 10HALF 0x0020
- #define LPA 10FULL 0x0040
- #define LPA\_100HALF 0x0080
- #define LPA 100FULL 0x0100
- #define LPA 100BASE4 0x0200
- #define LPA\_PAUSE\_CAP 0x0400
- #define LPA PAUSE ASYM 0x0800
- #define LPA RESV 0x1000
- #define LPA\_RFAULT 0x2000
- #define LPA\_LPACK 0x4000
- #define LPA NPAGE 0x8000
- #define LPA\_DUPLEX (LPA\_10FULL | LPA\_100FULL)
- #define LPA\_100 (LPA\_100FULL | LPA\_100HALF | LPA\_100BASE4)

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## **Expansion register for auto-negotiation**

- #define EXPANSION NWAY 0x0001
- #define EXPANSION\_LCWP 0x0002
- #define EXPANSION\_ENABLENPAGE 0x0004
- #define EXPANSION\_NPCAPABLE 0x0008
- #define EXPANSION\_MFAULTS 0x0010
- #define EXPANSION RESV 0xffe0

## N-way test register

- #define NWAYTEST\_RESV1 0x00ff
- #define NWAYTEST LOOPBACK 0x0100
- #define NWAYTEST\_RESV2 0xfe00

#### **PHY** identifiers

- #define MII\_DM9161\_ID 0x0181b8a0
- #define MII\_AM79C875\_ID 0x00225540
- #define MII\_KS8721\_ID 0x00221610
- #define MII\_STE101P\_ID 0x00061C50
- #define MII\_DP83848I\_ID 0x20005C90
- #define MII LAN8710A ID 0x0007C0F1
- #define MII\_LAN8720\_ID 0x0007C0F0
- #define MII\_LAN8742A\_ID 0x0007C130

## 7.25.2 Macro Definition Documentation

7.25.2.1 #define MII\_BMCR 0x00

Basic mode control register.

7.25.2.2 #define MII\_BMSR 0x01

Basic mode status register.

7.25.2.3 #define MII\_PHYSID1 0x02

PHYS ID 1.

7.25.2.4 #define MII\_PHYSID2 0x03

PHYS ID 2.

7.25.2.5 #define MII\_ADVERTISE 0x04

Advertisement control reg.

7.25.2.6 #define MII\_LPA 0x05

Link partner ability reg.

7.25.2.7 #define MII\_EXPANSION 0x06

Expansion register.

7.25.2.8 #define MII\_ANNPTR 0x07

1000BASE-T control.

7.25.2.9 #define MII\_CTRL1000 0x09

1000BASE-T control.

7.25.2.10 #define MII\_STAT1000 0x0a

1000BASE-T status.

7.25.2.11 #define MII\_ESTATUS 0x0f

Extended Status.

7.25.2.12 #define MII\_PHYSTS 0x10

PHY Status register.

7.25.2.13 #define MII\_MICR 0x11

MII Interrupt ctrl register.

7.25.2.14 #define MII\_DCOUNTER 0x12

Disconnect counter.

7.25.2.15 #define MII\_FCSCOUNTER 0x13

False carrier counter.

7.25.2.16 #define MII\_NWAYTEST 0x14

N-way auto-neg test reg.

7.25.2.17 #define MII\_RERRCOUNTER 0x15

Receive error counter.

7.25.2.18 #define MII\_SREVISION 0x16

Silicon revision.

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7.25.2.19 #define MII\_RESV1 0x17
Reserved.

7.25.2.20 #define MII\_LBRERROR 0x18

Lpback, rx, bypass error.

7.25.2.21 #define MII\_PHYADDR 0x19

PHY address.

7.25.2.22 #define MII\_RESV2 0x1a

Reserved.

7.25.2.23 #define MII\_TPISTATUS 0x1b

TPI status for 10Mbps.

7.25.2.24 #define MII\_NCONFIG 0x1c

Network interface config.

7.25.2.25 #define BMCR\_RESV 0x007f

Unused.

7.25.2.26 #define BMCR\_CTST 0x0080

Collision test.

7.25.2.27 #define BMCR\_FULLDPLX 0x0100

Full duplex.

7.25.2.28 #define BMCR\_ANRESTART 0x0200

Auto negotiation restart.

7.25.2.29 #define BMCR\_ISOLATE 0x0400

Disconnect DP83840 from MII.

7.25.2.30 #define BMCR\_PDOWN 0x0800

Powerdown.

ChibiOS/HAL

7.25.2.31 #define BMCR\_ANENABLE 0x1000

Enable auto negotiation.

7.25.2.32 #define BMCR\_SPEED100 0x2000

Select 100Mbps.

7.25.2.33 #define BMCR\_LOOPBACK 0x4000

TXD loopback bit.

7.25.2.34 #define BMCR\_RESET 0x8000

Reset.

7.25.2.35 #define BMSR\_ERCAP 0x0001

Ext-reg capability.

7.25.2.36 #define BMSR\_JCD 0x0002

Jabber detected.

7.25.2.37 #define BMSR\_LSTATUS 0x0004

Link status.

7.25.2.38 #define BMSR\_ANEGCAPABLE 0x0008

Able to do auto-negotiation.

7.25.2.39 #define BMSR\_RFAULT 0x0010

Remote fault detected.

7.25.2.40 #define BMSR\_ANEGCOMPLETE 0x0020

Auto-negotiation complete.

7.25.2.41 #define BMSR\_MFPRESUPPCAP 0x0040

Able to suppress preamble.

7.25.2.42 #define BMSR\_RESV 0x0780

Unused.

7.25 MII/RMII Header 223

7.25.2.43 #define BMSR\_10HALF 0x0800

Can do 10mbps, half-duplex.

7.25.2.44 #define BMSR\_10FULL 0x1000

Can do 10mbps, full-duplex.

7.25.2.45 #define BMSR\_100HALF 0x2000

Can do 100mbps, half-duplex.

7.25.2.46 #define BMSR\_100FULL 0x4000

Can do 100mbps, full-duplex.

7.25.2.47 #define BMSR\_100BASE4 0x8000

Can do 100mbps, 4k packets.

7.25.2.48 #define ADVERTISE\_SLCT 0x001f

Selector bits.

7.25.2.49 #define ADVERTISE\_CSMA 0x0001

Only selector supported.

7.25.2.50 #define ADVERTISE\_10HALF 0x0020

Try for 10mbps half-duplex.

7.25.2.51 #define ADVERTISE\_10FULL 0x0040

Try for 10mbps full-duplex.

7.25.2.52 #define ADVERTISE\_100HALF 0x0080

Try for 100mbps half-duplex.

7.25.2.53 #define ADVERTISE\_100FULL 0x0100

Try for 100mbps full-duplex.

7.25.2.54 #define ADVERTISE\_100BASE4 0x0200

Try for 100mbps 4k packets.

7.25.2.55 #define ADVERTISE\_PAUSE\_CAP 0x0400

Try for pause.

7.25.2.56 #define ADVERTISE\_PAUSE\_ASYM 0x0800

Try for asymetric pause.

7.25.2.57 #define ADVERTISE\_RESV 0x1000

Unused.

7.25.2.58 #define ADVERTISE\_RFAULT 0x2000

Say we can detect faults.

7.25.2.59 #define ADVERTISE\_LPACK 0x4000

Ack link partners response.

7.25.2.60 #define ADVERTISE\_NPAGE 0x8000

Next page bit.

7.25.2.61 #define LPA\_SLCT 0x001f

Same as advertise selector.

7.25.2.62 #define LPA\_10HALF 0x0020

Can do 10mbps half-duplex.

7.25.2.63 #define LPA\_10FULL 0x0040

Can do 10mbps full-duplex.

7.25.2.64 #define LPA\_100HALF 0x0080

Can do 100mbps half-duplex.

7.25.2.65 #define LPA\_100FULL 0x0100

Can do 100mbps full-duplex.

7.25.2.66 #define LPA\_100BASE4 0x0200

Can do 100mbps 4k packets.

7.25 MII/RMII Header 225

7.25.2.67 #define LPA\_PAUSE\_CAP 0x0400

Can pause.

7.25.2.68 #define LPA\_PAUSE\_ASYM 0x0800

Can pause asymetrically.

7.25.2.69 #define LPA\_RESV 0x1000

Unused.

7.25.2.70 #define LPA\_RFAULT 0x2000

Link partner faulted.

7.25.2.71 #define LPA\_LPACK 0x4000

Link partner acked us.

7.25.2.72 #define LPA\_NPAGE 0x8000

Next page bit.

7.25.2.73 #define EXPANSION\_NWAY 0x0001

Can do N-way auto-nego.

7.25.2.74 #define EXPANSION\_LCWP 0x0002

Got new RX page code word.

7.25.2.75 #define EXPANSION\_ENABLENPAGE 0x0004

This enables npage words.

7.25.2.76 #define EXPANSION\_NPCAPABLE 0x0008

Link partner supports npage.

7.25.2.77 #define EXPANSION\_MFAULTS 0x0010

Multiple faults detected.

7.25.2.78 #define EXPANSION\_RESV 0xffe0

Unused.

ChibiOS/HAL

7.25.2.79 #define NWAYTEST\_RESV1 0x00ff

Unused.

7.25.2.80 #define NWAYTEST\_LOOPBACK 0x0100

Enable loopback for N-way.

7.25.2.81 #define NWAYTEST\_RESV2 0xfe00

Unused.

7.26 MMC over SPI Driver 227

#### 7.26 MMC over SPI Driver

Generic MMC driver.

## 7.26.1 Detailed Description

Generic MMC driver.

This module implements a portable MMC/SD driver that uses a SPI driver as physical layer. Hot plugging and removal are supported through kernel events.

#### Precondition

In order to use the MMC\_SPI driver the  ${\tt HAL\_USE\_MMC\_SPI}$  and  ${\tt HAL\_USE\_SPI}$  options must be enabled in  ${\tt halconf.h.}$ 

#### 7.26.2 Driver State Machine

This driver implements a state machine internally, see the Abstract I/O Block Device module documentation for details

# 7.26.3 Driver Operations

This driver allows to read or write single or multiple 512 bytes blocks on a SD Card.

#### **Macros**

#define \_mmc\_driver\_methods \_mmcsd\_block\_device\_methods
 MMCDriver specific methods.

## MMC\_SPI configuration options

#define MMC\_NICE\_WAITING TRUE
 Delays insertions.

# **Macro Functions**

• #define mmclsCardInserted(mmcp) mmc\_lld\_is\_card\_inserted(mmcp)

Returns the card insertion status.

• #define mmclsWriteProtected(mmcp) mmc\_lld\_is\_write\_protected(mmcp)

Returns the write protect status.

#### **Data Structures**

struct MMCConfig

MMC/SD over SPI driver configuration structure.

struct MMCDriverVMT

MMCDriver virtual methods table.

struct MMCDriver

Structure representing a MMC/SD over SPI driver.

#### **Functions**

• static uint8\_t crc7 (uint8\_t crc, const uint8\_t \*buffer, size\_t len)

Calculate the MMC standard CRC-7 based on a lookup table.

static void wait (MMCDriver \*mmcp)

Waits an idle condition.

• static void send\_hdr (MMCDriver \*mmcp, uint8\_t cmd, uint32\_t arg)

Sends a command header.

static uint8\_t recvr1 (MMCDriver \*mmcp)

Receives a single byte response.

• static uint8\_t recvr3 (MMCDriver \*mmcp, uint8\_t \*buffer)

Receives a three byte response.

• static uint8\_t send\_command\_R1 (MMCDriver \*mmcp, uint8\_t cmd, uint32\_t arg)

Sends a command an returns a single byte response.

static uint8 t send command R3 (MMCDriver \*mmcp, uint8 t cmd, uint32 t arg, uint8 t \*response)

Sends a command which returns a five bytes response (R3).

• static bool read\_CxD (MMCDriver \*mmcp, uint8\_t cmd, uint32\_t cxd[4])

Reads the CSD.

static void sync (MMCDriver \*mmcp)

Waits that the card reaches an idle state.

· void mmcInit (void)

MMC over SPI driver initialization.

void mmcObjectInit (MMCDriver \*mmcp)

Initializes an instance.

void mmcStart (MMCDriver \*mmcp, const MMCConfig \*config)

Configures and activates the MMC peripheral.

void mmcStop (MMCDriver \*mmcp)

Disables the MMC peripheral.

• bool mmcConnect (MMCDriver \*mmcp)

Performs the initialization procedure on the inserted card.

• bool mmcDisconnect (MMCDriver \*mmcp)

Brings the driver in a state safe for card removal.

bool mmcStartSequentialRead (MMCDriver \*mmcp, uint32\_t startblk)

Starts a sequential read.

• bool mmcSequentialRead (MMCDriver \*mmcp, uint8\_t \*buffer)

Reads a block within a sequential read operation.

bool mmcStopSequentialRead (MMCDriver \*mmcp)

Stops a sequential read gracefully.

bool mmcStartSequentialWrite (MMCDriver \*mmcp, uint32\_t startblk)

Starts a sequential write.

• bool mmcSequentialWrite (MMCDriver \*mmcp, const uint8\_t \*buffer)

Writes a block within a sequential write operation.

bool mmcStopSequentialWrite (MMCDriver \*mmcp)

Stops a sequential write gracefully.

• bool mmcSync (MMCDriver \*mmcp)

Waits for card idle condition.

• bool mmcGetInfo (MMCDriver \*mmcp, BlockDeviceInfo \*bdip)

Returns the media info.

bool mmcErase (MMCDriver \*mmcp, uint32\_t startblk, uint32\_t endblk)

Erases blocks.

7.26 MMC over SPI Driver 229

## **Variables**

static const struct MMCDriverVMT mmc\_vmt

Virtual methods table.

static const uint8 t crc7 lookup table [256]

Lookup table for CRC-7 ( based on polynomial  $x^{\wedge}7 + x^{\wedge}3 + 1$ ).

#### 7.26.4 Macro Definition Documentation

7.26.4.1 #define MMC\_NICE\_WAITING TRUE

Delays insertions.

If enabled this options inserts delays into the MMC waiting routines releasing some extra CPU time for the threads with lower priority, this may slow down the driver a bit however. This option is recommended also if the SPI driver does not use a DMA channel and heavily loads the CPU.

7.26.4.2 #define \_mmc\_driver\_methods \_mmcsd\_block\_device\_methods

MMCDriver specific methods.

7.26.4.3 #define mmclsCardInserted( mmcp ) mmc\_lld\_is\_card\_inserted(mmcp)

Returns the card insertion status.

Note

This macro wraps a low level function named sdc\_lld\_is\_card\_inserted(), this function must be provided by the application because it is not part of the SDC driver.

#### **Parameters**

| in | ттср | pointer to the MMCDriver object |
|----|------|---------------------------------|

#### Returns

The card state.

#### Return values

| FALSE | card not inserted. |
|-------|--------------------|
| TRUE  | card inserted.     |

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.26.4.4 #define mmclsWriteProtected( mmcp ) mmc\_lld\_is\_write\_protected(mmcp)

Returns the write protect status.

#### **Parameters**

| ir | ттср | pointer to the MMCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### Returns

The card state.

## **Return values**

| FALSE | card not inserted. |
|-------|--------------------|
| TRUE  | card inserted.     |

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

# 7.26.5 Function Documentation

```
7.26.5.1 static uint8_t crc7 ( uint8_t crc, const uint8_t * buffer, size_t len ) [static]
```

Calculate the MMC standard CRC-7 based on a lookup table.

## **Parameters**

| in | crc    | start value for CRC    |
|----|--------|------------------------|
| in | buffer | pointer to data buffer |
| in | len    | length of data         |

#### Returns

Calculated CRC

**7.26.5.2 static void wait ( MMCDriver \* mmcp )** [static]

Waits an idle condition.

#### **Parameters**

| in | ттср | pointer to the MMCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

7.26 MMC over SPI Driver 231

Here is the call graph for this function:



7.26.5.3 static void send\_hdr ( MMCDriver \* mmcp, uint8\_t cmd, uint32\_t arg ) [static]

Sends a command header.

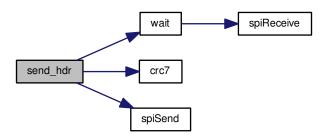
## **Parameters**

| in | ттср | pointer to the MMCDriver object |
|----|------|---------------------------------|
| in | cmd  | the command id                  |
| in | arg  | the command argument            |

# **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



**7.26.5.4 static uint8\_t recvr1 ( MMCDriver** \* *mmcp* ) [static]

Receives a single byte response.

## **Parameters**

| in | ттср | pointer to the MMCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### Returns

The response as an uint8\_t value.

## Return values

#### **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.26.5.5 static uint8\_t recvr3 ( MMCDriver \* mmcp, uint8\_t \* buffer ) [static]

Receives a three byte response.

#### **Parameters**

| in  | ттср   | pointer to the MMCDriver object   |
|-----|--------|-----------------------------------|
| out | buffer | pointer to four bytes wide buffer |

#### Returns

First response byte as an uint8\_t value.

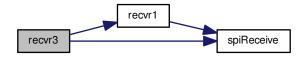
## Return values

## **Function Class:**

Not an API, this function is for internal use only.

7.26 MMC over SPI Driver 233

Here is the call graph for this function:



7.26.5.6 static uint8\_t send\_command\_R1 ( MMCDriver \* mmcp, uint8\_t cmd, uint32\_t arg ) [static]

Sends a command an returns a single byte response.

#### **Parameters**

| in | ттср | pointer to the MMCDriver object |
|----|------|---------------------------------|
| in | cmd  | the command id                  |
| in | arg  | the command argument            |

# Returns

The response as an uint8\_t value.

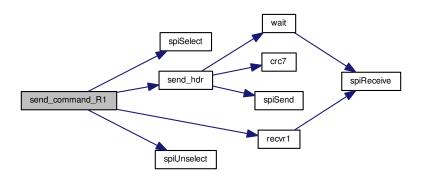
## Return values

| 0xFF | timed out. |
|------|------------|

#### **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.26.5.7 static uint8\_t send\_command\_R3 ( MMCDriver \* mmcp, uint8\_t cmd, uint32\_t arg, uint8\_t \* response ) [static]

Sends a command which returns a five bytes response (R3).

## **Parameters**

| in  | ттср     | pointer to the MMCDriver object           |
|-----|----------|---|
| in  | cmd      | the command id                            |
| in  | arg      | the command argument                      |
| out | response | pointer to four bytes wide uint8_t buffer |

## Returns

The first byte of the response (R1) as an uint8\_t value.

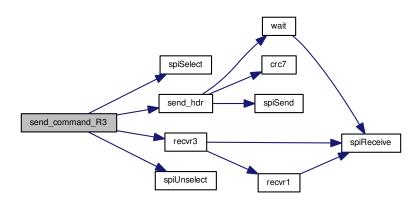
#### Return values

| 0xFF | timed out. |
|------|------------|
|------|------------|

#### **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.26.5.8 static bool read\_CxD ( MMCDriver \* mmcp, uint8\_t cmd, uint32\_t cxd[4] ) [static]

Reads the CSD.

## **Parameters**

| in  | ттср | pointer to the MMCDriver object |
|-----|------|---------------------------------|
| out | cmd  | command                         |
| out | cxd  | pointer to the CSD/CID buffer   |

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## Returns

The operation status.

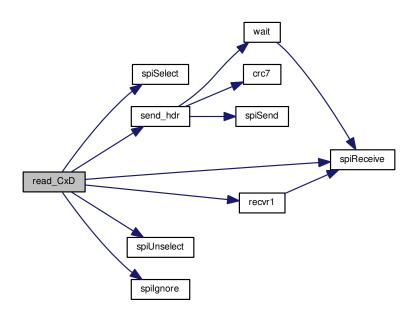
## Return values

| HAL_SUCCESS | the operation succeeded. |
|-------------|--------------------------|
| HAL_FAILED  | the operation failed.    |

## **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



**7.26.5.9 static void sync ( MMCDriver \* mmcp )** [static]

Waits that the card reaches an idle state.

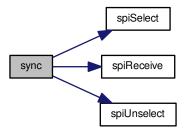
# Parameters

| in | ттср | pointer to the MMCDriver object |
|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.26.5.10 void mmcInit ( void )

MMC over SPI driver initialization.

Note

This function is implicitly invoked by halInit(), there is no need to explicitly initialize the driver.

#### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

7.26.5.11 void mmcObjectInit ( MMCDriver \* mmcp )

Initializes an instance.

#### **Parameters**

| out | ттср | pointer to the MMCDriver object |
|-----|------|---------------------------------|
|-----|------|---------------------------------|

## **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

7.26.5.12 void mmcStart ( MMCDriver \* mmcp, const MMCConfig \* config )

Configures and activates the MMC peripheral.

# Parameters

| in | ттср   | pointer to the MMCDriver object  |
|----|--------|----------------------------------|
| in | config | pointer to the MMCConfig object. |

7.26 MMC over SPI Driver 237

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

# 7.26.5.13 void mmcStop ( MMCDriver \* mmcp )

Disables the MMC peripheral.

#### **Parameters**

| in | ттср | pointer to the MMCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.26.5.14 bool mmcConnect ( MMCDriver \* mmcp )

Performs the initialization procedure on the inserted card.

This function should be invoked when a card is inserted and brings the driver in the  $\texttt{MMC}_{READY}$  state where it is possible to perform read and write operations.

### Note

It is possible to invoke this function from the insertion event handler.

### **Parameters**

| in | ттср | pointer to the MMCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

# Returns

The operation status.

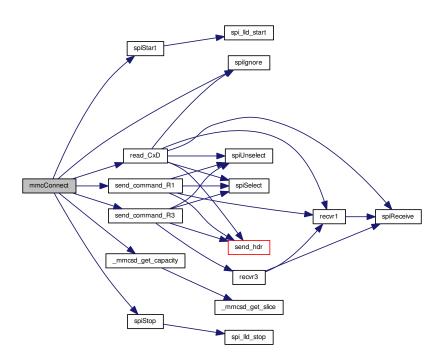
### **Return values**

| HAL_SUCCESS | the operation succeeded and the driver is now in the MMC_READY state. |
|-------------|---|
| HAL_FAILED  | the operation failed.   |

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.26.5.15 bool mmcDisconnect ( MMCDriver \* mmcp )

Brings the driver in a state safe for card removal.

# **Parameters**

| in | ттср | pointer to the MMCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

# Returns

The operation status.

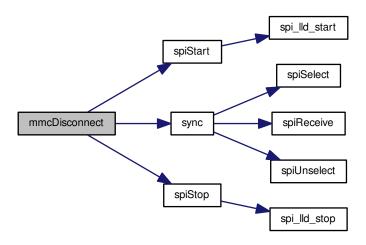
### Return values

| HAL_SUCCESS | the operation succeeded and the driver is now in the MMC_INSERTED state. |
|-------------|--|
| HAL_FAILED  | the operation failed.  |

# **Function Class:**

7.26 MMC over SPI Driver 239

Here is the call graph for this function:



# 7.26.5.16 bool mmcStartSequentialRead ( MMCDriver \* mmcp, uint32\_t startblk )

Starts a sequential read.

# **Parameters**

| in | ттср     | pointer to the MMCDriver object |
|----|----------|---------------------------------|
| in | startblk | first block to read             |

### Returns

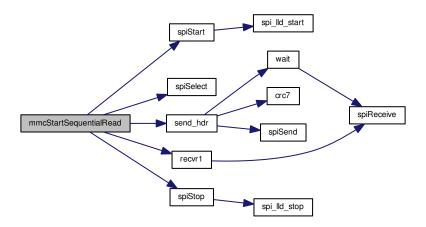
The operation status.

# Return values

| HAL_SUCCESS | the operation succeeded. |
|-------------|--------------------------|
| HAL_FAILED  | the operation failed.    |

# **Function Class:**

Here is the call graph for this function:



7.26.5.17 bool mmcSequentialRead (  $MMCDriver*{\it mmcp},\ uint8\_t*{\it buffer}$  )

Reads a block within a sequential read operation.

### **Parameters**

| l | in  | ттср   | pointer to the MMCDriver object |
|---|-----|--------|---------------------------------|
|   | out | buffer | pointer to the read buffer      |

# Returns

The operation status.

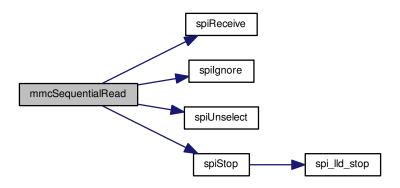
# Return values

| HAL_SUCCESS | the operation succeeded. |
|-------------|--------------------------|
| HAL_FAILED  | the operation failed.    |

# **Function Class:**

7.26 MMC over SPI Driver 241

Here is the call graph for this function:



# 7.26.5.18 bool mmcStopSequentialRead ( $MMCDriver*{\it mmcp}$ )

Stops a sequential read gracefully.

# **Parameters**

|  | in | ттср | pointer to the MMCDriver object |
|--|----|------|---------------------------------|
|--|----|------|---------------------------------|

# Returns

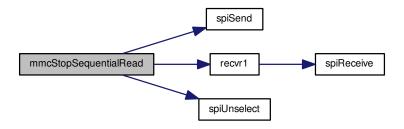
The operation status.

### **Return values**

| HAL_SUCCESS | the operation succeeded. |
|-------------|--------------------------|
| HAL_FAILED  | the operation failed.    |

# **Function Class:**

Here is the call graph for this function:



7.26.5.19 bool mmcStartSequentialWrite ( MMCDriver \* mmcp, uint32\_t startblk )

Starts a sequential write.

# **Parameters**

| in | ттср     | pointer to the MMCDriver object |
|----|----------|---------------------------------|
| in | startblk | first block to write            |

# Returns

The operation status.

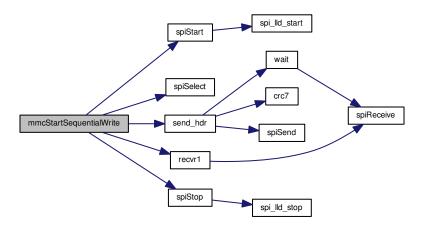
# Return values

| HAL_SUCCESS | the operation succeeded. |
|-------------|--------------------------|
| HAL_FAILED  | the operation failed.    |

# **Function Class:**

7.26 MMC over SPI Driver 243

Here is the call graph for this function:



7.26.5.20 bool mmcSequentialWrite (  $MMCDriver*{\it mmcp}, \ const \ uint8\_t*{\it buffer}$  )

Writes a block within a sequential write operation.

### **Parameters**

| in  | ттср   | pointer to the MMCDriver object |
|-----|--------|---------------------------------|
| out | buffer | pointer to the write buffer     |

# Returns

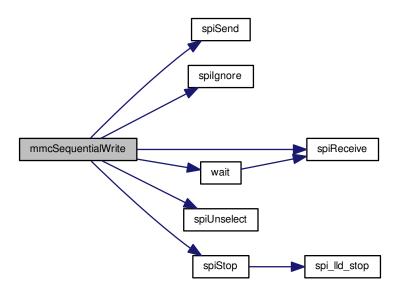
The operation status.

# Return values

| HAL_SUCCESS | the operation succeeded. |
|-------------|--------------------------|
| HAL_FAILED  | the operation failed.    |

# **Function Class:**

Here is the call graph for this function:



# 7.26.5.21 bool mmcStopSequentialWrite ( MMCDriver \* mmcp )

Stops a sequential write gracefully.

# **Parameters**

| in | ттср | pointer to the MMCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### Returns

The operation status.

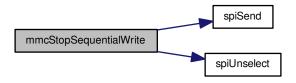
### Return values

| HAL_SUCCESS | the operation succeeded. |
|-------------|--------------------------|
| HAL_FAILED  | the operation failed.    |

# **Function Class:**

7.26 MMC over SPI Driver 245

Here is the call graph for this function:



7.26.5.22 bool mmcSync ( MMCDriver \* mmcp )

Waits for card idle condition.

# **Parameters**

| in | ттср | pointer to the MMCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

# Returns

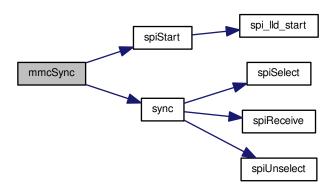
The operation status.

# Return values

| HAL_SUCCESS | the operation succeeded. |
|-------------|--------------------------|
| HAL_FAILED  | the operation failed.    |

# **Function Class:**

Here is the call graph for this function:



7.26.5.23 bool mmcGetInfo ( MMCDriver \* mmcp, BlockDeviceInfo \* bdip )

Returns the media info.

### **Parameters**

| in  | ттср | pointer to the MMCDriver object        |
|-----|------|--|
| out | bdip | pointer to a BlockDeviceInfo structure |

# Returns

The operation status.

# Return values

| HAL_SUCCESS | the operation succeeded. |
|-------------|--------------------------|
| HAL_FAILED  | the operation failed.    |

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.26.5.24 bool mmcErase ( MMCDriver \* mmcp, uint32\_t startblk, uint32\_t endblk )

Erases blocks.

# **Parameters**

| in | ттср     | pointer to the MMCDriver object |
|----|----------|---------------------------------|
| in | startblk | starting block number           |
| in | endblk   | ending block number             |

7.26 MMC over SPI Driver 247

#### Returns

The operation status.

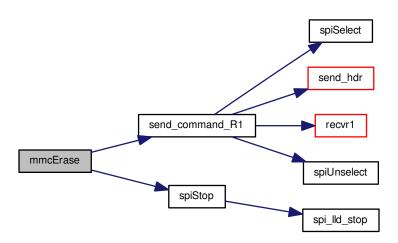
# Return values

| HAL_SUCCESS | the operation succeeded. |
|-------------|--------------------------|
| HAL_FAILED  | the operation failed.    |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.26.6 Variable Documentation

### **7.26.6.1 const struct MMCDriverVMT mmc\_vmt** [static]

# Initial value:

```
= {
  (bool (*) (void *))mmc_lld_is_card_inserted,
  (bool (*) (void *))mmc_lld_is_write_protected,
  (bool (*) (void *))mmcConnect,
  (bool (*) (void *))mmcDisconnect,
  mmc_read,
  mmc_write,
  (bool (*) (void *))mmcSync,
  (bool (*) (void *, BlockDeviceInfo *))mmcGetInfo
```

Virtual methods table.

7.26.6.2 const uint8\_t crc7\_lookup\_table[256] [static]

# Initial value:

```
0x00, 0x09, 0x12, 0x1b, 0x24, 0x2d, 0x36, 0x3f, 0x48, 0x41, 0x5a, 0x53,
0x6c, 0x65, 0x7e, 0x77, 0x19, 0x10, 0x0b, 0x02, 0x3d,
                                                      0x34, 0x2f,
0x51, 0x58, 0x43, 0x4a, 0x75, 0x7c,
                                    0x67, 0x6e, 0x32,
                                                       0x3b, 0x20,
                                                                   0x29,
0x16, 0x1f, 0x04, 0x0d, 0x7a, 0x73,
                                    0x68, 0x61, 0x5e,
                                                       0x57, 0x4c,
                                                                   0 \times 45.
0x2b, 0x22, 0x39, 0x30, 0x0f, 0x06,
                                    0x1d, 0x14, 0x63,
                                                       0x6a, 0x71,
                                                                   0x78.
                        0x64, 0x6d, 0x76, 0x7f, 0x40, 0x49, 0x52,
0x47, 0x4e, 0x55, 0x5c,
0x2c,
      0x25, 0x3e, 0x37,
                        0x08, 0x01,
                                    0x1a, 0x13,
                                                 0x7d,
                                                       0x74,
                                                             0x6f,
                                                 0x11,
0x59,
      0x50, 0x4b, 0x42,
                        0x35,
                              0x3c,
                                    0x27,
                                          0x2e,
                                                       0x18,
                                                             0x03,
0x56, 0x5f, 0x44, 0x4d, 0x72,
                              0x7b,
                                    0x60, 0x69, 0x1e,
                                                       0x17,
                                                             0x0c,
                                                                   0x05,
0x3a, 0x33, 0x28, 0x21, 0x4f, 0x46, 0x5d, 0x54, 0x6b, 0x62, 0x79,
                                                                   0x70,
0x07, 0x0e, 0x15, 0x1c, 0x23, 0x2a,
                                    0x31, 0x38, 0x41, 0x48, 0x53,
                                                                   0x5a,
0x65, 0x6c, 0x77, 0x7e,
                        0x09, 0x00,
                                    0x1b, 0x12, 0x2d,
                                                      0x24, 0x3f,
                                                                   0x36,
0x58, 0x51, 0x4a, 0x43, 0x7c, 0x75,
                                    0x6e, 0x67, 0x10, 0x19, 0x02,
0x34,
      0x3d, 0x26, 0x2f,
                        0x73, 0x7a,
                                    0x61, 0x68,
                                                 0x57,
                                                       0x5e,
                                                             0x45,
0x3b,
      0x32, 0x29, 0x20,
                        0x1f, 0x16,
                                    0x0d,
                                          0x04,
                                                 0x6a,
                                                       0x63,
                                                             0x78,
0x4e, 0x47, 0x5c, 0x55, 0x22, 0x2b,
                                    0x30, 0x39, 0x06, 0x0f, 0x14,
                                                                   0x1d.
0x25, 0x2c, 0x37, 0x3e, 0x01, 0x08, 0x13, 0x1a, 0x6d, 0x64, 0x7f, 0x76,
0x49, 0x40, 0x5b, 0x52, 0x3c, 0x35, 0x2e, 0x27, 0x18, 0x11, 0x0a, 0x03,
0x74, 0x7d, 0x66, 0x6f, 0x50, 0x59, 0x42, 0x4b, 0x17, 0x1e, 0x05, 0x0c,
0x33, 0x3a, 0x21, 0x28, 0x5f, 0x56, 0x4d, 0x44, 0x7b, 0x72, 0x69, 0x60,
0x0e, 0x07, 0x1c, 0x15,
                        0x2a, 0x23, 0x38, 0x31, 0x46, 0x4f, 0x54, 0x5d,
0x62, 0x6b, 0x70, 0x79
```

Lookup table for CRC-7 (based on polynomial  $x^7 + x^3 + 1$ ).

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# 7.27 MMC/SD Block Device

# 7.27.1 Detailed Description

This module implements a common ancestor for all device drivers accessing MMC or SD cards. This interface inherits the state machine and the interface from the Abstract I/O Block Device module.

#### **Macros**

• #define MMCSD BLOCK SIZE 512U

Fixed block size for MMC/SD block devices.

#define MMCSD R1 ERROR MASK 0xFDFFE008U

Mask of error bits in R1 responses.

#define MMCSD CMD8 PATTERN 0x000001AAU

Fixed pattern for CMD8.

• #define \_mmcsd\_block\_device\_methods \_base\_block device methods

MMCSDBlockDevice specific methods.

· #define mmcsd block device data

MMCSDBlockDevice specific data.

#### SD/MMC status conditions

- #define MMCSD\_STS\_IDLE 0U
- #define MMCSD STS READY 1U
- #define MMCSD\_STS\_IDENT 2U
- #define MMCSD STS STBY 3U
- #define MMCSD\_STS\_TRAN 4U
- #define MMCSD STS DATA 5U
- #define MMCSD\_STS\_RCV 6U
- #define MMCSD STS PRG 7U
- #define MMCSD\_STS\_DIS 8U

### SD/MMC commands

- #define MMCSD\_CMD\_GO\_IDLE\_STATE 0U
- #define MMCSD CMD INIT 1U
- #define MMCSD\_CMD\_ALL\_SEND\_CID 2U
- #define MMCSD CMD SEND RELATIVE ADDR 3U
- #define MMCSD CMD SET BUS WIDTH 6U
- #define MMCSD CMD SWITCH MMCSD CMD SET BUS WIDTH
- #define MMCSD CMD SEL DESEL CARD 7U
- #define MMCSD\_CMD\_SEND\_IF\_COND 8U
- #define MMCSD\_CMD\_SEND\_EXT\_CSD MMCSD\_CMD\_SEND\_IF\_COND
- #define MMCSD CMD SEND CSD 9U
- #define MMCSD\_CMD\_SEND\_CID 10U
- #define MMCSD\_CMD\_STOP\_TRANSMISSION 12U
- #define MMCSD\_CMD\_SEND\_STATUS 13U
- #define MMCSD\_CMD\_SET\_BLOCKLEN 16U
- #define MMCSD CMD READ SINGLE BLOCK 17U
- #define MMCSD\_CMD\_READ\_MULTIPLE\_BLOCK 18U
- #define MMCSD CMD SET BLOCK COUNT 23U
- #define MMCSD\_CMD\_WRITE\_BLOCK 24U

- #define MMCSD CMD WRITE MULTIPLE BLOCK 25U
- #define MMCSD CMD ERASE RW BLK START 32U
- #define MMCSD\_CMD\_ERASE\_RW\_BLK\_END 33U
- #define MMCSD CMD ERASE 38U
- #define MMCSD\_CMD\_APP\_OP\_COND 41U
- #define MMCSD CMD LOCK UNLOCK 42U
- #define MMCSD CMD APP CMD 55U
- #define MMCSD\_CMD\_READ\_OCR 58U

### **CSD** record offsets

- #define MMCSD\_CSD\_MMC\_CSD\_STRUCTURE\_SLICE 127U,126U
  - Slice position of values in CSD register.
- #define MMCSD CSD MMC SPEC VERS SLICE 125U,122U
- #define MMCSD\_CSD\_MMC\_TAAC\_SLICE 119U,112U
- #define MMCSD\_CSD\_MMC\_NSAC\_SLICE 111U,104U
- #define MMCSD CSD MMC TRAN SPEED SLICE 103U,96U
- #define MMCSD CSD MMC CCC SLICE 95U,84U
- #define MMCSD CSD MMC READ BL LEN SLICE 83U,80U
- #define MMCSD CSD MMC READ BL PARTIAL SLICE 79U,79U
- #define MMCSD\_CSD\_MMC\_WRITE\_BLK\_MISALIGN\_SLICE 78U,78U
- #define MMCSD\_CSD\_MMC\_READ\_BLK\_MISALIGN\_SLICE 77U,77U
- #define MMCSD CSD MMC DSR IMP SLICE 76U,76U
- #define MMCSD CSD MMC C SIZE SLICE 73U,62U
- #define MMCSD\_CSD\_MMC\_VDD\_R\_CURR\_MIN\_SLICE 61U,59U
- #define MMCSD CSD MMC VDD R CURR MAX SLICE 58U,56U
- #define MMCSD\_CSD\_MMC\_VDD\_W\_CURR\_MIN\_SLICE 55U,53U
- #define MMCSD\_CSD\_MMC\_VDD\_W\_CURR\_MAX\_SLICE 52U,50U
- #define MMCSD CSD MMC C SIZE MULT SLICE 49U,47U
- #define MMCSD CSD MMC ERASE GRP SIZE SLICE 46U,42U
- #define MMCSD CSD MMC ERASE GRP MULT SLICE 41U,37U
- #define MMCSD\_CSD\_MMC\_WP\_GRP\_SIZE\_SLICE 36U,32U
- #define MMCSD\_CSD\_MMC\_WP\_GRP\_ENABLE\_SLICE 31U,31U
- #define MMCSD CSD MMC DEFAULT ECC SLICE 30U,29U
- #define MMCSD CSD MMC R2W FACTOR SLICE 28U,26U
- #define MMCSD\_CSD\_MMC\_WRITE\_BL\_LEN\_SLICE 25U,22U
- #define MMCSD\_CSD\_MMC\_WRITE\_BL\_PARTIAL\_SLICE 21U,21U
- #define MMCSD\_CSD\_MMC\_CONTENT\_PROT\_APP\_SLICE 16U,16U
- #define MMCSD\_CSD\_MMC\_FILE\_FORMAT\_GRP\_SLICE 15U,15U
- #define MMCSD\_CSD\_MMC\_COPY\_SLICE 14U,14U
- #define MMCSD CSD MMC PERM WRITE PROTECT SLICE 13U,13U
- #define MMCSD CSD MMC TMP WRITE PROTECT SLICE 12U,12U
- #define MMCSD CSD MMC FILE FORMAT SLICE 11U,10U
- #define MMCSD\_CSD\_MMC\_ECC\_SLICE 9U,8U
- #define MMCSD CSD MMC CRC SLICE 7U,1U
- #define MMCSD CSD 20 CRC SLICE 7U,1U
- #define MMCSD CSD 20 FILE FORMAT SLICE 11U,10U
- #define MMCSD CSD 20 TMP WRITE PROTECT SLICE 12U,12U
- #define MMCSD\_CSD\_20\_PERM\_WRITE\_PROTECT\_SLICE 13U,13U
- #define MMCSD\_CSD\_20\_COPY\_SLICE 14U,14U
- #define MMCSD CSD 20 FILE FORMAT GRP SLICE 15U,15U
- #define MMCSD\_CSD\_20\_WRITE\_BL\_PARTIAL\_SLICE 21U,21U
- #define MMCSD CSD 20 WRITE BL LEN SLICE 25U,12U
- #define MMCSD\_CSD\_20\_R2W\_FACTOR\_SLICE 28U,26U

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- #define MMCSD\_CSD\_20\_WP\_GRP\_ENABLE\_SLICE 31U,31U
- #define MMCSD CSD 20 WP GRP SIZE SLICE 38U,32U
- #define MMCSD CSD 20 ERASE SECTOR SIZE SLICE 45U,39U
- #define MMCSD\_CSD\_20\_ERASE\_BLK\_EN\_SLICE 46U,46U
- #define MMCSD\_CSD\_20\_C\_SIZE\_SLICE 69U,48U
- #define MMCSD CSD 20 DSR IMP SLICE 76U,76U
- #define MMCSD CSD 20 READ BLK MISALIGN SLICE 77U,77U
- #define MMCSD\_CSD\_20\_WRITE\_BLK\_MISALIGN\_SLICE 78U,78U
- #define MMCSD CSD 20 READ BL PARTIAL SLICE 79U,79U
- #define MMCSD\_CSD\_20\_READ\_BL\_LEN\_SLICE 83U,80U
- #define MMCSD CSD 20 CCC SLICE 95U,84U
- #define MMCSD CSD 20 TRANS SPEED SLICE 103U,96U
- #define MMCSD CSD 20 NSAC SLICE 111U,104U
- #define MMCSD\_CSD\_20\_TAAC\_SLICE 119U,112U
- #define MMCSD CSD 20 CSD STRUCTURE SLICE 127U,126U
- #define MMCSD\_CSD\_10\_CRC\_SLICE MMCSD\_CSD\_20\_CRC\_SLICE
- #define MMCSD\_CSD\_10\_FILE\_FORMAT\_SLICE MMCSD\_CSD\_20\_FILE\_FORMAT\_SLICE
- #define MMCSD\_CSD\_10\_TMP\_WRITE\_PROTECT\_SLICE MMCSD\_CSD\_20\_TMP\_WRITE\_PROTEC

   T SLICE
- #define MMCSD CSD 10 COPY SLICE MMCSD CSD 20 COPY SLICE
- #define MMCSD\_CSD\_10\_FILE\_FORMAT\_GRP\_SLICE MMCSD\_CSD\_20\_FILE\_FORMAT\_GRP\_SLICE
- #define MMCSD CSD 10 WRITE BL PARTIAL SLICE MMCSD CSD 20 WRITE BL PARTIAL SLICE
- #define MMCSD\_CSD\_10\_WRITE\_BL\_LEN\_SLICE MMCSD\_CSD\_20\_WRITE\_BL\_LEN\_SLICE
- #define MMCSD CSD 10 R2W FACTOR SLICE MMCSD CSD 20 R2W FACTOR SLICE
- #define MMCSD\_CSD\_10\_WP\_GRP\_ENABLE\_SLICE MMCSD\_CSD\_20\_WP\_GRP\_ENABLE\_SLICE
- #define MMCSD CSD 10 WP GRP SIZE SLICE MMCSD CSD 20 WP GRP SIZE SLICE
- #define MMCSD CSD 10 ERASE BLK EN SLICE MMCSD CSD 20 ERASE BLK EN SLICE
- #define MMCSD\_CSD\_10\_C\_SIZE\_MULT\_SLICE 49U,47U
- #define MMCSD\_CSD\_10\_VDD\_W\_CURR\_MAX\_SLICE 52U,50U
- #define MMCSD CSD 10 VDD W CURR MIN SLICE 55U,53U
- #define MMCSD\_CSD\_10\_VDD\_R\_CURR\_MAX\_SLICE 58U,56U
- #define MMCSD CSD 10 VDD R CURR MIX SLICE 61U,59U
- #define MMCSD\_CSD\_10\_C\_SIZE\_SLICE 73U,62U
- #define MMCSD CSD 10 DSR IMP SLICE MMCSD CSD 20 DSR IMP SLICE
- #define MMCSD\_CSD\_10\_WRITE\_BLK\_MISALIGN\_SLICE MMCSD\_CSD\_20\_WRITE\_BLK\_MISALIG←
   N SLICE
- #define MMCSD\_CSD\_10\_READ\_BL\_PARTIAL\_SLICE MMCSD\_CSD\_20\_READ\_BL\_PARTIAL\_SLICE
- #define MMCSD\_CSD\_10\_READ\_BL\_LEN\_SLICE 83U,80U
- #define MMCSD\_CSD\_10\_CCC\_SLICE MMCSD\_CSD\_20\_CCC\_SLICE
- #define MMCSD CSD 10 TRANS SPEED SLICE MMCSD CSD 20 TRANS SPEED SLICE
- #define MMCSD\_CSD\_10\_NSAC\_SLICE MMCSD\_CSD\_20\_NSAC\_SLICE
- #define MMCSD\_CSD\_10\_TAAC\_SLICE MMCSD\_CSD\_20\_TAAC\_SLICE
- #define MMCSD CSD 10 CSD STRUCTURE SLICE MMCSD CSD 20 CSD STRUCTURE SLICE

### **CID** record offsets

#define MMCSD\_CID\_SDC\_CRC\_SLICE 7U,1U

Slice position of values in CID register.

- #define MMCSD CID SDC MDT M SLICE 11U,8U
- #define MMCSD\_CID\_SDC\_MDT\_Y\_SLICE 19U,12U
- #define MMCSD CID SDC PSN SLICE 55U,24U
- #define MMCSD\_CID\_SDC\_PRV\_M\_SLICE 59U,56U
- #define MMCSD\_CID\_SDC\_PRV\_N\_SLICE 63U,60U
- #define MMCSD CID SDC PNM0 SLICE 71U,64U
- #define MMCSD CID SDC PNM1 SLICE 79U,72U
- #define MMCSD CID SDC PNM2 SLICE 87U,80U
- #define MMCSD\_CID\_SDC\_PNM3\_SLICE 95U,88U
- #define MMCSD\_CID\_SDC\_PNM4\_SLICE 103U,96U
- #define MMCSD CID SDC OID SLICE 119U,104U
- #define MMCSD CID SDC MID SLICE 127U,120U
- #define MMCSD CID MMC CRC SLICE 7U,1U
- #define MMCSD\_CID\_MMC\_MDT\_Y\_SLICE 11U,8U
- #define MMCSD\_CID\_MMC\_MDT\_M\_SLICE 15U,12U
- #define MMCSD\_CID\_MMC\_PSN\_SLICE 47U,16U
- #define MMCSD CID MMC PRV M SLICE 51U,48U
- #define MMCSD CID MMC PRV N SLICE 55U,52U
- #define MMCSD CID MMC PNM0 SLICE 63U,56U
- #define MMCSD CID MMC PNM1 SLICE 71U,64U
- #define MMCSD CID MMC PNM2 SLICE 79U,72U
- #define MMCSD CID MMC PNM3 SLICE 87U,80U
- #define MMCSD\_CID\_MMC\_PNM4\_SLICE 95U,88U
- #define MMCSD CID MMC PNM5 SLICE 103U,96U
- #define MMCSD\_CID\_MMC\_OID\_SLICE 119U,104U
- #define MMCSD\_CID\_MMC\_MID\_SLICE 127U,120U

# R1 response utilities

#define MMCSD R1 ERROR(r1) (((r1) & MMCSD R1 ERROR MASK) != 0U)

Evaluates to TRUE if the R1 response contains error flags.

#define MMCSD\_R1\_STS(r1) (((r1) >> 9U) & 15U)

Returns the status field of an R1 response.

#define MMCSD R1 IS CARD LOCKED(r1) ((((r1) >> 21U) & 1U) != 0U)

Evaluates to TRUE if the R1 response indicates a locked card.

#### **Macro Functions**

#define mmcsdGetCardCapacity(ip) ((ip)->capacity)

Returns the card capacity in blocks.

#### **Data Structures**

struct MMCSDBlockDeviceVMT

MMCSDBlockDevice virtual methods table.

• struct MMCSDBlockDevice

MCC/SD block device class.

struct unpacked\_sdc\_cid\_t

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Unpacked CID register from SDC.

struct unpacked\_mmc\_cid\_t

Unpacked CID register from MMC.

struct unpacked\_sdc\_csd\_10\_t

Unpacked CSD v1.0 register from SDC.

struct unpacked sdc csd 20 t

Unpacked CSD v2.0 register from SDC.

· struct unpacked mmc csd t

Unpacked CSD register from MMC.

#### **Functions**

• uint32\_t \_mmcsd\_get\_slice (const uint32\_t \*data, uint32\_t end, uint32\_t start)

Gets a bit field from a words array.

uint32\_t \_mmcsd\_get\_capacity (const uint32\_t \*csd)

Extract card capacity from a CSD.

uint32\_t \_mmcsd\_get\_capacity\_ext (const uint8\_t \*ext\_csd)

Extract MMC card capacity from EXT\_CSD.

void \_mmcsd\_unpack\_sdc\_cid (const MMCSDBlockDevice \*sdcp, unpacked\_sdc\_cid\_t \*cidsdc)
 Unpacks SDC CID array in structure.

• void \_mmcsd\_unpack\_csd\_mmc (const MMCSDBlockDevice \*sdcp, unpacked\_mmc\_csd\_t \*csdmmc) Unpacks MMC CSD array in structure.

void \_mmcsd\_unpack\_csd\_v10 (const MMCSDBlockDevice \*sdcp, unpacked\_sdc\_csd\_10\_t \*csd10)
 Unpacks SDC CSD v1.0 array in structure.

void \_mmcsd\_unpack\_csd\_v20 (const MMCSDBlockDevice \*sdcp, unpacked\_sdc\_csd\_20\_t \*csd20)
 Unpacks SDC CSD v2.0 array in structure.

#### 7.27.2 Macro Definition Documentation

7.27.2.1 #define MMCSD BLOCK SIZE 512U

Fixed block size for MMC/SD block devices.

7.27.2.2 #define MMCSD\_R1\_ERROR\_MASK 0xFDFFE008U

Mask of error bits in R1 responses.

7.27.2.3 #define MMCSD\_CMD8\_PATTERN 0x000001AAU

Fixed pattern for CMD8.

7.27.2.4 #define MMCSD\_CSD\_MMC\_CSD\_STRUCTURE\_SLICE 127U,126U

Slice position of values in CSD register.

7.27.2.5 #define MMCSD\_CID\_SDC\_CRC\_SLICE 7U,1U

Slice position of values in CID register.

7.27.2.6 #define \_mmcsd\_block\_device\_methods \_base\_block\_device\_methods

MMCSDBlockDevice specific methods.

7.27.2.7 #define \_mmcsd\_block\_device\_data

#### Value:

```
_base_block_device_data

/* Card CID.*/
uint32_t cid[4];

/* Card CSD.*/
uint32_t csd[4];

/* Total number of blocks in card.*/
uint32_t capacity;
```

MMCSDBlockDevice specific data.

#### Note

It is empty because MMCSDBlockDevice is only an interface without implementation.

```
7.27.2.8 #define MMCSD_R1_ERROR( r1 ) (((r1) & MMCSD_R1_ERROR_MASK) != 0U)
```

Evaluates to TRUE if the R1 response contains error flags.

#### **Parameters**

```
in r1 the r1 response
```

```
7.27.2.9 #define MMCSD_R1_STS( r1 ) (((r1) >> 9U) & 15U)
```

Returns the status field of an R1 response.

#### **Parameters**

```
in r1 the r1 response
```

```
7.27.2.10 #define MMCSD_R1_IS_CARD_LOCKED( r1 ) ((((r1) >> 21U) & 1U) != 0U)
```

Evaluates to TRUE if the R1 response indicates a locked card.

### **Parameters**

| in | r1 | the r1 response |
|----|----|-----------------|

7.27.2.11 #define mmcsdGetCardCapacity( ip ) ((ip)->capacity)

Returns the card capacity in blocks.

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### **Parameters**

| in | <i>ip</i> pointer to a | MMCSDBlockDevice or derived class |
|----|------------------------|-----------------------------------|
|----|------------------------|-----------------------------------|

#### Returns

The card capacity.

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

### 7.27.3 Function Documentation

```
7.27.3.1 uint32_t _mmcsd_get_slice ( const uint32_t * data, uint32_t end, uint32_t start )
```

Gets a bit field from a words array.

Note

The bit zero is the LSb of the first word.

#### **Parameters**

| in | data   | pointer to the words array                          |  |
|----|--|---|--|
| in | end bit offset of the last bit of the field, inclusive |   |  |
| in | start  | bit offset of the first bit of the field, inclusive |  |

# Returns

The bits field value, left aligned.

# **Function Class:**

Not an API, this function is for internal use only.

7.27.3.2 uint32\_t \_mmcsd\_get\_capacity ( const uint32\_t \* csd )

Extract card capacity from a CSD.

The capacity is returned as number of available blocks.

# **Parameters**

| in | csd | the CSD record |
|----|-----|----------------|

#### Returns

The card capacity.

# Return values

| 0 CSD format error | - |
|--------------------|---|
|--------------------|---|

# **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.27.3.3 uint32\_t \_mmcsd\_get\_capacity\_ext ( const uint8\_t \* ext\_csd )

Extract MMC card capacity from EXT\_CSD.

The capacity is returned as number of available blocks.

#### **Parameters**

| in <i>ex</i> | csd | the extended CSD record |
|--------------|-----|-------------------------|
|--------------|-----|-------------------------|

### Returns

The card capacity.

# **Function Class:**

Not an API, this function is for internal use only.

7.27.3.4 void \_mmcsd\_unpack\_sdc\_cid ( const MMCSDBlockDevice \* sdcp, unpacked\_sdc\_cid\_t \* cidsdc )

Unpacks SDC CID array in structure.

### **Parameters**

| in  | sdcp   | pointer to the MMCSDBlockDevice object   |  |
|-----|--------|--|--|
| out | cidsdc | pointer to the unpacked_sdc_cid_t object |  |

# **Function Class:**

Not an API, this function is for internal use only.

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Here is the call graph for this function:



 $7.27.3.5 \quad \text{void} \ \_\text{mmcsd\_unpack\_mmc\_cid} \ ( \ \text{const} \ \textbf{MMCSDBlockDevice} * \textit{sdcp}, \ \textbf{unpacked\_mmc\_cid\_t} * \textit{cidmmc} \ )$ 

Unpacks MMC CID array in structure.

### **Parameters**

| i | .n | sdcp   | pointer to the MMCSDBlockDevice object   |  |
|---|----|--------|--|--|
| 0 | ut | cidmmc | pointer to the unpacked_mmc_cid_t object |  |

# **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



 $7.27.3.6 \quad \text{void} \ \_\text{mmcsd\_unpack\_csd\_mmc} \ ( \ \text{const} \ \textbf{MMCSDBlockDevice} * \textit{sdcp}, \ \textbf{unpacked\_mmc\_csd\_t} * \textit{csdmmc} \ )$ 

Unpacks MMC CSD array in structure.

### **Parameters**

| in  | sdcp   | pointer to the MMCSDBlockDevice object   |  |
|-----|--------|--|--|
| out | csdmmc | pointer to the unpacked_mmc_csd_t object |  |

# **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



 $7.27.3.7 \quad \text{void} \ \_\text{mmcsd\_unpack\_csd\_v10} \ ( \ \text{const} \ \text{MMCSDBlockDevice} * \textit{sdcp}, \ \text{unpacked\_sdc\_csd\_10\_t} * \textit{csd10} \ )$ 

Unpacks SDC CSD v1.0 array in structure.

### **Parameters**

| in  | sdcp  | pointer to the MMCSDBlockDevice object      |  |
|-----|-------|---|--|
| out | csd10 | pointer to the unpacked_sdc_csd_10_t object |  |

# **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



 $7.27.3.8 \quad \text{void} \ \_\text{mmcsd\_unpack\_csd\_v20} \ ( \ \text{const} \ \text{MMCSDBlockDevice} * \textit{sdcp}, \ \text{unpacked\_sdc\_csd\_20\_t} * \textit{csd20} \ )$ 

Unpacks SDC CSD v2.0 array in structure.

### **Parameters**

| in  | sdcp  | pointer to the MMCSDBlockDevice object      |  |
|-----|-------|---|--|
| out | csd20 | pointer to the unpacked_sdc_csd_20_t object |  |

# **Function Class:**

Not an API, this function is for internal use only.

7.27 MMC/SD Block Device 259

Here is the call graph for this function:



# 7.28 PAL Driver

I/O Ports Abstraction Layer.

### 7.28.1 Detailed Description

I/O Ports Abstraction Layer.

This module defines an abstract interface for digital I/O ports. Note that most I/O ports functions are just macros. The macros have default software implementations that can be redefined in a PAL Low Level Driver if the target hardware supports special features like, for example, atomic bit set/reset/masking. Please refer to the ports specific documentation for details.

The PAL Driver driver has the advantage to make the access to the I/O ports platform independent and still be optimized for the specific architectures.

Note that the PAL Low Level Driver may also offer non standard macro and functions in order to support specific features but, of course, the use of such interfaces would not be portable. Such interfaces shall be marked with the architecture name inside the function names.

#### Precondition

In order to use the PAL driver the HAL\_USE\_PAL option must be enabled in halconf.h.

### 7.28.2 Implementation Rules

In implementing a PAL Low Level Driver there are some rules/behaviors that should be respected.

### 7.28.2.1 Writing on input pads

The behavior is not specified but there are implementations better than others, this is the list of possible implementations, preferred options are on top:

- 1. The written value is not actually output but latched, should the pads be reprogrammed as outputs the value would be in effect.
- 2. The write operation is ignored.
- 3. The write operation has side effects, as example disabling/enabling pull up/down resistors or changing the pad direction. This scenario is discouraged, please try to avoid this scenario.

# 7.28.2.2 Reading from output pads

The behavior is not specified but there are implementations better than others, this is the list of possible implementations, preferred options are on top:

- 1. The actual pads states are read (not the output latch).
- 2. The output latch value is read (regardless of the actual pads states).
- 3. Unspecified, please try to avoid this scenario.

# 7.28.2.3 Writing unused or unimplemented port bits

The behavior is not specified.

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#### 7.28.2.4 Reading from unused or unimplemented port bits

The behavior is not specified.

#### 7.28.2.5 Reading or writing on pins associated to other functionalities

The behavior is not specified.

#### **Macros**

#define PAL PORT BIT(n) ((ioportmask t)(1U << (n)))</li>

Port bit helper macro.

#define PAL GROUP MASK(width) ((ioportmask t)(1U << (width)) - 1U)</li>

Bits group mask helper.

#define \_IOBUS\_DATA(name, port, width, offset) {port, PAL\_GROUP\_MASK(width), offset}

Data part of a static I/O bus initializer.

#define IOBUS\_DECL(name, port, width, offset) IOBus name = \_IOBUS\_DATA(name, port, width, offset)
 Static I/O bus initializer.

#define IOPORT1 0

First I/O port identifier.

#define pal\_lld\_init(config) \_pal\_lld\_init(config)

Low level PAL subsystem initialization.

#define pal\_lld\_readport(port) 0U

Reads the physical I/O port states.

#define pal IId readlatch(port) 0U

Reads the output latch.

#define pal\_lld\_writeport(port, bits)

Writes a bits mask on a I/O port.

• #define pal\_lld\_setport(port, bits)

Sets a bits mask on a I/O port.

#define pal\_lld\_clearport(port, bits)

Clears a bits mask on a I/O port.

#define pal\_lld\_toggleport(port, bits)

Toggles a bits mask on a I/O port.

#define pal\_lld\_readgroup(port, mask, offset) 0U

Reads a group of bits.

#define pal\_lld\_writegroup(port, mask, offset, bits)

Writes a group of bits.

#define pal\_lld\_setgroupmode(port, mask, offset, mode) \_pal\_lld\_setgroupmode(port, mask << offset, mode)</li>

Pads group mode setup.

#define pal\_lld\_readpad(port, pad) PAL\_LOW

Reads a logical state from an I/O pad.

#define pal\_Ild\_writepad(port, pad, bit)

Writes a logical state on an output pad.

• #define pal\_lld\_setpad(port, pad)

Sets a pad logical state to  $PAL\_HIGH$ .

#define pal\_lld\_clearpad(port, pad)

Clears a pad logical state to PAL\_LOW.

• #define pal\_lld\_togglepad(port, pad)

Toggles a pad logical state.

• #define pal\_lld\_setpadmode(port, pad, mode)

Pad mode setup.

### Pads mode constants

#define PAL MODE RESET 0U

After reset state.

• #define PAL MODE UNCONNECTED 1U

Safe state for unconnected pads.

• #define PAL MODE INPUT 2U

Regular input high-Z pad.

• #define PAL MODE INPUT PULLUP 3U

Input pad with weak pull up resistor.

#define PAL\_MODE\_INPUT\_PULLDOWN 4U

Input pad with weak pull down resistor.

#define PAL MODE INPUT ANALOG 5U

Analog input mode.

#define PAL MODE OUTPUT PUSHPULL 6U

Push-pull output pad.

#define PAL\_MODE\_OUTPUT\_OPENDRAIN 7U

Open-drain output pad.

# Logic level constants

• #define PAL LOW 0U

Logical low state.

• #define PAL\_HIGH 1U

Logical high state.

### **Macro Functions**

#define pallnit(config) pal lld init(config)

PAL subsystem initialization.

#define palReadPort(port) ((void)(port), 0U)

Reads the physical I/O port states.

#define palReadLatch(port) ((void)(port), 0U)

Reads the output latch.

• #define palWritePort(port, bits) ((void)(port), (void)(bits))

Writes a bits mask on a I/O port.

#define palSetPort(port, bits) palWritePort(port, palReadLatch(port) | (bits))

Sets a bits mask on a I/O port.

#define palClearPort(port, bits) palWritePort(port, palReadLatch(port) & ∼(bits))

Clears a bits mask on a I/O port.

#define palTogglePort(port, bits) palWritePort(port, palReadLatch(port) ^ (bits))

Toggles a bits mask on a I/O port.

#define palReadGroup(port, mask, offset) ((palReadPort(port) >> (offset)) & (mask))

Reads a group of bits.

• #define palWriteGroup(port, mask, offset, bits)

Writes a group of bits.

#define palSetGroupMode(port, mask, offset, mode)

Pads group mode setup.

#define palReadPad(port, pad) ((palReadPort(port) >> (pad)) & 1U)

Reads an input pad logic state.

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#define palWritePad(port, pad, bit)

Writes a logic state on an output pad.

#define palSetPad(port, pad) palSetPort(port, PAL\_PORT\_BIT(pad))

Sets a pad logic state to PAL\_HIGH.

#define palClearPad(port, pad) palClearPort(port, PAL\_PORT\_BIT(pad))

Clears a pad logic state to PAL\_LOW.

#define palTogglePad(port, pad) palTogglePort(port, PAL PORT BIT(pad))

Toggles a pad logic state.

• #define palSetPadMode(port, pad, mode) palSetGroupMode(port, PAL\_PORT\_BIT(pad), 0U, mode)

Pad mode setup.

#define palReadLine(line) palReadPad(PAL\_PORT(line), PAL\_PAD(line))

Reads an input line logic state.

• #define palWriteLine(line, bit) palWritePad(PAL\_PORT(line), PAL\_PAD(line), bit)

Writes a logic state on an output line.

• #define palSetLine(line) palSetPad(PAL\_PORT(line), PAL\_PAD(line))

Sets a line logic state to PAL\_HIGH.

#define palClearLine(line) palClearPad(PAL PORT(line), PAL PAD(line))

Clears a line logic state to PAL\_LOW.

#define palToggleLine(line) palTogglePad(PAL\_PORT(line), PAL\_PAD(line))

Toggles a line logic state.

#define palSetLineMode(line, mode) palSetPadMode(PAL\_PORT(line), PAL\_PAD(line), mode)

Line mode setup.

### Port related definitions

• #define PAL\_IOPORTS\_WIDTH 16U

Width, in bits, of an I/O port.

#define PAL\_WHOLE\_PORT ((ioportmask\_t)0xFFFFU)

Whole port mask.

### Line handling macros

#define PAL\_LINE(port, pad) ((ioline\_t)((uint32\_t)(port)) | ((uint32\_t)(pad)))

Forms a line identifier.

#define PAL\_PORT(line) ((stm32\_gpio\_t \*)(((uint32\_t)(line)) & 0xFFFFFF0U))

Decodes a port identifier from a line identifier.

#define PAL\_PAD(line) ((uint32\_t)(line) & 0x0000000FU))

Decodes a pad identifier from a line identifier.

#define PAL\_NOLINE 0U

Value identifying an invalid line.

# **Typedefs**

· typedef uint32\_t ioportmask\_t

Digital I/O port sized unsigned type.

typedef uint32\_t iomode\_t

Digital I/O modes.

typedef uint32\_t ioline\_t

Type of an I/O line.

typedef uint32\_t ioportid\_t

Port Identifier.

### **Data Structures**

struct IOBus

I/O bus descriptor.

struct PALConfig

Generic I/O ports static initializer.

### **Functions**

ioportmask t palReadBus (IOBus \*bus)

Read from an I/O bus.

void palWriteBus (IOBus \*bus, ioportmask\_t bits)

Write to an I/O bus.

void palSetBusMode (IOBus \*bus, iomode t mode)

Programs a bus with the specified mode.

void \_pal\_lld\_init (const PALConfig \*config)

STM32 I/O ports configuration.

• void \_pal\_lld\_setgroupmode (ioportid\_t port, ioportmask\_t mask, iomode\_t mode)

Pads mode setup.

#### 7.28.3 Macro Definition Documentation

7.28.3.1 #define PAL\_MODE\_RESET 0U

After reset state.

The state itself is not specified and is architecture dependent, it is guaranteed to be equal to the after-reset state. It is usually an input state.

7.28.3.2 #define PAL\_MODE\_UNCONNECTED 1U

# Safe state for unconnected pads.

The state itself is not specified and is architecture dependent, it may be mapped on PAL\_MODE\_INPUT\_PULLUP, PAL\_MODE\_INPUT\_PULLDOWN or PAL\_MODE\_OUTPUT\_PUSHPULL for example.

7.28.3.3 #define PAL\_MODE\_INPUT 2U

Regular input high-Z pad.

7.28.3.4 #define PAL\_MODE\_INPUT\_PULLUP 3U

Input pad with weak pull up resistor.

7.28.3.5 #define PAL MODE INPUT PULLDOWN 4U

Input pad with weak pull down resistor.

7.28.3.6 #define PAL\_MODE\_INPUT\_ANALOG 5U

Analog input mode.

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7.28.3.7 #define PAL\_MODE\_OUTPUT\_PUSHPULL 6U

Push-pull output pad.

7.28.3.8 #define PAL\_MODE\_OUTPUT\_OPENDRAIN 7U

Open-drain output pad.

7.28.3.9 #define PAL\_LOW 0U

Logical low state.

7.28.3.10 #define PAL\_HIGH 1U

Logical high state.

7.28.3.11 #define PAL\_PORT\_BIT( n) ((ioportmask\_t)(1U << (n)))

Port bit helper macro.

This macro calculates the mask of a bit within a port.

#### **Parameters**

| in | n | bit position within the port |
|----|---|------------------------------|
|----|---|------------------------------|

# Returns

The bit mask.

7.28.3.12 #define PAL\_GROUP\_MASK(  $\it width$  ) ((ioportmask\_t)(1U << (width)) - 1U)

Bits group mask helper.

This macro calculates the mask of a bits group.

### **Parameters**

| in | width | group width |
|----|-------|-------------|

# Returns

The group mask.

7.28.3.13 #define\_IOBUS\_DATA( name, port, width, offset ) {port, PAL\_GROUP\_MASK(width), offset}

Data part of a static I/O bus initializer.

This macro should be used when statically initializing an I/O bus that is part of a bigger structure.

#### **Parameters**

| in | name | name of the IOBus variable |
|----|------|----------------------------|
|----|------|----------------------------|

#### **Parameters**

| in | port   | I/O port descriptor            |  |
|----|--------|--------------------------------|--|
| in | width  | bus width in bits              |  |
| in | offset | bus bit offset within the port |  |

7.28.3.14 #define IOBUS\_DECL( name, port, width, offset ) IOBus name = \_IOBUS\_DATA(name, port, width, offset)

Static I/O bus initializer.

### **Parameters**

| in | name   | name of the IOBus variable     |
|----|--------|--------------------------------|
| in | port   | I/O port descriptor            |
| in | width  | bus width in bits              |
| in | offset | bus bit offset within the port |

7.28.3.15 #define pallnit( config ) pal\_IId\_init(config)

PAL subsystem initialization.

Note

This function is implicitly invoked by halInit(), there is no need to explicitly initialize the driver.

## **Parameters**

| in | config | pointer to an architecture specific configuration structure. This structure is defined in the low |  |
|----|--------|---|--|
|    |        | level driver header.  |  |

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

7.28.3.16 #define palReadPort( port ) ((void)(port), 0U)

Reads the physical I/O port states.

Note

The default implementation always return zero and computes the parameter eventual side effects. The function can be called from any context.

### **Parameters**

| in | port | port identifier |
|----|------|-----------------|
|----|------|-----------------|

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#### Returns

The port logic states.

#### **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.17 #define palReadLatch( port ) ((void)(port), 0U)

Reads the output latch.

The purpose of this function is to read back the latched output value.

### Note

The default implementation always return zero and computes the parameter eventual side effects. The function can be called from any context.

#### **Parameters**

| in <i>port</i> | port identifier |
|----------------|-----------------|
|----------------|-----------------|

#### Returns

The latched logic states.

# **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.18 #define palWritePort( port, bits ) ((void)(port), (void)(bits))

Writes a bits mask on a I/O port.

# Note

The default implementation does nothing except computing the parameters eventual side effects. The function can be called from any context.

#### **Parameters**

| in | port | port identifier                          |
|----|------|--|
| in | bits | bits to be written on the specified port |

### **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.19 #define palSetPort( port, bits ) palWritePort(port, palReadLatch(port) | (bits))

Sets a bits mask on a I/O port.

#### Note

The operation is not guaranteed to be atomic on all the architectures, for atomicity and/or portability reasons you may need to enclose port I/O operations between osalSysLock() and osalSysUnlock().

The default implementation is non atomic and not necessarily optimal. Low level drivers may optimize the function by using specific hardware or coding.

The function can be called from any context.

#### **Parameters**

| in | port | port identifier                       |
|----|------|---------------------------------------|
| in | bits | bits to be ORed on the specified port |

#### **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.20 #define palClearPort( port, bits ) palWritePort(port, palReadLatch(port) & ~(bits))

Clears a bits mask on a I/O port.

#### Note

The operation is not guaranteed to be atomic on all the architectures, for atomicity and/or portability reasons you may need to enclose port I/O operations between osalSysLock() and osalSysUnlock().

The default implementation is non atomic and not necessarily optimal. Low level drivers may optimize the function by using specific hardware or coding.

The function can be called from any context.

#### **Parameters**

| in | port | port identifier                          |
|----|------|--|
| in | bits | bits to be cleared on the specified port |

#### **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.21 #define palTogglePort( port, bits ) palWritePort(port, palReadLatch(port) ^ (bits))

Toggles a bits mask on a I/O port.

# Note

The operation is not guaranteed to be atomic on all the architectures, for atomicity and/or portability reasons you may need to enclose port I/O operations between osalSysLock() and osalSysUnlock().

The default implementation is non atomic and not necessarily optimal. Low level drivers may optimize the function by using specific hardware or coding.

The function can be called from any context.

# **Parameters**

| in | port | port identifier                        |
|----|------|--|
| in | bits | bits to be XORed on the specified port |

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# **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.22 #define palReadGroup( port, mask, offset ) ((palReadPort(port) >> (offset)) & (mask))

Reads a group of bits.

Note

The function can be called from any context.

### **Parameters**

| in | port   | port identifier  |
|----|--------|--|
| in | mask   | group mask, a logic AND is performed on the input data |
| in | offset | group bit offset within the port                       |

### Returns

The group logic states.

### **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.23 #define palWriteGroup( port, mask, offset, bits )

#### Value:

Writes a group of bits.

Note

The function can be called from any context.

#### **Parameters**

| in | port   | port identifier  |
|----|--------|--|
| in | mask   | group mask, a logic AND is performed on the output data          |
| in | offset | group bit offset within the port                                 |
| in | bits   | bits to be written. Values exceeding the group width are masked. |

# **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.24 #define palSetGroupMode( port, mask, offset, mode )

Pads group mode setup.

This function programs a pads group belonging to the same port with the specified mode.

### Note

Programming an unknown or unsupported mode is silently ignored.

The function can be called from any context.

#### **Parameters**

| in | port   | port identifier                  |
|----|--------|----------------------------------|
| in | mask   | group mask                       |
| in | offset | group bit offset within the port |
| in | mode   | group mode                       |

# **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.25 #define palReadPad( port, pad ) ((palReadPort(port) >> (pad)) & 1U)

Reads an input pad logic state.

# Note

The default implementation not necessarily optimal. Low level drivers may optimize the function by using specific hardware or coding.

The default implementation internally uses the palReadPort ().

The function can be called from any context.

#### **Parameters**

| in | port | port identifier            |
|----|------|----------------------------|
| in | pad  | pad number within the port |

### Returns

The logic state.

#### Return values

| PAL_LOW  | low logic state.  |  |
|----------|-------------------|--|
| PAL_HIGH | high logic state. |  |

### **Function Class:**

Special function, this function has special requirements see the notes.

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### 7.28.3.26 #define palWritePad( port, pad, bit )

#### Value:

Writes a logic state on an output pad.

#### Note

The operation is not guaranteed to be atomic on all the architectures, for atomicity and/or portability reasons you may need to enclose port I/O operations between osalSysLock() and osalSysUnlock().

The default implementation is non atomic and not necessarily optimal. Low level drivers may optimize the function by using specific hardware or coding.

The default implementation internally uses the palReadLatch() and palWritePort().

The function can be called from any context.

#### **Parameters**

| in | port | port identifier                                    |  |
|----|------|--|--|
| in | pad  | pad number within the port                         |  |
| in | bit  | logic value, the value must be PAL_LOW or PAL_HIGH |  |

### **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.27 #define palSetPad( port, pad ) palSetPort(port, PAL\_PORT\_BIT(pad))

Sets a pad logic state to PAL\_HIGH.

# Note

The operation is not guaranteed to be atomic on all the architectures, for atomicity and/or portability reasons you may need to enclose port I/O operations between osalSysLock() and osalSysUnlock().

The default implementation is non atomic and not necessarily optimal. Low level drivers may optimize the function by using specific hardware or coding.

The default implementation internally uses the palSetPort ().

The function can be called from any context.

#### **Parameters**

| in | port | port identifier            |
|----|------|----------------------------|
| in | pad  | pad number within the port |

### **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.28 #define palClearPad( port, pad ) palClearPort(port, PAL\_PORT\_BIT(pad))

Clears a pad logic state to PAL\_LOW.

### Note

The operation is not guaranteed to be atomic on all the architectures, for atomicity and/or portability reasons you may need to enclose port I/O operations between osalSysLock() and osalSysUnlock().

The default implementation is non atomic and not necessarily optimal. Low level drivers may optimize the function by using specific hardware or coding.

The default implementation internally uses the palClearPort ().

The function can be called from any context.

#### **Parameters**

| in | port | port identifier            |
|----|------|----------------------------|
| in | pad  | pad number within the port |

#### **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.29 #define palTogglePad( port, pad ) palTogglePort(port, PAL\_PORT\_BIT(pad))

Toggles a pad logic state.

### Note

The operation is not guaranteed to be atomic on all the architectures, for atomicity and/or portability reasons you may need to enclose port I/O operations between osalSysLock() and osalSysUnlock().

The default implementation is non atomic and not necessarily optimal. Low level drivers may optimize the function by using specific hardware or coding.

The default implementation internally uses the palTogglePort ().

The function can be called from any context.

# **Parameters**

| in | port | port identifier            |
|----|------|----------------------------|
| in | pad  | pad number within the port |

### **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.30 #define palSetPadMode( port, pad, mode ) palSetGroupMode(port, PAL\_PORT\_BIT(pad), 0U, mode)

Pad mode setup.

This function programs a pad with the specified mode.

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### Note

The default implementation not necessarily optimal. Low level drivers may optimize the function by using specific hardware or coding.

Programming an unknown or unsupported mode is silently ignored.

The function can be called from any context.

### **Parameters**

| in | port | port identifier            |
|----|------|----------------------------|
| in | pad  | pad number within the port |
| in | mode | pad mode                   |

## **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.31 #define palReadLine( line ) palReadPad(PAL\_PORT(line), PAL\_PAD(line))

Reads an input line logic state.

Note

The function can be called from any context.

### **Parameters**

| in | line | line identifier |
|----|------|-----------------|
|----|------|-----------------|

## Returns

The logic state.

## Return values

| PAL_LOW  | low logic state.  |
|----------|-------------------|
| PAL_HIGH | high logic state. |

## **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.32 #define palWriteLine( line, bit ) palWritePad(PAL\_PORT(line), PAL\_PAD(line), bit)

Writes a logic state on an output line.

Note

The function can be called from any context.

### **Parameters**

| in | line | line identifier                                    |
|----|------|--|
| in | bit  | logic value, the value must be PAL_LOW or PAL_HIGH |

### **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.33 #define palSetLine( line ) palSetPad(PAL PORT(line), PAL PAD(line))

Sets a line logic state to PAL\_HIGH.

Note

The function can be called from any context.

## **Parameters**

| in <i>line</i> | line identifier |
|----------------|-----------------|
|----------------|-----------------|

### **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.34 #define palClearLine( line ) palClearPad(PAL\_PORT(line), PAL\_PAD(line))

Clears a line logic state to PAL\_LOW.

Note

The function can be called from any context.

## **Parameters**

| in | line | line identifier |
|----|------|-----------------|

## **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.35 #define palToggleLine( line ) palTogglePad(PAL\_PORT(line), PAL\_PAD(line))

Toggles a line logic state.

Note

The function can be called from any context.

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## **Parameters**

|--|

### **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.36 #define palSetLineMode( line, mode ) palSetPadMode(PAL\_PORT(line), PAL\_PAD(line), mode)

Line mode setup.

Note

The function can be called from any context.

## **Parameters**

| in | line | line identifier |
|----|------|-----------------|
| in | mode | pad mode        |

## **Function Class:**

Special function, this function has special requirements see the notes.

7.28.3.37 #define PAL\_IOPORTS\_WIDTH 16U

Width, in bits, of an I/O port.

7.28.3.38 #define PAL\_WHOLE\_PORT ((ioportmask\_t)0xFFFFU)

Whole port mask.

This macro specifies all the valid bits into a port.

7.28.3.39 #define PAL\_LINE( port, pad ) ((ioline\_t)((uint32\_t)(port)) | ((uint32\_t)(pad)))

Forms a line identifier.

A port/pad pair are encoded into an ioline\_t type. The encoding of this type is platform-dependent.

7.28.3.40 #define PAL\_PORT( line ) ((stm32\_gpio\_t \*)(((uint32\_t)(line)) & 0xFFFFFF0U))

Decodes a port identifier from a line identifier.

7.28.3.41 #define PAL\_PAD( line ) ((uint32\_t)((uint32\_t)(line) & 0x0000000FU))

Decodes a pad identifier from a line identifier.

7.28.3.42 #define PAL\_NOLINE 0U

Value identifying an invalid line.

7.28.3.43 #define IOPORT1 0

First I/O port identifier.

Low level drivers can define multiple ports, it is suggested to use this naming convention.

7.28.3.44 #define pal\_lld\_init( config ) \_pal\_lld\_init(config)

Low level PAL subsystem initialization.

## **Parameters**

| in | config | architecture-dependent ports configuration |  |
|----|--------|--|--|
|----|--------|--|--|

## **Function Class:**

Not an API, this function is for internal use only.

7.28.3.45 #define pal\_lld\_readport( port ) 0U

Reads the physical I/O port states.

## **Parameters**

| in | port | port identifier |
|----|------|-----------------|

## Returns

The port bits.

**Function Class:** 

Not an API, this function is for internal use only.

7.28.3.46 #define pal\_lld\_readlatch( port ) 0U

Reads the output latch.

The purpose of this function is to read back the latched output value.

## **Parameters**

| in | port | port identifier |
|----|------|-----------------|

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### Returns

The latched logical states.

## **Function Class:**

Not an API, this function is for internal use only.

```
7.28.3.47 #define pal_lld_writeport( port, bits )
```

## Value:

```
do {
    (void)port;
    (void)bits;
} while (false)
```

Writes a bits mask on a I/O port.

## **Parameters**

| in | port | port identifier                          |
|----|------|--|
| in | bits | bits to be written on the specified port |

## **Function Class:**

Not an API, this function is for internal use only.

```
7.28.3.48 #define pal_lld_setport( port, bits )
```

## Value:

```
do {
     (void)port;
     (void)bits;
} while (false)
```

Sets a bits mask on a I/O port.

# Note

The PAL Driver provides a default software implementation of this functionality, implement this function if can optimize it by using special hardware functionalities or special coding.

## **Parameters**

| in | port | port identifier                       |
|----|------|---------------------------------------|
| in | bits | bits to be ORed on the specified port |

## **Function Class:**

Not an API, this function is for internal use only.

7.28.3.49 #define pal\_lld\_clearport( port, bits )

## Value:

```
do {
    (void)port;
    (void)bits;
} while (false)
```

Clears a bits mask on a I/O port.

## Note

The PAL Driver provides a default software implementation of this functionality, implement this function if can optimize it by using special hardware functionalities or special coding.

## **Parameters**

| in | port | port identifier                          |
|----|------|--|
| in | bits | bits to be cleared on the specified port |

## **Function Class:**

Not an API, this function is for internal use only.

```
7.28.3.50 #define pal_lld_toggleport( port, bits )
```

## Value:

```
do {
     (void)port;
     (void)bits;
} while (false)
```

Toggles a bits mask on a I/O port.

## Note

The PAL Driver provides a default software implementation of this functionality, implement this function if can optimize it by using special hardware functionalities or special coding.

### **Parameters**

| in | port | port identifier                        |
|----|------|--|
| in | bits | bits to be XORed on the specified port |

# **Function Class:**

Not an API, this function is for internal use only.

7.28.3.51 #define pal\_lld\_readgroup( port, mask, offset ) 0U

Reads a group of bits.

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### Note

The PAL Driver provides a default software implementation of this functionality, implement this function if can optimize it by using special hardware functionalities or special coding.

## **Parameters**

| in | port   | port identifier                  |
|----|--------|----------------------------------|
| in | mask   | group mask                       |
| in | offset | group bit offset within the port |

### Returns

The group logical states.

### **Function Class:**

Not an API, this function is for internal use only.

7.28.3.52 #define pal\_lld\_writegroup( port, mask, offset, bits )

### Value:

```
do {
    (void)port;
    (void)mask;
    (void)offset;
    (void)bits;
} while (false)
```

Writes a group of bits.

### Note

The PAL Driver provides a default software implementation of this functionality, implement this function if can optimize it by using special hardware functionalities or special coding.

## **Parameters**

| in | port   | port identifier  |  |
|----|--------|--|--|
| in | mask   | group mask   |  |
| in | offset | group bit offset within the port                                 |  |
| in | bits   | bits to be written. Values exceeding the group width are masked. |  |

## **Function Class:**

Not an API, this function is for internal use only.

7.28.3.53 #define pal\_lld\_setgroupmode( port, mask, offset, mode ) \_pal\_lld\_setgroupmode(port, mask << offset, mode)

Pads group mode setup.

This function programs a pads group belonging to the same port with the specified mode.

### Note

Programming an unknown or unsupported mode is silently ignored.

## **Parameters**

| in | port   | port identifier                  |
|----|--------|----------------------------------|
| in | mask   | group mask                       |
| in | offset | group bit offset within the port |
| in | mode   | group mode                       |

### **Function Class:**

Not an API, this function is for internal use only.

```
7.28.3.54 #define pal_lld_readpad( port, pad ) PAL_LOW
```

Reads a logical state from an I/O pad.

## Note

The PAL Driver provides a default software implementation of this functionality, implement this function if can optimize it by using special hardware functionalities or special coding.

## **Parameters**

|   | in | port | port identifier            |
|---|----|------|----------------------------|
| ĺ | in | pad  | pad number within the port |

## Returns

The logical state.

## Return values

| PAL_LOW  | low logical state.  |
|----------|---------------------|
| PAL_HIGH | high logical state. |

## **Function Class:**

Not an API, this function is for internal use only.

7.28.3.55 #define pal\_lld\_writepad( port, pad, bit )

## Value:

```
do {
    (void)port;
    (void)pad;
    (void)bit;
} while (false)
```

Writes a logical state on an output pad.

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### Note

This function is not meant to be invoked directly by the application code.

The PAL Driver provides a default software implementation of this functionality, implement this function if can optimize it by using special hardware functionalities or special coding.

## **Parameters**

|   | in | port | port identifier                                      |  |
|---|----|------|--|--|
|   | in | pad  | pad number within the port                           |  |
| Γ | in | bit  | logical value, the value must be PAL_LOW or PAL_HIGH |  |

## **Function Class:**

Not an API, this function is for internal use only.

```
7.28.3.56 #define pal_lld_setpad( port, pad )
```

## Value:

```
do {
     (void)port;
     (void)pad;
} while (false)
```

Sets a pad logical state to PAL\_HIGH.

## Note

The PAL Driver provides a default software implementation of this functionality, implement this function if can optimize it by using special hardware functionalities or special coding.

# **Parameters**

| in | port | port identifier            |
|----|------|----------------------------|
| in | pad  | pad number within the port |

## **Function Class:**

Not an API, this function is for internal use only.

```
7.28.3.57 #define pal_lld_clearpad( port, pad )
```

# Value:

```
do {
     (void)port;
     (void)pad;
} while (false)
```

Clears a pad logical state to PAL\_LOW.

Note

The PAL Driver provides a default software implementation of this functionality, implement this function if can optimize it by using special hardware functionalities or special coding.

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### **Parameters**

| in | port | port identifier            |
|----|------|----------------------------|
| in | pad  | pad number within the port |

### **Function Class:**

Not an API, this function is for internal use only.

```
7.28.3.58 #define pal_lld_togglepad( port, pad )
```

### Value:

```
do {
    (void)port;
    (void)pad;
} while (false)
```

Toggles a pad logical state.

### Note

The PAL Driver provides a default software implementation of this functionality, implement this function if can optimize it by using special hardware functionalities or special coding.

### **Parameters**

| in | port | port identifier            |
|----|------|----------------------------|
| in | pad  | pad number within the port |

# **Function Class:**

Not an API, this function is for internal use only.

```
7.28.3.59 #define pal_lld_setpadmode( port, pad, mode )
```

# Value:

```
do {
     (void)port;
     (void)pad;
     (void)mode;
} while (false)
```

# Pad mode setup.

This function programs a pad with the specified mode.

## Note

The PAL Driver provides a default software implementation of this functionality, implement this function if can optimize it by using special hardware functionalities or special coding.

Programming an unknown or unsupported mode is silently ignored.

### **Parameters**

| in | port | port identifier            |
|----|------|----------------------------|
| in | pad  | pad number within the port |
| in | mode | pad mode                   |

## **Function Class:**

Not an API, this function is for internal use only.

# 7.28.4 Typedef Documentation

7.28.4.1 typedef uint32\_t ioportmask\_t

Digital I/O port sized unsigned type.

7.28.4.2 typedef uint32\_t iomode\_t

Digital I/O modes.

7.28.4.3 typedef uint32\_t ioline\_t

Type of an I/O line.

7.28.4.4 typedef uint32\_t ioportid\_t

Port Identifier.

This type can be a scalar or some kind of pointer, do not make any assumption about it, use the provided macros when populating variables of this type.

## 7.28.5 Function Documentation

7.28.5.1 ioportmask t palReadBus ( IOBus \* bus )

Read from an I/O bus.

Note

The operation is not guaranteed to be atomic on all the architectures, for atomicity and/or portability reasons you may need to enclose port I/O operations between osalSysLock() and osalSysUnlock().

The function internally uses the palReadGroup () macro. The use of this function is preferred when you value code size, readability and error checking over speed.

The function can be called from any context.

## **Parameters**

| I | in  | hus | the I/O bus, pointer to a IOBus structure |
|---|-----|-----|---|
|   | T11 | Dus | the I/O bus, pointer to a TOBUS structure |

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#### Returns

The bus logical states.

### **Function Class:**

Special function, this function has special requirements see the notes.

7.28.5.2 void palWriteBus ( IOBus \* bus, ioportmask\_t bits )

Write to an I/O bus.

### Note

The operation is not guaranteed to be atomic on all the architectures, for atomicity and/or portability reasons you may need to enclose port I/O operations between osalSysLock() and osalSysUnlock().

The default implementation is non atomic and not necessarily entired. Low level drivers may entire the

The default implementation is non atomic and not necessarily optimal. Low level drivers may optimize the function by using specific hardware or coding.

The function can be called from any context.

### **Parameters**

| in | in bus the I/O bus, pointer to a IOBus structure |  |
|----|--|--|
| in | bits   | the bits to be written on the I/O bus. Values exceeding the bus width are masked so most |
|    |  | significant bits are lost.   |

### **Function Class:**

Special function, this function has special requirements see the notes.

7.28.5.3 void palSetBusMode ( IOBus \* bus, iomode\_t mode )

Programs a bus with the specified mode.

## Note

The operation is not guaranteed to be atomic on all the architectures, for atomicity and/or portability reasons you may need to enclose port I/O operations between osalSysLock() and osalSysUnlock().

The default implementation is non atomic and not necessarily optimal. Low level drivers may optimize the function by using specific hardware or coding.

The function can be called from any context.

### **Parameters**

| in | bus  | the I/O bus, pointer to a IOBus structure |
|----|------|---|
| in | mode | the mode                                  |

## **Function Class:**

Special function, this function has special requirements see the notes.

7.28.5.4 void \_pal\_lld\_init ( const PALConfig \* config )

STM32 I/O ports configuration.

Ports A-D(E, F, G, H) clocks enabled.

## **Parameters**

| in | config | the STM32 ports configuration |
|----|--------|-------------------------------|
|----|--------|-------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

7.28.5.5 void \_pal\_lld\_setgroupmode ( ioportid\_t port, ioportmask\_t mask, iomode\_t mode )

Pads mode setup.

This function programs a pads group belonging to the same port with the specified mode.

## **Parameters**

| in | port | the port identifier |
|----|------|---------------------|
| in | mask | the group mask      |
| in | mode | the mode            |

# **Function Class:**

Not an API, this function is for internal use only.

## 7.29 PWM Driver

Generic PWM Driver.

## 7.29.1 Detailed Description

Generic PWM Driver.

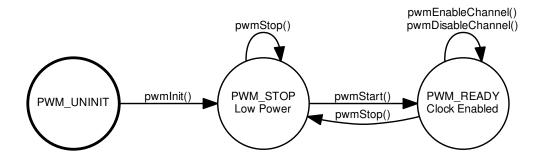
This module implements a generic PWM (Pulse Width Modulation) driver.

#### Precondition

In order to use the PWM driver the HAL\_USE\_PWM option must be enabled in halconf.h.

### 7.29.2 Driver State Machine

The driver implements a state machine internally, not all the driver functionalities can be used in any moment, any transition not explicitly shown in the following diagram has to be considered an error and shall be captured by an assertion (if enabled).



## 7.29.3 PWM Operations.

This driver abstracts a generic PWM timer composed of:

- · A clock prescaler.
- · A main up counter.
- A comparator register that resets the main counter to zero when the limit is reached. An optional callback can be generated when this happens.
- An array of PWM\_CHANNELS PWM channels, each channel has an output, a comparator and is able to invoke an optional callback when a comparator match with the main counter happens.

A PWM channel output can be in two different states:

- · IDLE, when the channel is disabled or after a match occurred.
- ACTIVE, when the channel is enabled and a match didn't occur yet in the current PWM cycle.

Note that the two states can be associated to both logical zero or one in the PWMChannelConfig structure.

### **Macros**

• #define PWM CHANNELS 4

Number of PWM channels per PWM driver.

#define pwm\_lld\_change\_period(pwmp, period)

Changes the period the PWM peripheral.

## PWM output mode macros

• #define PWM OUTPUT MASK 0x0FU

Standard output modes mask.

#define PWM OUTPUT DISABLED 0x00U

Output not driven, callback only.

• #define PWM OUTPUT ACTIVE HIGH 0x01U

Positive PWM logic, active is logic level one.

• #define PWM OUTPUT ACTIVE LOW 0x02U

Inverse PWM logic, active is logic level zero.

## PWM duty cycle conversion

• #define PWM\_FRACTION\_TO\_WIDTH(pwmp, denominator, numerator)

Converts from fraction to pulse width.

#define PWM\_DEGREES\_TO\_WIDTH(pwmp, degrees) PWM\_FRACTION\_TO\_WIDTH(pwmp, 36000, degrees)

Converts from degrees to pulse width.

#define PWM\_PERCENTAGE\_TO\_WIDTH(pwmp, percentage) PWM\_FRACTION\_TO\_WIDTH(pwmp, 10000, percentage)

Converts from percentage to pulse width.

### **Macro Functions**

• #define pwmChangePeriodI(pwmp, value)

Changes the period the PWM peripheral.

#define pwmEnableChannelI(pwmp, channel, width)

Enables a PWM channel.

#define pwmDisableChannelI(pwmp, channel)

Disables a PWM channel.

 #define pwmlsChannelEnabledI(pwmp, channel) (((pwmp)->enabled & ((pwmchnmsk\_t)1U << (pwmchnmsk→ \_t)(channel))) != 0U)

Returns a PWM channel status.

• #define pwmEnablePeriodicNotificationI(pwmp) pwm\_lld\_enable\_periodic\_notification(pwmp)

Enables the periodic activation edge notification.

• #define pwmDisablePeriodicNotificationI(pwmp) pwm\_lld\_disable\_periodic\_notification(pwmp)

Disables the periodic activation edge notification.

#define pwmEnableChannelNotificationI(pwmp, channel) pwm\_lld\_enable\_channel\_notification(pwmp, channel)

Enables a channel de-activation edge notification.

#define pwmDisableChannelNotificationI(pwmp, channel) pwm\_lld\_disable\_channel\_notification(pwmp, channel)

Disables a channel de-activation edge notification.

## **PLATFORM** configuration options

#define PLATFORM PWM USE PWM1 FALSE

PWMD1 driver enable switch.

## **Typedefs**

typedef struct PWMDriver PWMDriver

Type of a structure representing a PWM driver.

typedef void(\* pwmcallback\_t) (PWMDriver \*pwmp)

Type of a PWM notification callback.

typedef uint32\_t pwmmode\_t

Type of a PWM mode.

· typedef uint8\_t pwmchannel\_t

Type of a PWM channel.

typedef uint32\_t pwmchnmsk\_t

Type of a channels mask.

typedef uint32\_t pwmcnt\_t

Type of a PWM counter.

### **Data Structures**

· struct PWMChannelConfig

Type of a PWM driver channel configuration structure.

struct PWMConfig

Type of a PWM driver configuration structure.

struct PWMDriver

Structure representing a PWM driver.

## **Functions**

void pwmInit (void)

PWM Driver initialization.

void pwmObjectInit (PWMDriver \*pwmp)

Initializes the standard part of a PWMDriver structure.

void pwmStart (PWMDriver \*pwmp, const PWMConfig \*config)

Configures and activates the PWM peripheral.

void pwmStop (PWMDriver \*pwmp)

Deactivates the PWM peripheral.

void pwmChangePeriod (PWMDriver \*pwmp, pwmcnt\_t period)

Changes the period the PWM peripheral.

void pwmEnableChannel (PWMDriver \*pwmp, pwmchannel\_t channel, pwmcnt\_t width)

Enables a PWM channel.

void pwmDisableChannel (PWMDriver \*pwmp, pwmchannel t channel)

Disables a PWM channel and its notification.

void pwmEnablePeriodicNotification (PWMDriver \*pwmp)

Enables the periodic activation edge notification.

void pwmDisablePeriodicNotification (PWMDriver \*pwmp)

Disables the periodic activation edge notification.

• void pwmEnableChannelNotification (PWMDriver \*pwmp, pwmchannel\_t channel)

Enables a channel de-activation edge notification.

• void pwmDisableChannelNotification (PWMDriver \*pwmp, pwmchannel\_t channel)

Disables a channel de-activation edge notification.

void pwm Ild init (void)

Low level PWM driver initialization.

void pwm Ild start (PWMDriver \*pwmp)

Configures and activates the PWM peripheral.

void pwm Ild stop (PWMDriver \*pwmp)

Deactivates the PWM peripheral.

void pwm Ild enable channel (PWMDriver \*pwmp, pwmchannel t channel, pwmcnt t width)

Enables a PWM channel.

void pwm\_lld\_disable\_channel (PWMDriver \*pwmp, pwmchannel\_t channel)

Disables a PWM channel and its notification.

void pwm\_lld\_enable\_periodic\_notification (PWMDriver \*pwmp)

Enables the periodic activation edge notification.

void pwm Ild disable periodic notification (PWMDriver \*pwmp)

Disables the periodic activation edge notification.

void pwm IId enable channel notification (PWMDriver \*pwmp, pwmchannel t channel)

Enables a channel de-activation edge notification.

void pwm IId disable channel notification (PWMDriver \*pwmp, pwmchannel t channel)

Disables a channel de-activation edge notification.

## **Enumerations**

### **Variables**

• PWMDriver PWMD1

PWMD1 driver identifier.

## 7.29.4 Macro Definition Documentation

7.29.4.1 #define PWM\_OUTPUT\_MASK 0x0FU

Standard output modes mask.

7.29.4.2 #define PWM\_OUTPUT\_DISABLED 0x00U

Output not driven, callback only.

7.29.4.3 #define PWM\_OUTPUT\_ACTIVE\_HIGH 0x01U

Positive PWM logic, active is logic level one.

7.29.4.4 #define PWM\_OUTPUT\_ACTIVE\_LOW 0x02U

Inverse PWM logic, active is logic level zero.

7.29.4.5 #define PWM\_FRACTION\_TO\_WIDTH( pwmp, denominator, numerator )

### Value:

Converts from fraction to pulse width.

### Note

Be careful with rounding errors, this is integer math not magic. You can specify tenths of thousandth but make sure you have the proper hardware resolution by carefully choosing the clock source and prescaler settings, see PWM\_COMPUTE\_PSC.

#### **Parameters**

| in | pwmp        | pointer to a PWMDriver object |
|----|-------------|-------------------------------|
| in | denominator | denominator of the fraction   |
| in | numerator   | numerator of the fraction     |

### Returns

The pulse width to be passed to pwmEnableChannel().

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.29.4.6 #define PWM\_DEGREES\_TO\_WIDTH( pwmp, degrees ) PWM\_FRACTION\_TO\_WIDTH(pwmp, 36000, degrees)

Converts from degrees to pulse width.

# Note

Be careful with rounding errors, this is integer math not magic. You can specify hundredths of degrees but make sure you have the proper hardware resolution by carefully choosing the clock source and prescaler settings, see PWM\_COMPUTE\_PSC.

### **Parameters**

| in | pwmp    | pointer to a PWMDriver object             |
|----|---------|---|
| in | degrees | degrees as an integer between 0 and 36000 |

## Returns

The pulse width to be passed to pwmEnableChannel().

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.29.4.7 #define PWM\_PERCENTAGE\_TO\_WIDTH( pwmp, percentage ) PWM\_FRACTION\_TO\_WIDTH(pwmp, 10000, percentage)

Converts from percentage to pulse width.

### Note

Be careful with rounding errors, this is integer math not magic. You can specify tenths of thousandth but make sure you have the proper hardware resolution by carefully choosing the clock source and prescaler settings, see PWM COMPUTE PSC.

## **Parameters**

| in | pwmp       | pointer to a PWMDriver object                |
|----|------------|--|
| in | percentage | percentage as an integer between 0 and 10000 |

### Returns

The pulse width to be passed to pwmEnableChannel ().

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.29.4.8 #define pwmChangePeriodI( pwmp, value )

## Value:

```
{
    (pwmp)->period = (value);
    pwm_lld_change_period(pwmp, value);
}
```

Changes the period the PWM peripheral.

This function changes the period of a PWM unit that has already been activated using pwmStart ().

# Precondition

The PWM unit must have been activated using pwmStart ().

# Postcondition

The PWM unit period is changed to the new value.

# Note

If a period is specified that is shorter than the pulse width programmed in one of the channels then the behavior is not guaranteed.

## **Parameters**

| in | pwmp  | pointer to a PWMDriver object |
|----|-------|-------------------------------|
| in | value | new cycle time in ticks       |

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.29.4.9 #define pwmEnableChannell( pwmp, channel, width )

### Value:

```
do {
    (pwmp) ->enabled |= ((pwmchnmsk_t)1U << (pwmchnmsk_t)(channel));
    pwm_lld_enable_channel(pwmp, channel, width);
} while (false)</pre>
```

Enables a PWM channel.

## Precondition

The PWM unit must have been activated using pwmStart ().

### Postcondition

The channel is active using the specified configuration.

### Note

Depending on the hardware implementation this function has effect starting on the next cycle (recommended implementation) or immediately (fallback implementation).

### **Parameters**

| in | pwmp    | pointer to a PWMDriver object          |
|----|---------|--|
| in | channel | PWM channel identifier (0channels-1)   |
| in | width   | PWM pulse width as clock pulses number |

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.29.4.10 #define pwmDisableChannell( pwmp, channel)

### Value:

```
do {
    (pwmp) ->enabled &= ~((pwmchnmsk_t)1U << (pwmchnmsk_t) (channel));
    pwm_lld_disable_channel(pwmp, channel);
} while (false)</pre>
```

Disables a PWM channel.

## Precondition

The PWM unit must have been activated using pwmStart ().

## Postcondition

The channel is disabled and its output line returned to the idle state.

## Note

Depending on the hardware implementation this function has effect starting on the next cycle (recommended implementation) or immediately (fallback implementation).

## **Parameters**

| in | pwmp    | pointer to a PWMDriver object        |
|----|---------|--------------------------------------|
| in | channel | PWM channel identifier (0channels-1) |

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.29.4.11 #define pwmlsChannelEnabledI( pwmp, channel ) (((pwmp)->enabled & ((pwmchnmsk\_t)1U << (pwmchnmsk\_t)(channel))) != 0U)

Returns a PWM channel status.

### Precondition

The PWM unit must have been activated using pwmStart ().

## **Parameters**

| in | pwmp    | pointer to a PWMDriver object        |
|----|---------|--------------------------------------|
| in | channel | PWM channel identifier (0channels-1) |

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.29.4.12 #define pwmEnablePeriodicNotification(( pwmp ) pwm\_IId\_enable\_periodic\_notification(pwmp)

Enables the periodic activation edge notification.

## Precondition

The PWM unit must have been activated using  ${\tt pwmStart}$  () .

## Note

If the notification is already enabled then the call has no effect.

### **Parameters**

| in | pwmp | pointer to a PWMDriver object |
|----|------|-------------------------------|
|----|------|-------------------------------|

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers

7.29.4.13 #define pwmDisablePeriodicNotification( pwmp ) pwm\_IId\_disable\_periodic\_notification(pwmp)

Disables the periodic activation edge notification.

### Precondition

The PWM unit must have been activated using pwmStart ().

#### Note

If the notification is already disabled then the call has no effect.

### **Parameters**

|  | in | pwmp | pointer to a PWMDriver object |
|--|----|------|-------------------------------|
|--|----|------|-------------------------------|

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.29.4.14 #define pwmEnableChannelNotification(( pwmp, channel ) pwm\_IId\_enable\_channel\_notification(pwmp, channel)

Enables a channel de-activation edge notification.

### Precondition

The PWM unit must have been activated using pwmStart().

The channel must have been activated using pwmEnableChannel().

## Note

If the notification is already enabled then the call has no effect.

## **Parameters**

| in | pwmp    | pointer to a PWMDriver object        |
|----|---------|--------------------------------------|
| in | channel | PWM channel identifier (0channels-1) |

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.29.4.15 #define pwmDisableChannelNotification(( pwmp, channel ) pwm\_IId\_disable\_channel\_notification(pwmp, channel)

Disables a channel de-activation edge notification.

### Precondition

```
The PWM unit must have been activated using pwmStart().

The channel must have been activated using pwmEnableChannel().
```

### Note

If the notification is already disabled then the call has no effect.

### **Parameters**

| in | pwmp    | pointer to a PWMDriver object        |
|----|---------|--------------------------------------|
| in | channel | PWM channel identifier (0channels-1) |

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.29.4.16 #define PWM CHANNELS 4

Number of PWM channels per PWM driver.

7.29.4.17 #define PLATFORM\_PWM\_USE\_PWM1 FALSE

PWMD1 driver enable switch.

If set to TRUE the support for PWM1 is included.

Note

The default is FALSE.

7.29.4.18 #define pwm\_lld\_change\_period( pwmp, period )

Changes the period the PWM peripheral.

This function changes the period of a PWM unit that has already been activated using pwmStart ().

## Precondition

The PWM unit must have been activated using pwmStart ().

## Postcondition

The PWM unit period is changed to the new value.

## Note

The function has effect at the next cycle start.

If a period is specified that is shorter than the pulse width programmed in one of the channels then the behavior is not guaranteed.

## **Parameters**

| in | pwmp   | pointer to a PWMDriver object |
|----|--------|-------------------------------|
| in | period | new cycle time in ticks       |

### **Function Class:**

Not an API, this function is for internal use only.

# 7.29.5 Typedef Documentation

7.29.5.1 typedef struct PWMDriver PWMDriver

Type of a structure representing a PWM driver.

7.29.5.2 typedef void(\* pwmcallback\_t) (PWMDriver \*pwmp)

Type of a PWM notification callback.

## **Parameters**

| in | pwmp | pointer to a PWMDriver object |
|----|------|-------------------------------|

7.29.5.3 typedef uint32\_t pwmmode\_t

Type of a PWM mode.

7.29.5.4 typedef uint8\_t pwmchannel\_t

Type of a PWM channel.

7.29.5.5 typedef uint32\_t pwmchnmsk\_t

Type of a channels mask.

7.29.5.6 typedef uint32\_t pwmcnt\_t

Type of a PWM counter.

# 7.29.6 Enumeration Type Documentation

## 7.29.6.1 enum pwmstate\_t

Driver state machine possible states.

### Enumerator

PWM\_UNINIT Not initialized.PWM\_STOP Stopped.PWM\_READY Ready.

## 7.29.7 Function Documentation

7.29.7.1 void pwmlnit (void)

PWM Driver initialization.

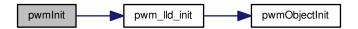
Note

This function is implicitly invoked by halInit(), there is no need to explicitly initialize the driver.

## **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



## 7.29.7.2 void pwmObjectInit ( PWMDriver \* pwmp )

Initializes the standard part of a PWMDriver structure.

### **Parameters**

| out | pwmp | pointer to a PWMDriver object |
|-----|------|-------------------------------|
|-----|------|-------------------------------|

# **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

# 7.29.7.3 void pwmStart ( PWMDriver \* pwmp, const PWMConfig \* config )

Configures and activates the PWM peripheral.

### Note

Starting a driver that is already in the PWM\_READY state disables all the active channels.

## **Parameters**

| in | pwmp   | pointer to a PWMDriver object |
|----|--------|-------------------------------|
| in | config | pointer to a PWMConfig object |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



## 7.29.7.4 void pwmStop ( PWMDriver \* pwmp )

Deactivates the PWM peripheral.

## **Parameters**

| in | pwmp | pointer to a PWMDriver object |
|----|------|-------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



## 7.29.7.5 void pwmChangePeriod ( PWMDriver \* pwmp, pwmcnt\_t period )

Changes the period the PWM peripheral.

This function changes the period of a PWM unit that has already been activated using pwmStart ().

## Precondition

The PWM unit must have been activated using pwmStart ().

## Postcondition

The PWM unit period is changed to the new value.

### Note

If a period is specified that is shorter than the pulse width programmed in one of the channels then the behavior is not guaranteed.

### **Parameters**

| in | pwmp   | pointer to a PWMDriver object |
|----|--------|-------------------------------|
| in | period | new cycle time in ticks       |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.29.7.6 void pwmEnableChannel ( PWMDriver \* pwmp, pwmchannel\_t channel, pwmcnt\_t width )

Enables a PWM channel.

## Precondition

The PWM unit must have been activated using pwmStart ().

## Postcondition

The channel is active using the specified configuration.

### Note

Depending on the hardware implementation this function has effect starting on the next cycle (recommended implementation) or immediately (fallback implementation).

### **Parameters**

| in | pwmp    | pointer to a PWMDriver object          |
|----|---------|--|
| in | channel | PWM channel identifier (0channels-1)   |
| in | width   | PWM pulse width as clock pulses number |

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.29.7.7 void pwmDisableChannel ( PWMDriver \* pwmp, pwmchannel t channel )

Disables a PWM channel and its notification.

### Precondition

The PWM unit must have been activated using pwmStart ().

## Postcondition

The channel is disabled and its output line returned to the idle state.

## Note

Depending on the hardware implementation this function has effect starting on the next cycle (recommended implementation) or immediately (fallback implementation).

### **Parameters**

| in | pwmp    | pointer to a PWMDriver object        |
|----|---------|--------------------------------------|
| in | channel | PWM channel identifier (0channels-1) |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.29.7.8 void pwmEnablePeriodicNotification ( PWMDriver \* pwmp )

Enables the periodic activation edge notification.

## Precondition

The PWM unit must have been activated using pwmStart ().

## Note

If the notification is already enabled then the call has no effect.

## **Parameters**

| in | pwmp | pointer to a PWMDriver object |
|----|------|-------------------------------|
|----|------|-------------------------------|

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.29.7.9 void pwmDisablePeriodicNotification ( PWMDriver \*pwmp )

Disables the periodic activation edge notification.

### Precondition

The PWM unit must have been activated using pwmStart ().

## Note

If the notification is already disabled then the call has no effect.

### **Parameters**

| in | pwmp | pointer to a PWMDriver object | 1 |
|----|------|-------------------------------|---|
|----|------|-------------------------------|---|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.29.7.10 void pwmEnableChannelNotification ( PWMDriver \* pwmp, pwmchannel\_t channel )

Enables a channel de-activation edge notification.

## Precondition

```
The PWM unit must have been activated using pwmStart().

The channel must have been activated using pwmEnableChannel().
```

#### Note

If the notification is already enabled then the call has no effect.

### **Parameters**

| in | pwmp    | pointer to a PWMDriver object        |
|----|---------|--------------------------------------|
| in | channel | PWM channel identifier (0channels-1) |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.29.7.11 void pwmDisableChannelNotification ( PWMDriver \* pwmp, pwmchannel\_t channel )

Disables a channel de-activation edge notification.

### Precondition

```
The PWM unit must have been activated using pwmStart(). The channel must have been activated using pwmEnableChannel().
```

## Note

If the notification is already disabled then the call has no effect.

## **Parameters**

| in | pwmp    | pointer to a PWMDriver object        |
|----|---------|--------------------------------------|
| in | channel | PWM channel identifier (0channels-1) |

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.29.7.12 void pwm\_lld\_init ( void )

Low level PWM driver initialization.

## **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.29.7.13 void pwm\_lld\_start ( PWMDriver \* pwmp )

Configures and activates the PWM peripheral.

Note

Starting a driver that is already in the PWM\_READY state disables all the active channels.

# **Parameters**

| in | pwmp | pointer to a PWMDriver object |
|----|------|-------------------------------|
|----|------|-------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

7.29.7.14 void pwm\_lld\_stop ( PWMDriver \* pwmp )

Deactivates the PWM peripheral.

## **Parameters**

| in | pwmp | pointer to a PWMDriver object |
|----|------|-------------------------------|
|----|------|-------------------------------|

# **Function Class:**

Not an API, this function is for internal use only.

7.29.7.15 void pwm\_lld\_enable\_channel ( PWMDriver \* pwmp, pwmchannel\_t channel, pwmcnt\_t width )

Enables a PWM channel.

## Precondition

The PWM unit must have been activated using pwmStart ().

## Postcondition

The channel is active using the specified configuration.

## Note

The function has effect at the next cycle start. Channel notification is not enabled.

### **Parameters**

| in | pwmp    | pointer to a PWMDriver object          |
|----|---------|--|
| in | channel | PWM channel identifier (0channels-1)   |
| in | width   | PWM pulse width as clock pulses number |

## **Function Class:**

Not an API, this function is for internal use only.

7.29.7.16 void pwm\_lld\_disable\_channel ( PWMDriver \* pwmp, pwmchannel\_t channel )

Disables a PWM channel and its notification.

## Precondition

The PWM unit must have been activated using pwmStart ().

## Postcondition

The channel is disabled and its output line returned to the idle state.

### Note

The function has effect at the next cycle start.

# **Parameters**

| in | pwmp    | pointer to a PWMDriver object        |
|----|---------|--------------------------------------|
| in | channel | PWM channel identifier (0channels-1) |

# **Function Class:**

Not an API, this function is for internal use only.

7.29.7.17 void pwm\_lld\_enable\_periodic\_notification ( PWMDriver \* pwmp )

Enables the periodic activation edge notification.

## Precondition

The PWM unit must have been activated using pwmStart ().

### Note

If the notification is already enabled then the call has no effect.

### **Parameters**

| in | pwmp | pointer to a PWMDriver object |
|----|------|-------------------------------|
|----|------|-------------------------------|

### **Function Class:**

Not an API, this function is for internal use only.

7.29.7.18 void pwm\_lld\_disable\_periodic\_notification ( PWMDriver \* pwmp )

Disables the periodic activation edge notification.

### Precondition

The PWM unit must have been activated using pwmStart ().

### Note

If the notification is already disabled then the call has no effect.

# Parameters

| in | pwmp | pointer to a PWMDriver object |
|----|------|-------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

7.29.7.19 void pwm\_lld\_enable\_channel\_notification ( PWMDriver \* pwmp, pwmchannel\_t channel )

Enables a channel de-activation edge notification.

## Precondition

The PWM unit must have been activated using pwmStart().

The channel must have been activated using pwmEnableChannel().

# Note

If the notification is already enabled then the call has no effect.

### **Parameters**

| in | pwmp    | pointer to a PWMDriver object        |
|----|---------|--------------------------------------|
| in | channel | PWM channel identifier (0channels-1) |

### ChibiOS/HAL

## **Function Class:**

Not an API, this function is for internal use only.

7.29.7.20 void pwm\_lld\_disable\_channel\_notification ( PWMDriver \* pwmp, pwmchannel\_t channel )

Disables a channel de-activation edge notification.

## Precondition

```
The PWM unit must have been activated using pwmStart().

The channel must have been activated using pwmEnableChannel().
```

### Note

If the notification is already disabled then the call has no effect.

## **Parameters**

| in | pwmp    | pointer to a PWMDriver object        |
|----|---------|--------------------------------------|
| in | channel | PWM channel identifier (0channels-1) |

## **Function Class:**

Not an API, this function is for internal use only.

# 7.29.8 Variable Documentation

## 7.29.8.1 PWMDriver PWMD1

PWMD1 driver identifier.

## Note

The driver PWMD1 allocates the complex timer TIM1 when enabled.

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## 7.30 RTC Driver

Generic RTC Driver.

## 7.30.1 Detailed Description

Generic RTC Driver.

This module defines an abstract interface for a Real Time Clock Peripheral.

## Precondition

In order to use the RTC driver the HAL\_USE\_RTC option must be enabled in halconf.h.

## **Macros**

• #define RTC\_BASE\_YEAR 1980U

Base year of the calendar.

• #define \_rtc\_driver\_methods \_file\_stream\_methods

FileStream specific methods.

## Date/Time bit masks for FAT format

- #define RTC FAT TIME SECONDS MASK 0x0000001FU
- #define RTC\_FAT\_TIME\_MINUTES\_MASK 0x000007E0U
- #define RTC\_FAT\_TIME\_HOURS\_MASK 0x0000F800U
- #define RTC\_FAT\_DATE\_DAYS\_MASK 0x001F0000U
- #define RTC\_FAT\_DATE\_MONTHS\_MASK 0x01E00000U
- #define RTC\_FAT\_DATE\_YEARS\_MASK 0xFE000000U

## Day of week encoding

- #define RTC\_DAY\_CATURDAY 0U
- #define RTC\_DAY\_MONDAY 1U
- #define RTC\_DAY\_TUESDAY 2U
- #define RTC DAY WEDNESDAY 3U
- #define RTC\_DAY\_THURSDAY 4U
- #define RTC\_DAY\_FRIDAY 5U
- #define RTC\_DAY\_SATURDAY 6U
- #define RTC\_DAY\_SUNDAY 7U

## Implementation capabilities

• #define RTC\_SUPPORTS\_CALLBACKS TRUE

Callback support int the driver.

#define RTC\_ALARMS 2

Number of alarms available.

#define RTC\_HAS\_STORAGE FALSE

Presence of a local persistent storage.

# **PLATFORM** configuration options

#define PLATFORM RTC USE RTC1 FALSE

RTCD1 driver enable switch.

## **Typedefs**

typedef struct RTCDriver RTCDriver

Type of a structure representing an RTC driver.

· typedef uint32\_t rtcalarm\_t

Type of an RTC alarm number.

typedef void(\* rtccb\_t) (RTCDriver \*rtcp, rtcevent\_t event)

Type of a generic RTC callback.

## **Data Structures**

struct RTCDateTime

Type of a structure representing an RTC date/time stamp.

struct RTCAlarm

Type of a structure representing an RTC alarm time stamp.

struct RTCDriverVMT

RTCDriver virtual methods table.

struct RTCDriver

Structure representing an RTC driver.

# **Functions**

· void rtclnit (void)

RTC Driver initialization.

void rtcObjectInit (RTCDriver \*rtcp)

Initializes a generic RTC driver object.

void rtcSetTime (RTCDriver \*rtcp, const RTCDateTime \*timespec)

Set current time.

void rtcGetTime (RTCDriver \*rtcp, RTCDateTime \*timespec)

Get current time.

void rtcSetAlarm (RTCDriver \*rtcp, rtcalarm\_t alarm, const RTCAlarm \*alarmspec)

Set alarm time.

void rtcGetAlarm (RTCDriver \*rtcp, rtcalarm\_t alarm, RTCAlarm \*alarmspec)

Get current alarm.

void rtcSetCallback (RTCDriver \*rtcp, rtccb\_t callback)

Enables or disables RTC callbacks.

• void rtcConvertDateTimeToStructTm (const RTCDateTime \*timespec, struct tm \*timp, uint32\_t \*tv\_msec)

Convert RTCDateTime to broken-down time structure.

void rtcConvertStructTmToDateTime (const struct tm \*timp, uint32 t tv msec, RTCDateTime \*timespec)

Convert broken-down time structure to RTCDateTime.

• uint32\_t rtcConvertDateTimeToFAT (const RTCDateTime \*timespec)

Get current time in format suitable for usage in FAT file system.

void rtc\_lld\_init (void)

Enable access to registers.

void rtc\_lld\_set\_time (RTCDriver \*rtcp, const RTCDateTime \*timespec)

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Set current time.

void rtc\_lld\_get\_time (RTCDriver \*rtcp, RTCDateTime \*timespec)

Get current time.

void rtc\_lld\_set\_alarm (RTCDriver \*rtcp, rtcalarm\_t alarm, const RTCAlarm \*alarmspec)
 Set alarm time.

void rtc\_lld\_get\_alarm (RTCDriver \*rtcp, rtcalarm\_t alarm, RTCAlarm \*alarmspec)
 Get alarm time.

### **Enumerations**

#### **Variables**

• RTCDriver RTCD1

RTC driver identifier.

### 7.30.2 Macro Definition Documentation

7.30.2.1 #define RTC\_BASE\_YEAR 1980U

Base year of the calendar.

7.30.2.2 #define RTC\_SUPPORTS\_CALLBACKS TRUE

Callback support int the driver.

7.30.2.3 #define RTC\_ALARMS 2

Number of alarms available.

7.30.2.4 #define RTC\_HAS\_STORAGE FALSE

Presence of a local persistent storage.

7.30.2.5 #define PLATFORM\_RTC\_USE\_RTC1 FALSE

RTCD1 driver enable switch.

If set to TRUE the support for RTC1 is included.

Note

The default is FALSE.

7.30.2.6 #define \_rtc\_driver\_methods \_file\_stream\_methods

FileStream specific methods.

### 7.30.3 Typedef Documentation

7.30.3.1 typedef struct RTCDriver RTCDriver

Type of a structure representing an RTC driver.

7.30.3.2 typedef uint32\_t rtcalarm\_t

Type of an RTC alarm number.

7.30.3.3 typedef void(\* rtccb\_t) (RTCDriver \*rtcp, rtcevent\_t event)

Type of a generic RTC callback.

# 7.30.4 Enumeration Type Documentation

7.30.4.1 enum rtcevent\_t

Type of an RTC event.

#### 7.30.5 Function Documentation

7.30.5.1 void rtclnit (void)

RTC Driver initialization.

Note

This function is implicitly invoked by halInit(), there is no need to explicitly initialize the driver.

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



# 7.30.5.2 void rtcObjectInit ( RTCDriver \* rtcp )

Initializes a generic RTC driver object.

The HW dependent part of the initialization has to be performed outside, usually in the hardware initialization code.

# **Parameters**

|  | out | rtcp | pointer to RTC driver structure |
|--|-----|------|---------------------------------|
|--|-----|------|---------------------------------|

#### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

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7.30.5.3 void rtcSetTime ( RTCDriver \* rtcp, const RTCDateTime \* timespec )

Set current time.

#### Note

This function can be called from any context but limitations could be imposed by the low level implementation. It is guaranteed that the function can be called from thread context.

The function can be reentrant or not reentrant depending on the low level implementation.

#### **Parameters**

| in | rtcp     | pointer to RTC driver structure    |
|----|----------|------------------------------------|
| in | timespec | pointer to a RTCDateTime structure |

### **Function Class:**

Special function, this function has special requirements see the notes.

Here is the call graph for this function:



7.30.5.4 void rtcGetTime ( RTCDriver \* rtcp, RTCDateTime \* timespec )

Get current time.

#### Note

This function can be called from any context but limitations could be imposed by the low level implementation. It is guaranteed that the function can be called from thread context.

The function can be reentrant or not reentrant depending on the low level implementation.

### **Parameters**

|   | in  | rtcp     | pointer to RTC driver structure    |
|---|-----|----------|------------------------------------|
| Ī | out | timespec | pointer to a RTCDateTime structure |

### **Function Class:**

Special function, this function has special requirements see the notes.

Here is the call graph for this function:



7.30.5.5 void rtcSetAlarm ( RTCDriver \* rtcp, rtcalarm\_t alarm, const RTCAlarm \* alarmspec )

Set alarm time.

#### Note

This function can be called from any context but limitations could be imposed by the low level implementation. It is guaranteed that the function can be called from thread context.

The function can be reentrant or not reentrant depending on the low level implementation.

#### **Parameters**

| in | rtcp      | pointer to RTC driver structure         |
|----|-----------|---|
| in | alarm     | alarm identifier                        |
| in | alarmspec | pointer to a RTCAlarm structure or NULL |

#### **Function Class:**

Special function, this function has special requirements see the notes.

Here is the call graph for this function:



7.30.5.6 void rtcGetAlarm ( RTCDriver \* rtcp, rtcalarm\_t alarm, RTCAlarm \* alarmspec )

Get current alarm.

#### Note

If an alarm has not been set then the returned alarm specification is not meaningful.

This function can be called from any context but limitations could be imposed by the low level implementation. It is guaranteed that the function can be called from thread context.

The function can be reentrant or not reentrant depending on the low level implementation.

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#### **Parameters**

| in  | rtcp      | pointer to RTC driver structure |
|-----|-----------|---------------------------------|
| in  | alarm     | alarm identifier                |
| out | alarmspec | pointer to a RTCAlarm structure |

### **Function Class:**

Special function, this function has special requirements see the notes.

Here is the call graph for this function:



7.30.5.7 void rtcSetCallback ( RTCDriver \* rtcp, rtccb\_t callback )

Enables or disables RTC callbacks.

This function enables or disables the callback, use a  $\mathtt{NULL}$  pointer in order to disable it.

### Note

This function can be called from any context but limitations could be imposed by the low level implementation. It is guaranteed that the function can be called from thread context.

The function can be reentrant or not reentrant depending on the low level implementation.

### **Parameters**

| in | rtcp     | pointer to RTC driver structure   |
|----|----------|-----------------------------------|
| in | callback | callback function pointer or NULL |

### **Function Class:**

Special function, this function has special requirements see the notes.

7.30.5.8 void rtcConvertDateTimeToStructTm ( const RTCDateTime \* timespec, struct tm \* timp, uint32\_t \* tv\_msec )

Convert RTCDateTime to broken-down time structure.

### **Parameters**

| in  | timespec | pointer to a RTCDateTime structure      |
|-----|----------|---|
| out | timp     | pointer to a broken-down time structure |
| out | tv msec  | pointer to milliseconds value or NULL   |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.30.5.9 void rtcConvertStructTmToDateTime ( const struct tm \* timp, uint32\_t tv\_msec, RTCDateTime \* timespec )

Convert broken-down time structure to RTCDateTime.

#### **Parameters**

| in  | timp     | pointer to a broken-down time structure |
|-----|----------|---|
| in  | tv_msec  | milliseconds value                      |
| out | timespec | pointer to a RTCDateTime structure      |

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.30.5.10 uint32\_t rtcConvertDateTimeToFAT ( const RTCDateTime \* timespec )

Get current time in format suitable for usage in FAT file system.

#### Note

The information about day of week and DST is lost in DOS format, the second field loses its least significant bit

#### **Parameters**

| out | timespec | pointer to a RTCDateTime structure |
|-----|----------|------------------------------------|

#### Returns

FAT date/time value.

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.30.5.11 void rtc\_lld\_init (void)

Enable access to registers.

### **Function Class:**

Not an API, this function is for internal use only.

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Here is the call graph for this function:



7.30.5.12 void rtc\_lld\_set\_time ( RTCDriver \* rtcp, const RTCDateTime \* timespec )

Set current time.

Note

Fractional part will be silently ignored. There is no possibility to set it on PLATFORM platform. The function can be called from any context.

#### **Parameters**

| in | rtcp     | pointer to RTC driver structure    |
|----|----------|------------------------------------|
| in | timespec | pointer to a RTCDateTime structure |

#### **Function Class:**

Not an API, this function is for internal use only.

7.30.5.13 void rtc\_lld\_get\_time ( RTCDriver \* rtcp, RTCDateTime \* timespec )

Get current time.

Note

The function can be called from any context.

## **Parameters**

| in  | rtcp     | pointer to RTC driver structure    |
|-----|----------|------------------------------------|
| out | timespec | pointer to a RTCDateTime structure |

# **Function Class:**

Not an API, this function is for internal use only.

7.30.5.14 void rtc\_lld\_set\_alarm ( RTCDriver \* rtcp, rtcalarm\_t alarm, const RTCAlarm \* alarmspec )

Set alarm time.

#### Note

Default value after BKP domain reset for both comparators is 0. Function does not performs any checks of alarm time validity. The function can be called from any context.

#### **Parameters**

| in | rtcp      | pointer to RTC driver structure. |
|----|-----------|----------------------------------|
| in | alarm     | alarm identifier. Can be 1 or 2. |
| in | alarmspec | pointer to a RTCAlarm structure. |

#### **Function Class:**

Not an API, this function is for internal use only.

7.30.5.15 void rtc\_lld\_get\_alarm ( RTCDriver \* rtcp, rtcalarm\_t alarm, RTCAlarm \* alarmspec )

Get alarm time.

#### Note

The function can be called from any context.

#### **Parameters**

| in  | rtcp      | pointer to RTC driver structure |
|-----|-----------|---------------------------------|
| in  | alarm     | alarm identifier                |
| out | alarmspec | pointer to a RTCAlarm structure |

# **Function Class:**

Not an API, this function is for internal use only.

7.30.6 Variable Documentation

7.30.6.1 RTCDriver RTCD1

RTC driver identifier.

### 7.31 SDC Driver

Generic SD Card Driver.

### 7.31.1 Detailed Description

Generic SD Card Driver.

This module implements a generic SDC (Secure Digital Card) driver.

#### Precondition

In order to use the SDC driver the <code>HAL\_USE\_SDC</code> option must be enabled in <code>halconf.h</code>.

#### 7.31.2 Driver State Machine

This driver implements a state machine internally, see the Abstract I/O Block Device module documentation for details.

### 7.31.3 Driver Operations

This driver allows to read or write single or multiple 512 bytes blocks on a SD Card.

### **Macros**

• #define \_sdc\_driver\_methods \_mmcsd\_block\_device\_methods

SDCDriver specific methods.

### SD card types

- #define SDC\_MODE\_CARDTYPE\_MASK 0xFU
- #define SDC MODE CARDTYPE SDV11 0U
- #define SDC\_MODE\_CARDTYPE\_SDV20 1U
- #define SDC\_MODE\_CARDTYPE\_MMC 2U
- #define SDC\_MODE\_HIGH\_CAPACITY 0x10U

#### SDC bus error conditions

- #define SDC\_NO\_ERROR 0U
- #define SDC CMD CRC ERROR 1U
- #define SDC\_DATA\_CRC\_ERROR 2U
- #define SDC\_DATA\_TIMEOUT 4U
- #define SDC COMMAND TIMEOUT 8U
- #define SDC\_TX\_UNDERRUN 16U
- #define SDC\_RX\_OVERRUN 32U
- #define SDC\_STARTBIT\_ERROR 64U
- #define SDC\_OVERFLOW\_ERROR 128U
- #define SDC\_UNHANDLED\_ERROR 0xFFFFFFFU

# **SDC** configuration options

• #define SDC\_INIT\_RETRY 100

Number of initialization attempts before rejecting the card.

• #define SDC MMC SUPPORT FALSE

Include support for MMC cards.

#define SDC\_NICE\_WAITING TRUE

Delays insertions.

#define SDC\_INIT\_OCR\_V20 0x50FF8000U

OCR initialization constant for V20 cards.

• #define SDC\_INIT\_OCR 0x80100000U

OCR initialization constant for non-V20 cards.

#### **Macro Functions**

#define sdclsCardInserted(sdcp) (sdc\_lld\_is\_card\_inserted(sdcp))

Returns the card insertion status.

• #define sdclsWriteProtected(sdcp) (sdc\_lld\_is\_write\_protected(sdcp))

Returns the write protect status.

# **PLATFORM** configuration options

• #define PLATFORM SDC USE SDC1 FALSE

PWMD1 driver enable switch.

# **Typedefs**

• typedef uint32\_t sdcmode\_t

Type of card flags.

• typedef uint32\_t sdcflags\_t

SDC Driver condition flags type.

• typedef struct SDCDriver SDCDriver

Type of a structure representing an SDC driver.

# **Data Structures**

• struct SDCConfig

Driver configuration structure.

struct SDCDriverVMT

SDCDriver virtual methods table.

struct SDCDriver

Structure representing an SDC driver.

### **Functions**

static bool mode\_detect (SDCDriver \*sdcp)

Detects card mode.

static bool mmc init (SDCDriver \*sdcp)

Init procedure for MMC.

• static bool sdc\_init (SDCDriver \*sdcp)

Init procedure for SDC.

• static uint32\_t mmc\_cmd6\_construct (mmc\_switch\_t access, uint32\_t idx, uint32\_t value, uint32\_t cmd\_set)

Constructs CMD6 argument for MMC.

static uint32\_t sdc\_cmd6\_construct (sd\_switch\_t mode, sd\_switch\_function\_t function, uint32\_t value)

Constructs CMD6 argument for SDC.

static uint16 t sdc cmd6 extract info (sd switch function t function, const uint8 t \*buf)

Extracts information from CMD6 answer.

static bool sdc cmd6 check status (sd switch function t function, const uint8 t \*buf)

Checks status after switching using CMD6.

static bool sdc\_detect\_bus\_clk (SDCDriver \*sdcp, sdcbusclk\_t \*clk)

Reads supported bus clock and switch SDC to appropriate mode.

static bool mmc\_detect\_bus\_clk (SDCDriver \*sdcp, sdcbusclk\_t \*clk)

Reads supported bus clock and switch MMC to appropriate mode.

• static bool detect\_bus\_clk (SDCDriver \*sdcp, sdcbusclk\_t \*clk)

Reads supported bus clock and switch card to appropriate mode.

static bool sdc\_set\_bus\_width (SDCDriver \*sdcp)

Sets bus width for SDC.

static bool mmc\_set\_bus\_width (SDCDriver \*sdcp)

Sets bus width for MMC.

bool sdc wait for transfer state (SDCDriver \*sdcp)

Wait for the card to complete pending operations.

· void sdcInit (void)

SDC Driver initialization.

void sdcObjectInit (SDCDriver \*sdcp)

Initializes the standard part of a SDCDriver structure.

void sdcStart (SDCDriver \*sdcp, const SDCConfig \*config)

Configures and activates the SDC peripheral.

void sdcStop (SDCDriver \*sdcp)

Deactivates the SDC peripheral.

bool sdcConnect (SDCDriver \*sdcp)

Performs the initialization procedure on the inserted card.

bool sdcDisconnect (SDCDriver \*sdcp)

Brings the driver in a state safe for card removal.

• bool sdcRead (SDCDriver \*sdcp, uint32\_t startblk, uint8\_t \*buf, uint32\_t n)

Reads one or more blocks.

• bool sdcWrite (SDCDriver \*sdcp, uint32\_t startblk, const uint8\_t \*buf, uint32\_t n)

Writes one or more blocks.

sdcflags\_t sdcGetAndClearErrors (SDCDriver \*sdcp)

Returns the errors mask associated to the previous operation.

bool sdcSync (SDCDriver \*sdcp)

Waits for card idle condition.

bool sdcGetInfo (SDCDriver \*sdcp, BlockDeviceInfo \*bdip)

Returns the media info.

bool sdcErase (SDCDriver \*sdcp, uint32\_t startblk, uint32\_t endblk)

Erases the supplied blocks.

void sdc\_lld\_init (void)

Low level SDC driver initialization.

void sdc\_lld\_start (SDCDriver \*sdcp)

Configures and activates the SDC peripheral.

void sdc\_lld\_stop (SDCDriver \*sdcp)

Deactivates the SDC peripheral.

void sdc\_lld\_start\_clk (SDCDriver \*sdcp)

Starts the SDIO clock and sets it to init mode (400kHz or less).

• void sdc lld set data clk (SDCDriver \*sdcp, sdcbusclk t clk)

Sets the SDIO clock to data mode (25MHz or less).

void sdc\_lld\_stop\_clk (SDCDriver \*sdcp)

Stops the SDIO clock.

void sdc\_lld\_set\_bus\_mode (SDCDriver \*sdcp, sdcbusmode\_t mode)

Switches the bus to 4 bits mode.

void sdc Ild send cmd none (SDCDriver \*sdcp, uint8 t cmd, uint32 t arg)

Sends an SDIO command with no response expected.

bool sdc\_lld\_send\_cmd\_short (SDCDriver \*sdcp, uint8\_t cmd, uint32\_t arg, uint32\_t \*resp)

Sends an SDIO command with a short response expected.

• bool sdc\_lld\_send\_cmd\_short\_crc (SDCDriver \*sdcp, uint8\_t cmd, uint32\_t arg, uint32\_t \*resp)

Sends an SDIO command with a short response expected and CRC.

bool sdc lld send cmd long crc (SDCDriver \*sdcp, uint8 t cmd, uint32 t arg, uint32 t \*resp)

Sends an SDIO command with a long response expected and CRC.

bool sdc\_lld\_read (SDCDriver \*sdcp, uint32\_t startblk, uint8\_t \*buf, uint32\_t n)

Reads one or more blocks.

• bool sdc\_lld\_write (SDCDriver \*sdcp, uint32\_t startblk, const uint8\_t \*buf, uint32\_t n)

Writes one or more blocks.

bool sdc\_lld\_sync (SDCDriver \*sdcp)

Waits for card idle condition.

#### **Enumerations**

# Variables

static const struct SDCDriverVMT sdc vmt

Virtual methods table.

SDCDriver SDCD1

SDCD1 driver identifier.

### 7.31.4 Macro Definition Documentation

### 7.31.4.1 #define SDC\_INIT\_RETRY 100

Number of initialization attempts before rejecting the card.

Note

Attempts are performed at 10mS intervals.

### 7.31.4.2 #define SDC\_MMC\_SUPPORT FALSE

Include support for MMC cards.

Note

MMC support is not yet implemented so this option must be kept at FALSE.

#### 7.31.4.3 #define SDC\_NICE\_WAITING TRUE

Delays insertions.

If enabled this options inserts delays into the MMC waiting routines releasing some extra CPU time for the threads with lower priority, this may slow down the driver a bit however.

7.31.4.4 #define SDC\_INIT\_OCR\_V20 0x50FF8000U

OCR initialization constant for V20 cards.

7.31.4.5 #define SDC\_INIT\_OCR 0x80100000U

OCR initialization constant for non-V20 cards.

7.31.4.6 #define sdclsCardInserted( sdcp ) (sdc\_lld\_is\_card\_inserted(sdcp))

Returns the card insertion status.

Note

This macro wraps a low level function named  $sdc\_lld\_is\_card\_inserted()$ , this function must be provided by the application because it is not part of the SDC driver.

#### **Parameters**

|  | in | sdcp | pointer to the SDCDriver object |
|--|----|------|---------------------------------|
|--|----|------|---------------------------------|

#### Returns

The card state.

### **Return values**

| FALSE | card not inserted. |
|-------|--------------------|
| TRUE  | card inserted.     |

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.31.4.7 #define sdclsWriteProtected( sdcp ) (sdc\_lld\_is\_write\_protected(sdcp))

Returns the write protect status.

Note

This macro wraps a low level function named  $sdc_lld_is_write_protected()$ , this function must be provided by the application because it is not part of the SDC driver.

#### **Parameters**

| in | sdcp | pointer to the SDCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### Returns

The card state.

#### **Return values**

| FALSE | not write protected. |
|-------|----------------------|
| TRUE  | write protected.     |

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.31.4.8 #define PLATFORM\_SDC\_USE\_SDC1 FALSE

PWMD1 driver enable switch.

If set to TRUE the support for PWM1 is included.

Note

The default is FALSE.

7.31.4.9 #define \_sdc\_driver\_methods \_mmcsd\_block\_device\_methods

SDCDriver specific methods.

# 7.31.5 Typedef Documentation

7.31.5.1 typedef uint32\_t sdcmode\_t

Type of card flags.

7.31.5.2 typedef uint32\_t sdcflags\_t

SDC Driver condition flags type.

7.31.5.3 typedef struct SDCDriver SDCDriver

Type of a structure representing an SDC driver.

# 7.31.6 Enumeration Type Documentation

7.31.6.1 enum mmc\_switch\_t

MMC switch mode.

7.31.6.2 enum sd\_switch\_t

SDC switch mode.

7.31.6.3 enum sd\_switch\_function\_t

SDC switch function.

7.31.6.4 enum sdcbusmode\_t

Type of SDIO bus mode.

7.31.6.5 enum sdcbusclk\_t

Max supported clock.

### 7.31.7 Function Documentation

**7.31.7.1** static bool mode\_detect ( SDCDriver \* sdcp ) [static]

Detects card mode.

#### **Parameters**

| in | sdcp | pointer to the SDCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### Returns

The operation status.

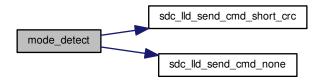
### Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

# **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



**7.31.7.2** static bool mmc\_init ( SDCDriver \* sdcp ) [static]

Init procedure for MMC.

# **Parameters**

| i | n | sdcp | pointer to the SDCDriver object |
|---|---|------|---------------------------------|
|---|---|------|---------------------------------|

### Returns

The operation status.

# **Return values**

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

### **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



**7.31.7.3** static bool sdc\_init( SDCDriver \* sdcp ) [static]

Init procedure for SDC.

### **Parameters**

| i | n | sdcp | pointer to the SDCDriver object |
|---|---|------|---------------------------------|
|---|---|------|---------------------------------|

# Returns

The operation status.

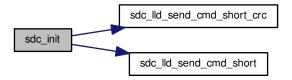
### Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

### **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.31.7.4 static uint32\_t mmc\_cmd6\_construct ( mmc\_switch\_t access, uint32\_t idx, uint32\_t value, uint32\_t cmd\_set ) [static]

Constructs CMD6 argument for MMC.

#### **Parameters**

| in | access  | EXT_CSD access mode                 |
|----|---------|-------------------------------------|
| in | idx     | EXT_CSD byte number                 |
| in | value   | value to be written in target field |
| in | cmd_set | switch current command set          |

### Returns

CMD6 argument.

# **Function Class:**

Not an API, this function is for internal use only.

7.31.7.5 static uint32\_t sdc\_cmd6\_construct ( sd\_switch\_t mode, sd\_switch\_function\_t function, uint32\_t value ) [static]

Constructs CMD6 argument for SDC.

#### **Parameters**

|   | in | mode     | switch/test mode                       |
|---|----|----------|--|
|   | in | function | function number to be switched         |
| ĺ | in | value    | value to be written in target function |

#### Returns

CMD6 argument.

# **Function Class:**

Not an API, this function is for internal use only.

7.31.7.6 static uint16\_t sdc\_cmd6\_extract\_info ( sd\_switch\_function\_t function, const uint8\_t \* buf ) [static]

Extracts information from CMD6 answer.

#### **Parameters**

| in | function | function number to be switched |
|----|----------|--------------------------------|
| in | buf      | buffer with answer             |

#### Returns

extracted answer.

### **Function Class:**

Not an API, this function is for internal use only.

7.31.7.7 static bool sdc\_cmd6\_check\_status ( sd\_switch\_function\_t function, const uint8\_t \* buf ) [static]

Checks status after switching using CMD6.

## Parameters

| in | function | function number to be switched |
|----|----------|--------------------------------|
| in | buf      | buffer with answer             |

#### Returns

The operation status.

### Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

## **Function Class:**

Not an API, this function is for internal use only.

7.31.7.8 static bool sdc\_detect\_bus\_clk ( SDCDriver \* sdcp, sdcbusclk\_t \* clk ) [static]

Reads supported bus clock and switch SDC to appropriate mode.

### **Parameters**

| in  | sdcp | pointer to the SDCDriver object |
|-----|------|---------------------------------|
| out | clk  | pointer to clock enum           |

#### Returns

The operation status.

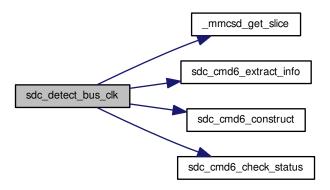
### Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

# **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.31.7.9 static bool mmc\_detect\_bus\_clk ( SDCDriver \* sdcp, sdcbusclk\_t \* clk ) [static]

Reads supported bus clock and switch MMC to appropriate mode.

## **Parameters**

| in  | sdcp | pointer to the SDCDriver object |
|-----|------|---------------------------------|
| out | clk  | pointer to clock enum           |

# Returns

The operation status.

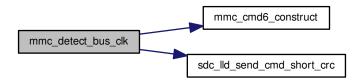
# Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

### **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.31.7.10 static bool detect\_bus\_clk( SDCDriver \* sdcp, sdcbusclk\_t \* clk ) [static]

Reads supported bus clock and switch card to appropriate mode.

### **Parameters**

| in  | sdcp | pointer to the SDCDriver object |
|-----|------|---------------------------------|
| out | clk  | pointer to clock enum           |

### Returns

The operation status.

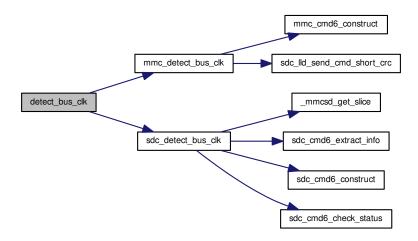
# Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

### **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



**7.31.7.11** static bool sdc\_set\_bus\_width ( SDCDriver \* sdcp ) [static]

Sets bus width for SDC.

#### **Parameters**

| in | sdcp | pointer to the SDCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

# Returns

The operation status.

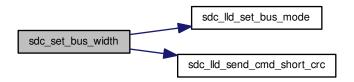
### Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

### **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



**7.31.7.12** static bool mmc\_set\_bus\_width ( SDCDriver \* sdcp ) [static]

Sets bus width for MMC.

#### **Parameters**

| in | sdcp | pointer to the SDCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### Returns

The operation status.

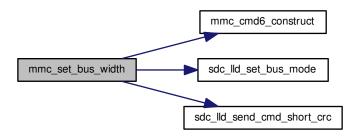
### Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

# **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



### 7.31.7.13 bool\_sdc\_wait\_for\_transfer\_state ( SDCDriver \* sdcp )

Wait for the card to complete pending operations.

### **Parameters**

| in | sdcp | pointer to the SDCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### Returns

The operation status.

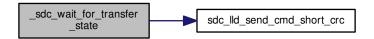
#### Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

### **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.31.7.14 void sdcInit (void)

SDC Driver initialization.

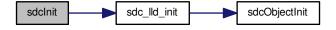
Note

This function is implicitly invoked by halInit(), there is no need to explicitly initialize the driver.

#### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



# 7.31.7.15 void sdcObjectInit ( SDCDriver \* sdcp )

Initializes the standard part of a SDCDriver structure.

#### **Parameters**

|  | out | sdcp | pointer to the SDCDriver object | Ì |
|--|-----|------|---------------------------------|---|
|--|-----|------|---------------------------------|---|

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

# 7.31.7.16 void sdcStart ( SDCDriver \* sdcp, const SDCConfig \* config )

Configures and activates the SDC peripheral.

#### **Parameters**

| in | sdcp   | pointer to the SDCDriver object  |
|----|--------|--|
| in | config | pointer to the SDCConfig object, can be NULL if the driver supports a default configuration or |
|    |        | requires no configuration  |

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.31.7.17 void sdcStop ( SDCDriver \* sdcp )

Deactivates the SDC peripheral.

#### **Parameters**

| Ī | in | sdcp | pointer to the SDCDriver object |  |
|---|----|------|---------------------------------|--|
|---|----|------|---------------------------------|--|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.31.7.18 bool sdcConnect ( SDCDriver \* sdcp )

Performs the initialization procedure on the inserted card.

This function should be invoked when a card is inserted and brings the driver in the  $BLK\_READY$  state where it is possible to perform read and write operations.

### **Parameters**

| in | sdcp | pointer to the SDCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### Returns

The operation status.

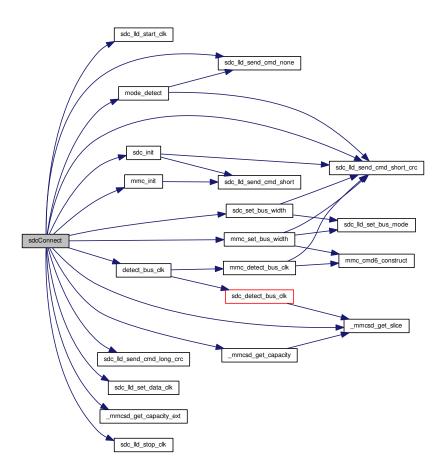
# Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.31.7.19 bool sdcDisconnect ( SDCDriver \* sdcp )

Brings the driver in a state safe for card removal.

### **Parameters**

| in | sdcp | pointer to the SDCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### Returns

The operation status.

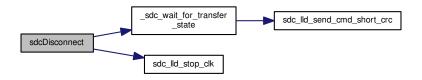
# Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.31.7.20 bool sdcRead ( SDCDriver \* sdcp, uint32\_t startblk, uint8\_t \* buf, uint32\_t n )

Reads one or more blocks.

### Precondition

The driver must be in the BLK\_READY state after a successful sdcConnect() invocation.

#### **Parameters**

| in  | sdcp     | pointer to the SDCDriver object |
|-----|----------|---------------------------------|
| in  | startblk | first block to read             |
| out | buf      | pointer to the read buffer      |
| in  | n        | number of blocks to read        |

## Returns

The operation status.

### Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.31.7.21 bool sdcWrite ( SDCDriver \* sdcp, uint32\_t startblk, const uint8\_t \* buf, uint32\_t n )

Writes one or more blocks.

### Precondition

The driver must be in the BLK\_READY state after a successful sdcConnect() invocation.

#### **Parameters**

| in  | sdcp     | pointer to the SDCDriver object |
|-----|----------|---------------------------------|
| in  | startblk | first block to write            |
| out | buf      | pointer to the write buffer     |
| in  | n        | number of blocks to write       |

### Returns

The operation status.

### Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.31.7.22 sdcflags\_t sdcGetAndClearErrors ( SDCDriver \* sdcp )

Returns the errors mask associated to the previous operation.

# **Parameters**

|  | in | sdcp | pointer to the SDCDriver object |  |
|--|----|------|---------------------------------|--|
|--|----|------|---------------------------------|--|

#### Returns

The errors mask.

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

### 7.31.7.23 bool sdcSync ( SDCDriver \* sdcp )

Waits for card idle condition.

#### **Parameters**

| in | sdcp | pointer to the SDCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### Returns

The operation status.

#### Return values

| HAL_SUCCESS | the operation succeeded. |
|-------------|--------------------------|
| HAL_FAILED  | the operation failed.    |

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



### 7.31.7.24 bool sdcGetInfo ( SDCDriver \* sdcp, BlockDeviceInfo \* bdip )

Returns the media info.

### **Parameters**

| in  | sdcp | pointer to the SDCDriver object        |
|-----|------|--|
| out | bdip | pointer to a BlockDeviceInfo structure |

#### Returns

The operation status.

#### Return values

| HAL_SUCCESS | the operation succeeded. |
|-------------|--------------------------|
| HAL_FAILED  | the operation failed.    |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.31.7.25 bool sdcErase ( SDCDriver \* sdcp, uint32\_t startblk, uint32\_t endblk )

Erases the supplied blocks.

### **Parameters**

| in | sdcp     | pointer to the SDCDriver object |
|----|----------|---------------------------------|
| in | startblk | starting block number           |
| in | endblk   | ending block number             |

#### Returns

The operation status.

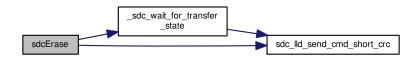
# Return values

| HAL_SUCCESS | the operation succeeded. |
|-------------|--------------------------|
| HAL_FAILED  | the operation failed.    |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.31.7.26 void sdc\_lld\_init (void)

Low level SDC driver initialization.

### **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.31.7.27 void sdc\_lld\_start ( SDCDriver \* sdcp )

Configures and activates the SDC peripheral.

#### **Parameters**

| in | sdcp | pointer to the SDCDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### **Function Class:**

Not an API, this function is for internal use only.

7.31.7.28 void sdc\_lld\_stop ( SDCDriver \* sdcp )

Deactivates the SDC peripheral.

### **Parameters**

```
in sdcp pointer to the SDCDriver object
```

# **Function Class:**

Not an API, this function is for internal use only.

7.31.7.29 void sdc\_lld\_start\_clk ( SDCDriver \* sdcp )

Starts the SDIO clock and sets it to init mode (400kHz or less).

#### **Parameters**

| in  | cdon | pointer to the SDCDriver object |
|-----|------|---------------------------------|
| T11 | Sucp | pointer to the SDCDIIVer object |

#### **Function Class:**

Not an API, this function is for internal use only.

7.31.7.30 void sdc\_lld\_set\_data\_clk ( SDCDriver \* sdcp, sdcbusclk\_t clk )

Sets the SDIO clock to data mode (25MHz or less).

### **Parameters**

| in | sdcp | pointer to the SDCDriver object |  |
|----|------|---------------------------------|--|
| in | clk  | the clock mode                  |  |

### **Function Class:**

Not an API, this function is for internal use only.

7.31.7.31 void sdc\_lld\_stop\_clk ( SDCDriver \* sdcp )

Stops the SDIO clock.

#### **Parameters**

| i | n | sdcp | pointer to the SDCDriver object |
|---|---|------|---------------------------------|
|---|---|------|---------------------------------|

### **Function Class:**

Not an API, this function is for internal use only.

7.31.7.32 void sdc\_lld\_set\_bus\_mode ( SDCDriver \* sdcp, sdcbusmode\_t mode )

Switches the bus to 4 bits mode.

# **Parameters**

| in | sdcp | pointer to the SDCDriver object |
|----|------|---------------------------------|
| in | mode | bus mode                        |

### **Function Class:**

Not an API, this function is for internal use only.

7.31.7.33 void sdc\_lld\_send\_cmd\_none ( SDCDriver \* sdcp, uint8\_t cmd, uint32\_t arg )

Sends an SDIO command with no response expected.

#### **Parameters**

| in | sdcp | pointer to the SDCDriver object |  |
|----|------|---------------------------------|--|
| in | cmd  | card command                    |  |
| in | arg  | command argument                |  |

### **Function Class:**

Not an API, this function is for internal use only.

 $7.31.7.34 \quad bool\ sdc\_lld\_send\_cmd\_short\ (\ \ SDCDriver** \textit{sdcp},\ uint8\_t\ \textit{cmd},\ uint32\_t\ \textit{arg},\ uint32\_t** \textit{resp}\ )$ 

Sends an SDIO command with a short response expected.

#### Note

The CRC is not verified.

#### **Parameters**

| in  | sdcp | pointer to the SDCDriver object           |
|-----|------|---|
| in  | cmd  | card command                              |
| in  | arg  | command argument                          |
| out | resp | pointer to the response buffer (one word) |

#### Returns

The operation status.

#### Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

# **Function Class:**

Not an API, this function is for internal use only.

7.31.7.35 bool sdc\_lld\_send\_cmd\_short\_crc ( SDCDriver \* sdcp, uint8\_t cmd, uint32\_t arg, uint32\_t \* resp )

Sends an SDIO command with a short response expected and CRC.

### Parameters

| in  | sdcp | pointer to the SDCDriver object           |  |
|-----|------|---|--|
| in  | cmd  | card command                              |  |
| in  | arg  | command argument                          |  |
| out | resp | pointer to the response buffer (one word) |  |

#### Returns

The operation status.

# Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

### **Function Class:**

Not an API, this function is for internal use only.

7.31.7.36 bool sdc\_lld\_send\_cmd\_long\_crc ( SDCDriver \* sdcp, uint8\_t cmd, uint32\_t arg, uint32\_t \* resp )

Sends an SDIO command with a long response expected and CRC.

#### **Parameters**

| in  | sdcp | pointer to the SDCDriver object             |  |
|-----|------|---|--|
| in  | cmd  | card command                                |  |
| in  | arg  | command argument                            |  |
| out | resp | pointer to the response buffer (four words) |  |

#### Returns

The operation status.

#### Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

### **Function Class:**

Not an API, this function is for internal use only.

7.31.7.37 bool sdc\_lld\_read ( SDCDriver \* sdcp, uint32\_t startblk, uint8\_t \* buf, uint32\_t n )

Reads one or more blocks.

# **Parameters**

| in  | sdcp     | pointer to the SDCDriver object |
|-----|----------|---------------------------------|
| in  | startblk | first block to read             |
| out | buf      | pointer to the read buffer      |
| in  | n        | number of blocks to read        |

### Returns

The operation status.

#### **Return values**

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

### **Function Class:**

Not an API, this function is for internal use only.

7.31.7.38 bool sdc\_lld\_write ( SDCDriver \* sdcp, uint32\_t startblk, const uint8\_t \* buf, uint32\_t n )

Writes one or more blocks.

### **Parameters**

| in  | sdcp     | pointer to the SDCDriver object |
|-----|----------|---------------------------------|
| in  | startblk | first block to write            |
| out | buf      | pointer to the write buffer     |
| in  | n        | number of blocks to write       |

#### Returns

The operation status.

#### Return values

| HAL_SUCCESS | operation succeeded. |
|-------------|----------------------|
| HAL_FAILED  | operation failed.    |

### **Function Class:**

Not an API, this function is for internal use only.

7.31.7.39 bool sdc\_lld\_sync ( SDCDriver \* sdcp )

Waits for card idle condition.

# **Parameters**

| in | sdcp | pointer to the SDCDriver object |
|----|------|---------------------------------|

#### Returns

The operation status.

### Return values

| HAL_SUCCESS | the operation succeeded. |
|-------------|--------------------------|
| HAL_FAILED  | the operation failed.    |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

### 7.31.8 Variable Documentation

# **7.31.8.1 const struct SDCDriverVMT sdc\_vmt** [static]

# Initial value:

```
{
  (bool (*)(void *))sdc_lld_is_card_inserted,
  (bool (*)(void *))sdc_lld_is_write_protected,
  (bool (*)(void *))sdcConnect,
  (bool (*)(void *))sdcDisconnect,
  (bool (*)(void *, uint32_t, uint8_t *, uint32_t))sdcRead,
  (bool (*)(void *, uint32_t, const uint8_t *, uint32_t))sdcWrite,
  (bool (*)(void *))sdcSync,
  (bool (*)(void *, BlockDeviceInfo *))sdcGetInfo
}
```

Virtual methods table.

### 7.31.8.2 SDCDriver SDCD1

SDCD1 driver identifier.

## 7.32 Serial Driver

Generic Serial Driver.

### 7.32.1 Detailed Description

#### Generic Serial Driver.

This module implements a generic full duplex serial driver. The driver implements a SerialDriver interface and uses I/O Queues for communication between the upper and the lower driver. Event flags are used to notify the application about incoming data, outgoing data and other I/O events.

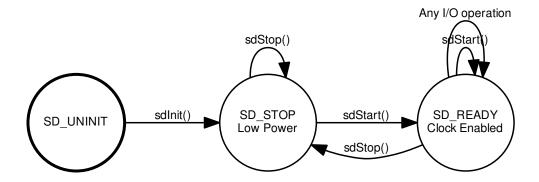
The module also contains functions that make the implementation of the interrupt service routines much easier.

#### Precondition

In order to use the SERIAL driver the HAL\_USE\_SERIAL option must be enabled in halconf.h.

#### 7.32.2 Driver State Machine

The driver implements a state machine internally, not all the driver functionalities can be used in any moment, any transition not explicitly shown in the following diagram has to be considered an error and shall be captured by an assertion (if enabled).



#### **Macros**

- #define \_serial\_driver\_methods \_base\_asynchronous\_channel\_methods
  - SerialDriver specific methods.
- #define \_serial\_driver\_data

SerialDriver specific data.

# Serial status flags

#define SD\_PARITY\_ERROR (eventflags\_t)32

Parity.

• #define SD\_FRAMING\_ERROR (eventflags\_t)64

Framing.

#define SD\_OVERRUN\_ERROR (eventflags\_t)128

Overflow.

#define SD\_NOISE\_ERROR (eventflags\_t)256

Line noise

#define SD\_BREAK\_DETECTED (eventflags\_t)512

LIN Break.

## Serial configuration options

• #define SERIAL DEFAULT BITRATE 38400

Default bit rate.

• #define SERIAL BUFFERS SIZE 16

Serial buffers size.

## **Macro Functions**

#define sdPut(sdp, b) oqPut(&(sdp)->oqueue, b)

Direct write to a SerialDriver.

• #define sdPutTimeout(sdp, b, t) oqPutTimeout(&(sdp)->oqueue, b, t)

Direct write to a SerialDriver with timeout specification.

#define sdGet(sdp) iqGet(&(sdp)->iqueue)

Direct read from a SerialDriver.

• #define sdGetTimeout(sdp, t) iqGetTimeout(&(sdp)->iqueue, t)

Direct read from a SerialDriver with timeout specification.

• #define sdWrite(sdp, b, n) oqWriteTimeout(&(sdp)->oqueue, b, n, TIME\_INFINITE)

Direct blocking write to a SerialDriver.

• #define sdWriteTimeout(sdp, b, n, t) oqWriteTimeout(&(sdp)->oqueue, b, n, t)

Direct blocking write to a SerialDriver with timeout specification.

#define sdAsynchronousWrite(sdp, b, n) ogWriteTimeout(&(sdp)->oqueue, b, n, TIME IMMEDIATE)

Direct non-blocking write to a SerialDriver.

#define sdRead(sdp, b, n) iqReadTimeout(&(sdp)->iqueue, b, n, TIME\_INFINITE)

Direct blocking read from a SerialDriver.

• #define sdReadTimeout(sdp, b, n, t) iqReadTimeout(&(sdp)->iqueue, b, n, t)

Direct blocking read from a SerialDriver with timeout specification.

#define sdAsynchronousRead(sdp, b, n) iqReadTimeout(&(sdp)->iqueue, b, n, TIME\_IMMEDIATE)

Direct non-blocking read from a SerialDriver.

## **PLATFORM** configuration options

• #define PLATFORM SERIAL USE USART1 FALSE

USART1 driver enable switch.

# **Typedefs**

· typedef struct SerialDriver SerialDriver

Structure representing a serial driver.

### **Data Structures**

struct SerialDriverVMT

SerialDriver virtual methods table.

struct SerialDriver

Full duplex serial driver class.

· struct SerialConfig

PLATFORM Serial Driver configuration structure.

### **Functions**

· void sdlnit (void)

Serial Driver initialization.

void sdObjectInit (SerialDriver \*sdp, qnotify\_t inotify, qnotify\_t onotify)

Initializes a generic full duplex driver object.

void sdStart (SerialDriver \*sdp, const SerialConfig \*config)

Configures and starts the driver.

void sdStop (SerialDriver \*sdp)

Stops the driver.

void sdlncomingDatal (SerialDriver \*sdp, uint8\_t b)

Handles incoming data.

msg\_t sdRequestDatal (SerialDriver \*sdp)

Handles outgoing data.

bool sdPutWouldBlock (SerialDriver \*sdp)

Direct output check on a SerialDriver.

bool sdGetWouldBlock (SerialDriver \*sdp)

Direct input check on a SerialDriver.

void sd\_lld\_init (void)

Low level serial driver initialization.

void sd\_lld\_start (SerialDriver \*sdp, const SerialConfig \*config)

Low level serial driver configuration and (re)start.

void sd\_lld\_stop (SerialDriver \*sdp)

Low level serial driver stop.

### **Enumerations**

# **Variables**

SerialDriver SD1

USART1 serial driver identifier.

· static const SerialConfig default config

Driver default configuration.

## 7.32.3 Macro Definition Documentation

7.32.3.1 #define SD\_PARITY\_ERROR (eventflags\_t)32

Parity.

7.32.3.2 #define SD\_FRAMING\_ERROR (eventflags\_t)64

Framing.

7.32.3.3 #define SD\_OVERRUN\_ERROR (eventflags\_t)128

Overflow.

7.32.3.4 #define SD\_NOISE\_ERROR (eventflags\_t)256

Line noise.

7.32.3.5 #define SD\_BREAK\_DETECTED (eventflags\_t)512

LIN Break.

7.32.3.6 #define SERIAL\_DEFAULT\_BITRATE 38400

Default bit rate.

Configuration parameter, this is the baud rate selected for the default configuration.

7.32.3.7 #define SERIAL\_BUFFERS\_SIZE 16

Serial buffers size.

Configuration parameter, you can change the depth of the queue buffers depending on the requirements of your application.

Note

The default is 16 bytes for both the transmission and receive buffers.

7.32.3.8 #define \_serial\_driver\_methods \_base\_asynchronous\_channel\_methods

SerialDriver specific methods.

7.32.3.9 #define sdPut( sdp, b) oqPut(&(sdp)->oqueue, b)

Direct write to a SerialDriver.

Note

This function bypasses the indirect access to the channel and writes directly on the output queue. This is faster but cannot be used to write to different channels implementations.

See also

chnPutTimeout()

**Function Class:** 

```
7.32.3.10 #define sdPutTimeout( sdp, b, t) oqPutTimeout(&(sdp)->oqueue, b, t)
```

Direct write to a SerialDriver with timeout specification.

Note

This function bypasses the indirect access to the channel and writes directly on the output queue. This is faster but cannot be used to write to different channels implementations.

See also

chnPutTimeout()

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

```
7.32.3.11 #define sdGet( sdp ) iqGet(&(sdp)->iqueue)
```

Direct read from a SerialDriver.

Note

This function bypasses the indirect access to the channel and reads directly from the input queue. This is faster but cannot be used to read from different channels implementations.

See also

chnGetTimeout()

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

```
7.32.3.12 #define sdGetTimeout( sdp, t ) iqGetTimeout(&(sdp)->iqueue, t)
```

Direct read from a SerialDriver with timeout specification.

Note

This function bypasses the indirect access to the channel and reads directly from the input queue. This is faster but cannot be used to read from different channels implementations.

See also

chnGetTimeout()

#### **Function Class:**

7.32.3.13 #define sdWrite( sdp, b, n ) oqWriteTimeout(&(sdp)->oqueue, b, n, TIME\_INFINITE)

Direct blocking write to a SerialDriver.

Note

This function bypasses the indirect access to the channel and writes directly to the output queue. This is faster but cannot be used to write from different channels implementations.

See also

chnWrite()

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.32.3.14 #define sdWriteTimeout( sdp, b, n, t) oqWriteTimeout(&(sdp)->oqueue, b, n, t)

Direct blocking write to a SerialDriver with timeout specification.

Note

This function bypasses the indirect access to the channel and writes directly to the output queue. This is faster but cannot be used to write to different channels implementations.

See also

chnWriteTimeout()

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.32.3.15 #define sdAsynchronousWrite( sdp, b, n) oqWriteTimeout(&(sdp)->oqueue, b, n, TIME\_IMMEDIATE)

Direct non-blocking write to a SerialDriver.

Note

This function bypasses the indirect access to the channel and writes directly to the output queue. This is faster but cannot be used to write to different channels implementations.

See also

chnWriteTimeout()

#### **Function Class:**

7.32.3.16 #define sdRead( sdp, b, n) iqReadTimeout(&(sdp)->iqueue, b, n, TIME\_INFINITE)

Direct blocking read from a SerialDriver.

Note

This function bypasses the indirect access to the channel and reads directly from the input queue. This is faster but cannot be used to read from different channels implementations.

See also

chnRead()

**Function Class:** 

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.32.3.17 #define sdReadTimeout( sdp, b, n, t) iqReadTimeout(&(sdp)->iqueue, b, n, t)

Direct blocking read from a SerialDriver with timeout specification.

Note

This function bypasses the indirect access to the channel and reads directly from the input queue. This is faster but cannot be used to read from different channels implementations.

See also

chnReadTimeout()

**Function Class:** 

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.32.3.18 #define sdAsynchronousRead( sdp, b, n) iqReadTimeout(&(sdp)->iqueue, b, n, TIME\_IMMEDIATE)

Direct non-blocking read from a SerialDriver.

Note

This function bypasses the indirect access to the channel and reads directly from the input queue. This is faster but cannot be used to read from different channels implementations.

See also

chnReadTimeout()

**Function Class:** 

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.32.3.19 #define PLATFORM\_SERIAL\_USE\_USART1 FALSE

USART1 driver enable switch.

If set to TRUE the support for USART1 is included.

Note

The default is FALSE.

### 7.32.3.20 #define \_serial\_driver\_data

### Value:

```
_base_asynchronous_channel_data
 /* Driver state.*/
 sdstate_t
                             state;
  /* Input queue.*/
 input_queue_t
                             iqueue;
  /* Output queue.*/
 output_queue_t
                             oqueue;
 /* Input circular buffer.*/
                             ib[SERIAL_BUFFERS_SIZE];
 uint8 t
 /* Output circular buffer.*/
                             ob[SERIAL_BUFFERS_SIZE];
 /\star End of the mandatory fields.\star/
```

SerialDriver specific data.

## 7.32.4 Typedef Documentation

## 7.32.4.1 typedef struct SerialDriver SerialDriver

Structure representing a serial driver.

# 7.32.5 Enumeration Type Documentation

```
7.32.5.1 enum sdstate_t
```

Driver state machine possible states.

Enumerator

```
SD_UNINIT Not initialized.SD_STOP Stopped.SD_READY Ready.
```

# 7.32.6 Function Documentation

```
7.32.6.1 void sdlnit ( void )
```

Serial Driver initialization.

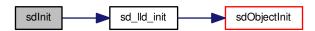
Note

This function is implicitly invoked by halinit (), there is no need to explicitly initialize the driver.

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



7.32.6.2 void sdObjectInit ( SerialDriver \* sdp, qnotify\_t inotify, qnotify\_t onotify )

Initializes a generic full duplex driver object.

The HW dependent part of the initialization has to be performed outside, usually in the hardware initialization code.

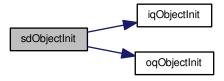
#### **Parameters**

| out | sdp     | pointer to a SerialDriver structure   |  |
|-----|---------|---|--|
| in  | inotify | pointer to a callback function that is invoked when some data is read from the Queue. The value can be NULL.  |  |
| in  | onotify | pointer to a callback function that is invoked when some data is written in the Queue. The value can be NULL. |  |

## **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



7.32.6.3 void sdStart ( SerialDriver \* sdp, const SerialConfig \* config )

Configures and starts the driver.

### **Parameters**

| in | sdp    | pointer to a SerialDriver object   |  |
|----|--------|--|--|
| in | config | the architecture-dependent serial driver configuration. If this parameter is set to $\mathtt{NULL}$ then a |  |
|    |        | default configuration is used.   |  |

## **Function Class:**

Here is the call graph for this function:



# 7.32.6.4 void sdStop ( SerialDriver \* sdp )

Stops the driver.

Any thread waiting on the driver's queues will be awakened with the message  $Q_RESET$ .

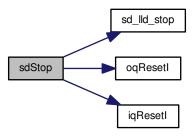
### **Parameters**

| in | sdp | pointer to a SerialDriver object |
|----|-----|----------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.32.6.5 void sdlncomingDatal ( SerialDriver \* sdp, uint8\_t b )

Handles incoming data.

This function must be called from the input interrupt service routine in order to enqueue incoming data and generate the related events.

## Note

The incoming data event is only generated when the input queue becomes non-empty. In order to gain some performance it is suggested to not use this function directly but copy this code directly into the interrupt service routine.

#### **Parameters**

| in | sdp | pointer to a SerialDriver structure                |  |
|----|-----|--|--|
| in | b   | the byte to be written in the driver's Input Queue |  |

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



7.32.6.6 msg\_t sdRequestDatal ( SerialDriver \* sdp )

Handles outgoing data.

Must be called from the output interrupt service routine in order to get the next byte to be transmitted.

Note

In order to gain some performance it is suggested to not use this function directly but copy this code directly into the interrupt service routine.

#### **Parameters**

|--|

# Returns

The byte value read from the driver's output queue.

#### **Return values**

| Q_EMPTY | if the queue is empty (the lower driver usually disables the interrupt source when this happens). |
|---------|---|
|---------|---|

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



# 7.32.6.7 bool sdPutWouldBlock ( SerialDriver \* sdp )

Direct output check on a SerialDriver.

#### Note

This function bypasses the indirect access to the channel and checks directly the output queue. This is faster but cannot be used to check different channels implementations.

### **Parameters**

| i | n | sdp | pointer to a SerialDriver structure |
|---|---|-----|-------------------------------------|
|---|---|-----|-------------------------------------|

## Returns

The queue status.

### Return values

| false | if the next write operation would not block. |
|-------|--|
| true  | if the next write operation would block.     |

# **Deprecated**

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.32.6.8 bool sdGetWouldBlock ( SerialDriver \* sdp )

Direct input check on a SerialDriver.

### Note

This function bypasses the indirect access to the channel and checks directly the input queue. This is faster but cannot be used to check different channels implementations.

### **Parameters**

| in | sdp | pointer to a SerialDriver structure |
|----|-----|-------------------------------------|
|----|-----|-------------------------------------|

### Returns

The queue status.

## Return values

| false | if the next write operation would not block. |
|-------|--|
| true  | if the next write operation would block.     |

# **Deprecated**

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

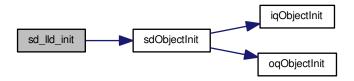
7.32.6.9 void sd\_lld\_init ( void )

Low level serial driver initialization.

## **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.32.6.10 void sd\_lld\_start ( SerialDriver \* sdp, const SerialConfig \* config )

Low level serial driver configuration and (re)start.

## **Parameters**

| in | sdp    | pointer to a SerialDriver object  |  |
|----|--------|---|--|
| in | config | the architecture-dependent serial driver configuration. If this parameter is set to NULL then a |  |
|    |        | default configuration is used.  |  |

## **Function Class:**

Not an API, this function is for internal use only.

```
7.32.6.11 void sd_lld_stop ( SerialDriver * sdp )
```

Low level serial driver stop.

De-initializes the USART, stops the associated clock, resets the interrupt vector.

### **Parameters**

```
in sdp pointer to a SerialDriver object
```

### **Function Class:**

Not an API, this function is for internal use only.

### 7.32.7 Variable Documentation

### 7.32.7.1 SerialDriver SD1

USART1 serial driver identifier.

**7.32.7.2** const SerialConfig default\_config [static]

## Initial value:

```
= {
 38400
}
```

Driver default configuration.

## 7.33 Serial over USB Driver

Serial over USB Driver.

# 7.33.1 Detailed Description

Serial over USB Driver.

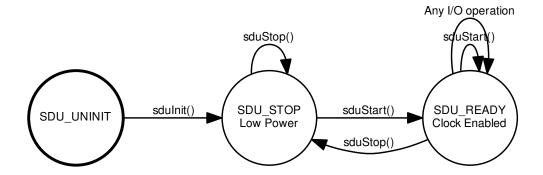
This module implements an USB Communication Device Class (CDC) as a normal serial communication port accessible from the device application.

#### Precondition

In order to use the USB over Serial driver the  ${\tt HAL\_USE\_SERIAL\_USB}$  option must be enabled in  ${\tt halconf.h.}$ 

#### 7.33.2 Driver State Machine

The driver implements a state machine internally, not all the driver functionalities can be used in any moment, any transition not explicitly shown in the following diagram has to be considered an error and shall be captured by an assertion (if enabled).



## **Macros**

• #define \_serial\_usb\_driver\_data

SerialDriver specific data.

• #define \_serial\_usb\_driver\_methods \_base\_asynchronous\_channel\_methods

 ${\it Serial USBDriver} \ \textit{specific methods}.$ 

# SERIAL\_USB configuration options

• #define SERIAL\_USB\_BUFFERS\_SIZE 256

Serial over USB buffers size.

• #define SERIAL\_USB\_BUFFERS\_NUMBER 2

Serial over USB number of buffers.

## **Typedefs**

• typedef struct SerialUSBDriver SerialUSBDriver

Structure representing a serial over USB driver.

## **Data Structures**

· struct SerialUSBConfig

Serial over USB Driver configuration structure.

struct SerialUSBDriverVMT

SerialDriver virtual methods table.

struct SerialUSBDriver

Full duplex serial driver class.

#### **Functions**

• static void ibnotify (io\_buffers\_queue\_t \*bqp)

Notification of empty buffer released into the input buffers queue.

static void obnotify (io\_buffers\_queue\_t \*bqp)

Notification of filled buffer inserted into the output buffers queue.

• void sdulnit (void)

Serial Driver initialization.

void sduObjectInit (SerialUSBDriver \*sdup)

Initializes a generic full duplex driver object.

void sduStart (SerialUSBDriver \*sdup, const SerialUSBConfig \*config)

Configures and starts the driver.

void sduStop (SerialUSBDriver \*sdup)

Stops the driver.

• void sduDisconnectI (SerialUSBDriver \*sdup)

USB device disconnection handler.

void sduConfigureHookI (SerialUSBDriver \*sdup)

USB device configured handler.

bool sduRequestsHook (USBDriver \*usbp)

Default requests hook.

void sduSOFHookI (SerialUSBDriver \*sdup)

SOF handler.

void sduDataTransmitted (USBDriver \*usbp, usbep\_t ep)

Default data transmitted callback.

void sduDataReceived (USBDriver \*usbp, usbep\_t ep)

Default data received callback.

void sduInterruptTransmitted (USBDriver \*usbp, usbep t ep)

Default data received callback.

### **Enumerations**

## 7.33.3 Macro Definition Documentation

7.33.3.1 #define SERIAL\_USB\_BUFFERS\_SIZE 256

Serial over USB buffers size.

Configuration parameter, the buffer size must be a multiple of the USB data endpoint maximum packet size.

Note

The default is 256 bytes for both the transmission and receive buffers.

7.33.3.2 #define SERIAL\_USB\_BUFFERS\_NUMBER 2

Serial over USB number of buffers.

Note

The default is 2 buffers.

7.33.3.3 #define \_serial\_usb\_driver\_data

### Value:

```
base asynchronous channel data
 /* Driver state.*/
 sdustate_t
 /* Input buffers queue.*/
 input_buffers_queue_t ibqueue;
 /* Output queue.*/
 output_buffers_queue_t obqueue;
 /* Input buffer.*/
                          ib[BQ_BUFFER_SIZE(
 uint8_t
     SERIAL_USB_BUFFERS_NUMBER,
                                             SERIAL_USB_BUFFERS_SIZE)];
 /* Output buffer.*/
                           ob[BQ_BUFFER_SIZE(
     SERIAL_USB_BUFFERS_NUMBER,
                                            SERIAL_USB_BUFFERS_SIZE)];
 /* End of the mandatory fields.*/
 /* Current configuration data.*/
 const SerialUSBConfig
                          *config;
```

SerialDriver specific data.

7.33.3.4 #define \_serial\_usb\_driver\_methods \_base\_asynchronous\_channel\_methods

SerialUSBDriver specific methods.

# 7.33.4 Typedef Documentation

7.33.4.1 typedef struct SerialUSBDriver SerialUSBDriver

Structure representing a serial over USB driver.

7.33 Serial over USB Driver 363

# 7.33.5 Enumeration Type Documentation

# 7.33.5.1 enum sdustate\_t

Driver state machine possible states.

#### Enumerator

SDU\_UNINIT Not initialized.

SDU\_STOP Stopped.

SDU\_READY Ready.

### 7.33.6 Function Documentation

```
7.33.6.1 static void ibnotify ( io_buffers_queue_t * bqp ) [static]
```

Notification of empty buffer released into the input buffers queue.

#### **Parameters**

| in | bqp | the buffers queue pointer. |
|----|-----|----------------------------|
|----|-----|----------------------------|

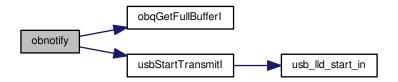
**7.33.6.2 static void obnotify ( io\_buffers\_queue\_t** \* *bqp* ) [static]

Notification of filled buffer inserted into the output buffers queue.

# Parameters

| in | bqp | the buffers queue pointer. |
|----|-----|----------------------------|

Here is the call graph for this function:



7.33.6.3 void sdulnit (void)

Serial Driver initialization.

Note

This function is implicitly invoked by hallnit (), there is no need to explicitly initialize the driver.

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

# 7.33.6.4 void sduObjectInit ( SerialUSBDriver \* sdup )

Initializes a generic full duplex driver object.

The HW dependent part of the initialization has to be performed outside, usually in the hardware initialization code.

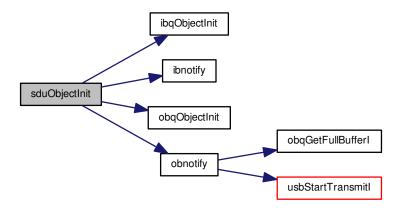
#### **Parameters**

|  | out | sdup | pointer to a SerialUSBDriver structure | 1 |
|--|-----|------|--|---|
|--|-----|------|--|---|

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



7.33.6.5 void sduStart ( SerialUSBDriver \* sdup, const SerialUSBConfig \* config )

Configures and starts the driver.

## **Parameters**

| in | sdup   | pointer to a SerialUSBDriver object      |
|----|--------|--|
| in | config | the serial over USB driver configuration |

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## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

## 7.33.6.6 void sduStop ( SerialUSBDriver \* sdup )

Stops the driver.

Any thread waiting on the driver's queues will be awakened with the message  $Q_RESET$ .

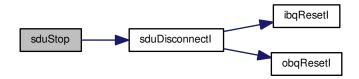
#### **Parameters**

```
in sdup pointer to a SerialUSBDriver object
```

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



## 7.33.6.7 void sduDisconnectl ( SerialUSBDriver \* sdup )

USB device disconnection handler.

Note

If this function is not called from an ISR then an explicit call to <code>osalOsRescheduleS()</code> in necessary afterward.

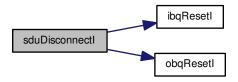
### **Parameters**

|  | in | sdup | pointer to a SerialUSBDriver object |
|--|----|------|-------------------------------------|
|--|----|------|-------------------------------------|

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



7.33.6.8 void sduConfigureHookl ( SerialUSBDriver \* sdup )

USB device configured handler.

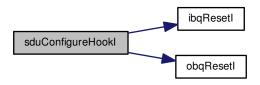
#### **Parameters**

|  | in | sdup | pointer to a SerialUSBDriver object |
|--|----|------|-------------------------------------|
|--|----|------|-------------------------------------|

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



7.33.6.9 bool sduRequestsHook ( USBDriver\*usbp )

Default requests hook.

Applications wanting to use the Serial over USB driver can use this function as requests hook in the USB configuration. The following requests are emulated:

- · CDC\_GET\_LINE\_CODING.
- CDC\_SET\_LINE\_CODING.
- CDC\_SET\_CONTROL\_LINE\_STATE.

### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### Returns

The hook status.

### **Return values**

| true  | Message handled internally. |
|-------|-----------------------------|
| false | Message not handled.        |

# 7.33.6.10 void sduSOFHookl ( SerialUSBDriver \* sdup )

### SOF handler.

The SOF interrupt is used for automatic flushing of incomplete buffers pending in the output queue.

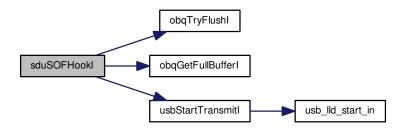
## **Parameters**

| in | sdup | pointer to a SerialUSBDriver object |
|----|------|-------------------------------------|
|----|------|-------------------------------------|

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



# 7.33.6.11 void sduDataTransmitted ( USBDriver \* usbp, usbep\_t ep )

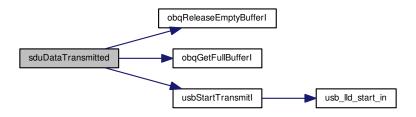
Default data transmitted callback.

The application must use this function as callback for the IN data endpoint.

### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | IN endpoint number              |

Here is the call graph for this function:



7.33.6.12 void sduDataReceived ( USBDriver \* usbp, usbep\_t ep )

Default data received callback.

The application must use this function as callback for the OUT data endpoint.

## **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | OUT endpoint number             |

Here is the call graph for this function:



7.33.6.13 void sdulnterruptTransmitted ( USBDriver \* usbp, usbep\_t ep )

Default data received callback.

The application must use this function as callback for the IN interrupt endpoint.

## **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

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## 7.34 SPI Driver

Generic SPI Driver.

## 7.34.1 Detailed Description

Generic SPI Driver.

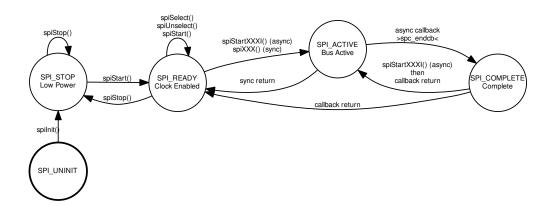
This module implements a generic SPI (Serial Peripheral Interface) driver allowing bidirectional and monodirectional transfers, complex atomic transactions are supported as well.

#### Precondition

In order to use the SPI driver the HAL\_USE\_SPI option must be enabled in halconf.h.

### 7.34.2 Driver State Machine

The driver implements a state machine internally, not all the driver functionalities can be used in any moment, any transition not explicitly shown in the following diagram has to be considered an error and shall be captured by an assertion (if enabled).



The driver is not thread safe for performance reasons, if you need to access the SPI bus from multiple threads then use the <code>spiAcquireBus()</code> and <code>spiReleaseBus()</code> APIs in order to gain exclusive access.

## **SPI** configuration options

• #define SPI USE WAIT TRUE

Enables synchronous APIs.

• #define SPI USE MUTUAL EXCLUSION TRUE

Enables the spiAcquireBus() and spiReleaseBus() APIs.

### **Macro Functions**

#define spiSelectI(spip)

Asserts the slave select signal and prepares for transfers.

• #define spiUnselectI(spip)

Deasserts the slave select signal.

• #define spiStartIgnoreI(spip, n)

Ignores data on the SPI bus.

• #define spiStartExchangel(spip, n, txbuf, rxbuf)

Exchanges data on the SPI bus.

#define spiStartSendI(spip, n, txbuf)

Sends data over the SPI bus.

• #define spiStartReceiveI(spip, n, rxbuf)

Receives data from the SPI bus.

• #define spiPolledExchange(spip, frame) spi\_lld\_polled\_exchange(spip, frame)

Exchanges one frame using a polled wait.

## Low level driver helper macros

#define \_spi\_wakeup\_isr(spip)

Wakes up the waiting thread.

• #define <u>\_spi\_isr\_code</u>(spip)

Common ISR code.

# **PLATFORM** configuration options

• #define PLATFORM SPI USE SPI1 FALSE

SPI1 driver enable switch.

# **Typedefs**

· typedef struct SPIDriver SPIDriver

Type of a structure representing an SPI driver.

typedef void(\* spicallback\_t) (SPIDriver \*spip)

SPI notification callback type.

# **Data Structures**

· struct SPIConfig

Driver configuration structure.

struct SPIDriver

Structure representing an SPI driver.

### **Functions**

void spilnit (void)

SPI Driver initialization.

void spiObjectInit (SPIDriver \*spip)

Initializes the standard part of a SPIDriver structure.

void spiStart (SPIDriver \*spip, const SPIConfig \*config)

Configures and activates the SPI peripheral.

• void spiStop (SPIDriver \*spip)

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Deactivates the SPI peripheral.

void spiSelect (SPIDriver \*spip)

Asserts the slave select signal and prepares for transfers.

void spiUnselect (SPIDriver \*spip)

Deasserts the slave select signal.

void spiStartIgnore (SPIDriver \*spip, size\_t n)

Ignores data on the SPI bus.

void spiStartExchange (SPIDriver \*spip, size t n, const void \*txbuf, void \*rxbuf)

Exchanges data on the SPI bus.

void spiStartSend (SPIDriver \*spip, size\_t n, const void \*txbuf)

Sends data over the SPI bus.

void spiStartReceive (SPIDriver \*spip, size\_t n, void \*rxbuf)

Receives data from the SPI bus.

void spilgnore (SPIDriver \*spip, size\_t n)

Ignores data on the SPI bus.

void spiExchange (SPIDriver \*spip, size\_t n, const void \*txbuf, void \*rxbuf)

Exchanges data on the SPI bus.

void spiSend (SPIDriver \*spip, size t n, const void \*txbuf)

Sends data over the SPI bus.

void spiReceive (SPIDriver \*spip, size\_t n, void \*rxbuf)

Receives data from the SPI bus.

void spiAcquireBus (SPIDriver \*spip)

Gains exclusive access to the SPI bus.

void spiReleaseBus (SPIDriver \*spip)

Releases exclusive access to the SPI bus.

void spi Ild init (void)

Low level SPI driver initialization.

void spi\_lld\_start (SPIDriver \*spip)

Configures and activates the SPI peripheral.

void spi\_lld\_stop (SPIDriver \*spip)

Deactivates the SPI peripheral.

void spi\_lld\_select (SPIDriver \*spip)

Asserts the slave select signal and prepares for transfers.

void spi\_lld\_unselect (SPIDriver \*spip)

Deasserts the slave select signal.

void spi\_lld\_ignore (SPIDriver \*spip, size\_t n)

Ignores data on the SPI bus.

void spi\_lld\_exchange (SPIDriver \*spip, size\_t n, const void \*txbuf, void \*rxbuf)

Exchanges data on the SPI bus.

void spi\_lld\_send (SPIDriver \*spip, size\_t n, const void \*txbuf)

Sends data over the SPI bus.

• void spi\_lld\_receive (SPIDriver \*spip, size\_t n, void \*rxbuf)

Receives data from the SPI bus.

• uint16\_t spi\_lld\_polled\_exchange (SPIDriver \*spip, uint16\_t frame)

Exchanges one frame using a polled wait.

### **Enumerations**

# Variables

SPIDriver SPID1

SPI1 driver identifier.

# 7.34.3 Macro Definition Documentation

## 7.34.3.1 #define SPI\_USE\_WAIT TRUE

Enables synchronous APIs.

Note

Disabling this option saves both code and data space.

## 7.34.3.2 #define SPI\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the spiAcquireBus() and spiReleaseBus() APIs.

Note

Disabling this option saves both code and data space.

```
7.34.3.3 #define spiSelectl( spip )
```

## Value:

```
{
    spi_lld_select(spip);
}
```

Asserts the slave select signal and prepares for transfers.

#### **Parameters**

```
in spip pointer to the SPIDriver object
```

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

# 7.34.3.4 #define spiUnselectI( spip )

## Value:

```
{
    spi_lld_unselect(spip);
    \
```

Deasserts the slave select signal.

The previously selected peripheral is unselected.

### **Parameters**

| in | spip | pointer to the SPIDriver object |
|----|------|---------------------------------|

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### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

# 7.34.3.5 #define spiStartIgnorel( spip, n)

#### Value:

```
{
  (spip) ->state = SPI_ACTIVE;
    spi_lld_ignore(spip, n);
}
```

Ignores data on the SPI bus.

This asynchronous function starts the transmission of a series of idle words on the SPI bus and ignores the received data.

## Precondition

A slave must have been selected using spiSelect() or spiSelectI().

### Postcondition

At the end of the operation the configured callback is invoked.

#### **Parameters**

| in | spip | pointer to the SPIDriver object |
|----|------|---------------------------------|
| in | n    | number of words to be ignored   |

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

```
7.34.3.6 #define spiStartExchangel( spip, n, txbuf, rxbuf)
```

### Value:

Exchanges data on the SPI bus.

This asynchronous function starts a simultaneous transmit/receive operation.

#### Precondition

A slave must have been selected using spiSelect() or spiSelectI().

## Postcondition

At the end of the operation the configured callback is invoked.

## Note

The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

### **Parameters**

| in  | spip  | pointer to the SPIDriver object    |
|-----|-------|------------------------------------|
| in  | n     | number of words to be exchanged    |
| in  | txbuf | the pointer to the transmit buffer |
| out | rxbuf | the pointer to the receive buffer  |

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

## 7.34.3.7 #define spiStartSendl( spip, n, txbuf)

### Value:

Sends data over the SPI bus.

This asynchronous function starts a transmit operation.

# Precondition

A slave must have been selected using spiSelect() or spiSelectI().

# Postcondition

At the end of the operation the configured callback is invoked.

# Note

The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

## **Parameters**

|   | in | spip  | pointer to the SPIDriver object    |
|---|----|-------|------------------------------------|
|   | in | n     | number of words to send            |
| ĺ | in | txbuf | the pointer to the transmit buffer |

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#### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

## 7.34.3.8 #define spiStartReceivel( spip, n, rxbuf)

#### Value:

Receives data from the SPI bus.

This asynchronous function starts a receive operation.

#### Precondition

A slave must have been selected using spiSelect() or spiSelectI().

#### Postcondition

At the end of the operation the configured callback is invoked.

#### Note

The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

# Parameters

| in  | spip  | pointer to the SPIDriver object   |
|-----|-------|-----------------------------------|
| in  | n     | number of words to receive        |
| out | rxbuf | the pointer to the receive buffer |

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.34.3.9 #define spiPolledExchange( spip, frame ) spi\_IId\_polled\_exchange(spip, frame)

Exchanges one frame using a polled wait.

This synchronous function exchanges one frame using a polled synchronization method. This function is useful when exchanging small amount of data on high speed channels, usually in this situation is much more efficient just wait for completion using polling than suspending the thread waiting for an interrupt.

# Note

This API is implemented as a macro in order to minimize latency.

## **Parameters**

|   | in | spip  | pointer to the SPIDriver object         |
|---|----|-------|---|
| Ī | in | frame | the data frame to send over the SPI bus |

#### Returns

The received data frame from the SPI bus.

## 7.34.3.10 #define \_spi\_wakeup\_isr( spip )

### Value:

```
{
    osalSysLockFromISR();
    osalThreadResumeI(&(spip)->thread, MSG_OK);
    osalSysUnlockFromISR();
}
```

Wakes up the waiting thread.

### **Parameters**

| in | spip | pointer to the SPIDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### **Function Class:**

Not an API, this function is for internal use only.

# 7.34.3.11 #define \_spi\_isr\_code( spip )

#### Value:

```
{
  if ((spip)->config->end_cb) {
    (spip)->state = SPI_COMPLETE;
    (spip)->config->end_cb(spip);
    if ((spip)->state == SPI_COMPLETE)
        (spip)->state = SPI_READY;
}
else
    (spip)->state = SPI_READY;
__spi_wakeup_isr(spip);
}
```

# Common ISR code.

This code handles the portable part of the ISR code:

- · Callback invocation.
- · Waiting thread wakeup, if any.
- Driver state transitions.

# Note

This macro is meant to be used in the low level drivers implementation only.

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### **Parameters**

| in | spip | pointer to the SPIDriver object | 1 |
|----|------|---------------------------------|---|
|----|------|---------------------------------|---|

### **Function Class:**

Not an API, this function is for internal use only.

7.34.3.12 #define PLATFORM\_SPI\_USE\_SPI1 FALSE

SPI1 driver enable switch.

If set to TRUE the support for SPI1 is included.

Note

The default is FALSE.

# 7.34.4 Typedef Documentation

7.34.4.1 typedef struct SPIDriver SPIDriver

Type of a structure representing an SPI driver.

7.34.4.2 typedef void(\* spicallback\_t) (SPIDriver \*spip)

SPI notification callback type.

### **Parameters**

|  | in | spip | pointer to the SPIDriver object triggering the callback |
|--|----|------|---|
|--|----|------|---|

## 7.34.5 Enumeration Type Documentation

7.34.5.1 enum spistate\_t

Driver state machine possible states.

Enumerator

SPI\_UNINIT Not initialized.

SPI\_STOP Stopped.

SPI\_READY Ready.

SPI\_ACTIVE Exchanging data.

SPI\_COMPLETE Asynchronous operation complete.

## 7.34.6 Function Documentation

7.34.6.1 void spilnit (void)

SPI Driver initialization.

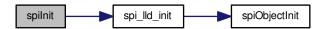
#### Note

This function is implicitly invoked by hallnit (), there is no need to explicitly initialize the driver.

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



## 7.34.6.2 void spiObjectInit ( SPIDriver \* spip )

Initializes the standard part of a  ${\tt SPIDriver}$  structure.

## **Parameters**

|  | out | spip | pointer to the SPIDriver object |
|--|-----|------|---------------------------------|
|--|-----|------|---------------------------------|

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

# 7.34.6.3 void spiStart ( SPIDriver \* spip, const SPIConfig \* config )

Configures and activates the SPI peripheral.

#### **Parameters**

| in | spip   | pointer to the SPIDriver object |
|----|--------|---------------------------------|
| in | config | pointer to the SPIConfig object |

## **Function Class:**

7.34 SPI Driver

Here is the call graph for this function:



7.34.6.4 void spiStop ( SPIDriver \* spip )

Deactivates the SPI peripheral.

Note

Deactivating the peripheral also enforces a release of the slave select line.

### **Parameters**

|  | in <i>spip</i> | pointer to the SPIDriver object |  |
|--|----------------|---------------------------------|--|
|--|----------------|---------------------------------|--|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.34.6.5 void spiSelect ( SPIDriver \* spip )

Asserts the slave select signal and prepares for transfers.

### **Parameters**

| in | spip | pointer to the SPIDriver object |
|----|------|---------------------------------|

## **Function Class:**

```
7.34.6.6 void spiUnselect ( SPIDriver * spip )
```

Deasserts the slave select signal.

The previously selected peripheral is unselected.

#### **Parameters**

|  | in | spip | pointer to the SPIDriver object |
|--|----|------|---------------------------------|
|--|----|------|---------------------------------|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

```
7.34.6.7 void spiStartIgnore ( SPIDriver * spip, size_t n )
```

Ignores data on the SPI bus.

This asynchronous function starts the transmission of a series of idle words on the SPI bus and ignores the received data.

### Precondition

A slave must have been selected using <code>spiSelect()</code> or <code>spiSelectI()</code>.

### Postcondition

At the end of the operation the configured callback is invoked.

### **Parameters**

| in | spip | pointer to the SPIDriver object |
|----|------|---------------------------------|
| in | n    | number of words to be ignored   |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

```
7.34.6.8 void spiStartExchange ( SPIDriver * spip, size_t n, const void * txbuf, void * rxbuf )
```

Exchanges data on the SPI bus.

This asynchronous function starts a simultaneous transmit/receive operation.

# Precondition

A slave must have been selected using spiSelect() or spiSelectI().

### Postcondition

At the end of the operation the configured callback is invoked.

7.34 SPI Driver



The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

#### **Parameters**

| in  | spip  | pointer to the SPIDriver object    |
|-----|-------|------------------------------------|
| in  | n     | number of words to be exchanged    |
| in  | txbuf | the pointer to the transmit buffer |
| out | rxbuf | the pointer to the receive buffer  |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.34.6.9 void spiStartSend ( SPIDriver \* spip, size\_t n, const void \* txbuf )

Sends data over the SPI bus.

This asynchronous function starts a transmit operation.

### Precondition

A slave must have been selected using spiSelect() or spiSelectI().

## Postcondition

At the end of the operation the configured callback is invoked.

#### Note

The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

### **Parameters**

| in | spip  | pointer to the SPIDriver object    |
|----|-------|------------------------------------|
| in | n     | number of words to send            |
| in | txbuf | the pointer to the transmit buffer |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.34.6.10 void spiStartReceive ( SPIDriver \* spip, size\_t n, void \* rxbuf )

Receives data from the SPI bus.

This asynchronous function starts a receive operation.

### Precondition

A slave must have been selected using spiSelect() or spiSelectI().

### Postcondition

At the end of the operation the configured callback is invoked.

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#### Note

The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

#### **Parameters**

| in  | spip  | pointer to the SPIDriver object   |
|-----|-------|-----------------------------------|
| in  | n     | number of words to receive        |
| out | rxbuf | the pointer to the receive buffer |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.34.6.11 void spilgnore ( SPIDriver \* spip, size\_t n )

Ignores data on the SPI bus.

This synchronous function performs the transmission of a series of idle words on the SPI bus and ignores the received data.

#### Precondition

In order to use this function the option SPI\_USE\_WAIT must be enabled.

In order to use this function the driver must have been configured without callbacks (end\_cb = NULL).

### **Parameters**

| in | spip | pointer to the SPIDriver object |
|----|------|---------------------------------|
| in | n    | number of words to be ignored   |

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.34.6.12 void spiExchange ( SPIDriver \* spip, size\_t n, const void \* txbuf, void \* rxbuf )

Exchanges data on the SPI bus.

This synchronous function performs a simultaneous transmit/receive operation.

# Precondition

In order to use this function the option SPI\_USE\_WAIT must be enabled.

In order to use this function the driver must have been configured without callbacks (end\_cb = NULL).

### Note

The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

#### **Parameters**

| in  | spip  | pointer to the SPIDriver object    |
|-----|-------|------------------------------------|
| in  | n     | number of words to be exchanged    |
| in  | txbuf | the pointer to the transmit buffer |
| out | rxbuf | the pointer to the receive buffer  |

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.34.6.13 void spiSend ( SPIDriver \* spip, size\_t n, const void \* txbuf )

Sends data over the SPI bus.

This synchronous function performs a transmit operation.

### Precondition

In order to use this function the option SPI\_USE\_WAIT must be enabled.

In order to use this function the driver must have been configured without callbacks (end\_cb = NULL).

### Note

The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

#### **Parameters**

| in | spip  | pointer to the SPIDriver object    |
|----|-------|------------------------------------|
| in | n     | number of words to send            |
| in | txbuf | the pointer to the transmit buffer |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.34.6.14 void spiReceive ( SPIDriver \* spip, size\_t n, void \* rxbuf )

Receives data from the SPI bus.

This synchronous function performs a receive operation.

## Precondition

In order to use this function the option  $SPI\_USE\_WAIT$  must be enabled. In order to use this function the driver must have been configured without callbacks (end\_cb = NULL).

## Note

The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

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#### **Parameters**

| in  | spip  | pointer to the SPIDriver object   |
|-----|-------|-----------------------------------|
| in  | n     | number of words to receive        |
| out | rxbuf | the pointer to the receive buffer |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.34.6.15 void spiAcquireBus ( SPIDriver \* spip )

Gains exclusive access to the SPI bus.

This function tries to gain ownership to the SPI bus, if the bus is already being used then the invoking thread is queued.

### Precondition

In order to use this function the option SPI\_USE\_MUTUAL\_EXCLUSION must be enabled.

#### **Parameters**

| in | spip | pointer to the SPIDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.34.6.16 void spiReleaseBus ( SPIDriver \* spip )

Releases exclusive access to the SPI bus.

#### Precondition

In order to use this function the option SPI\_USE\_MUTUAL\_EXCLUSION must be enabled.

### **Parameters**

| in | spip | pointer to the SPIDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.34.6.17 void spi\_lld\_init ( void )

Low level SPI driver initialization.

## **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.34.6.18 void spi\_lld\_start ( SPIDriver \* spip )

Configures and activates the SPI peripheral.

#### **Parameters**

| = | in | spip | pointer to the SPIDriver object |  |
|---|----|------|---------------------------------|--|
|---|----|------|---------------------------------|--|

### **Function Class:**

Not an API, this function is for internal use only.

7.34.6.19 void spi\_lld\_stop ( SPIDriver \* spip )

Deactivates the SPI peripheral.

# **Parameters**

| in | spip | pointer to the SPIDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

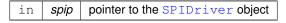
## **Function Class:**

Not an API, this function is for internal use only.

7.34.6.20 void spi\_lld\_select ( SPIDriver \* spip )

Asserts the slave select signal and prepares for transfers.

### **Parameters**



# **Function Class:**

Not an API, this function is for internal use only.

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7.34.6.21 void spi\_lld\_unselect ( SPIDriver \* spip )

Deasserts the slave select signal.

The previously selected peripheral is unselected.

#### **Parameters**

| in | spip | pointer to the SPIDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### **Function Class:**

Not an API, this function is for internal use only.

7.34.6.22 void spi\_lld\_ignore ( SPIDriver \* spip, size\_t n )

Ignores data on the SPI bus.

This asynchronous function starts the transmission of a series of idle words on the SPI bus and ignores the received data.

### Postcondition

At the end of the operation the configured callback is invoked.

#### **Parameters**

| in | spip | pointer to the SPIDriver object |
|----|------|---------------------------------|
| in | n    | number of words to be ignored   |

### **Function Class:**

Not an API, this function is for internal use only.

7.34.6.23 void spi\_lld\_exchange ( SPIDriver \* spip, size\_t n, const void \* txbuf, void \* rxbuf )

Exchanges data on the SPI bus.

This asynchronous function starts a simultaneous transmit/receive operation.

### Postcondition

At the end of the operation the configured callback is invoked.

## Note

The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

# **Parameters**

| in  | spip  | pointer to the SPIDriver object    |
|-----|-------|------------------------------------|
| in  | n     | number of words to be exchanged    |
| in  | txbuf | the pointer to the transmit buffer |
| out | rxbuf | the pointer to the receive buffer  |

ChibiOS/HAL

### **Function Class:**

Not an API, this function is for internal use only.

7.34.6.24 void spi\_lld\_send ( SPIDriver \* spip, size\_t n, const void \* txbuf )

Sends data over the SPI bus.

This asynchronous function starts a transmit operation.

### Postcondition

At the end of the operation the configured callback is invoked.

#### Note

The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

#### **Parameters**

| in | spip  | pointer to the SPIDriver object    |
|----|-------|------------------------------------|
| in | n     | number of words to send            |
| in | txbuf | the pointer to the transmit buffer |

## **Function Class:**

Not an API, this function is for internal use only.

7.34.6.25 void spi\_lld\_receive ( SPIDriver \* spip, size\_t n, void \* rxbuf )

Receives data from the SPI bus.

This asynchronous function starts a receive operation.

### Postcondition

At the end of the operation the configured callback is invoked.

# Note

The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

## **Parameters**

| in  | spip  | pointer to the SPIDriver object   |
|-----|-------|-----------------------------------|
| in  | n     | number of words to receive        |
| out | rxbuf | the pointer to the receive buffer |

#### **Function Class:**

Not an API, this function is for internal use only.

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7.34.6.26 uint16\_t spi\_lld\_polled\_exchange ( SPIDriver \* spip, uint16\_t frame )

Exchanges one frame using a polled wait.

This synchronous function exchanges one frame using a polled synchronization method. This function is useful when exchanging small amount of data on high speed channels, usually in this situation is much more efficient just wait for completion using polling than suspending the thread waiting for an interrupt.

## **Parameters**

| in | spip  | pointer to the SPIDriver object         |
|----|-------|---|
| in | frame | the data frame to send over the SPI bus |

#### Returns

The received data frame from the SPI bus.

7.34.7 Variable Documentation

7.34.7.1 SPIDriver SPID1

SPI1 driver identifier.

## 7.35 ST Driver

Generic System Tick Driver.

## 7.35.1 Detailed Description

Generic System Tick Driver.

This module implements a system tick timer in order to support the underlying operating system.

### **Macro Functions**

• #define stGetCounter() st\_lld\_get\_counter()

Returns the time counter value.

#define stlsAlarmActive() st\_lld\_is\_alarm\_active()

Determines if the alarm is active.

## **Functions**

void stlnit (void)

ST Driver initialization.

void stStartAlarm (systime\_t abstime)

Starts the alarm.

void stStopAlarm (void)

Stops the alarm interrupt.

void stSetAlarm (systime\_t abstime)

Sets the alarm time.

systime\_t stGetAlarm (void)

Returns the current alarm time.

void st\_lld\_init (void)

Low level ST driver initialization.

• static systime\_t st\_lld\_get\_counter (void)

Returns the time counter value.

static void st\_lld\_start\_alarm (systime\_t abstime)

Starts the alarm.

static void st\_lld\_stop\_alarm (void)

Stops the alarm interrupt.

• static void st\_lld\_set\_alarm (systime\_t abstime)

Sets the alarm time.

static systime\_t st\_lld\_get\_alarm (void)

Returns the current alarm time.

static bool st\_lld\_is\_alarm\_active (void)

Determines if the alarm is active.

### 7.35.2 Macro Definition Documentation

7.35.2.1 #define stGetCounter( ) st\_lld\_get\_counter()

Returns the time counter value.

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#### Note

This functionality is only available in free running mode, the behaviour in periodic mode is undefined.

## Returns

The counter value.

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

## 7.35.2.2 #define stlsAlarmActive( ) st\_lld\_is\_alarm\_active()

Determines if the alarm is active.

### Returns

The alarm status.

### Return values

| false | if the alarm is not active. |
|-------|-----------------------------|
| true  | is the alarm is active      |

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

# 7.35.3 Function Documentation

7.35.3.1 void stlnit ( void )

ST Driver initialization.

Note

This function is implicitly invoked by halInit(), there is no need to explicitly initialize the driver.

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.



7.35.3.2 void stStartAlarm ( systime\_t abstime )

Starts the alarm.

Note

Makes sure that no spurious alarms are triggered after this call.

This functionality is only available in free running mode, the behavior in periodic mode is undefined.

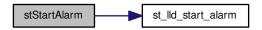
### **Parameters**

| in | abstime | the time to be set for the first alarm |
|----|---------|--|
|----|---------|--|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.35.3.3 void stStopAlarm (void)

Stops the alarm interrupt.

Note

This functionality is only available in free running mode, the behavior in periodic mode is undefined.

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.



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### 7.35.3.4 void stSetAlarm ( systime\_t abstime )

Sets the alarm time.

Note

This functionality is only available in free running mode, the behavior in periodic mode is undefined.

#### **Parameters**

| in | abstime | the time to be set for the next alarm |
|----|---------|---------------------------------------|
|----|---------|---------------------------------------|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.35.3.5 systime\_t stGetAlarm ( void )

Returns the current alarm time.

Note

This functionality is only available in free running mode, the behavior in periodic mode is undefined.

### Returns

The currently set alarm time.

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.



```
7.35.3.6 void st_lld_init (void)
```

Low level ST driver initialization.

### **Function Class:**

Not an API, this function is for internal use only.

```
7.35.3.7 static systime_t st_lld_get_counter( void ) [inline], [static]
```

Returns the time counter value.

#### Returns

The counter value.

#### **Function Class:**

Not an API, this function is for internal use only.

```
7.35.3.8 static void st_lld_start_alarm ( systime_t abstime ) [inline], [static]
```

Starts the alarm.

Note

Makes sure that no spurious alarms are triggered after this call.

#### **Parameters**

|  | in | abstime | the time to be set for the first alarm |
|--|----|---------|--|
|--|----|---------|--|

### **Function Class:**

Not an API, this function is for internal use only.

```
7.35.3.9 static void st_lld_stop_alarm( void ) [inline],[static]
```

Stops the alarm interrupt.

#### **Function Class:**

Not an API, this function is for internal use only.

```
7.35.3.10 static void st_lld_set_alarm ( systime_t abstime ) [inline], [static]
```

Sets the alarm time.

# **Parameters**

|   | in  | ahstime  | the time to be set for the next alarm  |
|---|-----|----------|--|
| ı | T11 | abstille | line time to be set for the next alarm |

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## **Function Class:**

Not an API, this function is for internal use only.

7.35.3.11 static systime\_t st\_lld\_get\_alarm ( void ) [inline], [static]

Returns the current alarm time.

#### Returns

The currently set alarm time.

# **Function Class:**

Not an API, this function is for internal use only.

7.35.3.12 static bool st\_lld\_is\_alarm\_active( void ) [inline],[static]

Determines if the alarm is active.

## Returns

The alarm status.

## Return values

| false | if the alarm is not active. |
|-------|-----------------------------|
| true  | is the alarm is active      |

# **Function Class:**

Not an API, this function is for internal use only.

### 7.36 UART Driver

Generic UART Driver.

### 7.36.1 Detailed Description

Generic UART Driver.

This driver abstracts a generic UART (Universal Asynchronous Receiver Transmitter) peripheral, the API is designed to be:

- Unbuffered and copy-less, transfers are always directly performed from/to the application-level buffers without extra copy operations.
- · Asynchronous, the API is always non blocking.
- · Callbacks capable, operations completion and other events are notified using callbacks.

Special hardware features like deep hardware buffers, DMA transfers are hidden to the user but fully supportable by the low level implementations.

This driver model is best used where communication events are meant to drive an higher level state machine, as example:

- · RS485 drivers.
- · Multipoint network drivers.
- · Serial protocol decoders.

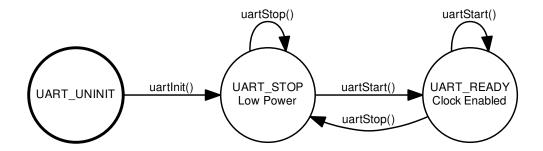
If your application requires a synchronous buffered driver then the Serial Driver should be used instead.

# Precondition

In order to use the UART driver the HAL\_USE\_UART option must be enabled in halconf.h.

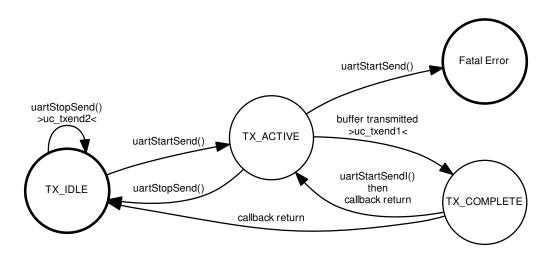
# 7.36.2 Driver State Machine

The driver implements a state machine internally, not all the driver functionalities can be used in any moment, any transition not explicitly shown in the following diagram has to be considered an error and shall be captured by an assertion (if enabled).



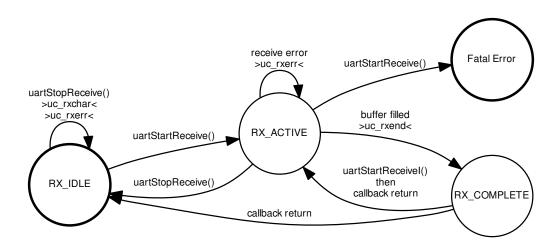
### 7.36.2.1 Transmitter sub State Machine

The follow diagram describes the transmitter state machine, this diagram is valid while the driver is in the  $UAR \leftarrow T\_READY$  state. This state machine is automatically reset to the  $TX\_IDLE$  state each time the driver enters the  $UART\_READY$  state.



### 7.36.2.2 Receiver sub State Machine

The follow diagram describes the receiver state machine, this diagram is valid while the driver is in the UART  $\leftarrow$  \_READY state. This state machine is automatically reset to the RX\_IDLE state each time the driver enters the UART\_READY state.



# **UART** status flags

• #define UART\_NO\_ERROR 0

No pending conditions.

#define UART\_PARITY\_ERROR 4

Parity error happened.

• #define UART\_FRAMING\_ERROR 8

Framing error happened.

• #define UART\_OVERRUN\_ERROR 16

Overflow happened.

#define UART\_NOISE\_ERROR 32

Noise on the line.

• #define UART\_BREAK\_DETECTED 64

Break detected.

## **UART** configuration options

• #define UART USE WAIT FALSE

Enables synchronous APIs.

• #define UART USE MUTUAL EXCLUSION FALSE

Enables the uartAcquireBus() and uartReleaseBus() APIs.

# Low level driver helper macros

#define \_uart\_wakeup\_tx1\_isr(uartp)

Wakes up the waiting thread in case of early TX complete.

• #define \_uart\_wakeup\_tx2\_isr(uartp)

Wakes up the waiting thread in case of late TX complete.

#define \_uart\_wakeup\_rx\_complete\_isr(uartp)

Wakes up the waiting thread in case of RX complete.

#define \_uart\_wakeup\_rx\_error\_isr(uartp)

Wakes up the waiting thread in case of RX error.

#define \_uart\_tx1\_isr\_code(uartp)

Common ISR code for early TX.

• #define \_uart\_tx2\_isr\_code(uartp)

Common ISR code for late TX.

#define \_uart\_rx\_complete\_isr\_code(uartp)

Common ISR code for RX complete.

#define \_uart\_rx\_error\_isr\_code(uartp, errors)

Common ISR code for RX error.

• #define \_uart\_rx\_idle\_code(uartp)

Common ISR code for RX on idle.

## **PLATFORM configuration options**

• #define PLATFORM UART USE UART1 FALSE

UART driver enable switch.

## **Typedefs**

· typedef uint32 t uartflags t

UART driver condition flags type.

typedef struct UARTDriver UARTDriver

Type of structure representing an UART driver.

typedef void(\* uartcb\_t) (UARTDriver \*uartp)

Generic UART notification callback type.

typedef void(\* uartccb\_t) (UARTDriver \*uartp, uint16\_t c)

Character received UART notification callback type.

typedef void(\* uartecb\_t) (UARTDriver \*uartp, uartflags\_t e)

Receive error UART notification callback type.

### **Data Structures**

struct UARTConfig

Driver configuration structure.

struct UARTDriver

Structure representing an UART driver.

#### **Functions**

· void uartInit (void)

UART Driver initialization.

void uartObjectInit (UARTDriver \*uartp)

Initializes the standard part of a UARTDriver structure.

void uartStart (UARTDriver \*uartp, const UARTConfig \*config)

Configures and activates the UART peripheral.

void uartStop (UARTDriver \*uartp)

Deactivates the UART peripheral.

void uartStartSend (UARTDriver \*uartp, size\_t n, const void \*txbuf)

Starts a transmission on the UART peripheral.

void uartStartSendl (UARTDriver \*uartp, size\_t n, const void \*txbuf)

Starts a transmission on the UART peripheral.

size\_t uartStopSend (UARTDriver \*uartp)

Stops any ongoing transmission.

• size\_t uartStopSendI (UARTDriver \*uartp)

Stops any ongoing transmission.

void uartStartReceive (UARTDriver \*uartp, size\_t n, void \*rxbuf)

Starts a receive operation on the UART peripheral.

• void uartStartReceivel (UARTDriver \*uartp, size\_t n, void \*rxbuf)

Starts a receive operation on the UART peripheral.

size\_t uartStopReceive (UARTDriver \*uartp)

Stops any ongoing receive operation.

• size t uartStopReceivel (UARTDriver \*uartp)

Stops any ongoing receive operation.

msg\_t uartSendTimeout (UARTDriver \*uartp, size\_t \*np, const void \*txbuf, systime\_t timeout)

Performs a transmission on the UART peripheral.

• msg\_t uartSendFullTimeout (UARTDriver \*uartp, size\_t \*np, const void \*txbuf, systime\_t timeout)

Performs a transmission on the UART peripheral.

msg\_t uartReceiveTimeout (UARTDriver \*uartp, size\_t \*np, void \*rxbuf, systime\_t timeout)

Performs a receive operation on the UART peripheral.

void uartAcquireBus (UARTDriver \*uartp)

Gains exclusive access to the UART bus.

void uartReleaseBus (UARTDriver \*uartp)

Releases exclusive access to the UART bus.

void uart Ild init (void)

Low level UART driver initialization.

void uart\_lld\_start (UARTDriver \*uartp)

Configures and activates the UART peripheral.

void uart\_lld\_stop (UARTDriver \*uartp)

Deactivates the UART peripheral.

• void uart\_lld\_start\_send (UARTDriver \*uartp, size\_t n, const void \*txbuf)

Starts a transmission on the UART peripheral.

size\_t uart\_lld\_stop\_send (UARTDriver \*uartp)

Stops any ongoing transmission.

void uart IId start receive (UARTDriver \*uartp, size t n, void \*rxbuf)

Starts a receive operation on the UART peripheral.

size\_t uart\_lld\_stop\_receive (UARTDriver \*uartp)

Stops any ongoing receive operation.

#### **Enumerations**

### **Variables**

• UARTDriver UARTD1

UART1 driver identifier.

### 7.36.3 Macro Definition Documentation

7.36.3.1 #define UART\_NO\_ERROR 0

No pending conditions.

7.36.3.2 #define UART\_PARITY\_ERROR 4

Parity error happened.

7.36.3.3 #define UART\_FRAMING\_ERROR 8

Framing error happened.

7.36.3.4 #define UART OVERRUN ERROR 16

Overflow happened.

7.36.3.5 #define UART\_NOISE\_ERROR 32

Noise on the line.

### 7.36.3.6 #define UART\_BREAK\_DETECTED 64

Break detected.

### 7.36.3.7 #define UART\_USE\_WAIT FALSE

Enables synchronous APIs.

Note

Disabling this option saves both code and data space.

# 7.36.3.8 #define UART\_USE\_MUTUAL\_EXCLUSION FALSE

Enables the uartAcquireBus () and uartReleaseBus () APIs.

Note

Disabling this option saves both code and data space.

```
7.36.3.9 #define _uart_wakeup_tx1_isr( uartp )
```

## Value:

```
{
  if ((uartp)->early == true) {
    osalSysLockFromISR();
    osalThreadResumeI(&(uartp)->threadtx, MSG_OK);
    osalSysUnlockFromISR();
  }
}
```

Wakes up the waiting thread in case of early TX complete.

## **Parameters**

```
in uartp pointer to the UARTDriver object
```

### **Function Class:**

Not an API, this function is for internal use only.

```
7.36.3.10 #define _uart_wakeup_tx2_isr( uartp )
```

## Value:

```
{
  if ((uartp)->early == false) {
    osalSysLockFromISR();
    osalThreadResumeI(&(uartp)->threadtx, MSG_OK);
    osalSysUnlockFromISR();
}
```

Wakes up the waiting thread in case of late TX complete.

### **Parameters**

|--|

### **Function Class:**

Not an API, this function is for internal use only.

7.36.3.11 #define \_uart\_wakeup\_rx\_complete\_isr( uartp )

### Value:

```
{
  osalSysLockFromISR();
  osalThreadResumeI(&(uartp)->threadrx, MSG_OK);
  osalSysUnlockFromISR();
```

Wakes up the waiting thread in case of RX complete.

# **Parameters**

```
in | uartp | pointer to the UARTDriver object
```

### **Function Class:**

Not an API, this function is for internal use only.

7.36.3.12 #define \_uart\_wakeup\_rx\_error\_isr( uartp )

# Value:

```
{
  osalSysLockFromISR();
  osalThreadResumeI(&(uartp)->threadrx, MSG_RESET);
  osalSysUnlockFromISR();
```

Wakes up the waiting thread in case of RX error.

### **Parameters**

| in   uartp   pointer to the UARTDriver object |
|---|
|---|

## **Function Class:**

Not an API, this function is for internal use only.

7.36.3.13 #define \_uart\_tx1\_isr\_code( uartp )

### Value:

```
(uartp)->txstate = UART_TX_COMPLETE;
```

```
if ((uartp)->config->txend1_cb != NULL) {
   (uartp)->config->txend1_cb(uartp);
}
if ((uartp)->txstate == UART_TX_COMPLETE) {
   (uartp)->txstate = UART_TX_IDLE;
}
_uart_wakeup_tx1_isr(uartp);
```

Common ISR code for early TX.

This code handles the portable part of the ISR code:

- · Callback invocation.
- · Waiting thread wakeup, if any.
- · Driver state transitions.

#### Note

This macro is meant to be used in the low level drivers implementation only.

#### **Parameters**

|  | in | uartp | pointer to the UARTDriver object | pointer to the |  |
|--|----|-------|----------------------------------|----------------|--|
|--|----|-------|----------------------------------|----------------|--|

#### **Function Class:**

Not an API, this function is for internal use only.

```
7.36.3.14 #define _uart_tx2_isr_code( uartp )
```

# Value:

```
{
  if ((uartp)->config->txend2_cb != NULL) {
    (uartp)->config->txend2_cb(uartp);
  }
  __uart_wakeup_tx2_isr(uartp);
}
```

Common ISR code for late TX.

This code handles the portable part of the ISR code:

- · Callback invocation.
- · Waiting thread wakeup, if any.
- · Driver state transitions.

#### Note

This macro is meant to be used in the low level drivers implementation only.

## **Parameters**

| in | uartp | pointer to the UARTDriver object |
|----|-------|----------------------------------|
|----|-------|----------------------------------|

### **Function Class:**

Not an API, this function is for internal use only.

7.36.3.15 #define \_uart\_rx\_complete\_isr\_code( uartp )

#### Value:

```
{
  (uartp)->rxstate = UART_RX_COMPLETE;
  if ((uartp)->config->rxend_cb != NULL) {
     (uartp)->config->rxend_cb(uartp);
}
  if ((uartp)->rxstate == UART_RX_COMPLETE) {
     (uartp)->rxstate = UART_RX_IDLE;
     uart_enter_rx_idle_loop(uartp);
}
  _uart_wakeup_rx_complete_isr(uartp);
}
```

Common ISR code for RX complete.

This code handles the portable part of the ISR code:

- · Callback invocation.
- · Waiting thread wakeup, if any.
- · Driver state transitions.

#### Note

This macro is meant to be used in the low level drivers implementation only.

#### **Parameters**

```
in uartp pointer to the UARTDriver object
```

# **Function Class:**

Not an API, this function is for internal use only.

7.36.3.16 #define \_uart\_rx\_error\_isr\_code( uartp, errors )

## Value:

```
{
    ((uartp)->config->rxerr_cb != NULL) {
        (uartp)->config->rxerr_cb(uartp, errors);
    }
    _uart_wakeup_rx_error_isr(uartp);
}
```

Common ISR code for RX error.

This code handles the portable part of the ISR code:

- · Callback invocation.
- · Waiting thread wakeup, if any.
- · Driver state transitions.

#### Note

This macro is meant to be used in the low level drivers implementation only.

### **Parameters**

| in | uartp  | pointer to the UARTDriver object |
|----|--------|----------------------------------|
| in | errors | mask of errors to be reported    |

## **Function Class:**

Not an API, this function is for internal use only.

```
7.36.3.17 #define _uart_rx_idle_code( uartp )
```

### Value:

```
{
  if ((uartp)->config->rxchar_cb != NULL)
     (uartp)->config->rxchar_cb(uartp, (uartp)->rxbuf);
}
```

Common ISR code for RX on idle.

This code handles the portable part of the ISR code:

- · Callback invocation.
- · Waiting thread wakeup, if any.
- · Driver state transitions.

### Note

This macro is meant to be used in the low level drivers implementation only.

### **Parameters**

| in   uartp   pointer to the UARTDriver obje | ct |
|---|----|
|---|----|

## **Function Class:**

Not an API, this function is for internal use only.

7.36.3.18 #define PLATFORM\_UART\_USE\_UART1 FALSE

UART driver enable switch.

If set to TRUE the support for UART1 is included.

Note

The default is FALSE.

# 7.36.4 Typedef Documentation

7.36.4.1 typedef uint32\_t uartflags\_t

UART driver condition flags type.

## 7.36.4.2 typedef struct UARTDriver UARTDriver

Type of structure representing an UART driver.

7.36.4.3 typedef void(\* uartcb\_t) (UARTDriver \*uartp)

Generic UART notification callback type.

#### **Parameters**

|  | in | uartp | pointer to the UARTDriver object | ter to the UARTDriver object |
|--|----|-------|----------------------------------|------------------------------|
|--|----|-------|----------------------------------|------------------------------|

## 7.36.4.4 typedef void(\* uartccb\_t) (UARTDriver \*uartp, uint16\_t c)

Character received UART notification callback type.

#### **Parameters**

| in | uartp | pointer to the UARTDriver object triggering the callback |
|----|-------|--|
| in | С     | received character                                       |

## 7.36.4.5 typedef void(\* uartecb\_t) (UARTDriver \*uartp, uartflags\_t e)

Receive error UART notification callback type.

### **Parameters**

| in | uartp | pointer to the UARTDriver object triggering the callback |
|----|-------|--|
| in | е     | receive error mask                                       |

# 7.36.5 Enumeration Type Documentation

## 7.36.5.1 enum uartstate\_t

Driver state machine possible states.

### Enumerator

UART\_UNINIT Not initialized.UART\_STOP Stopped.UART\_READY Ready.

### 7.36.5.2 enum uarttxstate\_t

Transmitter state machine states.

### **Enumerator**

UART\_TX\_IDLE Not transmitting.UART\_TX\_ACTIVE Transmitting.UART\_TX\_COMPLETE Buffer complete.

### 7.36.5.3 enum uartrxstate\_t

Receiver state machine states.

#### Enumerator

```
UART_RX_IDLE Not receiving.UART_RX_ACTIVE Receiving.UART_RX_COMPLETE Buffer complete.
```

### 7.36.6 Function Documentation

7.36.6.1 void uartInit (void)

UART Driver initialization.

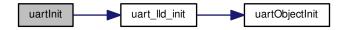
Note

This function is implicitly invoked by  ${\tt halInit}$  (), there is no need to explicitly initialize the driver.

# **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



# 7.36.6.2 void uartObjectInit ( UARTDriver \* uartp )

Initializes the standard part of a UARTDriver structure.

### **Parameters**

| out | uartp | pointer to the UARTDriver object |
|-----|-------|----------------------------------|

## **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

# 7.36.6.3 void uartStart ( UARTDriver \* uartp, const UARTConfig \* config )

Configures and activates the UART peripheral.

#### **Parameters**

| in | uartp  | pointer to the UARTDriver object |
|----|--------|----------------------------------|
| in | config | pointer to the UARTConfig object |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.36.6.4 void uartStop ( UARTDriver \* uartp )

Deactivates the UART peripheral.

# **Parameters**

| in | uartp | pointer to the UARTDriver object |
|----|-------|----------------------------------|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.



7.36.6.5 void uartStartSend ( UARTDriver \* uartp, size\_t n, const void \* txbuf )

Starts a transmission on the UART peripheral.

### Note

The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

### **Parameters**

| in | uartp | pointer to the UARTDriver object   |
|----|-------|------------------------------------|
| in | n     | number of data frames to send      |
| in | txbuf | the pointer to the transmit buffer |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.36.6.6 void uartStartSendl ( UARTDriver \* uartp, size\_t n, const void \* txbuf )

Starts a transmission on the UART peripheral.

### Note

The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

This function has to be invoked from a lock zone.

# **Parameters**

| in | uartp | pointer to the UARTDriver object   |
|----|-------|------------------------------------|
| in | n     | number of data frames to send      |
| in | txbuf | the pointer to the transmit buffer |

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



7.36.6.7 size\_t uartStopSend ( UARTDriver \* uartp )

Stops any ongoing transmission.

Note

Stopping a transmission also suppresses the transmission callbacks.

### **Parameters**

|  | in | uartp | pointer to the UARTDriver object |  |
|--|----|-------|----------------------------------|--|
|--|----|-------|----------------------------------|--|

### Returns

The number of data frames not transmitted by the stopped transmit operation.

### Return values

0 There was no transmit operation in progress.

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.36.6.8 size\_t uartStopSendl ( UARTDriver \* uartp )

Stops any ongoing transmission.

#### Note

Stopping a transmission also suppresses the transmission callbacks.

This function has to be invoked from a lock zone.

#### **Parameters**

|  | in | uartp | pointer to the UARTDriver object | pointer to the |
|--|----|-------|----------------------------------|----------------|
|--|----|-------|----------------------------------|----------------|

#### Returns

The number of data frames not transmitted by the stopped transmit operation.

#### Return values

O There was no transmit operation in progress.

#### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



7.36.6.9 void uartStartReceive ( UARTDriver \* uartp, size\_t n, void \* rxbuf )

Starts a receive operation on the UART peripheral.

#### Note

The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

### **Parameters**

|  |    |       | pointer to the UARTDriver object  |
|--|----|-------|-----------------------------------|
|  |    |       | number of data frames to receive  |
|  | in | rxbuf | the pointer to the receive buffer |

## **Function Class:**

Here is the call graph for this function:



7.36.6.10 void uartStartReceivel ( UARTDriver \* uartp, size\_t n, void \* rxbuf )

Starts a receive operation on the UART peripheral.

#### Note

The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

This function has to be invoked from a lock zone.

### **Parameters**

| in  | uartp | pointer to the UARTDriver object  |
|-----|-------|-----------------------------------|
| in  | n     | number of data frames to receive  |
| out | rxbuf | the pointer to the receive buffer |

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



7.36.6.11 size\_t uartStopReceive ( UARTDriver \* uartp )

Stops any ongoing receive operation.

## Note

Stopping a receive operation also suppresses the receive callbacks.

### **Parameters**

| i | n | uartp | pointer to the UARTDriver object |
|---|---|-------|----------------------------------|
|---|---|-------|----------------------------------|

#### Returns

The number of data frames not received by the stopped receive operation.

## Return values

0 There was no receive operation in progress.

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.36.6.12 size\_t uartStopReceivel ( UARTDriver \* uartp )

Stops any ongoing receive operation.

Note

Stopping a receive operation also suppresses the receive callbacks.

This function has to be invoked from a lock zone.

## **Parameters**

in uartp pointer to the UARTDriver object

# Returns

The number of data frames not received by the stopped receive operation.

### **Return values**

0 There was no receive operation in progress.

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



7.36.6.13 msg\_t uartSendTimeout ( UARTDriver \* uartp, size\_t \* np, const void \* txbuf, systime\_t timeout )

Performs a transmission on the UART peripheral.

#### Note

The function returns when the specified number of frames have been sent to the UART or on timeout. The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

### **Parameters**

| in     | uartp   | pointer to the UARTDriver object   |
|--------|---------|--|
| in,out | np      | number of data frames to transmit, on exit the number of frames actually transmitted |
| in     | txbuf   | the pointer to the transmit buffer   |
| in     | timeout | operation timeout  |

## Returns

The operation status.

# Return values

| MSG_OK      | if the operation completed successfully. |
|-------------|--|
| MSG_TIMEOUT | if the operation timed out.              |

## **Function Class:**

Here is the call graph for this function:



7.36.6.14 msg\_t uartSendFullTimeout ( UARTDriver \* uartp, size\_t \* np, const void \* txbuf, systime\_t timeout )

Performs a transmission on the UART peripheral.

### Note

The function returns when the specified number of frames have been physically transmitted or on timeout. The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

## **Parameters**

| in     | uartp   | pointer to the UARTDriver object   |
|--------|---------|--|
| in,out | np      | number of data frames to transmit, on exit the number of frames actually transmitted |
| in     | txbuf   | the pointer to the transmit buffer   |
| in     | timeout | operation timeout  |

### Returns

The operation status.

#### Return values

| MSG_OK      | if the operation completed successfully. |
|-------------|--|
| MSG_TIMEOUT | if the operation timed out.              |

## **Function Class:**

Here is the call graph for this function:



7.36.6.15 msg\_t uartReceiveTimeout ( UARTDriver \* uartp, size\_t \* np, void \* rxbuf, systime\_t timeout )

Performs a receive operation on the UART peripheral.

### Note

The function returns when the specified number of frames have been received or on error/timeout. The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

#### **Parameters**

| in     | uartp   | pointer to the UARTDriver object   |
|--------|---------|--|
| in,out | np      | number of data frames to receive, on exit the number of frames actually received |
| in     | rxbuf   | the pointer to the receive buffer  |
| in     | timeout | operation timeout  |

# Returns

The operation status.

### Return values

| MSG_OK      | if the operation completed successfully. |
|-------------|--|
| MSG_TIMEOUT | if the operation timed out.              |
| MSG_RESET   | in case of a receive error.              |

## **Function Class:**

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Here is the call graph for this function:



# 7.36.6.16 void uartAcquireBus ( UARTDriver \* uartp )

Gains exclusive access to the UART bus.

This function tries to gain ownership to the UART bus, if the bus is already being used then the invoking thread is queued.

### Precondition

In order to use this function the option  ${\tt UART\_USE\_MUTUAL\_EXCLUSION}$  must be enabled.

### **Parameters**

|  | in | uartp | pointer to the UARTDriver object |  |
|--|----|-------|----------------------------------|--|
|--|----|-------|----------------------------------|--|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

# 7.36.6.17 void uartReleaseBus ( UARTDriver \* uartp )

Releases exclusive access to the UART bus.

### Precondition

In order to use this function the option  ${\tt UART\_USE\_MUTUAL\_EXCLUSION}$  must be enabled.

### **Parameters**

| in   uartp   pointer to the UARTDriver object |
|---|
|---|

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.36.6.18 void uart\_lld\_init ( void )

Low level UART driver initialization.

## **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.36.6.19 void uart\_lld\_start ( UARTDriver \* uartp )

Configures and activates the UART peripheral.

### **Parameters**

| in | uartp | pointer to the UARTDriver object |
|----|-------|----------------------------------|
|----|-------|----------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

7.36.6.20 void uart\_lld\_stop ( UARTDriver \* uartp )

Deactivates the UART peripheral.

### **Parameters**

| in | uartp | pointer to the UARTDriver object |
|----|-------|----------------------------------|
|----|-------|----------------------------------|

# **Function Class:**

Not an API, this function is for internal use only.

7.36.6.21 void uart\_lld\_start\_send ( UARTDriver \* uartp, size\_t n, const void \* txbuf )

Starts a transmission on the UART peripheral.

### Note

The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

# **Parameters**

| in | uartp | pointer to the UARTDriver object   |
|----|-------|------------------------------------|
| in | n     | number of data frames to send      |
| in | txbuf | the pointer to the transmit buffer |

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### **Function Class:**

Not an API, this function is for internal use only.

7.36.6.22 size\_t uart\_lld\_stop\_send ( UARTDriver \* uartp )

Stops any ongoing transmission.

Note

Stopping a transmission also suppresses the transmission callbacks.

### **Parameters**

| in | uartp | pointer to the UARTDriver object |
|----|-------|----------------------------------|
|----|-------|----------------------------------|

### Returns

The number of data frames not transmitted by the stopped transmit operation.

### **Function Class:**

Not an API, this function is for internal use only.

7.36.6.23 void uart\_lld\_start\_receive ( UARTDriver \* uartp, size\_t n, void \* rxbuf )

Starts a receive operation on the UART peripheral.

Note

The buffers are organized as uint8\_t arrays for data sizes below or equal to 8 bits else it is organized as uint16\_t arrays.

# Parameters

| in  | uartp | pointer to the UARTDriver object  |
|-----|-------|-----------------------------------|
| in  | n     | number of data frames to send     |
| out | rxbuf | the pointer to the receive buffer |

### **Function Class:**

Not an API, this function is for internal use only.

7.36.6.24 size\_t uart\_lld\_stop\_receive ( UARTDriver \* uartp )

Stops any ongoing receive operation.

Note

Stopping a receive operation also suppresses the receive callbacks.

## **Parameters**

| in   uartp   pointer to the UARTDriver objection | ∍ct |
|--|-----|
|--|-----|

## Returns

The number of data frames not received by the stopped receive operation.

# **Function Class:**

Not an API, this function is for internal use only.

# 7.36.7 Variable Documentation

## 7.36.7.1 **UARTDriver UARTD1**

UART1 driver identifier.

## 7.37 USB Driver

Generic USB Driver.

## 7.37.1 Detailed Description

Generic USB Driver.

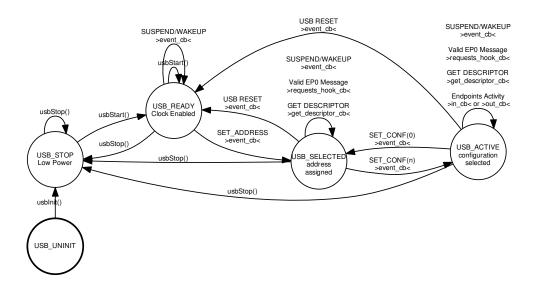
This module implements a generic USB (Universal Serial Bus) driver supporting device-mode operations.

#### Precondition

In order to use the USB driver the HAL\_USE\_USB option must be enabled in halconf.h.

### 7.37.2 Driver State Machine

The driver implements a state machine internally, not all the driver functionalities can be used in any moment, any transition not explicitly shown in the following diagram has to be considered an error and shall be captured by an assertion (if enabled).



# 7.37.3 USB Operations

The USB driver is quite complex and USB is complex in itself, it is recommended to study the USB specification before trying to use the driver.

### 7.37.3.1 USB Implementation

The USB driver abstracts the inner details of the underlying USB hardware. The driver works asynchronously and communicates with the application using callbacks. The application is responsible of the descriptors and strings

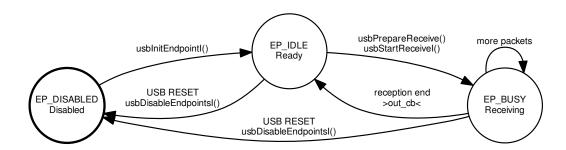
required by the USB device class to be implemented and of the handling of the specific messages sent over the endpoint zero. Standard messages are handled internally to the driver. The application can use hooks in order to handle custom messages or override the handling of the default handling of standard messages.

## 7.37.3.2 USB Endpoints

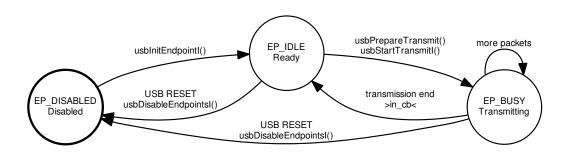
USB endpoints are the objects that the application uses to exchange data with the host. There are two kind of endpoints:

- IN endpoints are used by the application to transmit data to the host.
- OUT endpoints are used by the application to receive data from the host.

The driver invokes a callback after finishing an IN or OUT transaction. States diagram for OUT endpoints in transaction mode:



States diagram for IN endpoints in transaction mode:



#### 7.37.3.3 USB Callbacks

The USB driver uses callbacks in order to interact with the application. There are several kinds of callbacks to be handled:

- · Driver events callback. As example errors, suspend event, reset event etc.
- Messages Hook callback. This hook allows the application to implement handling of custom messages or to
  override the default handling of standard messages on endpoint zero.
- Descriptor Requested callback. When the driver endpoint zero handler receives a GET DESCRIPTOR message and needs to send a descriptor to the host it gueries the application using this callback.
- · Start of Frame callback. This callback is invoked each time a SOF packet is received.
- · Endpoint callbacks. Each endpoint informs the application about I/O conditions using those callbacks.

### **Macros**

• #define USB USE WAIT FALSE

Enables synchronous APIs.

#define USB MAX ENDPOINTS 4

Maximum endpoint address.

#define USB\_EP0\_STATUS\_STAGE USB\_EP0\_STATUS\_STAGE\_SW

Status stage handling method.

• #define USB\_SET\_ADDRESS\_MODE USB\_LATE\_SET\_ADDRESS

The address can be changed immediately upon packet reception.

#define USB\_SET\_ADDRESS\_ACK\_HANDLING USB\_SET\_ADDRESS\_ACK\_SW

Method for set address acknowledge.

• #define usb Ild get frame number(usbp) 0

Returns the current frame number.

• #define usb\_lld\_get\_transaction\_size(usbp, ep) ((usbp)->epc[ep]->out\_state->rxcnt)

Returns the exact size of a receive transaction.

#define usb Ild connect bus(usbp)

Connects the USB device.

#define usb\_lld\_disconnect\_bus(usbp)

Disconnect the USB device.

## Helper macros for USB descriptors

#define USB\_DESC\_INDEX(i) ((uint8\_t)(i))

Helper macro for index values into descriptor strings.

#define USB\_DESC\_BYTE(b) ((uint8\_t)(b))

Helper macro for byte values into descriptor strings.

• #define USB\_DESC\_WORD(w)

Helper macro for word values into descriptor strings.

#define USB\_DESC\_BCD(bcd)

Helper macro for BCD values into descriptor strings.

- #define USB DESC DEVICE SIZE 18U

Device Descriptor helper macro.

• #define USB\_DESC\_CONFIGURATION\_SIZE 9U

Configuration Descriptor size.

 #define USB\_DESC\_CONFIGURATION(wTotalLength, bNumInterfaces, bConfigurationValue, iConfiguration, bmAttributes, bMaxPower)

Configuration Descriptor helper macro.

#define USB\_DESC\_INTERFACE\_SIZE 9U

Interface Descriptor size.

 #define USB\_DESC\_INTERFACE(bInterfaceNumber, bAlternateSetting, bNumEndpoints, bInterfaceClass, bInterfaceSubClass, bInterfaceProtocol, iInterface)

Interface Descriptor helper macro.

• #define USB\_DESC\_INTERFACE\_ASSOCIATION\_SIZE 8U

Interface Association Descriptor size.

#define USB\_DESC\_INTERFACE\_ASSOCIATION(bFirstInterface, bInterfaceCount, bFunctionClass, b←
 FunctionSubClass, bFunctionProcotol, iInterface)

Interface Association Descriptor helper macro.

• #define USB\_DESC\_ENDPOINT\_SIZE 7U

Endpoint Descriptor size.

#define USB DESC ENDPOINT(bEndpointAddress, bmAttributes, wMaxPacketSize, bInterval)

Endpoint Descriptor helper macro.

# **Endpoint types and settings**

- #define USB EP MODE\_TYPE 0x0003U
- #define USB EP MODE TYPE CTRL 0x0000U
- #define USB\_EP\_MODE\_TYPE\_ISOC 0x0001U
- #define USB EP MODE TYPE BULK 0x0002U
- #define USB\_EP\_MODE\_TYPE\_INTR 0x0003U

### **Macro Functions**

• #define usbGetDriverStateI(usbp) ((usbp)->state)

Returns the driver state.

#define usbConnectBus(usbp) usb Ild connect bus(usbp)

Connects the USB device.

• #define usbDisconnectBus(usbp) usb\_lld\_disconnect\_bus(usbp)

Disconnect the USB device.

#define usbGetFrameNumberX(usbp) usb\_lld\_get\_frame\_number(usbp)

Returns the current frame number.

#define usbGetTransmitStatusI(usbp, ep) (((usbp)->transmitting & (uint16\_t)((unsigned)1U << (unsigned)(ep))) != 0U)</li>

Returns the status of an IN endpoint.

#define usbGetReceiveStatusI(usbp, ep) (((usbp)->receiving & (uint16\_t)((unsigned)1U << (unsigned)(ep)))</li>
 != 0U)

Returns the status of an OUT endpoint.

• #define usbGetReceiveTransactionSizeX(usbp, ep) usb\_lld\_get\_transaction\_size(usbp, ep)

Returns the exact size of a receive transaction.

#define usbSetupTransfer(usbp, buf, n, endcb)

Request transfer setup.

#define usbReadSetup(usbp, ep, buf) usb\_lld\_read\_setup(usbp, ep, buf)

Reads a setup packet from the dedicated packet buffer.

## Low level driver helper macros

#define <u>usb\_isr\_invoke\_event\_cb(usbp, evt)</u>

Common ISR code, usb event callback.

#define <u>usb\_isr\_invoke\_sof\_cb(usbp)</u>

Common ISR code, SOF callback.

• #define \_usb\_isr\_invoke\_setup\_cb(usbp, ep)

Common ISR code, setup packet callback.

#define <u>usb\_isr\_invoke\_in\_cb</u>(usbp, ep)

Common ISR code, IN endpoint callback.

• #define \_usb\_isr\_invoke\_out\_cb(usbp, ep)

Common ISR code, OUT endpoint event.

## **PLATFORM configuration options**

#define PLATFORM USB USE USB1 FALSE

USB driver enable switch.

# **Typedefs**

· typedef struct USBDriver USBDriver

Type of a structure representing an USB driver.

• typedef uint8\_t usbep\_t

Type of an endpoint identifier.

typedef void(\* usbcallback\_t) (USBDriver \*usbp)

Type of an USB generic notification callback.

typedef void(\* usbepcallback\_t) (USBDriver \*usbp, usbep\_t ep)

Type of an USB endpoint callback.

• typedef void(\* usbeventcb\_t) (USBDriver \*usbp, usbevent\_t event)

Type of an USB event notification callback.

typedef bool(\* usbreqhandler\_t) (USBDriver \*usbp)

Type of a requests handler callback.

typedef const USBDescriptor \*(\* usbgetdescriptor\_t) (USBDriver \*usbp, uint8\_t dtype, uint8\_t dindex, uint16\_t lang)

Type of an USB descriptor-retrieving callback.

### **Data Structures**

· struct USBDescriptor

Type of an USB descriptor.

• struct USBInEndpointState

Type of an IN endpoint state structure.

struct USBOutEndpointState

Type of an OUT endpoint state structure.

struct USBEndpointConfig

Type of an USB endpoint configuration structure.

struct USBConfig

Type of an USB driver configuration structure.

struct USBDriver

Structure representing an USB driver.

### **Functions**

 static void set address (USBDriver \*usbp) SET ADDRESS transaction callback. static bool default handler (USBDriver \*usbp) Standard requests handler. void usblnit (void) USB Driver initialization. void usbObjectInit (USBDriver \*usbp) Initializes the standard part of a USBDriver structure. void usbStart (USBDriver \*usbp, const USBConfig \*config) Configures and activates the USB peripheral. void usbStop (USBDriver \*usbp) Deactivates the USB peripheral. • void usbInitEndpointI (USBDriver \*usbp, usbep\_t ep, const USBEndpointConfig \*epcp) Enables an endpoint. void usbDisableEndpointsI (USBDriver \*usbp) Disables all the active endpoints. • void usbStartReceivel (USBDriver \*usbp, usbep\_t ep, uint8\_t \*buf, size\_t n) Starts a receive transaction on an OUT endpoint. void usbStartTransmitl (USBDriver \*usbp, usbep\_t ep, const uint8\_t \*buf, size\_t n) Starts a transmit transaction on an IN endpoint. • msg tusbReceive (USBDriver \*usbp, usbep tep, uint8 t \*buf, size t n) Performs a receive transaction on an OUT endpoint. msg\_t usbTransmit (USBDriver \*usbp, usbep\_t ep, const uint8\_t \*buf, size\_t n) Performs a transmit transaction on an IN endpoint. bool usbStallReceivel (USBDriver \*usbp, usbep t ep) Stalls an OUT endpoint. bool usbStallTransmitl (USBDriver \*usbp, usbep\_t ep) Stalls an IN endpoint. void usb reset (USBDriver \*usbp) USB reset routine. void <u>usb\_suspend</u> (USBDriver \*usbp) USB suspend routine. void <u>usb\_wakeup</u> (USBDriver \*usbp) USB wake-up routine. • void \_usb\_ep0setup (USBDriver \*usbp, usbep\_t ep) Default EP0 SETUP callback. void \_usb\_ep0in (USBDriver \*usbp, usbep\_t ep) Default EP0 IN callback. • void \_usb\_ep0out (USBDriver \*usbp, usbep t ep) Default EP0 OUT callback. void usb Ild init (void) Low level USB driver initialization. void usb Ild start (USBDriver \*usbp) Configures and activates the USB peripheral. void usb Ild stop (USBDriver \*usbp) Deactivates the USB peripheral. void usb IId reset (USBDriver \*usbp) USB low level reset routine.

void usb\_lld\_set\_address (USBDriver \*usbp)

```
Sets the USB address.

    void usb_lld_init_endpoint (USBDriver *usbp, usbep_t ep)

      Enables an endpoint.

    void usb_lld_disable_endpoints (USBDriver *usbp)

      Disables all the active endpoints except the endpoint zero.
• usbepstatus t usb Ild get status out (USBDriver *usbp, usbep t ep)
      Returns the status of an OUT endpoint.

    usbepstatus_t usb_lld_get_status_in (USBDriver *usbp, usbep_t ep)

      Returns the status of an IN endpoint.
• void usb_lld_read_setup (USBDriver *usbp, usbep_t ep, uint8_t *buf)
      Reads a setup packet from the dedicated packet buffer.
• void usb_lld_prepare_receive (USBDriver *usbp, usbep_t ep)
      Prepares for a receive operation.

    void usb IId prepare transmit (USBDriver *usbp, usbep t ep)

      Prepares for a transmit operation.

    void usb_lld_start_out (USBDriver *usbp, usbep_t ep)

      Starts a receive operation on an OUT endpoint.

    void usb_lld_start_in (USBDriver *usbp, usbep_t ep)

      Starts a transmit operation on an IN endpoint.

    void usb_lld_stall_out (USBDriver *usbp, usbep_t ep)

      Brings an OUT endpoint in the stalled state.

    void usb Ild stall in (USBDriver *usbp, usbep t ep)

      Brings an IN endpoint in the stalled state.

    void usb_lld_clear_out (USBDriver *usbp, usbep_t ep)

      Brings an OUT endpoint in the active state.

    void usb_lld_clear_in (USBDriver *usbp, usbep_t ep)

      Brings an IN endpoint in the active state.
```

# **Enumerations**

#### **Variables**

```
• USBDriver USBD1
```

USB1 driver identifier.

union {
 USBInEndpointState in
 IN EP0 state.
 USBOutEndpointState out
 OUT EP0 state.
 } ep0\_state

EP0 state.

static const USBEndpointConfig ep0config

EP0 initialization structure.

### 7.37.4 Macro Definition Documentation

7.37.4.1 #define USB\_DESC\_INDEX( *i* ) ((uint8\_t)(i))

Helper macro for index values into descriptor strings.

```
7.37.4.2 #define USB_DESC_BYTE( b ) ((uint8_t)(b))
```

Helper macro for byte values into descriptor strings.

```
7.37.4.3 #define USB_DESC_WORD( w )
```

### Value:

```
(uint8_t)((w) & 255U),
(uint8_t)(((w) >> 8) & 255U)
```

Helper macro for word values into descriptor strings.

```
7.37.4.4 #define USB_DESC_BCD( bcd )
```

#### Value:

```
(uint8_t)((bcd) & 255U),
  (uint8_t)(((bcd) >> 8) & 255)
```

Helper macro for BCD values into descriptor strings.

7.37.4.5 #define USB\_DESC\_DEVICE( bcdUSB, bDeviceClass, bDeviceSubClass, bDeviceProtocol, bMaxPacketSize, idVendor, idProduct, bcdDevice, iManufacturer, iProduct, iSerialNumber, bNumConfigurations)

#### Value:

```
USB_DESC_BYTE (USB_DESC_DEVICE_SIZE),

USB_DESC_BYTE (USB_DESCRIPTOR_DEVICE),

USB_DESC_BCD (bcdUSB),

USB_DESC_BYTE (bDeviceClass),

USB_DESC_BYTE (bDeviceSubClass),

USB_DESC_BYTE (bDeviceProtocol),

USB_DESC_BYTE (bMaxPacketSize),

USB_DESC_WORD (idVendor),

USB_DESC_WORD (idProduct),

USB_DESC_BCD (bcdDevice),

USB_DESC_INDEX (iManufacturer),

USB_DESC_INDEX (iProduct),

USB_DESC_INDEX (iProduct),

USB_DESC_INDEX (iSerialNumber),

USB_DESC_BYTE (bNumConfigurations)
```

Device Descriptor helper macro.

# 7.37.4.6 #define USB\_DESC\_CONFIGURATION\_SIZE 9U

Configuration Descriptor size.

7.37.4.7 #define USB\_DESC\_CONFIGURATION( wTotalLength, bNumInterfaces, bConfigurationValue, iConfiguration, bmAttributes, bMaxPower)

### Value:

```
USB_DESC_BYTE (USB_DESC_CONFIGURATION_SIZE),

USB_DESC_BYTE (USB_DESCRIPTOR_CONFIGURATION),

USB_DESC_WORD (wTotalLength),

USB_DESC_BYTE (bNumInterfaces),

USB_DESC_BYTE (bConfigurationValue),

USB_DESC_INDEX (iConfiguration),

USB_DESC_BYTE (bmAttributes),

USB_DESC_BYTE (bMaxPower)
```

Configuration Descriptor helper macro.

7.37.4.8 #define USB\_DESC\_INTERFACE\_SIZE 9U

Interface Descriptor size.

7.37.4.9 #define USB\_DESC\_INTERFACE( bInterfaceNumber, bAlternateSetting, bNumEndpoints, bInterfaceClass, bInterfaceSubClass, bInterfaceProtocol, iInterface)

#### Value:

```
USB_DESC_BYTE (USB_DESC_INTERFACE_SIZE),

USB_DESC_BYTE (USB_DESCRIPTOR_INTERFACE),

USB_DESC_BYTE (bInterfaceNumber),

USB_DESC_BYTE (bAlternateSetting),

USB_DESC_BYTE (bNumEndpoints),

USB_DESC_BYTE (bInterfaceClass),

USB_DESC_BYTE (bInterfaceSubClass),

USB_DESC_BYTE (bInterfaceProtocol),

USB_DESC_BYTE (bInterfaceProtocol),

USB_DESC_INDEX (iInterface)
```

Interface Descriptor helper macro.

7.37.4.10 #define USB\_DESC\_INTERFACE\_ASSOCIATION\_SIZE 8U

Interface Association Descriptor size.

7.37.4.11 #define USB\_DESC\_INTERFACE\_ASSOCIATION( bFirstInterface, bInterfaceCount, bFunctionClass, bFunctionSubClass, bFunctionProcotol, iInterface)

### Value:

```
USB_DESC_BYTE (USB_DESC_INTERFACE_ASSOCIATION_SIZE),

USB_DESC_BYTE (USB_DESCRIPTOR_INTERFACE_ASSOCIATION),

USB_DESC_BYTE (bFirstInterface),
```

```
USB_DESC_BYTE (bInterfaceCount),
  USB_DESC_BYTE (bFunctionClass) ,
  USB_DESC_BYTE (bFunctionSubClass),
  USB_DESC_BYTE (bFunctionProcotol),
  USB_DESC_INDEX(iInterface)
Interface Association Descriptor helper macro.
7.37.4.12 #define USB_DESC_ENDPOINT_SIZE 7U
Endpoint Descriptor size.
7.37.4.13 #define USB_DESC_ENDPOINT( bEndpointAddress, bmAttributes, wMaxPacketSize, bInterval )
Value:
USB_DESC_BYTE (USB_DESC_ENDPOINT_SIZE),
  USB_DESC_BYTE (USB_DESCRIPTOR_ENDPOINT),
  USB_DESC_BYTE (bEndpointAddress),
  USB_DESC_BYTE (bmAttributes),
  USB_DESC_WORD (wMaxPacketSize),
  USB_DESC_BYTE (bInterval)
Endpoint Descriptor helper macro.
7.37.4.14 #define USB_EP_MODE_TYPE 0x0003U
Endpoint type mask.
7.37.4.15 #define USB_EP_MODE_TYPE_CTRL 0x0000U
Control endpoint.
7.37.4.16 #define USB_EP_MODE_TYPE_ISOC 0x0001U
Isochronous endpoint.
7.37.4.17 #define USB_EP_MODE_TYPE_BULK 0x0002U
Bulk endpoint.
```

7.37.4.18 #define USB\_EP\_MODE\_TYPE\_INTR 0x0003U

Interrupt endpoint.

### 7.37.4.19 #define USB\_USE\_WAIT FALSE

Enables synchronous APIs.

Note

Disabling this option saves both code and data space.

7.37.4.20 #define usbGetDriverStatel( usbp ) ((usbp)->state)

Returns the driver state.

#### **Parameters**

| i | n | usbp | pointer to the USBDriver object |
|---|---|------|---------------------------------|
|---|---|------|---------------------------------|

#### Returns

The driver state.

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.37.4.21 #define usbConnectBus( usbp ) usb\_lld\_connect\_bus(usbp)

Connects the USB device.

#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.37.4.22 #define usbDisconnectBus( usbp ) usb\_IId\_disconnect\_bus(usbp)

Disconnect the USB device.

### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.37.4.23 #define usbGetFrameNumberX( usbp ) usb\_lld\_get\_frame\_number(usbp)

Returns the current frame number.

### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### Returns

The current frame number.

### **Function Class:**

This is an X-Class API, this function can be invoked from any context.

7.37.4.24 #define usbGetTransmitStatusl( usbp, ep ) (((usbp)->transmitting & (uint16\_t)((unsigned)1U << (unsigned)(ep))) != 0U)

Returns the status of an IN endpoint.

### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

### Returns

The operation status.

## Return values

| false | Endpoint ready.        |
|-------|------------------------|
| true  | Endpoint transmitting. |

#### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.37.4.25 #define usbGetReceiveStatusl( usbp, ep ) (((usbp)->receiving & (uint16\_t)((unsigned)1U << (unsigned)(ep))) != 0U)

Returns the status of an OUT endpoint.

#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

#### Returns

The operation status.

### Return values

| false | Endpoint ready.     |  |
|-------|---------------------|--|
| true  | Endpoint receiving. |  |

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.37.4.26 #define usbGetReceiveTransactionSizeX( usbp, ep ) usb\_IId\_get\_transaction\_size(usbp, ep)

Returns the exact size of a receive transaction.

The received size can be different from the size specified in usbStartReceiveI() because the last packet could have a size different from the expected one.

#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

#### Returns

Received data size.

# **Function Class:**

This is an X-Class API, this function can be invoked from any context.

7.37.4.27 #define usbSetupTransfer( usbp, buf, n, endcb)

# Value:

```
{
  (usbp)->ep0next = (buf);
  (usbp)->ep0n = (n);
  (usbp)->ep0endcb = (endcb);
}
```

Request transfer setup.

This macro is used by the request handling callbacks in order to prepare a transaction over the endpoint zero.

### **Parameters**

| in | usbp  | pointer to the USBDriver object                              |
|----|-------|--|
| in | buf   | pointer to a buffer for the transaction data                 |
| in | n     | number of bytes to be transferred                            |
| in | endcb | callback to be invoked after the transfer or $\mathtt{NULL}$ |

# **Function Class:**

Special function, this function has special requirements see the notes.

7.37.4.28 #define usbReadSetup( usbp, ep, buf ) usb\_IId\_read\_setup(usbp, ep, buf)

Reads a setup packet from the dedicated packet buffer.

This function must be invoked in the context of the setup\_cb callback in order to read the received setup packet.

### Precondition

In order to use this function the endpoint must have been initialized as a control endpoint.

### Note

This function can be invoked both in thread and IRQ context.

#### **Parameters**

| in  | usbp | pointer to the USBDriver object      |
|-----|------|--------------------------------------|
| in  | ер   | endpoint number                      |
| out | buf  | buffer where to copy the packet data |

#### **Function Class:**

Special function, this function has special requirements see the notes.

```
7.37.4.29 #define _usb_isr_invoke_event_cb( usbp, evt )
```

## Value:

```
{
  if (((usbp)->config->event_cb) != NULL) {
    (usbp)->config->event_cb(usbp, evt);
  }
}
```

Common ISR code, usb event callback.

### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | evt  | USB event code                  |

### **Function Class:**

Not an API, this function is for internal use only.

```
7.37.4.30 #define _usb_isr_invoke_sof_cb( usbp )
```

## Value:

```
{
  if (((usbp)->config->sof_cb) != NULL) {
    (usbp)->config->sof_cb(usbp);
     }
}
```

Common ISR code, SOF callback.

## **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

### **Function Class:**

Not an API, this function is for internal use only.

7.37.4.31 #define \_usb\_isr\_invoke\_setup\_cb( usbp, ep )

### Value:

```
{
  (usbp) ->epc[ep] ->setup_cb(usbp, ep);
}
```

Common ISR code, setup packet callback.

### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

#### **Function Class:**

Not an API, this function is for internal use only.

7.37.4.32 #define \_usb\_isr\_invoke\_in\_cb( usbp, ep )

### Value:

```
{
  (usbp)->transmitting &= ~(1 << (ep));
  if ((usbp)->epc[ep]->in_cb != NULL) {
    (usbp)->epc[ep]->in_cb(usbp, ep);
  }
  osalSysLockFromISR();
  osalSysUnlockFromISR();
```

Common ISR code, IN endpoint callback.

### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

### **Function Class:**

Not an API, this function is for internal use only.

7.37.4.33 #define \_usb\_isr\_invoke\_out\_cb( usbp, ep )

# Value:

Common ISR code, OUT endpoint event.

#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

## **Function Class:**

Not an API, this function is for internal use only.

7.37.4.34 #define USB\_MAX\_ENDPOINTS 4

Maximum endpoint address.

7.37.4.35 #define USB\_EP0\_STATUS\_STAGE USB\_EP0\_STATUS\_STAGE\_SW

Status stage handling method.

7.37.4.36 #define USB\_SET\_ADDRESS\_MODE USB\_LATE\_SET\_ADDRESS

The address can be changed immediately upon packet reception.

7.37.4.37 #define USB\_SET\_ADDRESS\_ACK\_HANDLING USB\_SET\_ADDRESS\_ACK\_SW

Method for set address acknowledge.

7.37.4.38 #define PLATFORM\_USB\_USE\_USB1 FALSE

USB driver enable switch.

If set to TRUE the support for USB1 is included.

Note

The default is FALSE.

7.37.4.39 #define usb\_lld\_get\_frame\_number( usbp ) 0

Returns the current frame number.

#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### Returns

The current frame number.

### **Function Class:**

Not an API, this function is for internal use only.

7.37.4.40 #define usb\_lld\_get\_transaction\_size( usbp, ep ) ((usbp)->epc[ep]->out\_state->rxcnt)

Returns the exact size of a receive transaction.

The received size can be different from the size specified in usbStartReceiveI() because the last packet could have a size different from the expected one.

#### Precondition

The OUT endpoint must have been configured in transaction mode in order to use this function.

#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

## Returns

Received data size.

### **Function Class:**

Not an API, this function is for internal use only.

7.37.4.41 #define usb\_lld\_connect\_bus( usbp )

Connects the USB device.

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.37.4.42 #define usb\_lld\_disconnect\_bus( usbp )

Disconnect the USB device.

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

## 7.37.5 Typedef Documentation

### 7.37.5.1 typedef struct USBDriver USBDriver

Type of a structure representing an USB driver.

7.37.5.2 typedef uint8\_t usbep\_t

Type of an endpoint identifier.

7.37.5.3 typedef void(\* usbcallback\_t) (USBDriver \*usbp)

Type of an USB generic notification callback.

### **Parameters**

| in | usbp | pointer to the USBDriver object triggering the callback |
|----|------|---|
|----|------|---|

7.37.5.4 typedef void(\* usbepcallback\_t) (USBDriver \*usbp, usbep\_t ep)

Type of an USB endpoint callback.

### **Parameters**

| in | usbp | pointer to the USBDriver object triggering the callback |
|----|------|---|
| in | ер   | endpoint number   |

7.37.5.5 typedef void(\* usbeventcb\_t) (USBDriver \*usbp, usbevent\_t event)

Type of an USB event notification callback.

# Parameters

| in | usbp  | pointer to the USBDriver object triggering the callback |
|----|-------|---|
| in | event | event type  |

7.37.5.6 typedef bool(\* usbreqhandler\_t) (USBDriver \*usbp)

Type of a requests handler callback.

The request is encoded in the usb\_setup buffer.

# **Parameters**

| in | usbp | pointer to the USBDriver object triggering the callback |
|----|------|---|
|----|------|---|

# Returns

The request handling exit code.

# Return values

| false | Request not recognized by the handler. |
|-------|--|
| true  | Request handled.                       |

7.37.5.7 typedef const USBDescriptor\*(\* usbgetdescriptor\_t) (USBDriver \*usbp, uint8\_t dtype, uint8\_t dindex, uint16\_t lang)

Type of an USB descriptor-retrieving callback.

### 7.37.6 Enumeration Type Documentation

7.37.6.1 enum usbstate\_t

Type of a driver state machine possible states.

#### Enumerator

USB\_UNINIT Not initialized.

USB\_STOP Stopped.

USB\_READY Ready, after bus reset.

USB\_SELECTED Address assigned.

USB\_ACTIVE Active, configuration selected.

USB\_SUSPENDED Suspended, low power mode.

### 7.37.6.2 enum usbepstatus\_t

Type of an endpoint status.

#### **Enumerator**

EP\_STATUS\_DISABLED Endpoint not active.EP\_STATUS\_STALLED Endpoint opened but stalled.

EP\_STATUS\_ACTIVE Active endpoint.

### 7.37.6.3 enum usbep0state\_t

Type of an endpoint zero state machine states.

#### Enumerator

USB\_EP0\_STP\_WAITING Waiting for SETUP data.

USB\_EP0\_IN\_TX Transmitting.

USB\_EP0\_IN\_WAITING\_TX0 Waiting transmit 0.

USB\_EP0\_IN\_SENDING\_STS Sending status.

USB\_EP0\_OUT\_WAITING\_STS Waiting status.

USB\_EP0\_OUT\_RX Receiving.

USB\_EP0\_ERROR Error, EP0 stalled.

#### 7.37.6.4 enum usbevent t

Type of an enumeration of the possible USB events.

### **Enumerator**

USB\_EVENT\_RESET Driver has been reset by host.

USB\_EVENT\_ADDRESS Address assigned.

USB\_EVENT\_CONFIGURED Configuration selected.

USB\_EVENT\_UNCONFIGURED Configuration removed.

USB\_EVENT\_SUSPEND Entering suspend mode.

USB\_EVENT\_WAKEUP Leaving suspend mode.

USB\_EVENT\_STALLED Endpoint 0 error, stalled.

# 7.37.7 Function Documentation

**7.37.7.1** static void set\_address ( USBDriver \* usbp ) [static]

SET ADDRESS transaction callback.

#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

Here is the call graph for this function:



**7.37.7.2** static bool default\_handler ( USBDriver \* usbp ) [static]

Standard requests handler.

This is the standard requests default handler, most standard requests are handled here, the user can override the standard handling using the  $requests\_hook\_cb$  hook in the USBConfig structure.

### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

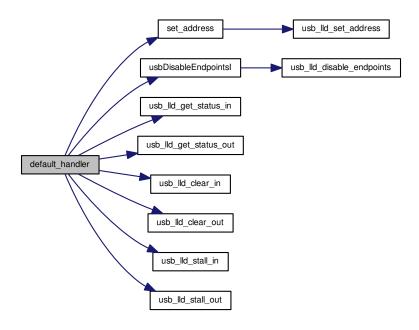
### Returns

The request handling exit code.

#### Return values

| false | Request not recognized by the handler or error. |  |
|-------|---|--|
| true  | Request handled.                                |  |

Here is the call graph for this function:



7.37.7.3 void usblnit (void)

USB Driver initialization.

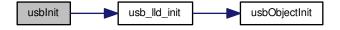
Note

This function is implicitly invoked by  ${\tt halInit}$  (), there is no need to explicitly initialize the driver.

# **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



## 7.37.7.4 void usbObjectInit ( USBDriver \* usbp )

Initializes the standard part of a USBDriver structure.

### **Parameters**

| out | usbp | pointer to the USBDriver object |
|-----|------|---------------------------------|
|-----|------|---------------------------------|

### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

7.37.7.5 void usbStart ( USBDriver \* usbp, const USBConfig \* config )

Configures and activates the USB peripheral.

#### **Parameters**

| in | usbp   | pointer to the USBDriver object |
|----|--------|---------------------------------|
| in | config | pointer to the USBConfig object |

### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.37.7.6 void usbStop ( USBDriver \* usbp )

Deactivates the USB peripheral.

#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.37.7.7 void usblnitEndpointl ( USBDriver \* usbp,  $usbep_t ep$ , const USBEndpointConfig \* epcp )

Enables an endpoint.

This function enables an endpoint, both IN and/or OUT directions depending on the configuration structure.

Note

This function must be invoked in response of a SET\_CONFIGURATION or SET\_INTERFACE message.

#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |
| in | ерср | the endpoint configuration      |

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



7.37.7.8 void usbDisableEndpointsI ( USBDriver\*usbp )

Disables all the active endpoints.

This function disables all the active endpoints except the endpoint zero.

Note

This function must be invoked in response of a SET\_CONFIGURATION message with configuration number zero.

### **Parameters**

|--|

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers

Here is the call graph for this function:



7.37.7.9 void usbStartReceivel ( USBDriver \* usbp, usbep\_t ep, uint8\_t \* buf, size\_t n )

Starts a receive transaction on an OUT endpoint.

#### Note

This function is meant to be called from ISR context outside critical zones because there is a potentially slow operation inside.

### **Parameters**

| in  | usbp | pointer to the USBDriver object  |  |
|-----|------|--|--|
| in  | ер   | endpoint number  |  |
| out | buf  | buffer where to copy the received data   |  |
| in  | n    | transaction size. It is recommended a multiple of the packet size because the excess is discarded. |  |

#### **Function Class**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



7.37.7.10 void usbStartTransmitl ( USBDriver \* usbp, usbep\_t ep, const uint8\_t \* buf, size\_t n )

Starts a transmit transaction on an IN endpoint.

## Note

This function is meant to be called from ISR context outside critical zones because there is a potentially slow operation inside.

### **Parameters**

| in | usbp | pointer to the USBDriver object                  |
|----|------|--|
| in | ер   | endpoint number                                  |
| in | buf  | buffer where to fetch the data to be transmitted |
| in | n    | transaction size                                 |

### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



7.37.7.11 msg\_t usbReceive ( USBDriver \* usbp, usbep\_t ep, uint8\_t \* buf, size\_t n )

Performs a receive transaction on an OUT endpoint.

### **Parameters**

| in  | usbp | pointer to the USBDriver object  |  |
|-----|------|--|--|
| in  | ер   | endpoint number  |  |
| out | buf  | buffer where to copy the received data   |  |
| in  | n    | transaction size. It is recommended a multiple of the packet size because the excess is discarded. |  |

### Returns

The received effective data size, it can be less than the amount specified.

# Return values

| MSG_RESET | driver not in USB_ACTIVE state or the operation has been aborted by an USB reset or a |  |
|-----------|---|--|
|           | transition to the USB_SUSPENDED state.  |  |

## **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.37.7.12 msg\_t usbTransmit ( USBDriver \* usbp, usbep\_t ep, const uint8\_t \* buf, size\_t n )

Performs a transmit transaction on an IN endpoint.

#### **Parameters**

| in | usbp | pointer to the USBDriver object                  |
|----|------|--|
| in | ер   | endpoint number                                  |
| in | buf  | buffer where to fetch the data to be transmitted |
| in | n    | transaction size                                 |

## Returns

The operation status.

# Return values

| MSG_OK    | operation performed successfully.   |  |
|-----------|---|--|
| MSG_RESET | driver not in USB_ACTIVE state or the operation has been aborted by an USB reset or a |  |
|           | transition to the USB_SUSPENDED state.  |  |

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



# 7.37.7.13 bool usbStallReceivel ( USBDriver \* usbp, usbep\_t ep )

Stalls an OUT endpoint.

## **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

## Returns

The operation status.

## Return values

| false | Endpoint stalled.           |
|-------|-----------------------------|
| true  | Endpoint busy, not stalled. |

# **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



# 7.37.7.14 bool usbStallTransmitl ( USBDriver \* usbp, usbep\_t ep )

Stalls an IN endpoint.

### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

# Returns

The operation status.

### **Return values**

| false | Endpoint stalled.           |
|-------|-----------------------------|
| true  | Endpoint busy, not stalled. |

## **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

Here is the call graph for this function:



7.37.7.15 void \_usb\_reset ( USBDriver \* usbp )

USB reset routine.

This function must be invoked when an USB bus reset condition is detected.

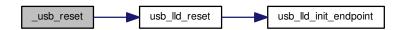
### **Parameters**

```
in usbp pointer to the USBDriver object
```

# **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.37.7.16 void usb\_suspend ( USBDriver \* usbp )

USB suspend routine.

This function must be invoked when an USB bus suspend condition is detected.

### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

## **Function Class:**

Not an API, this function is for internal use only.

7.37.7.17 void \_usb\_wakeup ( USBDriver \* usbp )

USB wake-up routine.

This function must be invoked when an USB bus wake-up condition is detected.

### **Parameters**

|  | in | usbp | pointer to the USBDriver object |
|--|----|------|---------------------------------|
|--|----|------|---------------------------------|

### **Function Class:**

Not an API, this function is for internal use only.

7.37.7.18 void usb\_ep0setup ( USBDriver \* usbp, usbep\_t ep )

Default EP0 SETUP callback.

This function is used by the low level driver as default handler for EP0 SETUP events.

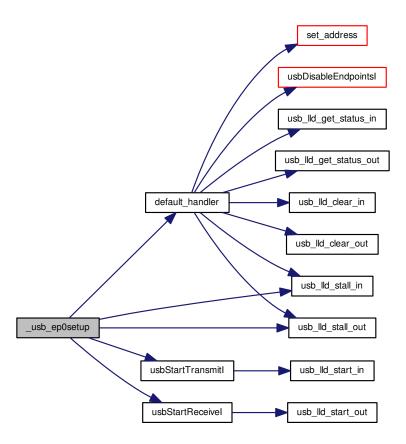
### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number, always zero    |

## **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.37.7.19 void \_usb\_ep0in ( USBDriver \* usbp, usbep\_t ep )

Default EP0 IN callback.

This function is used by the low level driver as default handler for EP0 IN events.

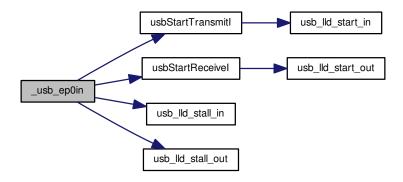
## **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number, always zero    |

## **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.37.7.20 void \_usb\_ep0out ( USBDriver \* usbp, usbep\_t ep )

Default EP0 OUT callback.

This function is used by the low level driver as default handler for EP0 OUT events.

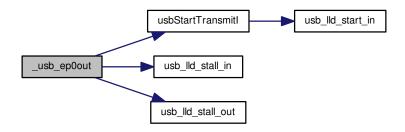
## **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number, always zero    |

## **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.37.7.21 void usb\_lld\_init ( void )

Low level USB driver initialization.

## **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.37.7.22 void usb\_lld\_start ( USBDriver \* usbp )

Configures and activates the USB peripheral.

### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

# **Function Class:**

Not an API, this function is for internal use only.

7.37.7.23 void usb\_lld\_stop ( USBDriver \* usbp )

Deactivates the USB peripheral.

### **Parameters**

| in usbp pointer to the USBDriver objection |
|--|
|--|

## **Function Class:**

Not an API, this function is for internal use only.

7.37.7.24 void usb\_lld\_reset ( USBDriver \* usbp )

USB low level reset routine.

## **Parameters**

| in usbp pointer to the USBDriver object |
|---|
|---|

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#### **Function Class:**

Not an API, this function is for internal use only.

Here is the call graph for this function:



7.37.7.25 void usb\_lld\_set\_address ( USBDriver \* usbp )

Sets the USB address.

#### **Parameters**

| ir | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### **Function Class:**

Not an API, this function is for internal use only.

7.37.7.26 void usb\_lld\_init\_endpoint ( USBDriver \* usbp, usbep\_t ep )

Enables an endpoint.

# Parameters

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

#### **Function Class:**

Not an API, this function is for internal use only.

7.37.7.27 void usb\_lld\_disable\_endpoints ( USBDriver\*usbp )

Disables all the active endpoints except the endpoint zero.

### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### **Function Class:**

Not an API, this function is for internal use only.

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7.37.7.28 usbepstatus\_t usb\_lld\_get\_status\_out ( USBDriver \* usbp, usbep\_t ep )

Returns the status of an OUT endpoint.

#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

#### Returns

The endpoint status.

#### Return values

| EP_STATUS_DISABLED | The endpoint is not active. |
|--------------------|-----------------------------|
| EP_STATUS_STALLED  | The endpoint is stalled.    |
| EP_STATUS_ACTIVE   | The endpoint is active.     |

#### **Function Class:**

Not an API, this function is for internal use only.

7.37.7.29 usbepstatus\_t usb\_lld\_get\_status\_in ( USBDriver \* usbp, usbep\_t ep )

Returns the status of an IN endpoint.

# Parameters

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

#### Returns

The endpoint status.

#### Return values

| EP_STATUS_DISABLED | The endpoint is not active. |
|--------------------|-----------------------------|
| EP_STATUS_STALLED  | The endpoint is stalled.    |
| EP_STATUS_ACTIVE   | The endpoint is active.     |

#### **Function Class:**

Not an API, this function is for internal use only.

7.37.7.30 void usb\_lld\_read\_setup ( USBDriver \* usbp, usbep\_t ep, uint8\_t \* buf )

Reads a setup packet from the dedicated packet buffer.

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This function must be invoked in the context of the setup\_cb callback in order to read the received setup packet.

#### Precondition

In order to use this function the endpoint must have been initialized as a control endpoint.

#### Postcondition

The endpoint is ready to accept another packet.

#### **Parameters**

| in  | usbp | pointer to the USBDriver object      |
|-----|------|--------------------------------------|
| in  | ер   | endpoint number                      |
| out | buf  | buffer where to copy the packet data |

#### **Function Class:**

Not an API, this function is for internal use only.

7.37.7.31 void usb\_lld\_prepare\_receive ( USBDriver \* usbp, usbep\_t ep )

Prepares for a receive operation.

#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

#### **Function Class:**

Not an API, this function is for internal use only.

7.37.7.32 void usb\_lld\_prepare\_transmit ( USBDriver \* usbp, usbep\_t ep )

Prepares for a transmit operation.

#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

#### **Function Class:**

Not an API, this function is for internal use only.

7.37.7.33 void usb\_lld\_start\_out ( USBDriver \* usbp, usbep\_t ep )

Starts a receive operation on an OUT endpoint.

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#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

#### **Function Class:**

Not an API, this function is for internal use only.

7.37.7.34 void usb\_lld\_start\_in ( USBDriver \* usbp, usbep\_t ep )

Starts a transmit operation on an IN endpoint.

#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

#### **Function Class:**

Not an API, this function is for internal use only.

7.37.7.35 void usb\_lld\_stall\_out ( USBDriver \* usbp, usbep\_t ep )

Brings an OUT endpoint in the stalled state.

#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

#### **Function Class:**

Not an API, this function is for internal use only.

7.37.7.36 void usb\_lld\_stall\_in ( USBDriver \* usbp, usbep\_t ep )

Brings an IN endpoint in the stalled state.

#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

#### **Function Class:**

Not an API, this function is for internal use only.

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```
7.37.7.37 void usb_lld_clear_out ( USBDriver * usbp, usbep_t ep )
```

Brings an OUT endpoint in the active state.

#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

#### **Function Class:**

Not an API, this function is for internal use only.

```
7.37.7.38 void usb_lld_clear_in ( USBDriver * usbp, usbep_t ep )
```

Brings an IN endpoint in the active state.

#### **Parameters**

| in | usbp | pointer to the USBDriver object |
|----|------|---------------------------------|
| in | ер   | endpoint number                 |

#### **Function Class:**

Not an API, this function is for internal use only.

#### 7.37.8 Variable Documentation

7.37.8.1 USBDriver USBD1

USB1 driver identifier.

```
7.37.8.2 union { ... } ep0_state [static]
```

EP0 state.

Note

It is an union because IN and OUT endpoints are never used at the same time for EP0.

7.37.8.3 USBInEndpointState { ... } in

IN EP0 state.

7.37.8.4 USBOutEndpointState { ... } out

OUT EP0 state.

7.37.8.5 const USBEndpointConfig ep0config [static]

# Initial value:

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```
= {
  USB_EP_MODE_TYPE_CTRL,
    _usb_ep0setup,
    _usb_ep0in,
    _usb_ep0out,
    0x40,
    0x40,
    &ep0_state.in,
    &ep0_state.out
}
```

EP0 initialization structure.

7.38 USB CDC Header 459

#### 7.38 USB CDC Header

USB CDC Support Header.

#### 7.38.1 Detailed Description

USB CDC Support Header.

This header contains definitions and types related to USB CDC.

#### CDC specific messages.

- #define CDC\_SEND\_ENCAPSULATED\_COMMAND 0x00U
- #define CDC\_GET\_ENCAPSULATED\_RESPONSE 0x01U
- #define CDC SET COMM FEATURE 0x02U
- #define CDC\_GET\_COMM\_FEATURE 0x03U
- #define CDC\_CLEAR\_COMM\_FEATURE 0x04U
- #define CDC\_SET\_AUX\_LINE\_STATE 0x10U
- #define CDC\_SET\_HOOK\_STATE 0x11U
- #define CDC\_PULSE\_SETUP 0x12U
- #define CDC\_SEND\_PULSE 0x13U
- #define CDC\_SET\_PULSE\_TIME 0x14U
- #define CDC\_RING\_AUX\_JACK 0x15U
- #define CDC\_SET\_LINE\_CODING 0x20U
- #define CDC GET LINE CODING 0x21U
- #define CDC\_SET\_CONTROL\_LINE\_STATE 0x22U
- #define CDC\_SEND\_BREAK 0x23U
- #define CDC\_SET\_RINGER\_PARMS 0x30U
- #define CDC\_GET\_RINGER\_PARMS 0x31U
- #define CDC\_SET\_OPERATION\_PARMS 0x32U
- #define CDC\_GET\_OPERATION\_PARMS 0x33U

#### **CDC** classes

- #define CDC\_COMMUNICATION\_INTERFACE\_CLASS 0x02U
- #define CDC\_DATA\_INTERFACE\_CLASS 0x0AU

#### **CDC** subclasses

#define CDC\_ABSTRACT\_CONTROL\_MODEL 0x02U

#### **CDC** descriptors

#define CDC\_CS\_INTERFACE 0x24U

# **CDC** subdescriptors

- #define CDC\_HEADER 0x00U
- #define CDC\_CALL\_MANAGEMENT 0x01U
- #define CDC\_ABSTRACT\_CONTROL\_MANAGEMENT 0x02U
- #define CDC\_UNION 0x06U

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# Line Control bit definitions.

- #define LC\_STOP\_1 0U
- #define LC\_STOP\_1P5 1U
- #define LC\_STOP\_2 2U
- #define LC\_PARITY\_NONE 0U
- #define **LC\_PARITY\_ODD** 1U
- #define LC\_PARITY\_EVEN 2U
- #define LC\_PARITY\_MARK 3U
- #define LC\_PARITY\_SPACE 4U

# **Data Structures**

• struct cdc\_linecoding\_t

Type of Line Coding structure.

7.39 WDG Driver 461

#### 7.39 WDG Driver

Generic WDG Driver.

# 7.39.1 Detailed Description

Generic WDG Driver.

This module defines an abstract interface for a watchdog timer.

#### Precondition

In order to use the WDG driver the HAL\_USE\_WDG option must be enabled in halconf.h.

#### **Macros**

• #define wdgResetI(wdgp) wdg\_lld\_reset(wdgp)

Resets WDG's counter.

# **Configuration options**

• #define PLATFORM\_WDG\_USE\_WDG1 FALSE

WDG1 driver enable switch.

# **Typedefs**

• typedef struct WDGDriver WDGDriver

Type of a structure representing an WDG driver.

#### **Data Structures**

struct WDGConfig

Driver configuration structure.

struct WDGDriver

Structure representing an WDG driver.

#### **Functions**

• void wdgInit (void)

WDG Driver initialization.

• void wdgStart (WDGDriver \*wdgp, const WDGConfig \*config)

Configures and activates the WDG peripheral.

void wdgStop (WDGDriver \*wdgp)

Deactivates the WDG peripheral.

void wdgReset (WDGDriver \*wdgp)

Resets WDG's counter.

void wdg\_lld\_init (void)

Low level WDG driver initialization.

void wdg lld start (WDGDriver \*wdgp)

Configures and activates the WDG peripheral.

void wdg\_lld\_stop (WDGDriver \*wdgp)

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Deactivates the WDG peripheral.

void wdg\_lld\_reset (WDGDriver \*wdgp)

Reloads WDG's counter.

#### **Enumerations**

#### 7.39.2 Macro Definition Documentation

7.39.2.1 #define wdgResetl( wdgp ) wdg\_lld\_reset(wdgp)

Resets WDG's counter.

#### **Parameters**

| in wdgp pointer to the WDGDrive |
|---------------------------------|
|---------------------------------|

#### **Function Class:**

This is an **I-Class** API, this function can be invoked from within a system lock zone by both threads and interrupt handlers.

7.39.2.2 #define PLATFORM\_WDG\_USE\_WDG1 FALSE

WDG1 driver enable switch.

Note

The default is FALSE.

# 7.39.3 Typedef Documentation

7.39.3.1 typedef struct WDGDriver WDGDriver

Type of a structure representing an WDG driver.

# 7.39.4 Enumeration Type Documentation

7.39.4.1 enum wdgstate\_t

Driver state machine possible states.

Enumerator

```
WDG_UNINIT Not initialized.WDG_STOP Stopped.WDG_READY Ready.
```

#### 7.39.5 Function Documentation

7.39.5.1 void wdglnit (void)

WDG Driver initialization.

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#### Note

This function is implicitly invoked by hallnit (), there is no need to explicitly initialize the driver.

#### **Function Class:**

Initializer, this function just initializes an object and can be invoked before the kernel is initialized.

Here is the call graph for this function:



7.39.5.2 void wdgStart ( WDGDriver \* wdgp, const WDGConfig \* config )

Configures and activates the WDG peripheral.

#### **Parameters**

| in | wdgp   | pointer to the WDGDriver object |
|----|--------|---------------------------------|
| in | config | pointer to the WDGConfig object |

# **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.39.5.3 void wdgStop ( WDGDriver \* wdgp )

Deactivates the WDG peripheral.

#### **Parameters**

| in | wdgp | pointer to the WDGDriver object |
|----|------|---------------------------------|

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#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

Here is the call graph for this function:



7.39.5.4 void wdgReset ( WDGDriver \* wdgp )

Resets WDG's counter.

#### **Parameters**

| in | wdgp | pointer to the WDGDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.39.5.5 void wdg\_lld\_init ( void )

Low level WDG driver initialization.

#### **Function Class:**

Not an API, this function is for internal use only.

7.39.5.6 void wdg\_lld\_start ( WDGDriver \* wdgp )

Configures and activates the WDG peripheral.

#### **Parameters**

| in | wdgp | pointer to the WDGDriver object |
|----|------|---------------------------------|

#### **Function Class:**

Not an API, this function is for internal use only.

7.39.5.7 void wdg\_lld\_stop ( WDGDriver \* wdgp )

Deactivates the WDG peripheral.

7.39 WDG Driver 465

# **Parameters**

| i | n | wdgp | pointer to the WDGDriver object |
|---|---|------|---------------------------------|
|---|---|------|---------------------------------|

#### **Function Class:**

Normal API, this function can be invoked by regular system threads but not from within a lock zone.

7.39.5.8 void wdg\_lld\_reset ( WDGDriver \* wdgp )

Reloads WDG's counter.

#### **Parameters**

| in | wdgp | pointer to the WDGDriver object |
|----|------|---------------------------------|
|----|------|---------------------------------|

#### **Function Class:**

Not an API, this function is for internal use only.

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# **Chapter 8**

# **Data Structure Documentation**

# 8.1 ADCConfig Struct Reference

Driver configuration structure.

#include <adc\_lld.h>

Collaboration diagram for ADCConfig:

ADCConfig + dummy

# 8.1.1 Detailed Description

Driver configuration structure.

Note

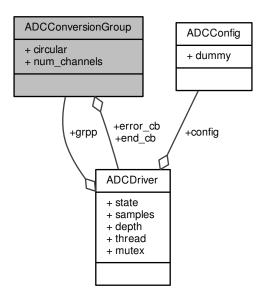
It could be empty on some architectures.

# 8.2 ADCConversionGroup Struct Reference

Conversion group configuration structure.

#include <adc\_lld.h>

Collaboration diagram for ADCConversionGroup:



# **Data Fields**

• bool circular

Enables the circular buffer mode for the group.

• adc\_channels\_num\_t num\_channels

Number of the analog channels belonging to the conversion group.

• adccallback\_t end\_cb

Callback function associated to the group or NULL.

adcerrorcallback\_t error\_cb

Error callback or NULL.

# 8.2.1 Detailed Description

Conversion group configuration structure.

This implementation-dependent structure describes a conversion operation.

Note

The use of this configuration structure requires knowledge of PLATFORM ADC cell registers interface, please refer to the PLATFORM reference manual for details.

#### 8.2.2 Field Documentation

8.2.2.1 bool ADCConversionGroup::circular

Enables the circular buffer mode for the group.

8.2.2.2 adc\_channels\_num\_t ADCConversionGroup::num\_channels

Number of the analog channels belonging to the conversion group.

8.2.2.3 adccallback\_t ADCConversionGroup::end\_cb

Callback function associated to the group or NULL.

8.2.2.4 adcerrorcallback\_t ADCConversionGroup::error\_cb

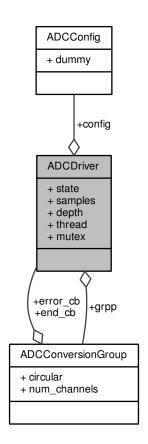
Error callback or NULL.

# 8.3 ADCDriver Struct Reference

Structure representing an ADC driver.

#include <adc\_lld.h>

Collaboration diagram for ADCDriver:



# **Data Fields**

· adcstate\_t state

Driver state.

• const ADCConfig \* config

Current configuration data.

• adcsample\_t \* samples

Current samples buffer pointer or NULL.

size\_t depth

Current samples buffer depth or 0.

const ADCConversionGroup \* grpp

Current conversion group pointer or NULL.

• thread\_reference\_t thread

Waiting thread.

mutex\_t mutex

Mutex protecting the peripheral.

# 8.3.1 Detailed Description

Structure representing an ADC driver.

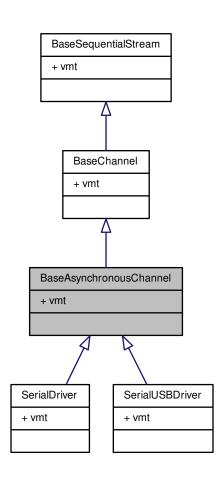
| 8.3.2 Field Documentation                         |
|---|
| 8.3.2.1 adcstate_t ADCDriver::state               |
| Driver state.                                     |
|   |
| 8.3.2.2 const ADCConfig* ADCDriver::config        |
| Current configuration data.                       |
|   |
| 8.3.2.3 adcsample_t* ADCDriver::samples           |
| Current samples buffer pointer or NULL.           |
|   |
| 8.3.2.4 size_t ADCDriver::depth                   |
| Current samples buffer depth or 0.                |
|   |
| 8.3.2.5 const ADCConversionGroup* ADCDriver::grpp |
| Current conversion group pointer or NULL.         |
|   |
| 8.3.2.6 thread_reference_t ADCDriver::thread      |
| Waiting thread.                                   |
|   |
| 8.3.2.7 mutex_t ADCDriver::mutex                  |
| Mutex protecting the peripheral.                  |

# 8.4 BaseAsynchronousChannel Struct Reference

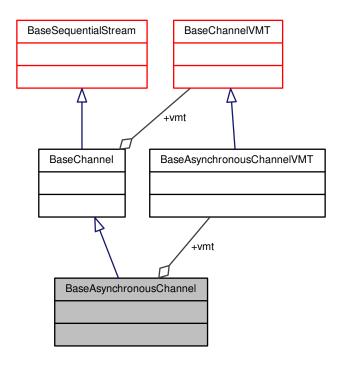
Base asynchronous channel class.

#include <hal\_channels.h>

Inheritance diagram for BaseAsynchronousChannel:



Collaboration diagram for BaseAsynchronousChannel:



#### **Data Fields**

const struct BaseAsynchronousChannelVMT \* vmt
 Virtual Methods Table.

# 8.4.1 Detailed Description

Base asynchronous channel class.

This class extends <code>BaseChannel</code> by adding event sources fields for asynchronous I/O for use in an event-driven environment.

#### 8.4.2 Field Documentation

8.4.2.1 const struct BaseAsynchronousChannelVMT\* BaseAsynchronousChannel::vmt

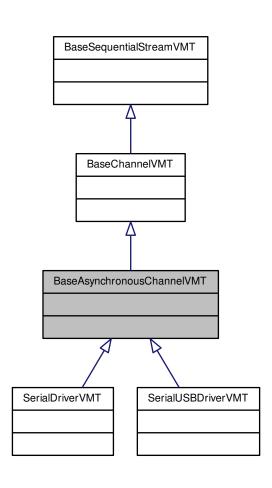
Virtual Methods Table.

# 8.5 BaseAsynchronousChannelVMT Struct Reference

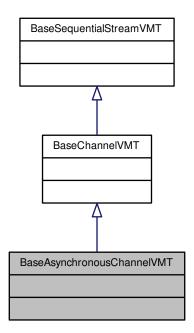
BaseAsynchronousChannel virtual methods table.

#include <hal\_channels.h>

Inheritance diagram for BaseAsynchronousChannelVMT:



Collaboration diagram for BaseAsynchronousChannelVMT:



# 8.5.1 Detailed Description

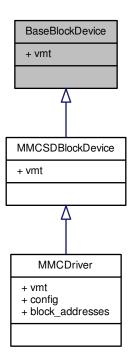
 ${\tt BaseAsynchronousChannel\ \it virtual\ methods\ table}.$ 

# 8.6 BaseBlockDevice Struct Reference

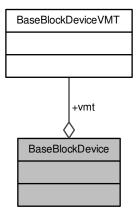
Base block device class.

#include <hal\_ioblock.h>

Inheritance diagram for BaseBlockDevice:



Collaboration diagram for BaseBlockDevice:



# **Data Fields**

• const struct BaseBlockDeviceVMT \* vmt

Virtual Methods Table.

# 8.6.1 Detailed Description

Base block device class.

This class represents a generic, block-accessible, device.

# 8.6.2 Field Documentation

8.6.2.1 const struct BaseBlockDeviceVMT\* BaseBlockDevice::vmt

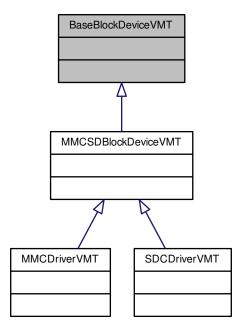
Virtual Methods Table.

# 8.7 BaseBlockDeviceVMT Struct Reference

BaseBlockDevice virtual methods table.

#include <hal\_ioblock.h>

Inheritance diagram for BaseBlockDeviceVMT:



Collaboration diagram for BaseBlockDeviceVMT:

BaseBlockDeviceVMT

# 8.7.1 Detailed Description

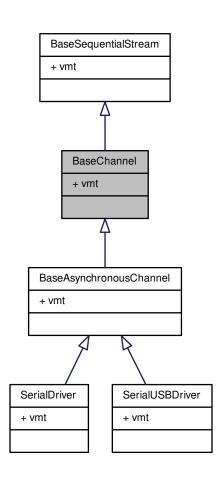
BaseBlockDevice virtual methods table.

# 8.8 BaseChannel Struct Reference

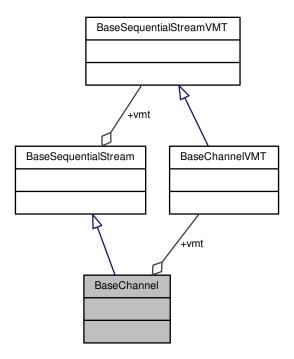
Base channel class.

#include <hal\_channels.h>

Inheritance diagram for BaseChannel:



Collaboration diagram for BaseChannel:



#### **Data Fields**

const struct BaseChannelVMT \* vmt
 Virtual Methods Table.

# 8.8.1 Detailed Description

Base channel class.

This class represents a generic, byte-wide, I/O channel. This class introduces generic I/O primitives with timeout specification.

#### 8.8.2 Field Documentation

8.8.2.1 const struct BaseChannelVMT\* BaseChannel::vmt

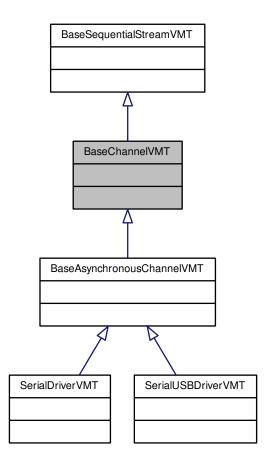
Virtual Methods Table.

# 8.9 BaseChannelVMT Struct Reference

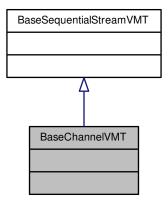
BaseChannel virtual methods table.

#include <hal\_channels.h>

Inheritance diagram for BaseChannelVMT:



Collaboration diagram for BaseChannelVMT:



# 8.9.1 Detailed Description

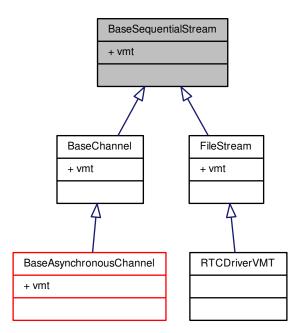
BaseChannel virtual methods table.

# 8.10 BaseSequentialStream Struct Reference

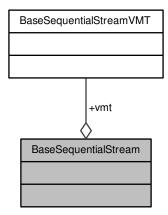
Base stream class.

#include <hal\_streams.h>

Inheritance diagram for BaseSequentialStream:



Collaboration diagram for BaseSequentialStream:



# **Data Fields**

const struct BaseSequentialStreamVMT \* vmt

Virtual Methods Table.

# 8.10.1 Detailed Description

Base stream class.

This class represents a generic blocking unbuffered sequential data stream.

#### 8.10.2 Field Documentation

8.10.2.1 const struct BaseSequentialStreamVMT\* BaseSequentialStream::vmt

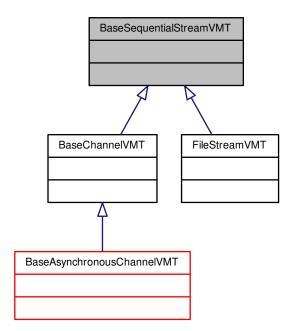
Virtual Methods Table.

# 8.11 BaseSequentialStreamVMT Struct Reference

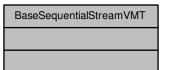
BaseSequentialStream virtual methods table.

#include <hal\_streams.h>

Inheritance diagram for BaseSequentialStreamVMT:



Collaboration diagram for BaseSequentialStreamVMT:



# 8.11.1 Detailed Description

BaseSequentialStream virtual methods table.

# 8.12 BlockDeviceInfo Struct Reference

Block device info.

#include <hal\_ioblock.h>

Collaboration diagram for BlockDeviceInfo:

BlockDeviceInfo
+ blk\_size
+ blk\_num

# **Data Fields**

• uint32\_t blk\_size

Block size in bytes.

• uint32\_t blk\_num

Total number of blocks.

# 8.12.1 Detailed Description

Block device info.

# 8.12.2 Field Documentation

8.12.2.1 uint32\_t BlockDeviceInfo::blk\_size

Block size in bytes.

8.12.2.2 uint32\_t BlockDeviceInfo::blk\_num

Total number of blocks.

# 8.13 CANConfig Struct Reference

Driver configuration structure.

#include <can\_lld.h>

Collaboration diagram for CANConfig:



# 8.13.1 Detailed Description

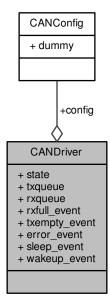
Driver configuration structure.

# 8.14 CANDriver Struct Reference

Structure representing an CAN driver.

#include <can\_lld.h>

Collaboration diagram for CANDriver:



# **Data Fields**

canstate\_t state

Driver state.

const CANConfig \* config

Current configuration data.

threads\_queue\_t txqueue

Transmission threads queue.

• threads\_queue\_t rxqueue

Receive threads queue.

event\_source\_t rxfull\_event

One or more frames become available.

event\_source\_t txempty\_event

One or more transmission mailbox become available.

· event source terror event

A CAN bus error happened.

• event\_source\_t sleep\_event

Entering sleep state event.

event\_source\_t wakeup\_event

Exiting sleep state event.

#### 8.14.1 Detailed Description

Structure representing an CAN driver.

#### 8.14.2 Field Documentation

8.14.2.1 canstate\_t CANDriver::state

Driver state.

8.14.2.2 const CANConfig\* CANDriver::config

Current configuration data.

8.14.2.3 threads\_queue\_t CANDriver::txqueue

Transmission threads queue.

8.14.2.4 threads\_queue\_t CANDriver::rxqueue

Receive threads queue.

8.14.2.5 event\_source\_t CANDriver::rxfull\_event

One or more frames become available.

Note

After broadcasting this event it will not be broadcasted again until the received frames queue has been completely emptied. It is **not** broadcasted for each received frame. It is responsibility of the application to empty the queue by repeatedly invoking <code>chReceive()</code> when listening to this event. This behavior minimizes the interrupt served by the system because CAN traffic.

The flags associated to the listeners will indicate which receive mailboxes become non-empty.

8.14.2.6 event\_source\_t CANDriver::txempty\_event

One or more transmission mailbox become available.

Note

The flags associated to the listeners will indicate which transmit mailboxes become empty.

8.14.2.7 event\_source\_t CANDriver::error\_event

A CAN bus error happened.

Note

The flags associated to the listeners will indicate the error(s) that have occurred.

8.14.2.8 event\_source\_t CANDriver::sleep\_event

Entering sleep state event.

8.14.2.9 event\_source\_t CANDriver::wakeup\_event

Exiting sleep state event.

# 8.15 CANRxFrame Struct Reference

CAN received frame.

#include <can\_lld.h>

Collaboration diagram for CANRxFrame:



#### **Data Fields**

```
• uint8_t FMI
```

Filter id.

uint16\_t TIME

Time stamp.

• uint8\_t DLC:4

Data length.

• uint8\_t RTR:1

Frame type.

• uint8\_t IDE:1

\_

Identifier type.

• uint32\_t SID:11

Standard identifier.

• uint32 t EID:29

Extended identifier.

• uint8\_t data8 [8]

Frame data.

uint16\_t data16 [4]

Frame data.

• uint32\_t data32 [2]

Frame data.

#### 8.15.1 Detailed Description

CAN received frame.

Note

Accessing the frame data as word16 or word32 is not portable because machine data endianness, it can be still useful for a quick filling.

#### 8.15.2 Field Documentation

8.15.2.1 uint8\_t CANRxFrame::FMI

Filter id.

8.15.2.2 uint16\_t CANRxFrame::TIME

Time stamp.

8.15.2.3 uint8\_t CANRxFrame::DLC

Data length.

8.15.2.4 uint8\_t CANRxFrame::RTR

Frame type.

8.15.2.5 uint8\_t CANRxFrame::IDE

Identifier type.

8.15.2.6 uint32\_t CANRxFrame::SID

Standard identifier.

8.15.2.7 uint32\_t CANRxFrame::EID

Extended identifier.

8.15.2.8 uint8\_t CANRxFrame::data8[8]

Frame data.

8.15.2.9 uint16\_t CANRxFrame::data16[4]

Frame data.

8.15.2.10 uint32\_t CANRxFrame::data32[2]

Frame data.

# 8.16 CANTxFrame Struct Reference

CAN transmission frame.

#include <can\_lld.h>

Collaboration diagram for CANTxFrame:

# CANTxFrame + DLC + RTR + IDE + SID + EID + \_align1 + @1 + data8 + data16 + data32 + @3

#### **Data Fields**

```
• uint8_t DLC:4
```

Data length.

• uint8\_t RTR:1

Frame type.

• uint8\_t IDE:1

Identifier type.

uint32\_t SID:11

Standard identifier.

uint32\_t EID:29

Extended identifier.

• uint8\_t data8 [8]

Frame data.

• uint16\_t data16 [4]

Frame data.

• uint32\_t data32 [2]

Frame data.

#### 8.16.1 Detailed Description

CAN transmission frame.

Note

Accessing the frame data as word16 or word32 is not portable because machine data endianness, it can be still useful for a quick filling.

#### 8.16.2 Field Documentation

8.16.2.1 uint8\_t CANTxFrame::DLC

Data length.

8.16.2.2 uint8\_t CANTxFrame::RTR

Frame type.

8.16.2.3 uint8\_t CANTxFrame::IDE

Identifier type.

8.16.2.4 uint32\_t CANTxFrame::SID

Standard identifier.

8.16.2.5 uint32\_t CANTxFrame::EID

Extended identifier.

8.16.2.6 uint8\_t CANTxFrame::data8[8]

Frame data.

8.16.2.7 uint16\_t CANTxFrame::data16[4]

Frame data.

8.16.2.8 uint32\_t CANTxFrame::data32[2]

Frame data.

# cdc\_linecoding\_t Struct Reference

Type of Line Coding structure.

#include <usb\_cdc.h>

Collaboration diagram for cdc\_linecoding\_t:

cdc\_linecoding\_t

- + dwDTERate
- + bCharFormat + bParityType
- + bDataBits

# 8.17.1 Detailed Description

Type of Line Coding structure.

#### **DACConfig Struct Reference** 8.18

Driver configuration structure.

#include <dac\_lld.h>

Collaboration diagram for DACConfig:



# 8.18.1 Detailed Description

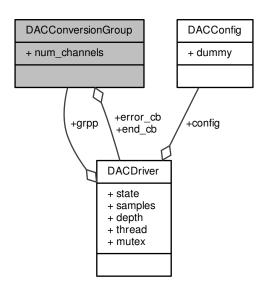
Driver configuration structure.

# 8.19 DACConversionGroup Struct Reference

DAC Conversion group structure.

#include <dac\_lld.h>

Collaboration diagram for DACConversionGroup:



#### **Data Fields**

• uint32\_t num\_channels

Number of DAC channels.

daccallback\_t end\_cb

Operation complete callback or NULL.

• dacerrorcallback\_t error\_cb

Error handling callback or NULL.

# 8.19.1 Detailed Description

DAC Conversion group structure.

#### 8.19.2 Field Documentation

8.19.2.1 uint32\_t DACConversionGroup::num\_channels

Number of DAC channels.

8.19.2.2 daccallback\_t DACConversionGroup::end\_cb

Operation complete callback or NULL.

 $8.19.2.3 \quad dacerror callback\_t \ DACC onversion Group::error\_cb$ 

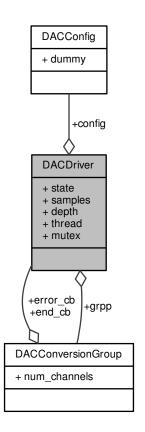
Error handling callback or  $\mathtt{NULL}$ .

# 8.20 DACDriver Struct Reference

Structure representing a DAC driver.

#include <dac\_lld.h>

Collaboration diagram for DACDriver:



# **Data Fields**

· dacstate\_t state

Driver state.

• const DACConversionGroup \* grpp

Conversion group.

• dacsample\_t \* samples

Samples buffer pointer.

uint16\_t depth

Samples buffer size.

• const DACConfig \* config

Current configuration data.

• thread\_reference\_t thread

Waiting thread.

• mutex\_t mutex

Mutex protecting the bus.

# 8.20.1 Detailed Description

Structure representing a DAC driver.

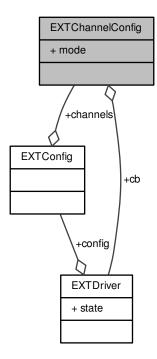


# 8.21 EXTChannelConfig Struct Reference

Channel configuration structure.

#include <ext\_lld.h>

Collaboration diagram for EXTChannelConfig:



# **Data Fields**

• uint32 t mode

Channel mode.

extcallback\_t cb

Channel callback.

# 8.21.1 Detailed Description

Channel configuration structure.

#### 8.21.2 Field Documentation

8.21.2.1 uint32\_t EXTChannelConfig::mode

Channel mode.

#### 8.21.2.2 extcallback\_t EXTChannelConfig::cb

Channel callback.

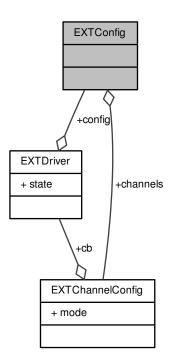
In the STM32 implementation a  $\mathtt{NULL}$  callback pointer is valid and configures the channel as an event sources instead of an interrupt source.

# 8.22 EXTConfig Struct Reference

Driver configuration structure.

#include <ext\_lld.h>

Collaboration diagram for EXTConfig:



#### **Data Fields**

• EXTChannelConfig channels [EXT\_MAX\_CHANNELS]

Channel configurations.

#### 8.22.1 Detailed Description

Driver configuration structure.

Note

It could be empty on some architectures.

## 8.22.2 Field Documentation

# 8.22.2.1 EXTChannelConfig EXTConfig::channels[EXT\_MAX\_CHANNELS]

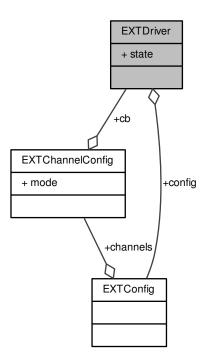
Channel configurations.

# 8.23 EXTDriver Struct Reference

Structure representing an EXT driver.

#include <ext\_lld.h>

Collaboration diagram for EXTDriver:



#### **Data Fields**

• extstate\_t state

Driver state.

• const EXTConfig \* config

Current configuration data.

# 8.23.1 Detailed Description

Structure representing an EXT driver.

# 8.23.2 Field Documentation

8.23.2.1 extstate\_t EXTDriver::state

Driver state.

8.23.2.2 const EXTConfig\* EXTDriver::config

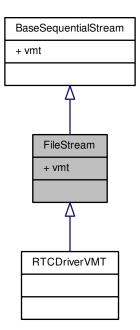
Current configuration data.

# 8.24 FileStream Struct Reference

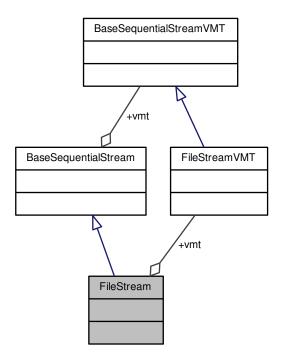
Base file stream class.

#include <hal\_files.h>

Inheritance diagram for FileStream:



Collaboration diagram for FileStream:



#### **Data Fields**

const struct FileStreamVMT \* vmt
 Virtual Methods Table.

# 8.24.1 Detailed Description

Base file stream class.

This class represents a generic file data stream.

#### 8.24.2 Field Documentation

8.24.2.1 const struct FileStreamVMT\* FileStream::vmt

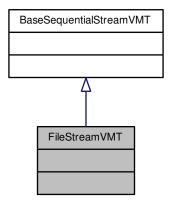
Virtual Methods Table.

# 8.25 FileStreamVMT Struct Reference

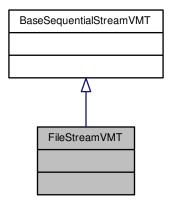
FileStream virtual methods table.

#include <hal\_files.h>

Inheritance diagram for FileStreamVMT:



Collaboration diagram for FileStreamVMT:



# 8.25.1 Detailed Description

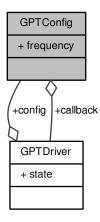
FileStream virtual methods table.

# 8.26 GPTConfig Struct Reference

Driver configuration structure.

#include <gpt\_lld.h>

Collaboration diagram for GPTConfig:



# **Data Fields**

• gptfreq\_t frequency

Timer clock in Hz.

· gptcallback\_t callback

Timer callback pointer.

# 8.26.1 Detailed Description

Driver configuration structure.

Note

It could be empty on some architectures.

#### 8.26.2 Field Documentation

8.26.2.1 gptfreq\_t GPTConfig::frequency

Timer clock in Hz.

Note

The low level can use assertions in order to catch invalid frequency specifications.

#### 8.26.2.2 gptcallback\_t GPTConfig::callback

Timer callback pointer.

Note

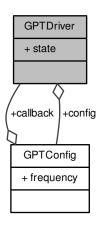
This callback is invoked on GPT counter events.

# 8.27 GPTDriver Struct Reference

Structure representing a GPT driver.

#include <gpt\_lld.h>

Collaboration diagram for GPTDriver:



#### **Data Fields**

• gptstate\_t state

Driver state.

• const GPTConfig \* config

Current configuration data.

# 8.27.1 Detailed Description

Structure representing a GPT driver.

## 8.27.2 Field Documentation

8.27.2.1 gptstate\_t GPTDriver::state

Driver state.

8.27.2.2 const GPTConfig\* GPTDriver::config

Current configuration data.

# 8.28 I2CConfig Struct Reference

Type of I2C driver configuration structure.

```
#include <i2c_lld.h>
```

Collaboration diagram for I2CConfig:



# 8.28.1 Detailed Description

Type of I2C driver configuration structure.

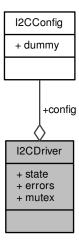
Note

Implementations may extend this structure to contain more, architecture dependent, fields.

# 8.29 I2CDriver Struct Reference

Structure representing an I2C driver.

Collaboration diagram for I2CDriver:



#### **Data Fields**

• i2cstate\_t state

Driver state.

• const I2CConfig \* config

Current configuration data.

i2cflags\_t errors

Error flags.

#### 8.29.1 Detailed Description

Structure representing an I2C driver.

#### 8.29.2 Field Documentation

8.29.2.1 i2cstate\_t I2CDriver::state

Driver state.

8.29.2.2 const I2CConfig\* I2CDriver::config

Current configuration data.

8.29.2.3 i2cflags\_t l2CDriver::errors

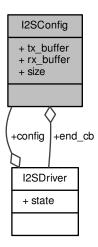
Error flags.

# 8.30 I2SConfig Struct Reference

Driver configuration structure.

#include <i2s\_lld.h>

Collaboration diagram for I2SConfig:



#### **Data Fields**

const void \* tx\_buffer

Transmission buffer pointer.

void \* rx\_buffer

Receive buffer pointer.

• size\_t size

TX and RX buffers size as number of samples.

• i2scallback\_t end\_cb

Callback function called during streaming.

#### 8.30.1 Detailed Description

Driver configuration structure.

Note

It could be empty on some architectures.

#### 8.30.2 Field Documentation

8.30.2.1 const void\* I2SConfig::tx\_buffer

Transmission buffer pointer.

Note

Can be NULL if TX is not required.

8.30.2.2 void\* I2SConfig::rx\_buffer

Receive buffer pointer.

Note

Can be  $\mathtt{NULL}$  if RX is not required.

8.30.2.3 size\_t l2SConfig::size

TX and RX buffers size as number of samples.

8.30.2.4 i2scallback\_t l2SConfig::end\_cb

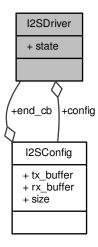
Callback function called during streaming.

#### 8.31 I2SDriver Struct Reference

Structure representing an I2S driver.

#include <i2s\_lld.h>

Collaboration diagram for I2SDriver:



#### **Data Fields**

• i2sstate\_t state

Driver state.

• const I2SConfig \* config

Current configuration data.

# 8.31.1 Detailed Description

Structure representing an I2S driver.

#### 8.31.2 Field Documentation

8.31.2.1 i2sstate\_t l2SDriver::state

Driver state.

8.31.2.2 const I2SConfig\* I2SDriver::config

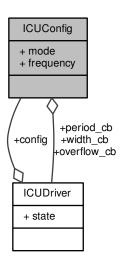
Current configuration data.

# 8.32 ICUConfig Struct Reference

Driver configuration structure.

#include <icu\_lld.h>

Collaboration diagram for ICUConfig:



#### **Data Fields**

• icumode\_t mode

Driver mode.

icufreq\_t frequency

Timer clock in Hz.

• icucallback\_t width\_cb

Callback for pulse width measurement.

icucallback\_t period\_cb

Callback for cycle period measurement.

icucallback\_t overflow\_cb

Callback for timer overflow.

#### 8.32.1 Detailed Description

Driver configuration structure.

Note

It could be empty on some architectures.

#### 8.32.2 Field Documentation

8.32.2.1 icumode\_t ICUConfig::mode

Driver mode.

8.32.2.2 icufreq\_t ICUConfig::frequency

Timer clock in Hz.

Note

The low level can use assertions in order to catch invalid frequency specifications.

8.32.2.3 icucallback\_t ICUConfig::width\_cb

Callback for pulse width measurement.

8.32.2.4 icucallback\_t ICUConfig::period\_cb

Callback for cycle period measurement.

8.32.2.5 icucallback\_t ICUConfig::overflow\_cb

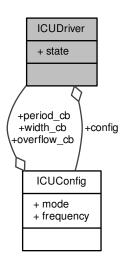
Callback for timer overflow.

# 8.33 ICUDriver Struct Reference

Structure representing an ICU driver.

#include <icu\_lld.h>

Collaboration diagram for ICUDriver:



#### **Data Fields**

· icustate\_t state

Driver state.

• const ICUConfig \* config

Current configuration data.

#### 8.33.1 Detailed Description

Structure representing an ICU driver.

#### 8.33.2 Field Documentation

8.33.2.1 icustate\_t ICUDriver::state

Driver state.

8.33.2.2 const ICUConfig\* ICUDriver::config

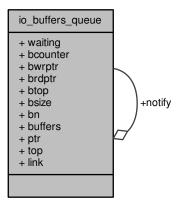
Current configuration data.

# 8.34 io\_buffers\_queue Struct Reference

Structure of a generic buffers queue.

#include <hal\_buffers.h>

Collaboration diagram for io\_buffers\_queue:



#### **Data Fields**

• threads\_queue\_t waiting

Queue of waiting threads.

· volatile size\_t bcounter

Active buffers counter.

• uint8\_t \* bwrptr

Buffer write pointer.

uint8\_t \* brdptr

Buffer read pointer.

uint8\_t \* btop

Pointer to the buffers boundary.

• size\_t bsize

Size of buffers.

size\_t bn

Number of buffers.

uint8\_t \* buffers

Queue of buffer objects.

uint8\_t \* ptr

Pointer for R/W sequential access.

uint8\_t \* top

Boundary for R/W sequential access.

• bqnotify\_t notify

Data notification callback.

void \* link

Application defined field.

#### 8.34.1 Detailed Description

Structure of a generic buffers queue.

8.34.2 Field Documentation

8.34.2.1 threads\_queue\_t io\_buffers\_queue::waiting

Queue of waiting threads.

8.34.2.2 volatile size\_t io\_buffers\_queue::bcounter

Active buffers counter.

8.34.2.3 uint8\_t\* io\_buffers\_queue::bwrptr

Buffer write pointer.

8.34.2.4 uint8\_t\* io\_buffers\_queue::brdptr

Buffer read pointer.

8.34.2.5 uint8\_t\* io\_buffers\_queue::btop

Pointer to the buffers boundary.

8.34.2.6 size\_t io\_buffers\_queue::bsize

Size of buffers.

Note

The buffer size must be not lower than  $sizeof(size_t) + 2$  because the first bytes are used to store the used size of the buffer.

8.34.2.7 size\_t io\_buffers\_queue::bn

Number of buffers.

8.34.2.8 uint8\_t\* io\_buffers\_queue::buffers

Queue of buffer objects.

8.34.2.9 uint8\_t\* io\_buffers\_queue::ptr

Pointer for R/W sequential access.

Note

It is  $\mathtt{NULL}$  if a new buffer must be fetched from the queue.

8.34.2.10 uint8\_t\* io\_buffers\_queue::top

Boundary for R/W sequential access.

8.34.2.11 bqnotify\_t io\_buffers\_queue::notify

Data notification callback.

8.34.2.12 void\* io\_buffers\_queue::link

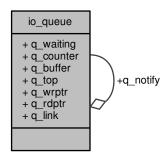
Application defined field.

# 8.35 io\_queue Struct Reference

Generic I/O queue structure.

#include <hal\_queues.h>

Collaboration diagram for io\_queue:



#### **Data Fields**

• threads\_queue\_t q\_waiting

Queue of waiting threads.

volatile size\_t q\_counter

Resources counter.

uint8\_t \* q\_buffer

Pointer to the queue buffer.

uint8\_t \* q\_top

Pointer to the first location after the buffer.

uint8\_t \* q\_wrptr

Write pointer.

uint8\_t \* q\_rdptr

Read pointer.

• qnotify\_t q\_notify

Data notification callback.

void \* q\_link

Application defined field.

#### 8.35.1 Detailed Description

Generic I/O queue structure.

This structure represents a generic Input or Output asymmetrical queue. The queue is asymmetrical because one end is meant to be accessed from a thread context, and thus can be blocking, the other end is accessible from interrupt handlers or from within a kernel lock zone and is non-blocking.

#### 8.35.2 Field Documentation

8.35.2.1 threads\_queue\_t io\_queue::q\_waiting

Queue of waiting threads.

8.35.2.2 volatile size\_t io\_queue::q\_counter

Resources counter.

8.35.2.3 uint8\_t\* io\_queue::q\_buffer

Pointer to the queue buffer.

8.35.2.4 uint8\_t\* io\_queue::q\_top

Pointer to the first location after the buffer.

8.35.2.5 uint8\_t\* io\_queue::q\_wrptr

Write pointer.

8.35.2.6 uint8\_t\* io\_queue::q\_rdptr

Read pointer.

8.35.2.7 qnotify\_t io\_queue::q\_notify

Data notification callback.

8.35.2.8 void\* io\_queue::q\_link

Application defined field.

## 8.36 IOBus Struct Reference

I/O bus descriptor.

#include <pal.h>

Collaboration diagram for IOBus:



#### **Data Fields**

• ioportid\_t portid

Port identifier.

· ioportmask\_t mask

Bus mask aligned to port bit 0.

· uint\_fast8\_t offset

Offset, within the port, of the least significant bit of the bus.

#### 8.36.1 Detailed Description

I/O bus descriptor.

This structure describes a group of contiguous digital I/O lines that have to be handled as bus.

Note

I/O operations on a bus do not affect I/O lines on the same port but not belonging to the bus.

#### 8.36.2 Field Documentation

8.36.2.1 ioportid\_t IOBus::portid

Port identifier.

8.36.2.2 ioportmask\_t IOBus::mask

Bus mask aligned to port bit 0.

Note

The bus mask implicitly define the bus width. A logic AND is performed on the bus data.

8.36.2.3 uint\_fast8\_t IOBus::offset

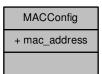
Offset, within the port, of the least significant bit of the bus.

# 8.37 MACConfig Struct Reference

Driver configuration structure.

#include <mac\_lld.h>

Collaboration diagram for MACConfig:



#### **Data Fields**

• uint8\_t \* mac\_address

MAC address.

#### 8.37.1 Detailed Description

Driver configuration structure.

#### 8.37.2 Field Documentation

8.37.2.1 uint8\_t\* MACConfig::mac\_address

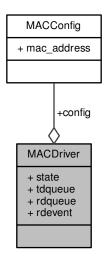
MAC address.

# 8.38 MACDriver Struct Reference

Structure representing a MAC driver.

#include <mac\_lld.h>

Collaboration diagram for MACDriver:



#### **Data Fields**

- · macstate\_t state
  - Driver state.
- const MACConfig \* config

Current configuration data.

- threads\_queue\_t tdqueue
  - Transmit semaphore.
- threads\_queue\_t rdqueue

Receive semaphore.

• event\_source\_t rdevent

Receive event.

# 8.38.1 Detailed Description

Structure representing a MAC driver.

## 8.38.2 Field Documentation

8.38.2.1 macstate\_t MACDriver::state

Driver state.

#### 8.38.2.2 const MACConfig\* MACDriver::config

Current configuration data.

8.38.2.3 threads\_queue\_t MACDriver::tdqueue

Transmit semaphore.

8.38.2.4 threads\_queue\_t MACDriver::rdqueue

Receive semaphore.

8.38.2.5 event\_source\_t MACDriver::rdevent

Receive event.

# 8.39 MACReceiveDescriptor Struct Reference

Structure representing a receive descriptor.

#include <mac\_lld.h>

Collaboration diagram for MACReceiveDescriptor:

MACReceiveDescriptor
+ offset
+ size

#### **Data Fields**

size\_t offset

Current read offset.

• size\_t size

Available data size.

#### 8.39.1 Detailed Description

Structure representing a receive descriptor.

## 8.39.2 Field Documentation

8.39.2.1 size\_t MACReceiveDescriptor::offset

Current read offset.

8.39.2.2 size\_t MACReceiveDescriptor::size

Available data size.

# 8.40 MACTransmitDescriptor Struct Reference

Structure representing a transmit descriptor.

```
#include <mac_lld.h>
```

Collaboration diagram for MACTransmitDescriptor:

MACTransmitDescriptor
+ offset
+ size

#### **Data Fields**

size\_t offset

Current write offset.

• size\_t size

Available space size.

#### 8.40.1 Detailed Description

Structure representing a transmit descriptor.

#### 8.40.2 Field Documentation

8.40.2.1 size\_t MACTransmitDescriptor::offset

Current write offset.

8.40.2.2 size\_t MACTransmitDescriptor::size

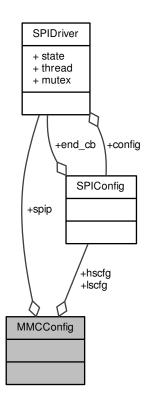
Available space size.

# 8.41 MMCConfig Struct Reference

MMC/SD over SPI driver configuration structure.

#include <mmc\_spi.h>

Collaboration diagram for MMCConfig:



#### **Data Fields**

• SPIDriver \* spip

SPI driver associated to this MMC driver.

const SPIConfig \* Iscfg

SPI low speed configuration used during initialization.

• const SPIConfig \* hscfg

SPI high speed configuration used during transfers.

# 8.41.1 Detailed Description

MMC/SD over SPI driver configuration structure.

#### 8.41.2 Field Documentation

# 8.41.2.1 SPIDriver\* MMCConfig::spip

SPI driver associated to this MMC driver.

8.41.2.2 const SPIConfig\* MMCConfig::lscfg

SPI low speed configuration used during initialization.

8.41.2.3 const SPIConfig\* MMCConfig::hscfg

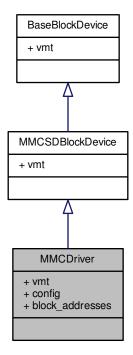
SPI high speed configuration used during transfers.

# 8.42 MMCDriver Struct Reference

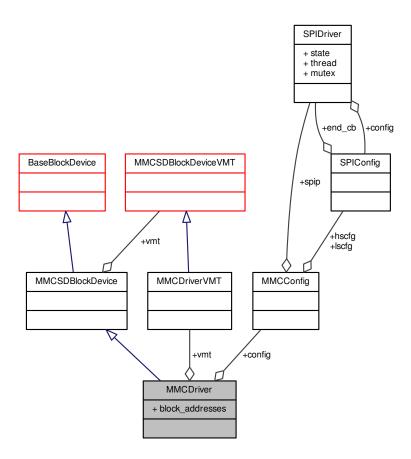
Structure representing a MMC/SD over SPI driver.

#include <mmc\_spi.h>

Inheritance diagram for MMCDriver:



Collaboration diagram for MMCDriver:



# **Data Fields**

- const struct MMCDriverVMT \* vmt
  - Virtual Methods Table.
- \_mmcsd\_block\_device\_data const MMCConfig \* config

Current configuration data.

# 8.42.1 Detailed Description

Structure representing a MMC/SD over SPI driver.

# 8.42.2 Field Documentation

#### 8.42.2.1 const struct MMCDriverVMT\* MMCDriver::vmt

Virtual Methods Table.

8.42.2.2 \_mmcsd\_block\_device\_data const MMCConfig\* MMCDriver::config

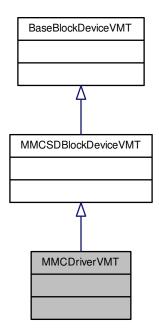
Current configuration data.

# 8.43 MMCDriverVMT Struct Reference

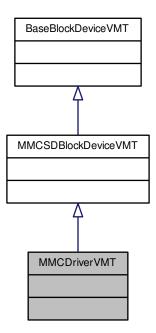
MMCDriver virtual methods table.

#include <mmc\_spi.h>

Inheritance diagram for MMCDriverVMT:



Collaboration diagram for MMCDriverVMT:



## 8.43.1 Detailed Description

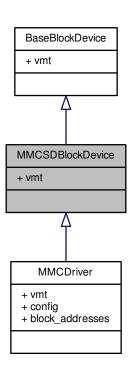
MMCDriver virtual methods table.

## 8.44 MMCSDBlockDevice Struct Reference

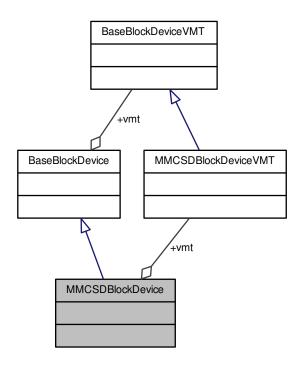
MCC/SD block device class.

#include <hal\_mmcsd.h>

Inheritance diagram for MMCSDBlockDevice:



Collaboration diagram for MMCSDBlockDevice:



## **Data Fields**

const struct MMCSDBlockDeviceVMT \* vmt
 Virtual Methods Table.

#### 8.44.1 Detailed Description

MCC/SD block device class.

This class represents a, block-accessible, MMC/SD device.

#### 8.44.2 Field Documentation

8.44.2.1 const struct MMCSDBlockDeviceVMT\* MMCSDBlockDevice::vmt

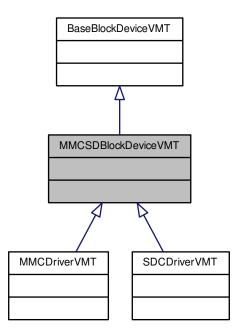
Virtual Methods Table.

## 8.45 MMCSDBlockDeviceVMT Struct Reference

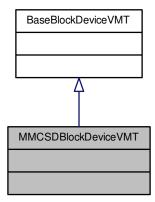
MMCSDBlockDevice virtual methods table.

#include <hal\_mmcsd.h>

Inheritance diagram for MMCSDBlockDeviceVMT:



Collaboration diagram for MMCSDBlockDeviceVMT:



## 8.45.1 Detailed Description

MMCSDBlockDevice virtual methods table.

# 8.46 PALConfig Struct Reference

Generic I/O ports static initializer.

#include <pal\_lld.h>

Collaboration diagram for PALConfig:



#### 8.46.1 Detailed Description

Generic I/O ports static initializer.

An instance of this structure must be passed to palInit () at system startup time in order to initialized the digital I/O subsystem. This represents only the initial setup, specific pads or whole ports can be reprogrammed at later time.

Note

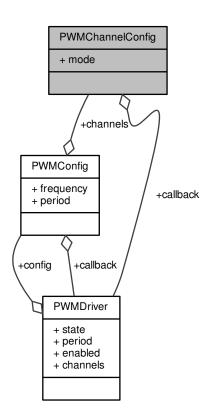
Implementations may extend this structure to contain more, architecture dependent, fields.

# 8.47 PWMChannelConfig Struct Reference

Type of a PWM driver channel configuration structure.

#include <pwm\_lld.h>

Collaboration diagram for PWMChannelConfig:



#### **Data Fields**

- pwmmode\_t mode
  - Channel active logic level.
- pwmcallback\_t callback

Channel callback pointer.

#### 8.47.1 Detailed Description

Type of a PWM driver channel configuration structure.

#### 8.47.2 Field Documentation

8.47.2.1 pwmmode\_t PWMChannelConfig::mode

Channel active logic level.

8.47.2.2 pwmcallback\_t PWMChannelConfig::callback

Channel callback pointer.

Note

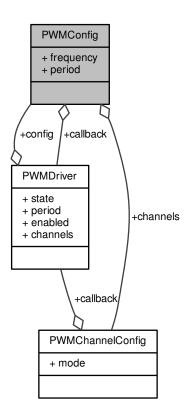
This callback is invoked on the channel compare event. If set to  $\mathtt{NULL}$  then the callback is disabled.

# 8.48 PWMConfig Struct Reference

Type of a PWM driver configuration structure.

#include <pwm\_lld.h>

Collaboration diagram for PWMConfig:



## **Data Fields**

• uint32\_t frequency

Timer clock in Hz.

· pwmcnt\_t period

PWM period in ticks.

• pwmcallback\_t callback

Periodic callback pointer.

• PWMChannelConfig channels [PWM\_CHANNELS]

Channels configurations.

#### 8.48.1 Detailed Description

Type of a PWM driver configuration structure.

#### 8.48.2 Field Documentation

8.48.2.1 uint32\_t PWMConfig::frequency

Timer clock in Hz.

Note

The low level can use assertions in order to catch invalid frequency specifications.

8.48.2.2 pwmcnt\_t PWMConfig::period

PWM period in ticks.

Note

The low level can use assertions in order to catch invalid period specifications.

8.48.2.3 pwmcallback\_t PWMConfig::callback

Periodic callback pointer.

Note

This callback is invoked on PWM counter reset. If set to  $\mathtt{NULL}$  then the callback is disabled.

8.48.2.4 PWMChannelConfig PWMConfig::channels[PWM\_CHANNELS]

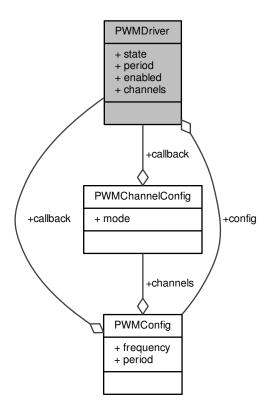
Channels configurations.

#### 8.49 PWMDriver Struct Reference

Structure representing a PWM driver.

#include <pwm\_lld.h>

Collaboration diagram for PWMDriver:



#### **Data Fields**

pwmstate\_t state

Driver state.

• const PWMConfig \* config

Current driver configuration data.

· pwmcnt\_t period

Current PWM period in ticks.

• pwmchnmsk\_t enabled

Mask of the enabled channels.

• pwmchannel\_t channels

Number of channels in this instance.

## 8.49.1 Detailed Description

Structure representing a PWM driver.

#### 8.49.2 Field Documentation

8.49.2.1 pwmstate\_t PWMDriver::state

Driver state.

8.49.2.2 const PWMConfig\* PWMDriver::config

Current driver configuration data.

8.49.2.3 pwmcnt\_t PWMDriver::period

Current PWM period in ticks.

8.49.2.4 pwmchnmsk\_t PWMDriver::enabled

Mask of the enabled channels.

8.49.2.5 pwmchannel\_t PWMDriver::channels

Number of channels in this instance.

## 8.50 RTCAlarm Struct Reference

Type of a structure representing an RTC alarm time stamp.

#include <rtc\_lld.h>

Collaboration diagram for RTCAlarm:



#### 8.50.1 Detailed Description

Type of a structure representing an RTC alarm time stamp.

# 8.51 RTCDateTime Struct Reference

Type of a structure representing an RTC date/time stamp.

#include <rtc.h>

Collaboration diagram for RTCDateTime:

#### RTCDateTime

- + year
- + month
- + dstflag + dayofweek
- + day + millisecond

#### **Data Fields**

```
• uint32_t year: 8
```

Years since 1980.

• uint32\_t month: 4

Months 1..12.

• uint32\_t dstflag: 1

DST correction flag.

• uint32\_t dayofweek: 3

Day of week 1..7.

• uint32\_t day: 5

Day of the month 1..31.

• uint32\_t millisecond: 27

Milliseconds since midnight.

#### 8.51.1 Detailed Description

Type of a structure representing an RTC date/time stamp.

#### 8.51.2 Field Documentation

8.51.2.1 uint32\_t RTCDateTime::year

Years since 1980.

8.51.2.2 uint32\_t RTCDateTime::month

Months 1..12.

8.51.2.3 uint32\_t RTCDateTime::dstflag

DST correction flag.

8.51.2.4 uint32\_t RTCDateTime::dayofweek

Day of week 1..7.

8.51.2.5 uint32\_t RTCDateTime::day

Day of the month 1..31.

8.51.2.6 uint32\_t RTCDateTime::millisecond

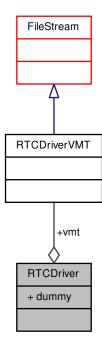
Milliseconds since midnight.

# 8.52 RTCDriver Struct Reference

Structure representing an RTC driver.

#include <rtc\_lld.h>

Collaboration diagram for RTCDriver:



#### **Data Fields**

• const struct RTCDriverVMT \* vmt

Virtual Methods Table.

# 8.52.1 Detailed Description

Structure representing an RTC driver.

#### 8.52.2 Field Documentation

#### 8.52.2.1 const struct RTCDriverVMT\* RTCDriver::vmt

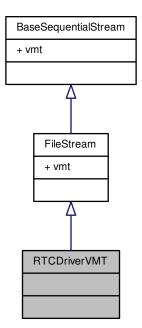
Virtual Methods Table.

# 8.53 RTCDriverVMT Struct Reference

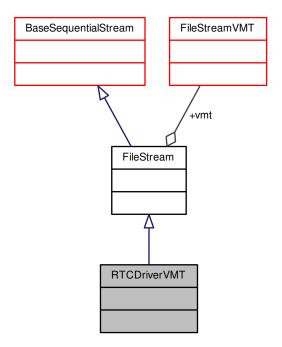
RTCDriver virtual methods table.

#include <rtc\_lld.h>

Inheritance diagram for RTCDriverVMT:



Collaboration diagram for RTCDriverVMT:



#### **Additional Inherited Members**

## 8.53.1 Detailed Description

RTCDriver virtual methods table.

# 8.54 SDCConfig Struct Reference

Driver configuration structure.

#include <sdc\_lld.h>

Collaboration diagram for SDCConfig:



#### **Data Fields**

uint8\_t \* scratchpad

Working area for memory consuming operations.

• sdcbusmode\_t bus\_width

Bus width.

#### 8.54.1 Detailed Description

Driver configuration structure.

Note

It could be empty on some architectures.

#### 8.54.2 Field Documentation

8.54.2.1 uint8\_t\* SDCConfig::scratchpad

Working area for memory consuming operations.

Note

It is mandatory for detecting MMC cards bigger than 2GB else it can be  $\mathtt{NULL}$ . Memory pointed by this buffer is only used by  $\mathtt{sdcConnect}$  (), afterward it can be reused for other purposes.

8.54.2.2 sdcbusmode\_t SDCConfig::bus\_width

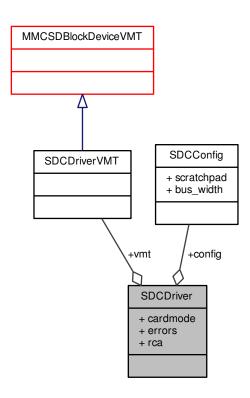
Bus width.

## 8.55 SDCDriver Struct Reference

Structure representing an SDC driver.

#include <sdc\_lld.h>

Collaboration diagram for SDCDriver:



#### **Data Fields**

• const struct SDCDriverVMT \* vmt

Virtual Methods Table.

• \_mmcsd\_block\_device\_data const SDCConfig \* config

Current configuration data.

• sdcmode\_t cardmode

Various flags regarding the mounted card.

sdcflags\_t errors

Errors flags.

• uint32\_t rca

Card RCA.

#### 8.55.1 Detailed Description

Structure representing an SDC driver.

#### 8.55.2 Field Documentation

#### 8.55.2.1 const struct SDCDriverVMT\* SDCDriver::vmt

Virtual Methods Table.

8.55.2.2 \_mmcsd\_block\_device\_data const SDCConfig\* SDCDriver::config

Current configuration data.

8.55.2.3 sdcmode\_t SDCDriver::cardmode

Various flags regarding the mounted card.

8.55.2.4 sdcflags\_t SDCDriver::errors

Errors flags.

8.55.2.5 uint32\_t SDCDriver::rca

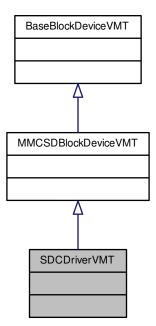
Card RCA.

## 8.56 SDCDriverVMT Struct Reference

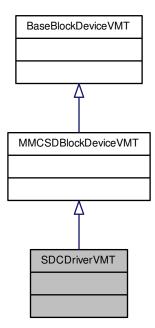
SDCDriver virtual methods table.

#include <sdc\_lld.h>

Inheritance diagram for SDCDriverVMT:



Collaboration diagram for SDCDriverVMT:



## 8.56.1 Detailed Description

SDCDriver virtual methods table.

# 8.57 SerialConfig Struct Reference

PLATFORM Serial Driver configuration structure.

#include <serial\_lld.h>

Collaboration diagram for SerialConfig:



#### **Data Fields**

• uint32\_t speed Bit rate.

## 8.57.1 Detailed Description

PLATFORM Serial Driver configuration structure.

An instance of this structure must be passed to sdStart () in order to configure and start a serial driver operations.

Note

This structure content is architecture dependent, each driver implementation defines its own version and the custom static initializers.

#### 8.57.2 Field Documentation

8.57.2.1 uint32\_t SerialConfig::speed

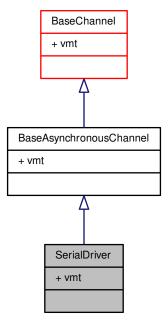
Bit rate.

## 8.58 SerialDriver Struct Reference

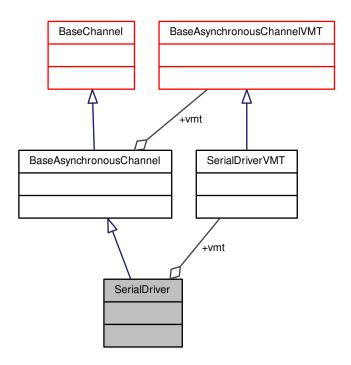
Full duplex serial driver class.

#include <serial.h>

Inheritance diagram for SerialDriver:



Collaboration diagram for SerialDriver:



## **Data Fields**

const struct SerialDriverVMT \* vmt
 Virtual Methods Table.

## 8.58.1 Detailed Description

Full duplex serial driver class.

This class extends BaseAsynchronousChannel by adding physical I/O queues.

#### 8.58.2 Field Documentation

8.58.2.1 const struct SerialDriverVMT\* SerialDriver::vmt

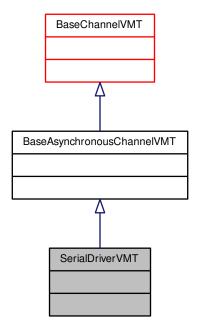
Virtual Methods Table.

## 8.59 SerialDriverVMT Struct Reference

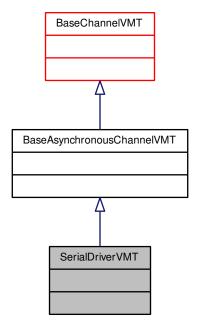
SerialDriver virtual methods table.

#include <serial.h>

Inheritance diagram for SerialDriverVMT:



Collaboration diagram for SerialDriverVMT:



#### 8.59.1 Detailed Description

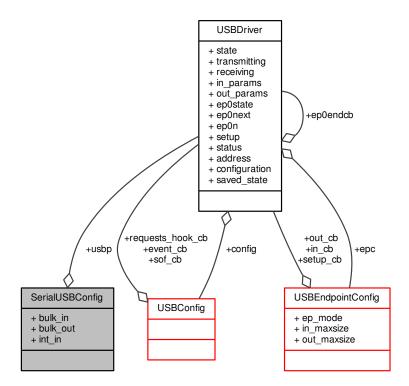
SerialDriver virtual methods table.

# 8.60 SerialUSBConfig Struct Reference

Serial over USB Driver configuration structure.

#include <serial\_usb.h>

Collaboration diagram for SerialUSBConfig:



#### **Data Fields**

USBDriver \* usbp

USB driver to use.

• usbep\_t bulk\_in

Bulk IN endpoint used for outgoing data transfer.

usbep\_t bulk\_out

Bulk OUT endpoint used for incoming data transfer.

• usbep\_t int\_in

Interrupt IN endpoint used for notifications.

#### 8.60.1 Detailed Description

Serial over USB Driver configuration structure.

An instance of this structure must be passed to sduStart () in order to configure and start the driver operations.

#### 8.60.2 Field Documentation

8.60.2.1 USBDriver\* SerialUSBConfig::usbp

USB driver to use.

8.60.2.2 usbep\_t SerialUSBConfig::bulk\_in

Bulk IN endpoint used for outgoing data transfer.

8.60.2.3 usbep\_t SerialUSBConfig::bulk\_out

Bulk OUT endpoint used for incoming data transfer.

8.60.2.4 usbep\_t SerialUSBConfig::int\_in

Interrupt IN endpoint used for notifications.

Note

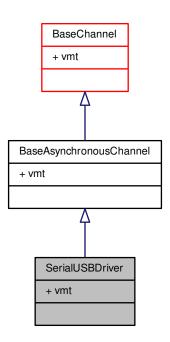
If set to zero then the INT endpoint is assumed to be not present, USB descriptors must be changed accordingly.

## 8.61 SerialUSBDriver Struct Reference

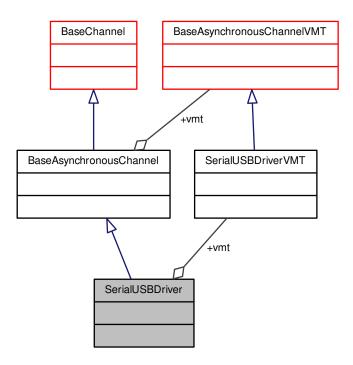
Full duplex serial driver class.

#include <serial\_usb.h>

Inheritance diagram for SerialUSBDriver:



Collaboration diagram for SerialUSBDriver:



## **Data Fields**

const struct SerialUSBDriverVMT \* vmt
 Virtual Methods Table.

#### 8.61.1 Detailed Description

Full duplex serial driver class.

This class extends  ${\tt BaseAsynchronousChannel}$  by adding physical I/O queues.

#### 8.61.2 Field Documentation

 $8.61.2.1 \quad const \ struct \ Serial USBDriver VMT* \ Serial USBDriver::vmt$ 

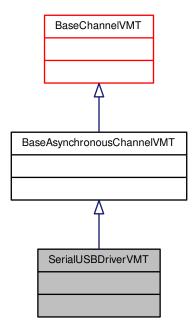
Virtual Methods Table.

## 8.62 SerialUSBDriverVMT Struct Reference

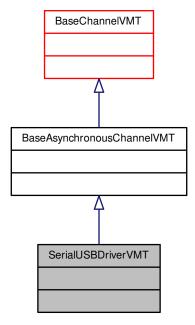
SerialDriver virtual methods table.

#include <serial\_usb.h>

Inheritance diagram for SerialUSBDriverVMT:



Collaboration diagram for SerialUSBDriverVMT:



## 8.62.1 Detailed Description

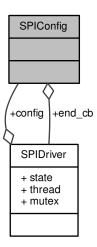
SerialDriver virtual methods table.

# 8.63 SPIConfig Struct Reference

Driver configuration structure.

#include <spi\_lld.h>

Collaboration diagram for SPIConfig:



#### **Data Fields**

spicallback\_t end\_cb

Operation complete callback or NULL.

#### 8.63.1 Detailed Description

Driver configuration structure.

Note

Implementations may extend this structure to contain more, architecture dependent, fields.

#### 8.63.2 Field Documentation

8.63.2.1 spicallback\_t SPIConfig::end\_cb

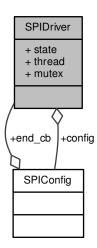
Operation complete callback or  $\mathtt{NULL}$ .

#### 8.64 SPIDriver Struct Reference

Structure representing an SPI driver.

#include <spi\_lld.h>

Collaboration diagram for SPIDriver:



#### **Data Fields**

spistate\_t state

Driver state.

• const SPIConfig \* config

Current configuration data.

• thread\_reference\_t thread

Waiting thread.

• mutex\_t mutex

Mutex protecting the peripheral.

#### 8.64.1 Detailed Description

Structure representing an SPI driver.

Note

Implementations may extend this structure to contain more, architecture dependent, fields.

#### 8.64.2 Field Documentation

8.64.2.1 spistate\_t SPIDriver::state

Driver state.

8.64.2.2 const SPIConfig\* SPIDriver::config

Current configuration data.

8.64.2.3 thread\_reference\_t SPIDriver::thread

Waiting thread.

8.64.2.4 mutex\_t SPIDriver::mutex

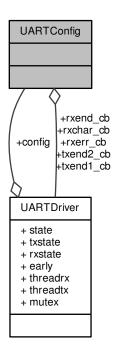
Mutex protecting the peripheral.

# 8.65 UARTConfig Struct Reference

Driver configuration structure.

#include <uart\_lld.h>

Collaboration diagram for UARTConfig:



#### **Data Fields**

uartcb\_t txend1\_cb

End of transmission buffer callback.

uartcb\_t txend2\_cb

Physical end of transmission callback.

· uartcb\_t rxend\_cb

Receive buffer filled callback.

uartccb\_t rxchar\_cb

Character received while out if the UART\_RECEIVE state.

· uartecb t rxerr cb

Receive error callback.

#### 8.65.1 Detailed Description

Driver configuration structure.

Note

Implementations may extend this structure to contain more, architecture dependent, fields.

#### 8.65.2 Field Documentation

8.65.2.1 uartcb\_t UARTConfig::txend1\_cb

End of transmission buffer callback.

8.65.2.2 uartcb\_t UARTConfig::txend2\_cb

Physical end of transmission callback.

8.65.2.3 uartcb\_t UARTConfig::rxend\_cb

Receive buffer filled callback.

8.65.2.4 uartccb\_t UARTConfig::rxchar\_cb

Character received while out if the UART\_RECEIVE state.

8.65.2.5 uartecb\_t UARTConfig::rxerr\_cb

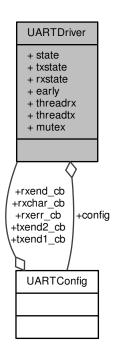
Receive error callback.

#### 8.66 UARTDriver Struct Reference

Structure representing an UART driver.

#include <uart\_lld.h>

Collaboration diagram for UARTDriver:



#### **Data Fields**

• uartstate\_t state

Driver state.

• uarttxstate\_t txstate

Transmitter state.

uartrxstate\_t rxstate

Receiver state.

• const UARTConfig \* config

Current configuration data.

· bool early

Synchronization flag for transmit operations.

• thread\_reference\_t threadrx

Waiting thread on RX.

• thread\_reference\_t threadtx

Waiting thread on TX.

mutex\_t mutex

Mutex protecting the peripheral.

#### 8.66.1 Detailed Description

Structure representing an UART driver.

Note

Implementations may extend this structure to contain more, architecture dependent, fields.

8.66.2 Field Documentation

8.66.2.1 uartstate\_t UARTDriver::state

Driver state.

8.66.2.2 uarttxstate\_t UARTDriver::txstate

Transmitter state.

8.66.2.3 uartrxstate\_t UARTDriver::rxstate

Receiver state.

8.66.2.4 const UARTConfig\* UARTDriver::config

Current configuration data.

8.66.2.5 bool UARTDriver::early

Synchronization flag for transmit operations.

8.66.2.6 thread\_reference\_t UARTDriver::threadrx

Waiting thread on RX.

8.66.2.7 thread\_reference\_t UARTDriver::threadtx

Waiting thread on TX.

8.66.2.8 mutex\_t UARTDriver::mutex

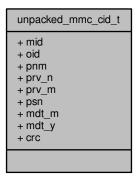
Mutex protecting the peripheral.

# 8.67 unpacked\_mmc\_cid\_t Struct Reference

Unpacked CID register from MMC.

#include <hal\_mmcsd.h>

Collaboration diagram for unpacked\_mmc\_cid\_t:



## 8.67.1 Detailed Description

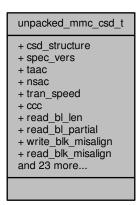
Unpacked CID register from MMC.

# 8.68 unpacked\_mmc\_csd\_t Struct Reference

Unpacked CSD register from MMC.

#include <hal\_mmcsd.h>

Collaboration diagram for unpacked\_mmc\_csd\_t:



#### 8.68.1 Detailed Description

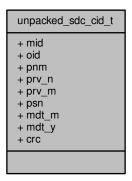
Unpacked CSD register from MMC.

# 8.69 unpacked\_sdc\_cid\_t Struct Reference

Unpacked CID register from SDC.

#include <hal\_mmcsd.h>

Collaboration diagram for unpacked\_sdc\_cid\_t:



## 8.69.1 Detailed Description

Unpacked CID register from SDC.

# 8.70 unpacked\_sdc\_csd\_10\_t Struct Reference

Unpacked CSD v1.0 register from SDC.

#include <hal\_mmcsd.h>

Collaboration diagram for unpacked\_sdc\_csd\_10\_t:

# unpacked\_sdc\_csd\_10\_t + csd\_structure + taac + nsac + tran\_speed + ccc + read\_bl\_len + read\_bl\_partial + write\_blk\_misalign + read\_blk\_misalign + dsr\_imp and 19 more...

#### 8.70.1 Detailed Description

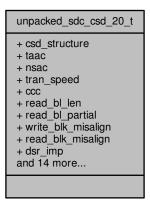
Unpacked CSD v1.0 register from SDC.

# 8.71 unpacked\_sdc\_csd\_20\_t Struct Reference

Unpacked CSD v2.0 register from SDC.

#include <hal\_mmcsd.h>

Collaboration diagram for unpacked\_sdc\_csd\_20\_t:



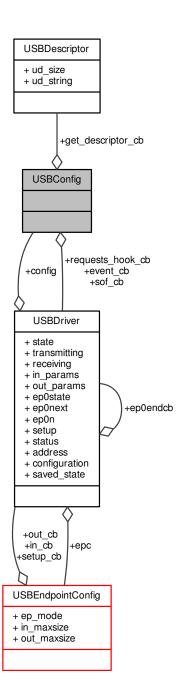
Unpacked CSD v2.0 register from SDC.

# 8.72 USBConfig Struct Reference

Type of an USB driver configuration structure.

#include <usb\_lld.h>

Collaboration diagram for USBConfig:



# **Data Fields**

• usbeventcb\_t event\_cb

USB events callback.

• usbgetdescriptor\_t get\_descriptor\_cb

Device GET\_DESCRIPTOR request callback.

• usbreqhandler\_t requests\_hook\_cb

Requests hook callback.

• usbcallback\_t sof\_cb

Start Of Frame callback.

# 8.72.1 Detailed Description

Type of an USB driver configuration structure.

#### 8.72.2 Field Documentation

8.72.2.1 usbeventcb\_t USBConfig::event\_cb

USB events callback.

This callback is invoked when an USB driver event is registered.

8.72.2.2 usbgetdescriptor\_t USBConfig::get\_descriptor\_cb

Device GET\_DESCRIPTOR request callback.

Note

This callback is mandatory and cannot be set to NULL.

8.72.2.3 usbreqhandler\_t USBConfig::requests\_hook\_cb

Requests hook callback.

This hook allows to be notified of standard requests or to handle non standard requests.

8.72.2.4 usbcallback\_t USBConfig::sof\_cb

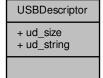
Start Of Frame callback.

# 8.73 USBDescriptor Struct Reference

Type of an USB descriptor.

#include <usb.h>

Collaboration diagram for USBDescriptor:



# **Data Fields**

size\_t ud\_size

Descriptor size in unicode characters.

const uint8 t \* ud string

Pointer to the descriptor.

# 8.73.1 Detailed Description

Type of an USB descriptor.

# 8.73.2 Field Documentation

8.73.2.1 size\_t USBDescriptor::ud\_size

Descriptor size in unicode characters.

8.73.2.2 const uint8\_t\* USBDescriptor::ud\_string

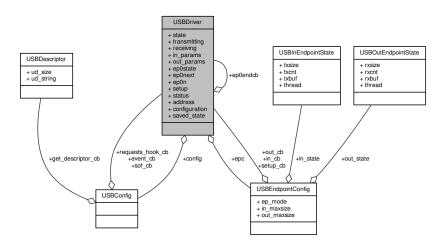
Pointer to the descriptor.

# 8.74 USBDriver Struct Reference

Structure representing an USB driver.

#include <usb\_lld.h>

Collaboration diagram for USBDriver:



# **Data Fields**

• usbstate\_t state

Driver state.

• const USBConfig \* config

Current configuration data.

uint16\_t transmitting

Bit map of the transmitting IN endpoints.

· uint16 t receiving

Bit map of the receiving OUT endpoints.

const USBEndpointConfig \* epc [USB\_MAX\_ENDPOINTS+1]

Active endpoints configurations.

void \* in params [USB MAX ENDPOINTS]

Fields available to user, it can be used to associate an application-defined handler to an IN endpoint.

void \* out\_params [USB\_MAX\_ENDPOINTS]

Fields available to user, it can be used to associate an application-defined handler to an OUT endpoint.

• usbep0state\_t ep0state

Endpoint 0 state.

uint8\_t \* ep0next

Next position in the buffer to be transferred through endpoint 0.

• size\_t ep0n

Number of bytes yet to be transferred through endpoint 0.

· usbcallback tep0endcb

Endpoint 0 end transaction callback.

• uint8\_t setup [8]

Setup packet buffer.

· uint16 t status

Current USB device status.

uint8\_t address

Assigned USB address.

· uint8\_t configuration

Current USB device configuration.

usbstate\_t saved\_state

State of the driver when a suspend happened.

### 8.74.1 Detailed Description

Structure representing an USB driver.

#### 8.74.2 Field Documentation

8.74.2.1 usbstate t USBDriver::state

Driver state.

8.74.2.2 const USBConfig\* USBDriver::config

Current configuration data.

8.74.2.3 uint16\_t USBDriver::transmitting

Bit map of the transmitting IN endpoints.

8.74.2.4 uint16\_t USBDriver::receiving

Bit map of the receiving OUT endpoints.

8.74.2.5 const USBEndpointConfig\* USBDriver::epc[USB\_MAX\_ENDPOINTS+1]

Active endpoints configurations.

8.74.2.6 void\* USBDriver::in\_params[USB\_MAX\_ENDPOINTS]

Fields available to user, it can be used to associate an application-defined handler to an IN endpoint.

Note

The base index is one, the endpoint zero does not have a reserved element in this array.

8.74.2.7 void\* USBDriver::out\_params[USB\_MAX\_ENDPOINTS]

Fields available to user, it can be used to associate an application-defined handler to an OUT endpoint.

Note

The base index is one, the endpoint zero does not have a reserved element in this array.

8.74.2.8 usbep0state\_t USBDriver::ep0state

Endpoint 0 state.

8.74.2.9 uint8\_t\* USBDriver::ep0next

Next position in the buffer to be transferred through endpoint 0.

8.74.2.10 size\_t USBDriver::ep0n

Number of bytes yet to be transferred through endpoint 0.

8.74.2.11 usbcallback\_t USBDriver::ep0endcb

Endpoint 0 end transaction callback.

8.74.2.12 uint8\_t USBDriver::setup[8]

Setup packet buffer.

8.74.2.13 uint16\_t USBDriver::status

Current USB device status.

8.74.2.14 uint8\_t USBDriver::address

Assigned USB address.

8.74.2.15 uint8\_t USBDriver::configuration

Current USB device configuration.

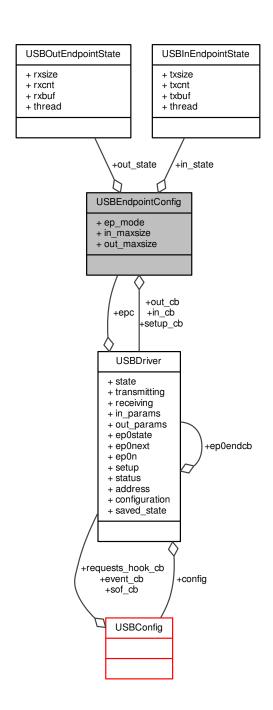
566 **Data Structure Documentation** 8.74.2.16 usbstate\_t USBDriver::saved\_state State of the driver when a suspend happened.

# 8.75 USBEndpointConfig Struct Reference

Type of an USB endpoint configuration structure.

#include <usb\_lld.h>

Collaboration diagram for USBEndpointConfig:



# **Data Fields**

• uint32\_t ep\_mode

Type and mode of the endpoint.

usbepcallback\_t setup\_cb

Setup packet notification callback.

usbepcallback\_t in\_cb

IN endpoint notification callback.

usbepcallback\_t out\_cb

OUT endpoint notification callback.

• uint16 t in maxsize

IN endpoint maximum packet size.

• uint16\_t out\_maxsize

OUT endpoint maximum packet size.

USBInEndpointState \* in\_state

USBEndpointState associated to the IN endpoint.

USBOutEndpointState \* out state

USBEndpointState associated to the OUT endpoint.

# 8.75.1 Detailed Description

Type of an USB endpoint configuration structure.

Note

Platform specific restrictions may apply to endpoints.

#### 8.75.2 Field Documentation

8.75.2.1 uint32\_t USBEndpointConfig::ep\_mode

Type and mode of the endpoint.

8.75.2.2 usbepcallback\_t USBEndpointConfig::setup\_cb

Setup packet notification callback.

This callback is invoked when a setup packet has been received.

Postcondition

The application must immediately call usbReadPacket () in order to access the received packet.

Note

This field is only valid for <code>USB\_EP\_MODE\_TYPE\_CTRL</code> endpoints, it should be set to <code>NULL</code> for other endpoint types.

8.75.2.3 usbepcallback\_t USBEndpointConfig::in\_cb

IN endpoint notification callback.

This field must be set to  $\mathtt{NULL}$  if the IN endpoint is not used.

8.75.2.4 usbepcallback\_t USBEndpointConfig::out\_cb

OUT endpoint notification callback.

This field must be set to  $\mathtt{NULL}$  if the OUT endpoint is not used.

8.75.2.5 uint16\_t USBEndpointConfig::in\_maxsize

IN endpoint maximum packet size.

This field must be set to zero if the IN endpoint is not used.

8.75.2.6 uint16\_t USBEndpointConfig::out\_maxsize

OUT endpoint maximum packet size.

This field must be set to zero if the OUT endpoint is not used.

8.75.2.7 USBInEndpointState\* USBEndpointConfig::in\_state

USBEndpointState associated to the IN endpoint.

This structure maintains the state of the IN endpoint.

8.75.2.8 USBOutEndpointState\* USBEndpointConfig::out\_state

USBEndpointState associated to the OUT endpoint.

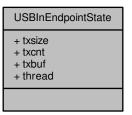
This structure maintains the state of the OUT endpoint.

# 8.76 USBInEndpointState Struct Reference

Type of an IN endpoint state structure.

#include <usb\_lld.h>

Collaboration diagram for USBInEndpointState:



#### **Data Fields**

• size t txsize

Requested transmit transfer size.

· size\_t txcnt

Transmitted bytes so far.

const uint8\_t \* txbuf

Pointer to the transmission linear buffer.

• thread\_reference\_t thread Waiting thread.

# 8.76.1 Detailed Description

Type of an IN endpoint state structure.

# 8.76.2 Field Documentation

8.76.2.1 size\_t USBInEndpointState::txsize

Requested transmit transfer size.

8.76.2.2 size\_t USBInEndpointState::txcnt

Transmitted bytes so far.

8.76.2.3 const uint8\_t\* USBInEndpointState::txbuf

Pointer to the transmission linear buffer.

8.76.2.4 thread\_reference\_t USBInEndpointState::thread

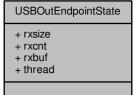
Waiting thread.

# 8.77 USBOutEndpointState Struct Reference

Type of an OUT endpoint state structure.

#include <usb\_lld.h>

Collaboration diagram for USBOutEndpointState:



### **Data Fields**

• size\_t rxsize

Requested receive transfer size.

• size\_t rxcnt

Received bytes so far.

uint8\_t \* rxbuf

Pointer to the receive linear buffer.

• thread\_reference\_t thread

Waiting thread.

# 8.77.1 Detailed Description

Type of an OUT endpoint state structure.

#### 8.77.2 Field Documentation

8.77.2.1 size\_t USBOutEndpointState::rxsize

Requested receive transfer size.

8.77.2.2 size\_t USBOutEndpointState::rxcnt

Received bytes so far.

8.77.2.3 uint8\_t\* USBOutEndpointState::rxbuf

Pointer to the receive linear buffer.

8.77.2.4 thread\_reference\_t USBOutEndpointState::thread

Waiting thread.

# 8.78 WDGConfig Struct Reference

Driver configuration structure.

#include <wdg\_lld.h>

Collaboration diagram for WDGConfig:

WDGConfig

# 8.78.1 Detailed Description

Driver configuration structure.

Note

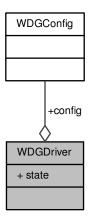
It could be empty on some architectures.

# 8.79 WDGDriver Struct Reference

Structure representing an WDG driver.

#include <wdg\_lld.h>

Collaboration diagram for WDGDriver:



# **Data Fields**

wdgstate\_t state

Driver state.

• const WDGConfig \* config

Current configuration data.

# 8.79.1 Detailed Description

Structure representing an WDG driver.

# 8.79.2 Field Documentation

8.79.2.1 wdgstate\_t WDGDriver::state

Driver state.

8.79.2.2 const WDGConfig\* WDGDriver::config

Current configuration data.

# **Chapter 9**

# **File Documentation**

# 9.1 adc.c File Reference

ADC Driver code.

```
#include "hal.h"
```

#### **Functions**

void adcInit (void)

ADC Driver initialization.

void adcObjectInit (ADCDriver \*adcp)

Initializes the standard part of a ADCDriver structure.

void adcStart (ADCDriver \*adcp, const ADCConfig \*config)

Configures and activates the ADC peripheral.

void adcStop (ADCDriver \*adcp)

Deactivates the ADC peripheral.

void adcStartConversion (ADCDriver \*adcp, const ADCConversionGroup \*grpp, adcsample\_t \*samples, size\_t depth)

Starts an ADC conversion.

void adcStartConversionI (ADCDriver \*adcp, const ADCConversionGroup \*grpp, adcsample\_t \*samples, size\_t depth)

Starts an ADC conversion.

void adcStopConversion (ADCDriver \*adcp)

Stops an ongoing conversion.

void adcStopConversionI (ADCDriver \*adcp)

Stops an ongoing conversion.

msg\_t adcConvert (ADCDriver \*adcp, const ADCConversionGroup \*grpp, adcsample\_t \*samples, size\_t depth)

Performs an ADC conversion.

void adcAcquireBus (ADCDriver \*adcp)

Gains exclusive access to the ADC peripheral.

void adcReleaseBus (ADCDriver \*adcp)

Releases exclusive access to the ADC peripheral.

### 9.1.1 Detailed Description

ADC Driver code.

### 9.2 adc.h File Reference

#### ADC Driver macros and structures.

```
#include "adc_lld.h"
```

#### **Macros**

#### **ADC** configuration options

• #define ADC USE WAIT TRUE

Enables synchronous APIs.

• #define ADC\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the adcAcquireBus() and adcReleaseBus() APIs.

#### Low level driver helper macros

• #define \_adc\_reset\_i(adcp) osalThreadResumel(&(adcp)->thread, MSG\_RESET)

Resumes a thread waiting for a conversion completion.

#define adc reset s(adcp) osalThreadResumeS(&(adcp)->thread, MSG RESET)

Resumes a thread waiting for a conversion completion.

#define \_adc\_wakeup\_isr(adcp)

Wakes up the waiting thread.

#define \_adc\_timeout\_isr(adcp)

Wakes up the waiting thread with a timeout message.

#define \_adc\_isr\_half\_code(adcp)

Common ISR code, half buffer event.

#define \_adc\_isr\_full\_code(adcp)

Common ISR code, full buffer event.

#define \_adc\_isr\_error\_code(adcp, err)

Common ISR code, error event.

### **Enumerations**

### **Functions**

· void adcInit (void)

ADC Driver initialization.

void adcObjectInit (ADCDriver \*adcp)

 ${\it Initializes the standard part of a ADCDriver structure}.$ 

void adcStart (ADCDriver \*adcp, const ADCConfig \*config)

Configures and activates the ADC peripheral.

void adcStop (ADCDriver \*adcp)

Deactivates the ADC peripheral.

void adcStartConversion (ADCDriver \*adcp, const ADCConversionGroup \*grpp, adcsample\_t \*samples, size\_t depth)

Starts an ADC conversion.

void adcStartConversionI (ADCDriver \*adcp, const ADCConversionGroup \*grpp, adcsample\_t \*samples, size\_t depth)

Starts an ADC conversion.

void adcStopConversion (ADCDriver \*adcp)

Stops an ongoing conversion.

void adcStopConversionI (ADCDriver \*adcp)

Stops an ongoing conversion.

msg\_t adcConvert (ADCDriver \*adcp, const ADCConversionGroup \*grpp, adcsample\_t \*samples, size\_t depth)

Performs an ADC conversion.

void adcAcquireBus (ADCDriver \*adcp)

Gains exclusive access to the ADC peripheral.

void adcReleaseBus (ADCDriver \*adcp)

Releases exclusive access to the ADC peripheral.

# 9.2.1 Detailed Description

ADC Driver macros and structures.

# 9.3 adc\_lld.c File Reference

PLATFORM ADC subsystem low level driver source.

```
#include "hal.h"
```

#### **Functions**

· void adc Ild init (void)

Low level ADC driver initialization.

void adc\_lld\_start (ADCDriver \*adcp)

Configures and activates the ADC peripheral.

void adc\_lld\_stop (ADCDriver \*adcp)

Deactivates the ADC peripheral.

void adc\_lld\_start\_conversion (ADCDriver \*adcp)

Starts an ADC conversion.

void adc\_lld\_stop\_conversion (ADCDriver \*adcp)

Stops an ongoing conversion.

#### **Variables**

ADCDriver ADCD1

ADC1 driver identifier.

# 9.3.1 Detailed Description

PLATFORM ADC subsystem low level driver source.

# 9.4 adc\_lld.h File Reference

PLATFORM ADC subsystem low level driver header.

### **Data Structures**

struct ADCConversionGroup

Conversion group configuration structure.

struct ADCConfig

Driver configuration structure.

struct ADCDriver

Structure representing an ADC driver.

#### **Macros**

#### **PLATFORM** configuration options

#define PLATFORM\_ADC\_USE\_ADC1 FALSE
 ADC1 driver enable switch.

# **Typedefs**

typedef uint16\_t adcsample\_t

ADC sample data type.

• typedef uint16\_t adc\_channels\_num\_t

Channels number in a conversion group.

· typedef struct ADCDriver ADCDriver

Type of a structure representing an ADC driver.

• typedef void(\* adccallback\_t) (ADCDriver \*adcp, adcsample\_t \*buffer, size\_t n)

ADC notification callback type.

typedef void(\* adcerrorcallback\_t) (ADCDriver \*adcp, adcerror\_t err)

ADC error callback type.

### **Enumerations**

# **Functions**

• void adc\_lld\_init (void)

Low level ADC driver initialization.

void adc\_lld\_start (ADCDriver \*adcp)

Configures and activates the ADC peripheral.

void adc\_lld\_stop (ADCDriver \*adcp)

Deactivates the ADC peripheral.

void adc\_lld\_start\_conversion (ADCDriver \*adcp)

Starts an ADC conversion.

void adc\_lld\_stop\_conversion (ADCDriver \*adcp)

Stops an ongoing conversion.

# 9.4.1 Detailed Description

PLATFORM ADC subsystem low level driver header.

9.5 can.c File Reference 579

### 9.5 can.c File Reference

#### CAN Driver code.

```
#include "hal.h"
```

# **Functions**

void canInit (void)

CAN Driver initialization.

void canObjectInit (CANDriver \*canp)

Initializes the standard part of a CANDriver structure.

void canStart (CANDriver \*canp, const CANConfig \*config)

Configures and activates the CAN peripheral.

void canStop (CANDriver \*canp)

Deactivates the CAN peripheral.

• bool canTryTransmitI (CANDriver \*canp, canmbx\_t mailbox, const CANTxFrame \*ctfp)

Can frame transmission attempt.

• bool canTryReceivel (CANDriver \*canp, canmbx\_t mailbox, CANRxFrame \*crfp)

Can frame receive attempt.

- msg\_t canTransmit (CANDriver \*canp, canmbx\_t mailbox, const CANTxFrame \*ctfp, systime\_t timeout)
   Can frame transmission.
- msg\_t canReceive (CANDriver \*canp, canmbx\_t mailbox, CANRxFrame \*crfp, systime\_t timeout)

Can frame receive.
• void canSleep (CANDriver \*canp)

Enters the sleep mode.

void canWakeup (CANDriver \*canp)

Enforces leaving the sleep mode.

### 9.5.1 Detailed Description

CAN Driver code.

### 9.6 can.h File Reference

CAN Driver macros and structures.

```
#include "can_lld.h"
```

#### **Macros**

• #define CAN\_ANY\_MAILBOX 0

Special mailbox identifier.

# **CAN** status flags

• #define CAN\_LIMIT\_WARNING 1U

Errors rate warning.

#define CAN\_LIMIT\_ERROR 2U

Errors rate error.

• #define CAN\_BUS\_OFF\_ERROR 4U

Bus off condition reached.

• #define CAN\_FRAMING\_ERROR 8U

Framing error of some kind on the CAN bus.

• #define CAN\_OVERFLOW\_ERROR 16U

Overflow in receive queue.

#### **CAN** configuration options

• #define CAN\_USE\_SLEEP\_MODE TRUE

Sleep mode related APIs inclusion switch.

#### **Macro Functions**

#define CAN\_MAILBOX\_TO\_MASK(mbx) (1U << ((mbx) - 1U))</li>

Converts a mailbox index to a bit mask.

#### **Enumerations**

#### **Functions**

void canInit (void)

CAN Driver initialization.

void canObjectInit (CANDriver \*canp)

Initializes the standard part of a CANDriver structure.

void canStart (CANDriver \*canp, const CANConfig \*config)

Configures and activates the CAN peripheral.

void canStop (CANDriver \*canp)

Deactivates the CAN peripheral.

• bool canTryTransmitI (CANDriver \*canp, canmbx\_t mailbox, const CANTxFrame \*ctfp)

Can frame transmission attempt.

bool canTryReceivel (CANDriver \*canp, canmbx\_t mailbox, CANRxFrame \*crfp)

Can frame receive attempt.

- msg\_t canTransmit (CANDriver \*canp, canmbx\_t mailbox, const CANTxFrame \*ctfp, systime\_t timeout)
   Can frame transmission.
- msg\_t canReceive (CANDriver \*canp, canmbx\_t mailbox, CANRxFrame \*crfp, systime\_t timeout)
   Can frame receive.

### 9.6.1 Detailed Description

CAN Driver macros and structures.

# 9.7 can\_lld.c File Reference

PLATFORM CAN subsystem low level driver source.

#include "hal.h"

#### **Functions**

void can\_lld\_init (void)

Low level CAN driver initialization.

void can\_lld\_start (CANDriver \*canp)

Configures and activates the CAN peripheral.

void can\_lld\_stop (CANDriver \*canp)

Deactivates the CAN peripheral.

bool can\_lld\_is\_tx\_empty (CANDriver \*canp, canmbx\_t mailbox)

Determines whether a frame can be transmitted.

void can\_lld\_transmit (CANDriver \*canp, canmbx\_t mailbox, const CANTxFrame \*ctfp)

Inserts a frame into the transmit queue.

• bool can\_lld\_is\_rx\_nonempty (CANDriver \*canp, canmbx\_t mailbox)

Determines whether a frame has been received.

• void can\_lld\_receive (CANDriver \*canp, canmbx\_t mailbox, CANRxFrame \*crfp)

Receives a frame from the input queue.

void can\_lld\_sleep (CANDriver \*canp)

Enters the sleep mode.

void can\_lld\_wakeup (CANDriver \*canp)

Enforces leaving the sleep mode.

### **Variables**

CANDriver CAND1

CAN1 driver identifier.

# 9.7.1 Detailed Description

PLATFORM CAN subsystem low level driver source.

# 9.8 can\_lld.h File Reference

PLATFORM CAN subsystem low level driver header.

### **Data Structures**

struct CANTxFrame

CAN transmission frame.

struct CANRxFrame

CAN received frame.

struct CANConfig

Driver configuration structure.

struct CANDriver

Structure representing an CAN driver.

### **Macros**

#define CAN\_TX\_MAILBOXES 1

Number of transmit mailboxes.

#define CAN RX MAILBOXES 1

Number of receive mailboxes.

#### **PLATFORM** configuration options

• #define PLATFORM\_CAN\_USE\_CAN1 FALSE CAN1 driver enable switch.

# **Typedefs**

typedef uint32\_t canmbx\_t

Type of a transmission mailbox index.

### **Functions**

void can\_lld\_init (void)

Low level CAN driver initialization.

void can\_lld\_start (CANDriver \*canp)

Configures and activates the CAN peripheral.

void can\_lld\_stop (CANDriver \*canp)

Deactivates the CAN peripheral.

bool can\_lld\_is\_tx\_empty (CANDriver \*canp, canmbx\_t mailbox)

Determines whether a frame can be transmitted.

• void can\_lld\_transmit (CANDriver \*canp, canmbx\_t mailbox, const CANTxFrame \*ctfp)

Inserts a frame into the transmit queue.

• bool can\_lld\_is\_rx\_nonempty (CANDriver \*canp, canmbx\_t mailbox)

Determines whether a frame has been received.

void can\_Ild\_receive (CANDriver \*canp, canmbx\_t mailbox, CANRxFrame \*crfp)

Receives a frame from the input queue.

void can\_lld\_sleep (CANDriver \*canp)

Enters the sleep mode.

void can\_Ild\_wakeup (CANDriver \*canp)

Enforces leaving the sleep mode.

#### 9.8.1 Detailed Description

PLATFORM CAN subsystem low level driver header.

### 9.9 dac.c File Reference

#### DAC Driver code.

#include "hal.h"

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#### **Functions**

· void dacInit (void)

DAC Driver initialization.

void dacObjectInit (DACDriver \*dacp)

Initializes the standard part of a DACDriver structure.

void dacStart (DACDriver \*dacp, const DACConfig \*config)

Configures and activates the DAC peripheral.

void dacStop (DACDriver \*dacp)

Deactivates the DAC peripheral.

void dacPutChannelX (DACDriver \*dacp, dacchannel\_t channel, dacsample\_t sample)

Outputs a value directly on a DAC channel.

 void dacStartConversion (DACDriver \*dacp, const DACConversionGroup \*grpp, dacsample\_t \*samples, size\_t depth)

Starts a DAC conversion.

void dacStartConversionI (DACDriver \*dacp, const DACConversionGroup \*grpp, dacsample\_t \*samples, size\_t depth)

Starts a DAC conversion.

void dacStopConversion (DACDriver \*dacp)

Stops an ongoing conversion.

void dacStopConversionI (DACDriver \*dacp)

Stops an ongoing conversion.

msg\_t dacConvert (DACDriver \*dacp, const DACConversionGroup \*grpp, dacsample\_t \*samples, size\_t depth)

Performs a DAC conversion.

void dacAcquireBus (DACDriver \*dacp)

Gains exclusive access to the DAC bus.

void dacReleaseBus (DACDriver \*dacp)

Releases exclusive access to the DAC bus.

### 9.9.1 Detailed Description

DAC Driver code.

### 9.10 dac.h File Reference

DAC Driver macros and structures.

```
#include "dac_lld.h"
```

### **Macros**

#### **DAC** configuration options

#define DAC\_USE\_WAIT TRUE

Enables synchronous APIs.

• #define DAC\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the dacAcquireBus () and dacReleaseBus () APIs.

#### Low level driver helper macros

#define \_dac\_wait\_s(dacp) osalThreadSuspendS(&(dacp)->thread)

Waits for operation completion.

#define \_dac\_reset\_i(dacp) osalThreadResumel(&(dacp)->thread, MSG\_RESET)

Resumes a thread waiting for a conversion completion.

#define \_dac\_reset\_s(dacp) osalThreadResumeS(&(dacp)->thread, MSG\_RESET)

Resumes a thread waiting for a conversion completion.

#define \_dac\_wakeup\_isr(dacp)

Wakes up the waiting thread.

#define dac timeout isr(dacp)

Wakes up the waiting thread with a timeout message.

#define \_dac\_isr\_half\_code(dacp)

Common ISR code, half buffer event.

#define \_dac\_isr\_full\_code(dacp)

Common ISR code, full buffer event.

#define \_dac\_isr\_error\_code(dacp, err)

Common ISR code, error event.

#### **Enumerations**

#### **Functions**

· void dacInit (void)

DAC Driver initialization.

void dacObjectInit (DACDriver \*dacp)

Initializes the standard part of a DACDriver structure.

void dacStart (DACDriver \*dacp, const DACConfig \*config)

Configures and activates the DAC peripheral.

void dacStop (DACDriver \*dacp)

Deactivates the DAC peripheral.

void dacPutChannelX (DACDriver \*dacp, dacchannel t channel, dacsample t sample)

Outputs a value directly on a DAC channel.

void dacStartConversion (DACDriver \*dacp, const DACConversionGroup \*grpp, dacsample\_t \*samples, size\_t depth)

Starts a DAC conversion.

void dacStartConversionI (DACDriver \*dacp, const DACConversionGroup \*grpp, dacsample\_t \*samples, size\_t depth)

Starts a DAC conversion.

void dacStopConversion (DACDriver \*dacp)

Stops an ongoing conversion.

void dacStopConversionI (DACDriver \*dacp)

Stops an ongoing conversion.

#### 9.10.1 Detailed Description

DAC Driver macros and structures.

# 9.11 dac Ild.c File Reference

PLATFORM DAC subsystem low level driver source.

#include "hal.h"

#### **Functions**

void dac\_lld\_init (void)

Low level DAC driver initialization.

void dac\_lld\_start (DACDriver \*dacp)

Configures and activates the DAC peripheral.

void dac\_lld\_stop (DACDriver \*dacp)

Deactivates the DAC peripheral.

void dac\_lld\_put\_channel (DACDriver \*dacp, dacchannel\_t channel, dacsample\_t sample)

Outputs a value directly on a DAC channel.

void dac\_lld\_start\_conversion (DACDriver \*dacp)

Starts a DAC conversion.

void dac\_lld\_stop\_conversion (DACDriver \*dacp)

Stops an ongoing conversion.

#### **Variables**

DACDriver DACD1

DAC1 driver identifier.

### 9.11.1 Detailed Description

PLATFORM DAC subsystem low level driver source.

# 9.12 dac\_lld.h File Reference

PLATFORM DAC subsystem low level driver header.

### **Data Structures**

• struct DACConversionGroup

DAC Conversion group structure.

· struct DACConfig

Driver configuration structure.

struct DACDriver

Structure representing a DAC driver.

#### **Macros**

• #define DAC MAX CHANNELS 2

Maximum number of DAC channels per unit.

#### **Configuration options**

• #define PLATFORM DAC USE DAC1 FALSE

DAC1 CH1 driver enable switch.

# **Typedefs**

typedef uint32 t dacchannel t

Type of a DAC channel index.

typedef struct DACDriver DACDriver

Type of a structure representing an DAC driver.

typedef uint16\_t dacsample\_t

Type representing a DAC sample.

typedef void(\* daccallback\_t) (DACDriver \*dacp, dacsample\_t \*buffer, size\_t n)

DAC notification callback type.

typedef void(\* dacerrorcallback\_t) (DACDriver \*dacp, dacerror\_t err)

ADC error callback type.

#### **Enumerations**

#### **Functions**

void dac\_lld\_init (void)

Low level DAC driver initialization.

void dac\_lld\_start (DACDriver \*dacp)

Configures and activates the DAC peripheral.

void dac\_lld\_stop (DACDriver \*dacp)

Deactivates the DAC peripheral.

void dac\_lld\_put\_channel (DACDriver \*dacp, dacchannel\_t channel, dacsample\_t sample)

Outputs a value directly on a DAC channel.

void dac\_lld\_start\_conversion (DACDriver \*dacp)

Starts a DAC conversion.

void dac\_lld\_stop\_conversion (DACDriver \*dacp)

Stops an ongoing conversion.

### 9.12.1 Detailed Description

PLATFORM DAC subsystem low level driver header.

# 9.13 ext.c File Reference

#### EXT Driver code.

```
#include "hal.h"
```

### **Functions**

· void extInit (void)

EXT Driver initialization.

void extObjectInit (EXTDriver \*extp)

Initializes the standard part of a EXTDriver structure.

void extStart (EXTDriver \*extp, const EXTConfig \*config)

Configures and activates the EXT peripheral.

void extStop (EXTDriver \*extp)

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Deactivates the EXT peripheral.

void extChannelEnable (EXTDriver \*extp, expchannel\_t channel)

Enables an EXT channel.

void extChannelDisable (EXTDriver \*extp, expchannel\_t channel)

Disables an EXT channel.

void extSetChannelModel (EXTDriver \*extp, expchannel\_t channel, const EXTChannelConfig \*extcp)

Changes the operation mode of a channel.

### 9.13.1 Detailed Description

EXT Driver code.

### 9.14 ext.h File Reference

EXT Driver macros and structures.

```
#include "ext_lld.h"
```

#### **Macros**

#### **EXT channel modes**

• #define EXT\_CH\_MODE\_EDGES\_MASK 3U

Mask of edges field.

• #define EXT\_CH\_MODE\_DISABLED 0U

Channel disabled.

• #define EXT\_CH\_MODE\_RISING\_EDGE 1U

Rising edge callback.

• #define EXT\_CH\_MODE\_FALLING\_EDGE 2U

Falling edge callback.

• #define EXT\_CH\_MODE\_BOTH\_EDGES 3U

Both edges callback.

• #define EXT\_CH\_MODE\_AUTOSTART 4U

Channel started automatically on driver start.

### **Macro Functions**

• #define extChannelEnableI(extp, channel) ext\_lld\_channel\_enable(extp, channel)

Enables an EXT channel.

#define extChannelDisableI(extp, channel) ext\_lld\_channel\_disable(extp, channel)

Disables an EXT channel.

#define extSetChannelMode(extp, channel, extcp)

Changes the operation mode of a channel.

# **Typedefs**

typedef struct EXTDriver EXTDriver

Type of a structure representing a EXT driver.

#### **Enumerations**

#### **Functions**

void extInit (void)

EXT Driver initialization.

void extObjectInit (EXTDriver \*extp)

Initializes the standard part of a EXTDriver structure.

void extStart (EXTDriver \*extp, const EXTConfig \*config)

Configures and activates the EXT peripheral.

void extStop (EXTDriver \*extp)

Deactivates the EXT peripheral.

void extChannelEnable (EXTDriver \*extp, expchannel\_t channel)

Enables an EXT channel.

void extChannelDisable (EXTDriver \*extp, expchannel\_t channel)

Disables an EXT channel.

• void extSetChannelModel (EXTDriver \*extp, expchannel\_t channel, const EXTChannelConfig \*extcp)

Changes the operation mode of a channel.

### 9.14.1 Detailed Description

EXT Driver macros and structures.

# 9.15 ext Ild.c File Reference

PLATFORM EXT subsystem low level driver source.

```
#include "hal.h"
```

# **Functions**

void ext\_lld\_init (void)

Low level EXT driver initialization.

void ext\_lld\_start (EXTDriver \*extp)

Configures and activates the EXT peripheral.

void ext\_lld\_stop (EXTDriver \*extp)

Deactivates the EXT peripheral.

• void ext\_lld\_channel\_enable (EXTDriver \*extp, expchannel\_t channel)

Enables an EXT channel.

• void ext\_lld\_channel\_disable (EXTDriver \*extp, expchannel\_t channel)

Disables an EXT channel.

#### **Variables**

EXTDriver EXTD1

EXT1 driver identifier.

#### 9.15.1 Detailed Description

PLATFORM EXT subsystem low level driver source.

# 9.16 ext\_lld.h File Reference

PLATFORM EXT subsystem low level driver header.

#### **Data Structures**

· struct EXTChannelConfig

Channel configuration structure.

struct EXTConfig

Driver configuration structure.

struct EXTDriver

Structure representing an EXT driver.

#### **Macros**

#define EXT\_MAX\_CHANNELS 20

Available number of EXT channels.

# **PLATFORM** configuration options

 #define PLATFORM\_EXT\_USE\_EXT1 FALSE EXT driver enable switch.

# **Typedefs**

typedef uint32\_t expchannel\_t

EXT channel identifier.

typedef void(\* extcallback\_t) (EXTDriver \*extp, expchannel\_t channel)

Type of an EXT generic notification callback.

### **Functions**

void ext\_lld\_init (void)

Low level EXT driver initialization.

void ext\_lld\_start (EXTDriver \*extp)

Configures and activates the EXT peripheral.

void ext\_lld\_stop (EXTDriver \*extp)

Deactivates the EXT peripheral.

void ext\_lld\_channel\_enable (EXTDriver \*extp, expchannel\_t channel)

Enables an EXT channel.

• void ext\_lld\_channel\_disable (EXTDriver \*extp, expchannel\_t channel)

Disables an EXT channel.

### 9.16.1 Detailed Description

PLATFORM EXT subsystem low level driver header.

# 9.17 gpt.c File Reference

### GPT Driver code.

```
#include "hal.h"
```

#### **Functions**

void gptInit (void)

GPT Driver initialization.

void gptObjectInit (GPTDriver \*gptp)

Initializes the standard part of a GPTDriver structure.

void gptStart (GPTDriver \*gptp, const GPTConfig \*config)

Configures and activates the GPT peripheral.

void gptStop (GPTDriver \*gptp)

Deactivates the GPT peripheral.

void gptChangeInterval (GPTDriver \*gptp, gptcnt\_t interval)

Changes the interval of GPT peripheral.

void gptStartContinuous (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in continuous mode.

void gptStartContinuousI (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in continuous mode.

void gptStartOneShot (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in one shot mode.

void gptStartOneShotI (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in one shot mode.

void gptStopTimer (GPTDriver \*gptp)

Stops the timer.

void gptStopTimerI (GPTDriver \*gptp)

Stops the timer.

void gptPolledDelay (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in one shot mode and waits for completion.

### 9.17.1 Detailed Description

GPT Driver code.

# 9.18 gpt.h File Reference

GPT Driver macros and structures.

```
#include "gpt_lld.h"
```

### **Macros**

• #define gptChangeIntervall(gptp, interval)

Changes the interval of GPT peripheral.

• #define gptGetIntervalX(gptp) gpt\_lld\_get\_interval(gptp)

Returns the interval of GPT peripheral.

#define gptGetCounterX(gptp) gpt\_lld\_get\_counter(gptp)

Returns the counter value of GPT peripheral.

### **Typedefs**

· typedef struct GPTDriver GPTDriver

Type of a structure representing a GPT driver.

typedef void(\* gptcallback\_t) (GPTDriver \*gptp)

GPT notification callback type.

#### **Enumerations**

#### **Functions**

void gptInit (void)

GPT Driver initialization.

void gptObjectInit (GPTDriver \*gptp)

Initializes the standard part of a GPTDriver structure.

void gptStart (GPTDriver \*gptp, const GPTConfig \*config)

Configures and activates the GPT peripheral.

void gptStop (GPTDriver \*gptp)

Deactivates the GPT peripheral.

• void gptStartContinuous (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in continuous mode.

void gptStartContinuousI (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in continuous mode.

void gptChangeInterval (GPTDriver \*gptp, gptcnt\_t interval)

Changes the interval of GPT peripheral.

void gptStartOneShot (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in one shot mode.

void gptStartOneShotI (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in one shot mode.

void gptStopTimer (GPTDriver \*gptp)

Stops the timer.

void gptStopTimerI (GPTDriver \*gptp)

Stops the timer.

void gptPolledDelay (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in one shot mode and waits for completion.

### 9.18.1 Detailed Description

GPT Driver macros and structures.

# 9.19 gpt\_lld.c File Reference

PLATFORM GPT subsystem low level driver source.

```
#include "hal.h"
```

#### **Functions**

void gpt\_lld\_init (void)

Low level GPT driver initialization.

void gpt\_lld\_start (GPTDriver \*gptp)

Configures and activates the GPT peripheral.

void gpt\_lld\_stop (GPTDriver \*gptp)

Deactivates the GPT peripheral.

void gpt\_lld\_start\_timer (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in continuous mode.

void gpt\_lld\_stop\_timer (GPTDriver \*gptp)

Stops the timer.

void gpt\_lld\_polled\_delay (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in one shot mode and waits for completion.

#### **Variables**

GPTDriver GPTD1

GPTD1 driver identifier.

# 9.19.1 Detailed Description

PLATFORM GPT subsystem low level driver source.

# 9.20 gpt\_lld.h File Reference

PLATFORM GPT subsystem low level driver header.

### **Data Structures**

struct GPTConfig

Driver configuration structure.

struct GPTDriver

Structure representing a GPT driver.

#### **Macros**

#define gpt\_lld\_change\_interval(gptp, interval)
 Changes the interval of GPT peripheral.

# **PLATFORM** configuration options

• #define PLATFORM\_GPT\_USE\_GPT1 FALSE GPTD1 driver enable switch.

### **Typedefs**

• typedef uint32\_t gptfreq\_t

GPT frequency type.

• typedef uint16\_t gptcnt\_t

GPT counter type.

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### **Functions**

void gpt\_lld\_init (void)

Low level GPT driver initialization.

void gpt\_lld\_start (GPTDriver \*gptp)

Configures and activates the GPT peripheral.

void gpt\_lld\_stop (GPTDriver \*gptp)

Deactivates the GPT peripheral.

void gpt\_lld\_start\_timer (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in continuous mode.

void gpt\_lld\_stop\_timer (GPTDriver \*gptp)

Stops the timer.

void gpt\_lld\_polled\_delay (GPTDriver \*gptp, gptcnt\_t interval)

Starts the timer in one shot mode and waits for completion.

# 9.20.1 Detailed Description

PLATFORM GPT subsystem low level driver header.

# 9.21 hal.c File Reference

HAL subsystem code.

#include "hal.h"

### **Functions**

· void hallnit (void)

HAL initialization.

# 9.21.1 Detailed Description

HAL subsystem code.

# 9.22 hal.h File Reference

HAL subsystem header.

```
#include "osal.h"
#include "board.h"
#include "halconf.h"
#include "hal_lld.h"
#include "hal_streams.h"
#include "hal_channels.h"
#include "hal_files.h"
#include "hal_ioblock.h"
#include "hal_mmcsd.h"
#include "hal_buffers.h"
#include "hal_queues.h"
#include "pal.h"
#include "adc.h"
#include "can.h"
#include "dac.h"
#include "ext.h"
#include "gpt.h"
#include "i2c.h"
#include "i2s.h"
#include "icu.h"
#include "mac.h"
#include "mii.h"
#include "pwm.h"
#include "rtc.h"
#include "serial.h"
#include "sdc.h"
#include "spi.h"
#include "uart.h"
#include "usb.h"
#include "wdg.h"
#include "mmc_spi.h"
#include "serial_usb.h"
#include "hal_community.h"
```

#### **Macros**

• #define \_CHIBIOS\_HAL\_

ChibiOS/HAL identification macro.

#define CH\_HAL\_STABLE 1

Stable release flag.

#### ChibiOS/HAL version identification

• #define HAL VERSION "4.0.9"

HAL version string.

• #define CH\_HAL\_MAJOR 4

HAL version major number.

#define CH\_HAL\_MINOR 0

HAL version minor number.

• #define CH\_HAL\_PATCH 9

HAL version patch number.

#### Return codes

- #define HAL\_SUCCESS false
- #define HAL\_FAILED true

#### **Functions**

void hallnit (void)
 HAL initialization.

### 9.22.1 Detailed Description

HAL subsystem header.

# 9.23 hal\_buffers.c File Reference

```
I/O Buffers code.
```

```
#include <string.h>
#include "hal.h"
```

#### **Functions**

- void ibqObjectInit (input\_buffers\_queue\_t \*ibqp, uint8\_t \*bp, size\_t size, size\_t n, bqnotify\_t infy, void \*link)
   Initializes an input buffers queue object.
- void ibqResetl (input\_buffers\_queue\_t \*ibqp)

Resets an input buffers queue.

uint8\_t \* ibqGetEmptyBufferI (input\_buffers\_queue\_t \*ibqp)

Gets the next empty buffer from the queue.

void ibqPostFullBufferI (input\_buffers\_queue\_t \*ibqp, size\_t size)

Posts a new filled buffer to the queue.

• msg\_t ibqGetFullBufferTimeout (input\_buffers\_queue\_t \*ibqp, systime\_t timeout)

Gets the next filled buffer from the queue.

msg\_t ibqGetFullBufferTimeoutS (input\_buffers\_queue\_t \*ibqp, systime\_t timeout)

Gets the next filled buffer from the queue.

void ibqReleaseEmptyBuffer (input\_buffers\_queue\_t \*ibqp)

Releases the buffer back in the queue.

void ibqReleaseEmptyBufferS (input\_buffers\_queue\_t \*ibqp)

Releases the buffer back in the queue.

msg\_t ibqGetTimeout (input\_buffers\_queue\_t \*ibqp, systime\_t timeout)

Input queue read with timeout.

• size\_t ibqReadTimeout (input\_buffers\_queue\_t \*ibqp, uint8\_t \*bp, size\_t n, systime\_t timeout)

Input queue read with timeout.

 void obqObjectInit (output\_buffers\_queue\_t \*obqp, uint8\_t \*bp, size\_t size, size\_t n, bqnotify\_t onfy, void \*link)

Initializes an output buffers queue object.

void obqResetI (output\_buffers\_queue\_t \*obqp)

Resets an output buffers queue.

• uint8\_t \* obqGetFullBufferI (output\_buffers\_queue\_t \*obqp, size\_t \*sizep)

Gets the next filled buffer from the queue.

void obqReleaseEmptyBufferI (output\_buffers\_queue\_t \*obqp)

Releases the next filled buffer back in the queue.

msg\_t obgGetEmptyBufferTimeout (output\_buffers\_queue\_t \*obgp, systime\_t timeout)

Gets the next empty buffer from the queue.

• msg\_t obqGetEmptyBufferTimeoutS (output\_buffers\_queue\_t \*obqp, systime\_t timeout)

Gets the next empty buffer from the queue.

void obqPostFullBuffer (output\_buffers\_queue\_t \*obqp, size\_t size)

Posts a new filled buffer to the queue.

void obqPostFullBufferS (output\_buffers\_queue\_t \*obqp, size\_t size)

Posts a new filled buffer to the queue.

• msg\_t obqPutTimeout (output\_buffers\_queue\_t \*obqp, uint8\_t b, systime\_t timeout)

Output queue write with timeout.

• size t obgWriteTimeout (output buffers queue t \*obgp, const uint8 t \*bp, size t n, systime t timeout)

Output queue write with timeout.

• bool obqTryFlushI (output\_buffers\_queue\_t \*obqp)

Flushes the current, partially filled, buffer to the queue.

void obqFlush (output\_buffers\_queue\_t \*obqp)

Flushes the current, partially filled, buffer to the queue.

### 9.23.1 Detailed Description

I/O Buffers code.

# 9.24 hal buffers.h File Reference

I/O Buffers macros and structures.

#### **Data Structures**

• struct io\_buffers\_queue

Structure of a generic buffers queue.

#### Macros

• #define BQ\_BUFFER\_SIZE(n, size) (((size\_t)(size) + sizeof (size\_t)) \* (size\_t)(n))

Computes the size of a buffers queue buffer size.

#### **Macro Functions**

#define bqSizeX(bqp) ((bqp)->bn)

Returns the queue's number of buffers.

• #define bqSpaceI(bqp) ((bqp)->bcounter)

Return the ready buffers number.

#define bqGetLinkX(bqp) ((bqp)->link)

Returns the queue application-defined link.

• #define ibqlsEmptyl(ibqp) ((bool)(bqSpacel(ibqp) == 0U))

Evaluates to TRUE if the specified input buffers queue is empty.

• #define ibqlsFullI(ibqp)

Evaluates to TRUE if the specified input buffers queue is full.

#define obqlsEmptyl(obqp)

Evaluates to true if the specified output buffers queue is empty.

#define obglsFullI(obgp) ((bool)(bgSpaceI(obgp) == 0U))

Evaluates to true if the specified output buffers queue is full.

### **Typedefs**

• typedef struct io buffers queue io buffers queue t

Type of a generic queue of buffers.

typedef void(\* bqnotify\_t) (io\_buffers\_queue\_t \*bqp)

Double buffer notification callback type.

• typedef io\_buffers\_queue\_t input\_buffers\_queue\_t

Type of an input buffers queue.

typedef io\_buffers\_queue\_t output\_buffers\_queue\_t

Type of an output buffers queue.

## **Functions**

• void ibqObjectInit (input\_buffers\_queue\_t \*ibqp, uint8\_t \*bp, size\_t size, size\_t n, bqnotify\_t infy, void \*link)

Initializes an input buffers queue object.

void ibqResetl (input\_buffers\_queue\_t \*ibqp)

Resets an input buffers queue.

uint8\_t \* ibqGetEmptyBufferI (input\_buffers\_queue\_t \*ibqp)

Gets the next empty buffer from the queue.

void ibqPostFullBufferI (input\_buffers\_queue\_t \*ibqp, size\_t size)

Posts a new filled buffer to the queue.

msg\_t ibqGetFullBufferTimeout (input\_buffers\_queue\_t \*ibqp, systime\_t timeout)

Gets the next filled buffer from the queue.

msg\_t ibqGetFullBufferTimeoutS (input\_buffers\_queue\_t \*ibqp, systime\_t timeout)

Gets the next filled buffer from the queue.

void ibqReleaseEmptyBuffer (input\_buffers\_queue\_t \*ibqp)

Releases the buffer back in the queue.

void ibqReleaseEmptyBufferS (input\_buffers\_queue\_t \*ibqp)

Releases the buffer back in the queue.

msg\_t ibqGetTimeout (input\_buffers\_queue\_t \*ibqp, systime\_t timeout)

Input queue read with timeout.

size\_t ibqReadTimeout (input\_buffers\_queue\_t \*ibqp, uint8\_t \*bp, size\_t n, systime\_t timeout)

Input queue read with timeout.

void obqObjectInit (output\_buffers\_queue\_t \*obqp, uint8\_t \*bp, size\_t size, size\_t n, bqnotify\_t onfy, void \*link)

Initializes an output buffers queue object.

void obqResetl (output\_buffers\_queue\_t \*obqp)

Resets an output buffers queue.

uint8\_t \* obqGetFullBufferI (output\_buffers\_queue\_t \*obqp, size\_t \*sizep)

Gets the next filled buffer from the queue.

void obgReleaseEmptyBufferI (output buffers queue t \*obgp)

Releases the next filled buffer back in the queue.

msg\_t obqGetEmptyBufferTimeout (output\_buffers\_queue\_t \*obqp, systime\_t timeout)

Gets the next empty buffer from the queue.

msg\_t obqGetEmptyBufferTimeoutS (output\_buffers\_queue\_t \*obqp, systime\_t timeout)

Gets the next empty buffer from the queue.

• void obqPostFullBuffer (output\_buffers\_queue\_t \*obqp, size\_t size)

Posts a new filled buffer to the queue.

void obgPostFullBufferS (output buffers queue t \*obgp, size t size)

Posts a new filled buffer to the queue.

• msg\_t obqPutTimeout (output\_buffers\_queue\_t \*obqp, uint8\_t b, systime\_t timeout)

Output queue write with timeout.

• size\_t obqWriteTimeout (output\_buffers\_queue\_t \*obqp, const uint8\_t \*bp, size\_t n, systime\_t timeout)

Output queue write with timeout.

bool obqTryFlushI (output\_buffers\_queue\_t \*obqp)

Flushes the current, partially filled, buffer to the queue.

void obqFlush (output\_buffers\_queue\_t \*obqp)

Flushes the current, partially filled, buffer to the queue.

### 9.24.1 Detailed Description

I/O Buffers macros and structures.

# 9.25 hal\_channels.h File Reference

I/O channels access.

### **Data Structures**

struct BaseChannelVMT

BaseChannel virtual methods table.

struct BaseChannel

Base channel class.

struct BaseAsynchronousChannelVMT

BaseAsynchronousChannel virtual methods table.

struct BaseAsynchronousChannel

Base asynchronous channel class.

### **Macros**

• #define base channel methods

BaseChannel specific methods.

• #define \_base\_channel\_data \_base\_sequential\_stream\_data

BaseChannel specific data.

#define \_base\_asynchronous\_channel\_methods \_base\_channel\_methods \

BaseAsynchronousChannel specific methods.

• #define \_base\_asynchronous\_channel\_data

BaseAsynchronousChannel specific data.

## Macro Functions (BaseChannel)

• #define chnPutTimeout(ip, b, time) ((ip)->vmt->putt(ip, b, time))

Channel blocking byte write with timeout.

#define chnGetTimeout(ip, time) ((ip)->vmt->gett(ip, time))

Channel blocking byte read with timeout.

• #define chnWrite(ip, bp, n) streamWrite(ip, bp, n)

Channel blocking write.

• #define chnWriteTimeout(ip, bp, n, time) ((ip)->vmt->writet(ip, bp, n, time))

Channel blocking write with timeout.

• #define chnRead(ip, bp, n) streamRead(ip, bp, n)

Channel blocking read.

#define chnReadTimeout(ip, bp, n, time) ((ip)->vmt->readt(ip, bp, n, time))

Channel blocking read with timeout.

## I/O status flags added to the event listener

• #define CHN\_NO\_ERROR (eventflags\_t)0

No pending conditions.

#define CHN CONNECTED (eventflags t)1

Connection happened.

#define CHN\_DISCONNECTED (eventflags\_t)2

Disconnection happened.

#define CHN INPUT AVAILABLE (eventflags t)4

Data available in the input queue.

• #define CHN\_OUTPUT\_EMPTY (eventflags\_t)8

Output queue empty.

• #define CHN\_TRANSMISSION\_END (eventflags\_t)16

Transmission end.

### Macro Functions (BaseAsynchronousChannel)

#define chnGetEventSource(ip) (&((ip)->event))

Returns the I/O condition event source.

• #define chnAddFlagsI(ip, flags)

Adds status flags to the listeners's flags mask.

## 9.25.1 Detailed Description

I/O channels access.

This header defines an abstract interface useful to access generic I/O serial devices in a standardized way.

# 9.26 hal\_files.h File Reference

Data files.

### **Data Structures**

struct FileStreamVMT

FileStream virtual methods table.

struct FileStream

Base file stream class.

### **Macros**

• #define \_file\_stream\_methods

FileStream specific methods.

• #define \_file\_stream\_data \_base\_sequential\_stream\_data

FileStream specific data.

#### Files return codes

• #define FILE OK STM OK

No error return code.

#define FILE\_ERROR STM\_TIMEOUT

Error code from the file stream methods.

• #define FILE EOF STM RESET

End-of-file condition for file get/put methods.

### Macro Functions (FileStream)

• #define fileStreamWrite(ip, bp, n) streamWrite(ip, bp, n)

File stream write.

• #define fileStreamRead(ip, bp, n) streamRead(ip, bp, n)

File stream read.

#define fileStreamPut(ip, b) streamPut(ip, b)

File stream blocking byte write.

#define fileStreamGet(ip) streamGet(ip)

File stream blocking byte read.

#define fileStreamClose(ip) ((ip)->vmt->close(ip))

File Stream close.

• #define fileStreamGetError(ip) ((ip)->vmt->geterror(ip))

Returns an implementation dependent error code.

#define fileStreamGetSize(ip) ((ip)->vmt->getsize(ip))

Returns the current file size.

#define fileStreamGetPosition(ip) ((ip)->vmt->getposition(ip))

Returns the current file pointer position.

• #define fileStreamSeek(ip, offset) ((ip)->vmt->lseek(ip, offset))

Moves the file current pointer to an absolute position.

## **Typedefs**

· typedef uint32\_t fileoffset\_t

File offset type.

## 9.26.1 Detailed Description

Data files.

This header defines abstract interfaces useful to access generic data files in a standardized way.

# 9.27 hal\_ioblock.h File Reference

I/O block devices access.

## **Data Structures**

· struct BlockDeviceInfo

Block device info.

struct BaseBlockDeviceVMT

BaseBlockDevice virtual methods table.

• struct BaseBlockDevice

Base block device class.

### **Macros**

• #define \_base\_block\_device\_methods

BaseBlockDevice specific methods.

#define \_base\_block\_device\_data

BaseBlockDevice specific data.

#### Macro Functions (BaseBlockDevice)

• #define blkGetDriverState(ip) ((ip)->state)

Returns the driver state.

#define blklsTransferring(ip)

Determines if the device is transferring data.

#define blklsInserted(ip) ((ip)->vmt->is\_inserted(ip))

Returns the media insertion status.

#define blklsWriteProtected(ip) ((ip)->vmt->is\_protected(ip))

Returns the media write protection status.

#define blkConnect(ip) ((ip)->vmt->connect(ip))

Performs the initialization procedure on the block device.

#define blkDisconnect(ip) ((ip)->vmt->disconnect(ip))

Terminates operations on the block device.

#define blkRead(ip, startblk, buf, n) ((ip)->vmt->read(ip, startblk, buf, n))

Reads one or more blocks.

• #define blkWrite(ip, startblk, buf, n) ((ip)->vmt->write(ip, startblk, buf, n))

Writes one or more blocks.

#define blkSync(ip) ((ip)->vmt->sync(ip))

Ensures write synchronization.

#define blkGetInfo(ip, bdip) ((ip)->vmt->get\_info(ip, bdip))

Returns a media information structure.

## **Enumerations**

### 9.27.1 Detailed Description

I/O block devices access.

This header defines an abstract interface useful to access generic I/O block devices in a standardized way.

## 9.28 hal\_lld.c File Reference

PLATFORM HAL subsystem low level driver source.

```
#include "hal.h"
```

#### **Functions**

void hal\_lld\_init (void)

Low level HAL driver initialization.

## 9.28.1 Detailed Description

PLATFORM HAL subsystem low level driver source.

# 9.29 hal Ild.h File Reference

PLATFORM HAL subsystem low level driver header.

### **Macros**

#### Platform identification macros

• #define PLATFORM\_NAME "templates"

### **Functions**

void hal\_lld\_init (void)
 Low level HAL driver initialization.

## 9.29.1 Detailed Description

PLATFORM HAL subsystem low level driver header.

## 9.30 hal mmcsd.c File Reference

MMC/SD cards common code.

```
#include "hal.h"
```

## **Functions**

- uint32\_t \_mmcsd\_get\_slice (const uint32\_t \*data, uint32\_t end, uint32\_t start)
   Gets a bit field from a words array.
- uint32\_t \_mmcsd\_get\_capacity (const uint32\_t \*csd)

Extract card capacity from a CSD.

- uint32\_t \_mmcsd\_get\_capacity\_ext (const uint8\_t \*ext\_csd)
  - Extract MMC card capacity from EXT\_CSD.
- void \_mmcsd\_unpack\_mmc\_cid (const MMCSDBlockDevice \*sdcp, unpacked\_mmc\_cid\_t \*cidmmc)

  Unpacks MMC CID array in structure.
- void \_mmcsd\_unpack\_csd\_mmc (const MMCSDBlockDevice \*sdcp, unpacked\_mmc\_csd\_t \*csdmmc)
   Unpacks MMC CSD array in structure.
- void \_mmcsd\_unpack\_csd\_v10 (const MMCSDBlockDevice \*sdcp, unpacked\_sdc\_csd\_10\_t \*csd10) Unpacks SDC CSD v1.0 array in structure.
- void \_mmcsd\_unpack\_csd\_v20 (const MMCSDBlockDevice \*sdcp, unpacked\_sdc\_csd\_20\_t \*csd20) Unpacks SDC CSD v2.0 array in structure.

## 9.30.1 Detailed Description

MMC/SD cards common code.

# 9.31 hal\_mmcsd.h File Reference

MMC/SD cards common header.

### **Data Structures**

struct MMCSDBlockDeviceVMT

MMCSDBlockDevice virtual methods table.

struct MMCSDBlockDevice

MCC/SD block device class.

· struct unpacked sdc cid t

Unpacked CID register from SDC.

• struct unpacked\_mmc\_cid\_t

Unpacked CID register from MMC.

struct unpacked sdc csd 10 t

Unpacked CSD v1.0 register from SDC.

struct unpacked sdc csd 20 t

Unpacked CSD v2.0 register from SDC.

· struct unpacked\_mmc\_csd\_t

Unpacked CSD register from MMC.

#### **Macros**

• #define MMCSD BLOCK SIZE 512U

Fixed block size for MMC/SD block devices.

#define MMCSD\_R1\_ERROR\_MASK 0xFDFFE008U

Mask of error bits in R1 responses.

• #define MMCSD\_CMD8\_PATTERN 0x000001AAU

Fixed pattern for CMD8.

• #define \_mmcsd\_block\_device\_methods \_base\_block\_device\_methods

MMCSDBlockDevice specific methods.

• #define \_mmcsd\_block\_device\_data

MMCSDBlockDevice specific data.

## SD/MMC status conditions

- #define MMCSD STS IDLE 0U
- #define MMCSD\_STS\_READY 1U
- #define MMCSD STS IDENT 2U
- #define MMCSD\_STS\_STBY 3U
- #define MMCSD\_STS\_TRAN 4U
- #define MMCSD\_STS\_DATA 5U
   #define MMCSD\_STS\_RCV 6U
- #define MMCSD\_STS\_RCV 00
   #define MMCSD\_STS\_PRG 7U
- #define MMCSD STS DIS 8U

#### SD/MMC commands

- #define MMCSD\_CMD\_GO\_IDLE\_STATE 0U
- #define MMCSD\_CMD\_INIT 1U
- #define MMCSD\_CMD\_ALL\_SEND\_CID 2U
- #define MMCSD\_CMD\_SEND\_RELATIVE\_ADDR 3U
- #define MMCSD\_CMD\_SET\_BUS\_WIDTH 6U
- #define MMCSD\_CMD\_SWITCH MMCSD\_CMD\_SET\_BUS\_WIDTH

- #define MMCSD\_CMD\_SEL\_DESEL\_CARD 7U
- #define MMCSD CMD SEND IF COND 8U
- #define MMCSD CMD SEND EXT CSD MMCSD CMD SEND IF COND
- #define MMCSD CMD SEND CSD 9U
- #define MMCSD CMD SEND CID 10U
- #define MMCSD CMD STOP TRANSMISSION 12U
- #define MMCSD CMD SEND STATUS 13U
- #define MMCSD\_CMD\_SET\_BLOCKLEN 16U
- #define MMCSD\_CMD\_READ\_SINGLE\_BLOCK 17U
- #define MMCSD\_CMD\_READ\_MULTIPLE\_BLOCK 18U
- #define MMCSD\_CMD\_SET\_BLOCK\_COUNT 23U
- #define MMCSD\_CMD\_WRITE\_BLOCK 24U
- #define MMCSD\_CMD\_WRITE\_MULTIPLE\_BLOCK 25U
- #define MMCSD CMD ERASE RW BLK START 32U
- #define MMCSD CMD ERASE RW BLK END 33U
- #define MMCSD\_CMD\_ERASE 38U
- #define MMCSD\_CMD\_APP\_OP\_COND 41U
- #define MMCSD CMD LOCK UNLOCK 42U
- #define MMCSD\_CMD\_APP\_CMD 55U
- #define MMCSD CMD READ OCR 58U

### **CSD** record offsets

- #define MMCSD\_CSD\_MMC\_CSD\_STRUCTURE\_SLICE 127U,126U
  - Slice position of values in CSD register.
- #define MMCSD\_CSD\_MMC\_SPEC\_VERS\_SLICE 125U,122U
- #define MMCSD CSD MMC TAAC SLICE 119U,112U
- #define MMCSD\_CSD\_MMC\_NSAC\_SLICE 111U,104U
- #define MMCSD\_CSD\_MMC\_TRAN\_SPEED\_SLICE 103U,96U
- #define MMCSD\_CSD\_MMC\_CCC\_SLICE 95U,84U
- #define MMCSD\_CSD\_MMC\_READ\_BL\_LEN\_SLICE 83U,80U
   #define MMCSD\_CSD\_MMC\_READ\_BL\_PARTIAL\_SLICE 79U,79U
- #define MMCSD\_CSD\_MMC\_WRITE\_BLK\_MISALIGN\_SLICE 78U,78U
- #define MMCSD CSD MMC READ BLK MISALIGN SLICE 77U,77U
- #define MMCSD\_CSD\_MMC\_DSR\_IMP\_SLICE 76U,76U
- #define MMCSD\_CSD\_MMC\_C\_SIZE\_SLICE 73U,62U
- #define MMCSD\_CSD\_MMC\_VDD\_R\_CURR\_MIN\_SLICE 61U,59U
- #define MMCSD CSD MMC VDD R CURR MAX SLICE 58U,56U
- #define MMCSD CSD MMC VDD W CURR MIN SLICE 55U,53U
- #define MMCSD CSD MMC VDD W CURR MAX SLICE 52U,50U
- #define MMCSD\_CSD\_MMC\_C\_SIZE\_MULT\_SLICE 49U,47U
- #define MMCSD\_CSD\_MMC\_ERASE\_GRP\_SIZE\_SLICE 46U,42U
- #define MMCSD\_CSD\_MMC\_ERASE\_GRP\_MULT\_SLICE 41U,37U
- #define MMCSD\_CSD\_MMC\_WP\_GRP\_SIZE\_SLICE 36U,32U
- #define MMCSD\_CSD\_MMC\_WP\_GRP\_ENABLE\_SLICE 31U,31U
- #define MMCSD\_CSD\_MMC\_DEFAULT\_ECC\_SLICE 30U,29U #define MMCSD\_CSD\_MMC\_R2W\_FACTOR\_SLICE 28U,26U
- #define MMCSD\_CSD\_MMC\_WRITE\_BL\_LEN\_SLICE 25U,22U #define MMCSD CSD MMC WRITE BL PARTIAL SLICE 21U,21U
- #define MMCSD\_CSD\_MMC\_CONTENT\_PROT\_APP\_SLICE 16U,16U
- #define MMCSD\_CSD\_MMC\_FILE\_FORMAT\_GRP\_SLICE 15U,15U
- #define MMCSD CSD MMC COPY SLICE 14U,14U
- #define MMCSD CSD MMC PERM WRITE PROTECT SLICE 13U,13U
- #define MMCSD CSD MMC TMP WRITE PROTECT SLICE 12U,12U
- #define MMCSD CSD MMC FILE FORMAT SLICE 11U,10U
- #define MMCSD\_CSD\_MMC\_ECC\_SLICE 9U,8U
- #define MMCSD\_CSD\_MMC\_CRC\_SLICE 7U,1U
- #define MMCSD\_CSD\_20\_CRC\_SLICE 7U,1U
- #define MMCSD\_CSD\_20\_FILE\_FORMAT\_SLICE 11U,10U
- #define MMCSD\_CSD\_20\_TMP\_WRITE\_PROTECT\_SLICE 12U,12U
- #define MMCSD CSD 20 PERM WRITE PROTECT SLICE 13U,13U
- #define MMCSD CSD 20 COPY SLICE 14U,14U
- #define MMCSD\_CSD\_20\_FILE\_FORMAT\_GRP\_SLICE 15U,15U
- #define MMCSD CSD 20 WRITE BL PARTIAL SLICE 21U,21U

- #define MMCSD\_CSD\_20\_WRITE\_BL\_LEN\_SLICE 25U,12U
- #define MMCSD CSD 20 R2W FACTOR SLICE 28U,26U
- #define MMCSD\_CSD\_20\_WP\_GRP\_ENABLE\_SLICE 31U,31U
- #define MMCSD\_CSD\_20\_WP\_GRP\_SIZE\_SLICE 38U,32U
- #define MMCSD CSD 20 ERASE SECTOR SIZE SLICE 45U,39U
- #define MMCSD CSD 20 ERASE BLK EN SLICE 46U.46U
- #define MMCSD CSD 20 C SIZE SLICE 69U,48U
- #define MMCSD CSD 20 DSR IMP SLICE 76U,76U
- #define MMCSD\_CSD\_20\_READ\_BLK\_MISALIGN\_SLICE 77U,77U
- #define MMCSD\_CSD\_20\_WRITE\_BLK\_MISALIGN\_SLICE 78U,78U
- #define MMCSD\_CSD\_20\_READ\_BL\_PARTIAL\_SLICE 79U,79U
- #define MMCSD\_CSD\_20\_READ\_BL\_LEN\_SLICE 83U,80U
- #define MMCSD\_CSD\_20\_CCC\_SLICE 95U,84U
- #define MMCSD\_CSD\_20\_TRANS\_SPEED\_SLICE 103U,96U
- #define MMCSD\_CSD\_20\_NSAC\_SLICE 111U,104U
- #define MMCSD CSD 20 TAAC SLICE 119U,112U
- #define MMCSD\_CSD\_20\_CSD\_STRUCTURE\_SLICE 127U,126U
- #define MMCSD\_CSD\_10\_CRC\_SLICE MMCSD\_CSD\_20\_CRC\_SLICE
- #define MMCSD\_CSD\_10\_FILE\_FORMAT\_SLICE MMCSD\_CSD\_20\_FILE\_FORMAT\_SLICE
- #define MMCSD\_CSD\_10\_TMP\_WRITE\_PROTECT\_SLICE MMCSD\_CSD\_20\_TMP\_WRITE\_PROT

   ECT SLICE
- #define MMCSD\_CSD\_10\_COPY\_SLICE MMCSD\_CSD\_20\_COPY\_SLICE
- #define MMCSD\_CSD\_10\_FILE\_FORMAT\_GRP\_SLICE MMCSD\_CSD\_20\_FILE\_FORMAT\_GRP\_S

   LICE
- #define **MMCSD\_CSD\_10\_WRITE\_BL\_PARTIAL\_SLICE** MMCSD\_CSD\_20\_WRITE\_BL\_PARTIAL\_S↔ LICE
- #define MMCSD CSD 10 WRITE BL LEN SLICE MMCSD CSD 20 WRITE BL LEN SLICE
- #define MMCSD\_CSD\_10\_R2W\_FACTOR\_SLICE MMCSD\_CSD\_20\_R2W\_FACTOR\_SLICE
- #define MMCSD\_CSD\_10\_WP\_GRP\_ENABLE\_SLICE MMCSD\_CSD\_20\_WP\_GRP\_ENABLE\_SLICE
- #define MMCSD\_CSD\_10\_WP\_GRP\_SIZE\_SLICE MMCSD\_CSD\_20\_WP\_GRP\_SIZE\_SLICE
- #define MMCSD\_CSD\_10\_ERASE\_SECTOR\_SIZE\_SLICE MMCSD\_CSD\_20\_ERASE\_SECTOR\_SI
   ZE SLICE
- #define MMCSD\_CSD\_10\_ERASE\_BLK\_EN\_SLICE MMCSD\_CSD\_20\_ERASE\_BLK\_EN\_SLICE
- #define MMCSD\_CSD\_10\_C\_SIZE\_MULT\_SLICE 49U,47U
- #define MMCSD CSD 10 VDD W CURR MAX SLICE 52U,50U
- #define MMCSD CSD 10 VDD W CURR MIN SLICE 55U,53U
- #define MMCSD\_CSD\_10\_VDD\_R\_CURR\_MAX\_SLICE 58U,56U
- #define MMCSD\_CSD\_10\_VDD\_R\_CURR\_MIX\_SLICE 61U,59U
- #define MMCSD\_CSD\_10\_C\_SIZE\_SLICE 73U,62U
- #define MMCSD\_CSD\_10\_DSR\_IMP\_SLICE MMCSD\_CSD\_20\_DSR\_IMP\_SLICE
- #define MMCSD\_CSD\_10\_READ\_BLK\_MISALIGN\_SLICE MMCSD\_CSD\_20\_READ\_BLK\_MISALIG←
   N SLICE
- #define MMCSD\_CSD\_10\_WRITE\_BLK\_MISALIGN\_SLICE MMCSD\_CSD\_20\_WRITE\_BLK\_MISALI
   GN\_SLICE
- #define MMCSD\_CSD\_10\_READ\_BL\_PARTIAL\_SLICE MMCSD\_CSD\_20\_READ\_BL\_PARTIAL\_SLI←
  CF
- #define MMCSD\_CSD\_10\_READ\_BL\_LEN\_SLICE 83U,80U
- · #define MMCSD CSD 10 CCC SLICE MMCSD CSD 20 CCC SLICE
- #define MMCSD CSD 10 TRANS SPEED SLICE MMCSD CSD 20 TRANS SPEED SLICE
- #define MMCSD\_CSD\_10\_NSAC\_SLICE MMCSD\_CSD\_20\_NSAC\_SLICE
- #define MMCSD\_CSD\_10\_TAAC\_SLICE MMCSD\_CSD\_20\_TAAC\_SLICE
- #define MMCSD CSD 10 CSD STRUCTURE SLICE MMCSD CSD 20 CSD STRUCTURE SLICE

### CID record offsets

- #define MMCSD CID SDC CRC SLICE 7U,1U
  - Slice position of values in CID register.
- #define MMCSD\_CID\_SDC\_MDT\_M\_SLICE 11U,8U
- #define MMCSD\_CID\_SDC\_MDT\_Y\_SLICE 19U,12U
- #define MMCSD\_CID\_SDC\_PSN\_SLICE 55U,24U
- #define MMCSD\_CID\_SDC\_PRV\_M\_SLICE 59U,56U

```
• #define MMCSD CID SDC PRV N SLICE 63U,60U
```

- #define MMCSD CID SDC PNM0 SLICE 71U,64U
- #define MMCSD CID SDC PNM1 SLICE 79U,72U
- #define MMCSD CID SDC PNM2 SLICE 87U,80U
- #define MMCSD CID SDC PNM3 SLICE 95U,88U
- #define MMCSD CID SDC PNM4 SLICE 103U,96U
- #define MMCSD CID SDC OID SLICE 119U,104U
- #define MMCSD\_CID\_SDC\_MID\_SLICE 127U,120U
- #define MMCSD CID MMC CRC SLICE 7U,1U
- #define MMCSD\_CID\_MMC\_MDT\_Y\_SLICE 11U,8U
- #define MMCSD\_CID\_MMC\_MDT\_M\_SLICE 15U,12U
- #define MMCSD\_CID\_MMC\_PSN\_SLICE 47U,16U
- #define MMCSD\_CID\_MMC\_PRV\_M\_SLICE 51U,48U
- #define MMCSD\_CID\_MMC\_PRV\_N\_SLICE 55U,52U
- #define MMCSD\_CID\_MMC\_PNM0\_SLICE 63U,56U
- #define MMCSD\_CID\_MMC\_PNM1\_SLICE 71U,64U
- #define MMCSD\_CID\_MMC\_PNM2\_SLICE 79U,72U
- #define MMCSD\_CID\_MMC\_PNM3\_SLICE 87U,80U
- #define MMCSD\_CID\_MMC\_PNM4\_SLICE 95U,88U
- #define MMCSD CID MMC PNM5 SLICE 103U,96U
- #define MMCSD\_CID\_MMC\_OID\_SLICE 119U,104U
- #define MMCSD CID MMC MID SLICE 127U,120U

### R1 response utilities

- #define MMCSD\_R1\_ERROR(r1) (((r1) & MMCSD\_R1\_ERROR\_MASK) != 0U)
  - Evaluates to TRUE if the R1 response contains error flags.
- #define MMCSD\_R1\_STS(r1) (((r1) >> 9U) & 15U)
  - Returns the status field of an R1 response.
- #define MMCSD R1 IS CARD LOCKED(r1) ((((r1) >> 21U) & 1U) != 0U)

Evaluates to TRUE if the R1 response indicates a locked card.

#### **Macro Functions**

#define mmcsdGetCardCapacity(ip) ((ip)->capacity)
 Returns the card capacity in blocks.

# **Functions**

- uint32 t mmcsd get slice (const uint32 t \*data, uint32 t end, uint32 t start)
  - Gets a bit field from a words array.
- uint32\_t \_mmcsd\_get\_capacity (const uint32\_t \*csd)
  - Extract card capacity from a CSD.
- uint32\_t \_mmcsd\_get\_capacity\_ext (const uint8\_t \*ext\_csd)
  - Extract MMC card capacity from EXT\_CSD.
- void \_mmcsd\_unpack\_sdc\_cid (const MMCSDBlockDevice \*sdcp, unpacked\_sdc\_cid\_t \*cidsdc)
  - Unpacks SDC CID array in structure.
- void \_mmcsd\_unpack\_mmc\_cid (const MMCSDBlockDevice \*sdcp, unpacked\_mmc\_cid\_t \*cidmmc)
  - Unpacks MMC CID array in structure.
- void \_mmcsd\_unpack\_csd\_mmc (const MMCSDBlockDevice \*sdcp, unpacked\_mmc\_csd\_t \*csdmmc)
  - Unpacks MMC CSD array in structure.
- void \_mmcsd\_unpack\_csd\_v10 (const MMCSDBlockDevice \*sdcp, unpacked\_sdc\_csd\_10\_t \*csd10)
  - Unpacks SDC CSD v1.0 array in structure.
- void \_mmcsd\_unpack\_csd\_v20 (const MMCSDBlockDevice \*sdcp, unpacked\_sdc\_csd\_20\_t \*csd20)
  - Unpacks SDC CSD v2.0 array in structure.

## 9.31.1 Detailed Description

MMC/SD cards common header.

This header defines an abstract interface useful to access MMC/SD I/O block devices in a standardized way.

# 9.32 hal\_queues.c File Reference

```
I/O Queues code.
```

```
#include "hal.h"
```

#### **Functions**

- void iqObjectInit (input\_queue\_t \*iqp, uint8\_t \*bp, size\_t size, qnotify\_t infy, void \*link)
   Initializes an input queue.
- void iqResetI (input\_queue\_t \*iqp)

Resets an input queue.

msg\_t iqPutl (input\_queue\_t \*iqp, uint8\_t b)

Input queue write.

msg\_t iqGetTimeout (input\_queue\_t \*iqp, systime\_t timeout)

Input queue read with timeout.

• size\_t iqReadTimeout (input\_queue\_t \*iqp, uint8\_t \*bp, size\_t n, systime\_t timeout)

Input queue read with timeout.

void oqObjectInit (output\_queue\_t \*oqp, uint8\_t \*bp, size\_t size, qnotify\_t onfy, void \*link)

Initializes an output queue.

void oqResetI (output\_queue\_t \*oqp)

Resets an output queue.

• msg\_t oqPutTimeout (output\_queue\_t \*oqp, uint8\_t b, systime\_t timeout)

Output queue write with timeout.

msg\_t oqGetI (output\_queue\_t \*oqp)

Output queue read.

• size\_t oqWriteTimeout (output\_queue\_t \*oqp, const uint8\_t \*bp, size\_t n, systime\_t timeout)

Output queue write with timeout.

## 9.32.1 Detailed Description

I/O Queues code.

# 9.33 hal\_queues.h File Reference

I/O Queues macros and structures.

## **Data Structures**

struct io\_queue

Generic I/O queue structure.

### **Macros**

#### Queue functions returned status value

```
    #define Q_OK MSG_OK
```

Operation successful.

#define Q TIMEOUT MSG TIMEOUT

Timeout condition.

#define Q\_RESET MSG\_RESET

Queue has been reset.

#define Q\_EMPTY (msg\_t)-3

Queue empty.

• #define Q FULL (msg t)-4

Queue full..

#### **Macro Functions**

#define qSizeX(qp)

Returns the queue's buffer size.

#define qSpaceI(qp) ((qp)->q\_counter)

Queue space.

#define qGetLink(qp) ((qp)->q\_link)

Returns the queue application-defined link.

• #define iqGetFullI(iqp) qSpaceI(iqp)

Returns the filled space into an input queue.

#define iqGetEmptyl(iqp) (qSizeX(iqp) - qSpacel(iqp))

Returns the empty space into an input queue.

#define iqlsEmptyl(iqp) ((bool)(qSpacel(iqp) == 0U))

Evaluates to true if the specified input queue is empty.

#define iqlsFull(iqp)

Evaluates to true if the specified input queue is full.

#define iqGet(iqp) iqGetTimeout(iqp, TIME\_INFINITE)

Input queue read.

#define oqGetFullI(oqp) (qSizeX(oqp) - qSpaceI(oqp))

Returns the filled space into an output queue.

#define oqGetEmptyl(oqp) qSpacel(oqp)

Returns the empty space into an output queue.

#define oqlsEmptyl(oqp)

Evaluates to true if the specified output queue is empty.

#define oglsFullI(oqp) ((bool)(qSpaceI(oqp) == 0U))

Evaluates to true if the specified output queue is full.

#define ogPut(ogp, b) ogPutTimeout(ogp, b, TIME\_INFINITE)

Output queue write.

## **Typedefs**

• typedef struct io\_queue io\_queue\_t

Type of a generic I/O queue structure.

typedef void(\* qnotify\_t) (io\_queue\_t \*qp)

Queue notification callback type.

• typedef io\_queue\_t input\_queue\_t

Type of an input queue structure.

• typedef io\_queue\_t output\_queue\_t

Type of an output queue structure.

### **Functions**

void iqObjectInit (input\_queue\_t \*iqp, uint8\_t \*bp, size\_t size, qnotify\_t infy, void \*link)

Initializes an input queue.

void iqResetl (input\_queue\_t \*iqp)

Resets an input queue.

msg\_t iqPutI (input\_queue\_t \*iqp, uint8\_t b)

Input queue write.

msg\_t iqGetTimeout (input\_queue\_t \*iqp, systime\_t timeout)

Input queue read with timeout.

size\_t iqReadTimeout (input\_queue\_t \*iqp, uint8\_t \*bp, size\_t n, systime\_t timeout)

Input queue read with timeout.

void oqObjectInit (output\_queue\_t \*oqp, uint8\_t \*bp, size\_t size, qnotify\_t onfy, void \*link)

Initializes an output queue.

void oqResetl (output\_queue\_t \*oqp)

Resets an output queue.

msg\_t oqPutTimeout (output\_queue\_t \*oqp, uint8\_t b, systime\_t timeout)

Output queue write with timeout.

msg\_t oqGetl (output\_queue\_t \*oqp)

Output queue read.

• size\_t oqWriteTimeout (output\_queue\_t \*oqp, const uint8\_t \*bp, size\_t n, systime\_t timeout)

Output queue write with timeout.

### 9.33.1 Detailed Description

I/O Queues macros and structures.

## 9.34 hal streams.h File Reference

Data streams.

### **Data Structures**

struct BaseSequentialStreamVMT

BaseSequentialStream virtual methods table.

• struct BaseSequentialStream

Base stream class.

### Macros

• #define \_base\_sequential\_stream\_methods

BaseSequentialStream specific methods.

• #define \_base\_sequential\_stream\_data

BaseSequentialStream specific data.

#### Streams return codes

- #define **STM\_OK** MSG\_OK
- #define STM\_TIMEOUT MSG\_TIMEOUT
- #define STM\_RESET MSG\_RESET

### Macro Functions (BaseSequentialStream)

• #define streamWrite(ip, bp, n) ((ip)->vmt->write(ip, bp, n))

Sequential Stream write.

#define streamRead(ip, bp, n) ((ip)->vmt->read(ip, bp, n))

Sequential Stream read.

#define streamPut(ip, b) ((ip)->vmt->put(ip, b))

Sequential Stream blocking byte write.

#define streamGet(ip) ((ip)->vmt->get(ip))

Sequential Stream blocking byte read.

## 9.34.1 Detailed Description

Data streams.

This header defines abstract interfaces useful to access generic data streams in a standardized way.

## 9.35 halconf.h File Reference

```
HAL configuration header.
```

```
#include "mcuconf.h"
```

#### **Macros**

#### **Drivers enable switches**

- #define HAL\_USE\_PAL TRUE
  - Enables the PAL subsystem.
- #define HAL\_USE\_ADC TRUE

Enables the ADC subsystem.

- #define HAL\_USE\_CAN TRUE
  - Enables the CAN subsystem.
- #define HAL\_USE\_DAC FALSE
  - Enables the DAC subsystem.
- #define HAL\_USE\_EXT TRUE
  - Enables the EXT subsystem.
- #define HAL\_USE\_GPT TRUE
  - Enables the GPT subsystem.
- #define HAL\_USE\_I2C TRUE
- Enables the I2C subsystem.

   #define HAL\_USE\_I2S TRUE
  - Enables the I2S subsystem.
- #define HAL\_USE\_ICU TRUE
  - Enables the ICU subsystem.
- #define HAL\_USE\_MAC TRUE
  - Enables the MAC subsystem.
- #define HAL\_USE\_MMC\_SPI TRUE
  - Enables the MMC\_SPI subsystem.
- #define HAL\_USE\_PWM TRUE
  - Enables the PWM subsystem.
- #define HAL USE RTC TRUE
  - Enables the RTC subsystem.
- #define HAL\_USE\_SDC TRUE
  - Enables the SDC subsystem.
- #define HAL\_USE\_SERIAL TRUE

Enables the SERIAL subsystem.

#define HAL\_USE\_SERIAL\_USB TRUE

Enables the SERIAL over USB subsystem.

#define HAL USE SPI TRUE

Enables the SPI subsystem.

#define HAL\_USE\_UART TRUE

Enables the UART subsystem.

#define HAL USE USB TRUE

Enables the USB subsystem.

• #define HAL\_USE\_WDG TRUE

Enables the WDG subsystem.

### ADC driver related setting

#define ADC USE WAIT TRUE

Enables synchronous APIs.

• #define ADC\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the adcAcquireBus() and adcReleaseBus() APIs.

### **CAN** driver related setting

• #define CAN USE SLEEP MODE TRUE

Sleep mode related APIs inclusion switch.

### I2C driver related setting

• #define I2C USE MUTUAL EXCLUSION TRUE

Enables the mutual exclusion APIs on the I2C bus.

## MAC driver related setting

#define MAC\_USE\_ZERO\_COPY TRUE

Enables an event sources for incoming packets.

• #define MAC USE EVENTS TRUE

Enables an event sources for incoming packets.

### MMC\_SPI driver related setting

• #define MMC NICE WAITING TRUE

Delays insertions.

## SDC driver related setting

• #define SDC\_INIT\_RETRY 100

Number of initialization attempts before rejecting the card.

• #define SDC MMC SUPPORT TRUE

Include support for MMC cards.

• #define SDC\_NICE\_WAITING TRUE

Delays insertions.

## SERIAL driver related setting

• #define SERIAL\_DEFAULT\_BITRATE 38400

Default bit rate.

• #define SERIAL BUFFERS SIZE 16

Serial buffers size.

## SERIAL\_USB driver related setting

• #define SERIAL\_USB\_BUFFERS\_SIZE 256

Serial over USB buffers size.

#define SERIAL\_USB\_BUFFERS\_NUMBER 2

Serial over USB number of buffers.

### SPI driver related setting

• #define SPI USE WAIT TRUE

Enables synchronous APIs.

• #define SPI\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the spiAcquireBus() and spiReleaseBus() APIs.

#### **UART** driver related setting

#define UART USE WAIT TRUE

Enables synchronous APIs.

#define UART\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the uartAcquireBus() and uartReleaseBus() APIs.

### **USB** driver related setting

• #define USB\_USE\_WAIT TRUE

Enables synchronous APIs.

## 9.35.1 Detailed Description

HAL configuration header.

HAL configuration file, this file allows to enable or disable the various device drivers from your application. You may also use this file in order to override the device drivers default settings.

## 9.36 i2c.c File Reference

I2C Driver code.

#include "hal.h"

## **Functions**

· void i2clnit (void)

I2C Driver initialization.

void i2cObjectInit (I2CDriver \*i2cp)

Initializes the standard part of a I2CDriver structure.

• void i2cStart (I2CDriver \*i2cp, const I2CConfig \*config)

Configures and activates the I2C peripheral.

void i2cStop (I2CDriver \*i2cp)

Deactivates the I2C peripheral.

i2cflags\_t i2cGetErrors (I2CDriver \*i2cp)

Returns the errors mask associated to the previous operation.

 msg\_t i2cMasterTransmitTimeout (I2CDriver \*i2cp, i2caddr\_t addr, const uint8\_t \*txbuf, size\_t txbytes, uint8\_t \*rxbuf, size\_t rxbytes, systime\_t timeout)

Sends data via the I2C bus.

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msg\_t i2cMasterReceiveTimeout (I2CDriver \*i2cp, i2caddr\_t addr, uint8\_t \*rxbuf, size\_t rxbytes, systime\_t timeout)

Receives data from the I2C bus.

• void i2cAcquireBus (I2CDriver \*i2cp)

Gains exclusive access to the I2C bus.

void i2cReleaseBus (I2CDriver \*i2cp)

Releases exclusive access to the I2C bus.

## 9.36.1 Detailed Description

I2C Driver code.

## 9.37 i2c.h File Reference

I2C Driver macros and structures.

```
#include "i2c_lld.h"
```

#### **Macros**

• #define I2C\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the mutual exclusion APIs on the I2C bus.

#define \_i2c\_wakeup\_isr(i2cp)

Wakes up the waiting thread notifying no errors.

• #define \_i2c\_wakeup\_error\_isr(i2cp)

Wakes up the waiting thread notifying errors.

#define i2cMasterTransmit(i2cp, addr, txbuf, txbytes, rxbuf, rxbytes)

Wrap i2cMasterTransmitTimeout function with TIME\_INFINITE timeout.

 #define i2cMasterReceive(i2cp, addr, rxbuf, rxbytes) (i2cMasterReceiveTimeout(i2cp, addr, rxbuf, rxbytes, TIME\_INFINITE))

Wrap i2cMasterReceiveTimeout function with TIME\_INFINITE timeout.

## I2C bus error conditions

• #define I2C\_NO\_ERROR 0x00

No error.

• #define I2C\_BUS\_ERROR 0x01

Bus Error.

• #define I2C ARBITRATION LOST 0x02

Arbitration Lost.

#define I2C\_ACK\_FAILURE 0x04

Acknowledge Failure.

#define I2C\_OVERRUN 0x08

Overrun/Underrun.

• #define I2C\_PEC\_ERROR 0x10

PEC Error in reception.

• #define I2C\_TIMEOUT 0x20

Hardware timeout.

• #define I2C SMB ALERT 0x40

SMBus Alert.

### **Enumerations**

## **Functions**

void i2clnit (void)

I2C Driver initialization.

void i2cObjectInit (I2CDriver \*i2cp)

Initializes the standard part of a I2CDriver structure.

void i2cStart (I2CDriver \*i2cp, const I2CConfig \*config)

Configures and activates the I2C peripheral.

void i2cStop (I2CDriver \*i2cp)

Deactivates the I2C peripheral.

• i2cflags\_t i2cGetErrors (I2CDriver \*i2cp)

Returns the errors mask associated to the previous operation.

msg\_t i2cMasterTransmitTimeout (I2CDriver \*i2cp, i2caddr\_t addr, const uint8\_t \*txbuf, size\_t txbytes, uint8 t \*rxbuf, size t rxbytes, systime t timeout)

Sends data via the I2C bus.

msg\_t i2cMasterReceiveTimeout (I2CDriver \*i2cp, i2caddr\_t addr, uint8\_t \*rxbuf, size\_t rxbytes, systime\_t timeout)

Receives data from the I2C bus.

• void i2cAcquireBus (I2CDriver \*i2cp)

Gains exclusive access to the I2C bus.

void i2cReleaseBus (I2CDriver \*i2cp)

Releases exclusive access to the I2C bus.

## 9.37.1 Detailed Description

I2C Driver macros and structures.

## 9.38 i2c\_lld.c File Reference

PLATFORM I2C subsystem low level driver source.

```
#include "hal.h"
```

### **Functions**

void i2c\_lld\_init (void)

Low level I2C driver initialization.

• void i2c\_lld\_start (I2CDriver \*i2cp)

Configures and activates the I2C peripheral.

• void i2c\_lld\_stop (I2CDriver \*i2cp)

Deactivates the I2C peripheral.

msg\_t i2c\_lld\_master\_receive\_timeout (I2CDriver \*i2cp, i2caddr\_t addr, uint8\_t \*rxbuf, size\_t rxbytes, systime\_t timeout)

Receives data via the I2C bus as master.

msg\_t i2c\_lld\_master\_transmit\_timeout (l2CDriver \*i2cp, i2caddr\_t addr, const uint8\_t \*txbuf, size\_t txbytes, uint8\_t \*rxbuf, size\_t rxbytes, systime\_t timeout)

Transmits data via the I2C bus as master.

### **Variables**

I2CDriver I2CD1

I2C1 driver identifier.

## 9.38.1 Detailed Description

PLATFORM I2C subsystem low level driver source.

# 9.39 i2c\_lld.h File Reference

PLATFORM I2C subsystem low level driver header.

## **Data Structures**

struct I2CConfig

Type of I2C driver configuration structure.

struct I2CDriver

Structure representing an I2C driver.

#### **Macros**

#define i2c\_lld\_get\_errors(i2cp) ((i2cp)->errors)
 Get errors from I2C driver.

## **PLATFORM** configuration options

• #define PLATFORM\_I2C\_USE\_I2C1 FALSE I2C1 driver enable switch.

# **Typedefs**

• typedef uint16\_t i2caddr\_t

Type representing an I2C address.

· typedef uint32\_t i2cflags\_t

Type of I2C Driver condition flags.

• typedef struct I2CDriver I2CDriver

Type of a structure representing an I2C driver.

### **Functions**

• void i2c\_lld\_init (void)

Low level I2C driver initialization.

void i2c\_lld\_start (l2CDriver \*i2cp)

Configures and activates the I2C peripheral.

• void i2c\_lld\_stop (I2CDriver \*i2cp)

Deactivates the I2C peripheral.

msg\_t i2c\_lld\_master\_transmit\_timeout (I2CDriver \*i2cp, i2caddr\_t addr, const uint8\_t \*txbuf, size\_t txbytes, uint8\_t \*rxbuf, size\_t rxbytes, systime\_t timeout)

Transmits data via the I2C bus as master.

msg\_t i2c\_lld\_master\_receive\_timeout (I2CDriver \*i2cp, i2caddr\_t addr, uint8\_t \*rxbuf, size\_t rxbytes, systime\_t timeout)

Receives data via the I2C bus as master.

## 9.39.1 Detailed Description

PLATFORM I2C subsystem low level driver header.

## 9.40 i2s.c File Reference

```
I2S Driver code.
```

```
#include "hal.h"
```

### **Functions**

void i2sInit (void)

12S Driver initialization.

• void i2sObjectInit (I2SDriver \*i2sp)

Initializes the standard part of a I2SDriver structure.

void i2sStart (I2SDriver \*i2sp, const I2SConfig \*config)

Configures and activates the I2S peripheral.

void i2sStop (I2SDriver \*i2sp)

Deactivates the I2S peripheral.

void i2sStartExchange (I2SDriver \*i2sp)

Starts a I2S data exchange.

void i2sStopExchange (I2SDriver \*i2sp)

Stops the ongoing data exchange.

## 9.40.1 Detailed Description

I2S Driver code.

## 9.41 i2s.h File Reference

I2S Driver macros and structures.

```
#include "i2s_lld.h"
```

## **Macros**

## I2S modes

- #define I2S MODE SLAVE 0
- #define I2S MODE MASTER 1

## **Macro Functions**

#define i2sStartExchangel(i2sp)

Starts a I2S data exchange.

• #define i2sStopExchangel(i2sp)

Stops the ongoing data exchange.

• #define \_i2s\_isr\_half\_code(i2sp)

Common ISR code, half buffer event.

• #define \_i2s\_isr\_full\_code(i2sp)

Common ISR code.

### **Enumerations**

## **Functions**

· void i2sInit (void)

I2S Driver initialization.

• void i2sObjectInit (I2SDriver \*i2sp)

Initializes the standard part of a I2SDriver structure.

void i2sStart (I2SDriver \*i2sp, const I2SConfig \*config)

Configures and activates the I2S peripheral.

void i2sStop (I2SDriver \*i2sp)

Deactivates the I2S peripheral.

void i2sStartExchange (I2SDriver \*i2sp)

Starts a I2S data exchange.

void i2sStopExchange (I2SDriver \*i2sp)

Stops the ongoing data exchange.

## 9.41.1 Detailed Description

I2S Driver macros and structures.

# 9.42 i2s\_lld.c File Reference

PLATFORM I2S subsystem low level driver source.

```
#include "hal.h"
```

### **Functions**

void i2s\_lld\_init (void)

Low level I2S driver initialization.

void i2s\_lld\_start (l2SDriver \*i2sp)

Configures and activates the I2S peripheral.

## **Variables**

• I2SDriver I2SD1

I2S2 driver identifier.

## 9.42.1 Detailed Description

PLATFORM I2S subsystem low level driver source.

# 9.43 i2s\_lld.h File Reference

PLATFORM I2S subsystem low level driver header.

## **Data Structures**

• struct I2SConfig

Driver configuration structure.

• struct I2SDriver

Structure representing an I2S driver.

### **Macros**

## **PLATFORM** configuration options

# **Typedefs**

• typedef struct I2SDriver I2SDriver

Type of a structure representing an I2S driver.

• typedef void(\* i2scallback\_t) (I2SDriver \*i2sp, size\_t offset, size\_t n)

I2S notification callback type.

## **Functions**

• void i2s\_lld\_init (void)

Low level I2S driver initialization.

• void i2s\_lld\_start (l2SDriver \*i2sp)

Configures and activates the I2S peripheral.

# 9.43.1 Detailed Description

PLATFORM I2S subsystem low level driver header.

## 9.44 icu.c File Reference

## ICU Driver code.

#include "hal.h"

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### **Functions**

· void iculnit (void)

ICU Driver initialization.

void icuObjectInit (ICUDriver \*icup)

Initializes the standard part of a ICUDriver structure.

void icuStart (ICUDriver \*icup, const ICUConfig \*config)

Configures and activates the ICU peripheral.

void icuStop (ICUDriver \*icup)

Deactivates the ICU peripheral.

void icuStartCapture (ICUDriver \*icup)

Starts the input capture.

bool icuWaitCapture (ICUDriver \*icup)

Waits for a completed capture.

void icuStopCapture (ICUDriver \*icup)

Stops the input capture.

• void icuEnableNotifications (ICUDriver \*icup)

Enables notifications.

• void icuDisableNotifications (ICUDriver \*icup)

Disables notifications.

## 9.44.1 Detailed Description

ICU Driver code.

## 9.45 icu.h File Reference

ICU Driver macros and structures.

```
#include "icu_lld.h"
```

### **Macros**

#### **Macro Functions**

#define icuStartCaptureI(icup)

Starts the input capture.

#define icuStopCaptureI(icup)

Stops the input capture.

• #define icuEnableNotificationsI(icup) icu\_lld\_enable\_notifications(icup)

Enables notifications.

• #define icuDisableNotificationsI(icup) icu\_lld\_disable\_notifications(icup)

Disables notifications.

• #define icuAreNotificationsEnabledX(icup) icu\_lld\_are\_notifications\_enabled(icup)

Check on notifications status.

• #define icuGetWidthX(icup) icu\_lld\_get\_width(icup)

Returns the width of the latest pulse.

#define icuGetPeriodX(icup) icu\_lld\_get\_period(icup)

Returns the width of the latest cycle.

## Low level driver helper macros

```
    #define _icu_isr_invoke_width_cb(icup)
```

Common ISR code, ICU width event.

• #define \_icu\_isr\_invoke\_period\_cb(icup)

Common ISR code, ICU period event.

#define \_icu\_isr\_invoke\_overflow\_cb(icup)

Common ISR code, ICU timer overflow event.

## **Typedefs**

· typedef struct ICUDriver ICUDriver

Type of a structure representing an ICU driver.

typedef void(\* icucallback\_t) (ICUDriver \*icup)

ICU notification callback type.

### **Enumerations**

### **Functions**

· void iculnit (void)

ICU Driver initialization.

void icuObjectInit (ICUDriver \*icup)

Initializes the standard part of a ICUDriver structure.

• void icuStart (ICUDriver \*icup, const ICUConfig \*config)

Configures and activates the ICU peripheral.

void icuStop (ICUDriver \*icup)

Deactivates the ICU peripheral.

• void icuStartCapture (ICUDriver \*icup)

Starts the input capture.

bool icuWaitCapture (ICUDriver \*icup)

Waits for a completed capture.

void icuStopCapture (ICUDriver \*icup)

Stops the input capture.

void icuEnableNotifications (ICUDriver \*icup)

Enables notifications.

void icuDisableNotifications (ICUDriver \*icup)

Disables notifications.

# 9.45.1 Detailed Description

ICU Driver macros and structures.

# 9.46 icu\_lld.c File Reference

PLATFORM ADC subsystem low level driver source.

```
#include "hal.h"
```

### **Functions**

void icu\_lld\_init (void)

Low level ICU driver initialization.

void icu\_lld\_start (ICUDriver \*icup)

Configures and activates the ICU peripheral.

void icu\_lld\_stop (ICUDriver \*icup)

Deactivates the ICU peripheral.

void icu\_lld\_start\_capture (ICUDriver \*icup)

Starts the input capture.

bool icu\_lld\_wait\_capture (ICUDriver \*icup)

Waits for a completed capture.

void icu\_lld\_stop\_capture (ICUDriver \*icup)

Stops the input capture.

• void icu\_lld\_enable\_notifications (ICUDriver \*icup)

Enables notifications.

void icu\_lld\_disable\_notifications (ICUDriver \*icup)

Disables notifications.

### **Variables**

ICUDriver ICUD1

ICUD1 driver identifier.

### 9.46.1 Detailed Description

PLATFORM ADC subsystem low level driver source.

# 9.47 icu\_lld.h File Reference

PLATFORM ICU subsystem low level driver header.

### **Data Structures**

· struct ICUConfig

Driver configuration structure.

struct ICUDriver

Structure representing an ICU driver.

### Macros

• #define icu\_lld\_get\_width(icup) 0

Returns the width of the latest pulse.

• #define icu\_lld\_get\_period(icup) 0

Returns the width of the latest cycle.

• #define icu\_lld\_are\_notifications\_enabled(icup) false

Check on notifications status.

## **PLATFORM** configuration options

#define PLATFORM\_ICU\_USE\_ICU1 FALSE
 ICUD1 driver enable switch.

## **Typedefs**

```
    typedef uint32 t icufreq t
```

ICU frequency type.

• typedef uint32\_t icucnt\_t

ICU counter type.

## **Enumerations**

#### **Functions**

void icu\_lld\_init (void)

Low level ICU driver initialization.

• void icu\_lld\_start (ICUDriver \*icup)

Configures and activates the ICU peripheral.

• void icu\_lld\_stop (ICUDriver \*icup)

Deactivates the ICU peripheral.

• void icu\_lld\_start\_capture (ICUDriver \*icup)

Starts the input capture.

• bool icu\_lld\_wait\_capture (ICUDriver \*icup)

Waits for a completed capture.

• void icu\_lld\_stop\_capture (ICUDriver \*icup)

Stops the input capture.

• void icu\_lld\_enable\_notifications (ICUDriver \*icup)

Enables notifications.

• void icu\_lld\_disable\_notifications (ICUDriver \*icup)

Disables notifications.

### 9.47.1 Detailed Description

PLATFORM ICU subsystem low level driver header.

# 9.48 mac.c File Reference

```
MAC Driver code.
```

```
#include "hal.h"
```

### **Functions**

· void macInit (void)

MAC Driver initialization.

void macObjectInit (MACDriver \*macp)

Initialize the standard part of a MACDriver structure.

void macStart (MACDriver \*macp, const MACConfig \*config)

Configures and activates the MAC peripheral.

void macStop (MACDriver \*macp)

Deactivates the MAC peripheral.

• msg\_t macWaitTransmitDescriptor (MACDriver \*macp, MACTransmitDescriptor \*tdp, systime\_t timeout)

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Allocates a transmission descriptor.

void macReleaseTransmitDescriptor (MACTransmitDescriptor \*tdp)

Releases a transmit descriptor and starts the transmission of the enqueued data as a single frame.

msg\_t macWaitReceiveDescriptor (MACDriver \*macp, MACReceiveDescriptor \*rdp, systime\_t timeout)
 Waits for a received frame.

void macReleaseReceiveDescriptor (MACReceiveDescriptor \*rdp)

Releases a receive descriptor.

• bool macPollLinkStatus (MACDriver \*macp)

Updates and returns the link status.

## 9.48.1 Detailed Description

MAC Driver code.

## 9.49 mac.h File Reference

MAC Driver macros and structures.

```
#include "mac_lld.h"
```

#### **Macros**

### **MAC** configuration options

• #define MAC USE ZERO COPY FALSE

Enables an event sources for incoming packets.

#define MAC\_USE\_EVENTS TRUE

Enables an event sources for incoming packets.

#### **Macro Functions**

• #define macGetReceiveEventSource(macp) (&(macp)->rdevent)

Returns the received frames event source.

- #define macWriteTransmitDescriptor(tdp, buf, size) mac\_lld\_write\_transmit\_descriptor(tdp, buf, size)
   Writes to a transmit descriptor's stream.
- #define macReadReceiveDescriptor(rdp, buf, size) mac\_lld\_read\_receive\_descriptor(rdp, buf, size)

  Reads from a receive descriptor's stream.
- #define macGetNextTransmitBuffer(tdp, size, sizep) mac\_lld\_get\_next\_transmit\_buffer(tdp, size, sizep)

  Returns a pointer to the next transmit buffer in the descriptor chain.
- #define macGetNextReceiveBuffer(rdp, sizep) mac\_lld\_get\_next\_receive\_buffer(rdp, sizep)

  Returns a pointer to the next receive buffer in the descriptor chain.

## **Typedefs**

typedef struct MACDriver MACDriver

Type of a structure representing a MAC driver.

#### **Enumerations**

## **Functions**

void macInit (void)

MAC Driver initialization.

void macObjectInit (MACDriver \*macp)

Initialize the standard part of a MACDriver structure.

void macStart (MACDriver \*macp, const MACConfig \*config)

Configures and activates the MAC peripheral.

void macStop (MACDriver \*macp)

Deactivates the MAC peripheral.

 $\bullet \ \ msg\_t \ macWaitTransmitDescriptor \ (MACDriver * macp, \ MACTransmitDescriptor * tdp, \ systime\_t \ timeout)$ 

Allocates a transmission descriptor.

void macReleaseTransmitDescriptor (MACTransmitDescriptor \*tdp)

Releases a transmit descriptor and starts the transmission of the enqueued data as a single frame.

- msg\_t macWaitReceiveDescriptor (MACDriver \*macp, MACReceiveDescriptor \*rdp, systime\_t timeout) Waits for a received frame.
- void macReleaseReceiveDescriptor (MACReceiveDescriptor \*rdp)

Releases a receive descriptor.

bool macPollLinkStatus (MACDriver \*macp)

Updates and returns the link status.

## 9.49.1 Detailed Description

MAC Driver macros and structures.

## 9.50 mac Ild.c File Reference

PLATFORM MAC subsystem low level driver source.

```
#include <string.h>
#include "hal.h"
#include "mii.h"
```

## **Functions**

void mac\_lld\_init (void)

Low level MAC initialization.

void mac\_lld\_start (MACDriver \*macp)

Configures and activates the MAC peripheral.

void mac\_lld\_stop (MACDriver \*macp)

Deactivates the MAC peripheral.

• msg\_t mac\_lld\_get\_transmit\_descriptor (MACDriver \*macp, MACTransmitDescriptor \*tdp)

Returns a transmission descriptor.

void mac\_lld\_release\_transmit\_descriptor (MACTransmitDescriptor \*tdp)

Releases a transmit descriptor and starts the transmission of the enqueued data as a single frame.

msg t mac IId get receive descriptor (MACDriver \*macp, MACReceiveDescriptor \*rdp)

Returns a receive descriptor.

void mac\_lld\_release\_receive\_descriptor (MACReceiveDescriptor \*rdp)

Releases a receive descriptor.

bool mac\_lld\_poll\_link\_status (MACDriver \*macp)

Updates and returns the link status.

size\_t mac\_lld\_write\_transmit\_descriptor (MACTransmitDescriptor \*tdp, uint8\_t \*buf, size\_t size)

Writes to a transmit descriptor's stream.

• size\_t mac\_lld\_read\_receive\_descriptor (MACReceiveDescriptor \*rdp, uint8\_t \*buf, size\_t size)

Reads from a receive descriptor's stream.

uint8\_t \* mac\_lld\_get\_next\_transmit\_buffer (MACTransmitDescriptor \*tdp, size\_t size, size\_t \*sizep)

Returns a pointer to the next transmit buffer in the descriptor chain.

const uint8\_t \* mac\_lld\_get\_next\_receive\_buffer (MACReceiveDescriptor \*rdp, size\_t \*sizep)

Returns a pointer to the next receive buffer in the descriptor chain.

### **Variables**

MACDriver ETHD1

MAC1 driver identifier.

## 9.50.1 Detailed Description

PLATFORM MAC subsystem low level driver source.

# 9.51 mac\_lld.h File Reference

PLATFORM MAC subsystem low level driver header.

## **Data Structures**

· struct MACConfig

Driver configuration structure.

struct MACDriver

Structure representing a MAC driver.

struct MACTransmitDescriptor

Structure representing a transmit descriptor.

• struct MACReceiveDescriptor

Structure representing a receive descriptor.

## Macros

• #define MAC\_SUPPORTS\_ZERO\_COPY TRUE

This implementation supports the zero-copy mode API.

# **PLATFORM** configuration options

#define PLATFORM\_MAC\_USE\_MAC1 FALSE
 MAC driver enable switch.

### **Functions**

void mac Ild init (void)

Low level MAC initialization.

void mac\_lld\_start (MACDriver \*macp)

Configures and activates the MAC peripheral.

void mac\_lld\_stop (MACDriver \*macp)

Deactivates the MAC peripheral.

• msg\_t mac\_lld\_get\_transmit\_descriptor (MACDriver \*macp, MACTransmitDescriptor \*tdp)

Returns a transmission descriptor.

void mac\_lld\_release\_transmit\_descriptor (MACTransmitDescriptor \*tdp)

Releases a transmit descriptor and starts the transmission of the enqueued data as a single frame.

msg\_t mac\_lld\_get\_receive\_descriptor (MACDriver \*macp, MACReceiveDescriptor \*rdp)

Returns a receive descriptor.

void mac\_lld\_release\_receive\_descriptor (MACReceiveDescriptor \*rdp)

Releases a receive descriptor.

bool mac IId poll link status (MACDriver \*macp)

Updates and returns the link status.

• size\_t mac\_lld\_write\_transmit\_descriptor (MACTransmitDescriptor \*tdp, uint8\_t \*buf, size\_t size)

Writes to a transmit descriptor's stream.

• size\_t mac\_lld\_read\_receive\_descriptor (MACReceiveDescriptor \*rdp, uint8\_t \*buf, size\_t size)

Reads from a receive descriptor's stream.

uint8\_t \* mac\_lld\_get\_next\_transmit\_buffer (MACTransmitDescriptor \*tdp, size\_t size, size\_t \*sizep)

Returns a pointer to the next transmit buffer in the descriptor chain.

const uint8\_t \* mac\_lld\_get\_next\_receive\_buffer (MACReceiveDescriptor \*rdp, size\_t \*sizep)

Returns a pointer to the next receive buffer in the descriptor chain.

## 9.51.1 Detailed Description

PLATFORM MAC subsystem low level driver header.

### 9.52 mii.h File Reference

MII macros and structures.

## Macros

### Generic MII registers

- #define MII BMCR 0x00
- #define MII\_BMSR 0x01
- #define MII PHYSID1 0x02
- #define MII\_PHYSID2 0x03
- #define MII\_ADVERTISE 0x04
- #define MII LPA 0x05
- #define MII\_EXPANSION 0x06
- #define MII\_ANNPTR 0x07
- #define MII\_CTRL1000 0x09
- #define MII\_STAT1000 0x0a
- #define MII\_ESTATUS 0x0f
- #define MII\_PHYSTS 0x10
- #define MII\_MICR 0x11
- #define MII\_DCOUNTER 0x12

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- #define MII\_FCSCOUNTER 0x13
- #define MII\_NWAYTEST 0x14
- #define MII RERRCOUNTER 0x15
- #define MII SREVISION 0x16
- #define MII RESV1 0x17
- #define MII LBRERROR 0x18
- #define MII PHYADDR 0x19
- #define MII RESV2 0x1a
- #define MII\_TPISTATUS 0x1b
- #define MII NCONFIG 0x1c

### Basic mode control register

- #define BMCR\_RESV 0x007f
- #define BMCR\_CTST 0x0080
- #define BMCR FULLDPLX 0x0100
- #define BMCR ANRESTART 0x0200
- #define BMCR ISOLATE 0x0400
- #define BMCR\_PDOWN 0x0800
- #define BMCR\_ANENABLE 0x1000
- #define BMCR\_SPEED100 0x2000 #define BMCR\_LOOPBACK 0x4000
- #define BMCR\_RESET 0x8000

#### Basic mode status register

- #define BMSR ERCAP 0x0001
- #define BMSR JCD 0x0002
- #define BMSR LSTATUS 0x0004
- #define BMSR ANEGCAPABLE 0x0008
- #define BMSR\_RFAULT 0x0010
- #define BMSR\_ANEGCOMPLETE 0x0020
- #define BMSR MFPRESUPPCAP 0x0040
- #define BMSR\_RESV 0x0780
- #define BMSR\_10HALF 0x0800
- #define BMSR\_10FULL 0x1000
- #define BMSR 100HALF 0x2000 #define BMSR 100FULL 0x4000
- #define BMSR 100BASE4 0x8000

## Advertisement control register

- #define ADVERTISE SLCT 0x001f
- #define ADVERTISE\_CSMA 0x0001
- #define ADVERTISE\_10HALF 0x0020
- #define ADVERTISE\_10FULL 0x0040
- #define ADVERTISE\_100HALF 0x0080

- #define ADVERTISE\_100FULL 0x0100
  #define ADVERTISE\_100BASE4 0x0200
  #define ADVERTISE\_PAUSE\_CAP 0x0400
- #define ADVERTISE\_PAUSE\_ASYM 0x0800
- #define ADVERTISE RESV 0x1000
- #define ADVERTISE RFAULT 0x2000
- #define ADVERTISE LPACK 0x4000
- #define ADVERTISE NPAGE 0x8000
- #define ADVERTISE FULL
- #define ADVERTISE\_ALL

# Link partner ability register

- #define LPA SLCT 0x001f
- #define LPA\_10HALF 0x0020
- #define LPA\_10FULL 0x0040

- #define LPA\_100HALF 0x0080
- #define LPA\_100FULL 0x0100
- #define LPA\_100BASE4 0x0200
- #define LPA\_PAUSE\_CAP 0x0400
- #define LPA PAUSE ASYM 0x0800
- #define LPA RESV 0x1000
- #define LPA RFAULT 0x2000
- #define LPA LPACK 0x4000
- #define LPA\_NPAGE 0x8000
- #define LPA\_DUPLEX (LPA\_10FULL | LPA\_100FULL)
- #define LPA\_100 (LPA\_100FULL | LPA\_100HALF | LPA\_100BASE4)

## **Expansion register for auto-negotiation**

- #define EXPANSION\_NWAY 0x0001
- #define EXPANSION\_LCWP 0x0002
- #define EXPANSION\_ENABLENPAGE 0x0004
- #define EXPANSION\_NPCAPABLE 0x0008
- #define EXPANSION\_MFAULTS 0x0010
- #define EXPANSION\_RESV 0xffe0

#### N-way test register

- #define NWAYTEST\_RESV1 0x00ff
- #define NWAYTEST\_LOOPBACK 0x0100
- #define NWAYTEST\_RESV2 0xfe00

### **PHY** identifiers

- #define MII\_DM9161\_ID 0x0181b8a0
- #define MII\_AM79C875\_ID 0x00225540
- #define MII\_KS8721\_ID 0x00221610
- #define **MII\_STE101P\_ID** 0x00061C50
- #define MII\_DP83848I\_ID 0x20005C90
- #define MII\_LAN8710A\_ID 0x0007C0F1
- #define MII\_LAN8720\_ID 0x0007C0F0
   #define MII\_LAN8742A\_ID 0x0007C130

## 9.52.1 Detailed Description

MII macros and structures.

# 9.53 mmc\_spi.c File Reference

MMC over SPI driver code.

```
#include <string.h>
#include "hal.h"
```

### **Functions**

- static uint8\_t crc7 (uint8\_t crc, const uint8\_t \*buffer, size\_t len)
  - Calculate the MMC standard CRC-7 based on a lookup table.
- static void wait (MMCDriver \*mmcp)

Waits an idle condition.

• static void send\_hdr (MMCDriver \*mmcp, uint8\_t cmd, uint32\_t arg)

Sends a command header.

• static uint8\_t recvr1 (MMCDriver \*mmcp)

Receives a single byte response.

• static uint8\_t recvr3 (MMCDriver \*mmcp, uint8\_t \*buffer)

Receives a three byte response.

• static uint8\_t send\_command\_R1 (MMCDriver \*mmcp, uint8\_t cmd, uint32\_t arg)

Sends a command an returns a single byte response.

static uint8\_t send\_command\_R3 (MMCDriver \*mmcp, uint8\_t cmd, uint32\_t arg, uint8\_t \*response)

Sends a command which returns a five bytes response (R3).

static bool read\_CxD (MMCDriver \*mmcp, uint8\_t cmd, uint32\_t cxd[4])

Reads the CSD.

static void sync (MMCDriver \*mmcp)

Waits that the card reaches an idle state.

void mmcInit (void)

MMC over SPI driver initialization.

void mmcObjectInit (MMCDriver \*mmcp)

Initializes an instance.

void mmcStart (MMCDriver \*mmcp, const MMCConfig \*config)

Configures and activates the MMC peripheral.

void mmcStop (MMCDriver \*mmcp)

Disables the MMC peripheral.

bool mmcConnect (MMCDriver \*mmcp)

Performs the initialization procedure on the inserted card.

bool mmcDisconnect (MMCDriver \*mmcp)

Brings the driver in a state safe for card removal.

bool mmcStartSequentialRead (MMCDriver \*mmcp, uint32\_t startblk)

Starts a sequential read.

• bool mmcSequentialRead (MMCDriver \*mmcp, uint8\_t \*buffer)

Reads a block within a sequential read operation.

bool mmcStopSequentialRead (MMCDriver \*mmcp)

Stops a sequential read gracefully.

• bool mmcStartSequentialWrite (MMCDriver \*mmcp, uint32\_t startblk)

Starts a sequential write.

bool mmcSequentialWrite (MMCDriver \*mmcp, const uint8\_t \*buffer)

Writes a block within a sequential write operation.

• bool mmcStopSequentialWrite (MMCDriver \*mmcp)

Stops a sequential write gracefully.

• bool mmcSync (MMCDriver \*mmcp)

Waits for card idle condition.

• bool mmcGetInfo (MMCDriver \*mmcp, BlockDeviceInfo \*bdip)

Returns the media info.

• bool mmcErase (MMCDriver \*mmcp, uint32 t startblk, uint32 t endblk)

Erases blocks.

## **Variables**

• static const struct MMCDriverVMT mmc\_vmt

Virtual methods table.

static const uint8\_t crc7\_lookup\_table [256]

Lookup table for CRC-7 ( based on polynomial  $x^{\hat{}}7 + x^{\hat{}}3 + 1$ ).

## 9.53.1 Detailed Description

MMC over SPI driver code.

# 9.54 mmc\_spi.h File Reference

MMC over SPI driver header.

### **Data Structures**

• struct MMCConfig

MMC/SD over SPI driver configuration structure.

struct MMCDriverVMT

MMCDriver virtual methods table.

struct MMCDriver

Structure representing a MMC/SD over SPI driver.

### **Macros**

#define \_mmc\_driver\_methods \_mmcsd\_block\_device\_methods
 MMCDriver specific methods.

### MMC\_SPI configuration options

#define MMC\_NICE\_WAITING TRUE
 Delays insertions.

### **Macro Functions**

• #define mmclsCardInserted(mmcp) mmc\_lld\_is\_card\_inserted(mmcp)

Returns the card insertion status.

• #define mmclsWriteProtected(mmcp) mmc\_lld\_is\_write\_protected(mmcp)

Returns the write protect status.

## **Functions**

• void mmcInit (void)

MMC over SPI driver initialization.

void mmcObjectInit (MMCDriver \*mmcp)

Initializes an instance.

• void mmcStart (MMCDriver \*mmcp, const MMCConfig \*config)

Configures and activates the MMC peripheral.

void mmcStop (MMCDriver \*mmcp)

Disables the MMC peripheral.

bool mmcConnect (MMCDriver \*mmcp)

Performs the initialization procedure on the inserted card.

bool mmcDisconnect (MMCDriver \*mmcp)

Brings the driver in a state safe for card removal.

bool mmcStartSequentialRead (MMCDriver \*mmcp, uint32 t startblk)

Starts a sequential read.

bool mmcSequentialRead (MMCDriver \*mmcp, uint8\_t \*buffer)

Reads a block within a sequential read operation.

bool mmcStopSequentialRead (MMCDriver \*mmcp)

Stops a sequential read gracefully.

bool mmcStartSequentialWrite (MMCDriver \*mmcp, uint32\_t startblk)

Starts a sequential write.

bool mmcSequentialWrite (MMCDriver \*mmcp, const uint8\_t \*buffer)

Writes a block within a sequential write operation.

bool mmcStopSequentialWrite (MMCDriver \*mmcp)

Stops a sequential write gracefully.

bool mmcSync (MMCDriver \*mmcp)

Waits for card idle condition.

• bool mmcGetInfo (MMCDriver \*mmcp, BlockDeviceInfo \*bdip)

Returns the media info.

• bool mmcErase (MMCDriver \*mmcp, uint32\_t startblk, uint32\_t endblk)

Erases blocks.

### 9.54.1 Detailed Description

MMC over SPI driver header.

# 9.55 pal.c File Reference

I/O Ports Abstraction Layer code.

```
#include "hal.h"
```

## **Functions**

ioportmask\_t palReadBus (IOBus \*bus)

Read from an I/O bus.

void palWriteBus (IOBus \*bus, ioportmask\_t bits)

Write to an I/O bus.

void palSetBusMode (IOBus \*bus, iomode\_t mode)

Programs a bus with the specified mode.

## 9.55.1 Detailed Description

I/O Ports Abstraction Layer code.

# 9.56 pal.h File Reference

I/O Ports Abstraction Layer macros, types and structures.

```
#include "pal_lld.h"
```

### **Data Structures**

struct IOBus

I/O bus descriptor.

#### **Macros**

#define PAL\_PORT\_BIT(n) ((ioportmask\_t)(1U << (n)))</li>

Port bit helper macro.

#define PAL\_GROUP\_MASK(width) ((ioportmask\_t)(1U << (width)) - 1U)</li>

Bits group mask helper.

• #define \_IOBUS\_DATA(name, port, width, offset) {port, PAL\_GROUP\_MASK(width), offset}

Data part of a static I/O bus initializer.

#define IOBUS\_DECL(name, port, width, offset) IOBus name = \_IOBUS\_DATA(name, port, width, offset)
 Static I/O bus initializer.

#### Pads mode constants

• #define PAL MODE RESET 0U

After reset state.

• #define PAL\_MODE\_UNCONNECTED 1U

Safe state for unconnected pads.

• #define PAL MODE INPUT 2U

Regular input high-Z pad.

• #define PAL MODE INPUT PULLUP 3U

Input pad with weak pull up resistor.

• #define PAL\_MODE\_INPUT\_PULLDOWN 4U

Input pad with weak pull down resistor.

• #define PAL\_MODE\_INPUT\_ANALOG 5U

Analog input mode.

#define PAL\_MODE\_OUTPUT\_PUSHPULL 6U

Push-pull output pad.

• #define PAL\_MODE\_OUTPUT\_OPENDRAIN 7U

Open-drain output pad.

### Logic level constants

• #define PAL\_LOW 0U

Logical low state.

#define PAL HIGH 1U

Logical high state.

### **Macro Functions**

#define pallnit(config) pal\_lld\_init(config)

PAL subsystem initialization.

• #define palReadPort(port) ((void)(port), 0U)

Reads the physical I/O port states.

• #define palReadLatch(port) ((void)(port), 0U)

Reads the output latch.

• #define palWritePort(port, bits) ((void)(port), (void)(bits))

Writes a bits mask on a I/O port.

#define palSetPort(port, bits) palWritePort(port, palReadLatch(port) | (bits))

Sets a bits mask on a I/O port.

#define palClearPort(port, bits) palWritePort(port, palReadLatch(port) & ∼(bits))

Clears a bits mask on a I/O port.

• #define palTogglePort(port, bits) palWritePort(port, palReadLatch(port) ^ (bits))

Toggles a bits mask on a I/O port.

#define palReadGroup(port, mask, offset) ((palReadPort(port) >> (offset)) & (mask))

Reads a group of bits.

#define palWriteGroup(port, mask, offset, bits)

Writes a group of bits.

#define palSetGroupMode(port, mask, offset, mode)

Pads group mode setup.

#define palReadPad(port, pad) ((palReadPort(port) >> (pad)) & 1U)

Reads an input pad logic state.

• #define palWritePad(port, pad, bit)

Writes a logic state on an output pad.

#define palSetPad(port, pad) palSetPort(port, PAL\_PORT\_BIT(pad))

Sets a pad logic state to PAL\_HIGH.

#define palClearPad(port, pad) palClearPort(port, PAL\_PORT\_BIT(pad))

Clears a pad logic state to PAL\_LOW.

#define palTogglePad(port, pad) palTogglePort(port, PAL\_PORT\_BIT(pad))

Toggles a pad logic state.

#define palSetPadMode(port, pad, mode) palSetGroupMode(port, PAL\_PORT\_BIT(pad), 0U, mode)
 Pad mode setup.

#define palReadLine(line) palReadPad(PAL\_PORT(line), PAL\_PAD(line))

Reads an input line logic state.

#define palWriteLine(line, bit) palWritePad(PAL\_PORT(line), PAL\_PAD(line), bit)

Writes a logic state on an output line.

#define palSetLine(line) palSetPad(PAL\_PORT(line), PAL\_PAD(line))

Sets a line logic state to PAL\_HIGH.

• #define palClearLine(line) palClearPad(PAL\_PORT(line), PAL\_PAD(line))

Clears a line logic state to PAL\_LOW.

#define palToggleLine(line) palTogglePad(PAL\_PORT(line), PAL\_PAD(line))

Toggles a line logic state.

• #define palSetLineMode(line, mode) palSetPadMode(PAL\_PORT(line), PAL\_PAD(line), mode)

Line mode setup.

### **Functions**

• ioportmask t palReadBus (IOBus \*bus)

Read from an I/O bus.

void palWriteBus (IOBus \*bus, ioportmask\_t bits)

Write to an I/O bus.

void palSetBusMode (IOBus \*bus, iomode\_t mode)

Programs a bus with the specified mode.

#### 9.56.1 Detailed Description

I/O Ports Abstraction Layer macros, types and structures.

### 9.57 pal\_lld.c File Reference

PLATFORM PAL subsystem low level driver source.

#include "hal.h"

#### **Functions**

void \_pal\_lld\_init (const PALConfig \*config)

STM32 I/O ports configuration.

void \_pal\_lld\_setgroupmode (ioportid\_t port, ioportmask\_t mask, iomode\_t mode)

Pads mode setup.

### 9.57.1 Detailed Description

PLATFORM PAL subsystem low level driver source.

### 9.58 pal\_lld.h File Reference

PLATFORM PAL subsystem low level driver header.

#### **Data Structures**

· struct PALConfig

Generic I/O ports static initializer.

#### **Macros**

• #define IOPORT1 0

First I/O port identifier.

• #define pal\_lld\_init(config) \_pal\_lld\_init(config)

Low level PAL subsystem initialization.

• #define pal\_lld\_readport(port) 0U

Reads the physical I/O port states.

• #define pal\_lld\_readlatch(port) 0U

Reads the output latch.

#define pal\_lld\_writeport(port, bits)

Writes a bits mask on a I/O port.

#define pal\_lld\_setport(port, bits)

Sets a bits mask on a I/O port.

• #define pal\_lld\_clearport(port, bits)

Clears a bits mask on a I/O port.

• #define pal\_lld\_toggleport(port, bits)

Toggles a bits mask on a I/O port.

#define pal\_lld\_readgroup(port, mask, offset) 0U

Reads a group of bits.

• #define pal\_lld\_writegroup(port, mask, offset, bits)

Writes a group of bits.

#define pal\_lld\_setgroupmode(port, mask, offset, mode) \_pal\_lld\_setgroupmode(port, mask << offset, mode)</li>

Pads group mode setup.

#define pal\_lld\_readpad(port, pad) PAL\_LOW

Reads a logical state from an I/O pad.

• #define pal\_lld\_writepad(port, pad, bit)

Writes a logical state on an output pad.

• #define pal\_lld\_setpad(port, pad)

Sets a pad logical state to PAL\_HIGH.

#define pal\_lld\_clearpad(port, pad)

Clears a pad logical state to PAL\_LOW.

• #define pal\_lld\_togglepad(port, pad)

Toggles a pad logical state.

• #define pal\_lld\_setpadmode(port, pad, mode)

Pad mode setup.

#### Port related definitions

#define PAL\_IOPORTS\_WIDTH 16U

Width, in bits, of an I/O port.

#define PAL\_WHOLE\_PORT ((ioportmask\_t)0xFFFFU)

Whole port mask.

### Line handling macros

 $\bullet \ \ \text{\#define PAL\_LINE}(port, pad) \ ((ioline\_t)((uint32\_t)(port)) \ | \ ((uint32\_t)(pad))) \\$ 

Forms a line identifier.

#define PAL\_PORT(line) ((stm32\_gpio\_t \*)(((uint32\_t)(line)) & 0xFFFFFF0U))

Decodes a port identifier from a line identifier.

#define PAL\_PAD(line) ((uint32\_t)(line) & 0x0000000FU))

Decodes a pad identifier from a line identifier.

• #define PAL\_NOLINE 0U

Value identifying an invalid line.

### **Typedefs**

typedef uint32\_t ioportmask\_t

Digital I/O port sized unsigned type.

• typedef uint32\_t iomode\_t

Digital I/O modes.

typedef uint32\_t ioline\_t

Type of an I/O line.

typedef uint32\_t ioportid\_t

Port Identifier.

#### **Functions**

void \_pal\_lld\_init (const PALConfig \*config)

STM32 I/O ports configuration.

• void \_pal\_lld\_setgroupmode (ioportid\_t port, ioportmask\_t mask, iomode\_t mode)

Pads mode setup.

### 9.58.1 Detailed Description

PLATFORM PAL subsystem low level driver header.

### 9.59 pwm.c File Reference

#### PWM Driver code.

```
#include "hal.h"
```

### **Functions**

void pwmInit (void)

PWM Driver initialization.

void pwmObjectInit (PWMDriver \*pwmp)

Initializes the standard part of a PWMDriver structure.

void pwmStart (PWMDriver \*pwmp, const PWMConfig \*config)

Configures and activates the PWM peripheral.

void pwmStop (PWMDriver \*pwmp)

Deactivates the PWM peripheral.

void pwmChangePeriod (PWMDriver \*pwmp, pwmcnt\_t period)

Changes the period the PWM peripheral.

void pwmEnableChannel (PWMDriver \*pwmp, pwmchannel\_t channel, pwmcnt\_t width)

Enables a PWM channel.

void pwmDisableChannel (PWMDriver \*pwmp, pwmchannel\_t channel)

Disables a PWM channel and its notification.

void pwmEnablePeriodicNotification (PWMDriver \*pwmp)

Enables the periodic activation edge notification.

void pwmDisablePeriodicNotification (PWMDriver \*pwmp)

Disables the periodic activation edge notification.

 $\bullet \ \ void \ pwmEnableChannelNotification \ (PWMDriver \ *pwmp, \ pwmchannel\_t \ channel)$ 

Enables a channel de-activation edge notification.

• void pwmDisableChannelNotification (PWMDriver \*pwmp, pwmchannel\_t channel)

Disables a channel de-activation edge notification.

### 9.59.1 Detailed Description

PWM Driver code.

### 9.60 pwm.h File Reference

PWM Driver macros and structures.

```
#include "pwm_lld.h"
```

### **Macros**

### PWM output mode macros

• #define PWM OUTPUT MASK 0x0FU

Standard output modes mask.

• #define PWM\_OUTPUT\_DISABLED 0x00U

Output not driven, callback only.

#define PWM\_OUTPUT\_ACTIVE\_HIGH 0x01U

Positive PWM logic, active is logic level one.

#define PWM OUTPUT ACTIVE LOW 0x02U

Inverse PWM logic, active is logic level zero.

#### PWM duty cycle conversion

• #define PWM\_FRACTION\_TO\_WIDTH(pwmp, denominator, numerator)

Converts from fraction to pulse width.

#define PWM\_DEGREES\_TO\_WIDTH(pwmp, degrees) PWM\_FRACTION\_TO\_WIDTH(pwmp, 36000, degrees)

Converts from degrees to pulse width.

#define PWM\_PERCENTAGE\_TO\_WIDTH(pwmp, percentage) PWM\_FRACTION\_TO\_WIDTH(pwmp, 10000, percentage)

Converts from percentage to pulse width.

#### **Macro Functions**

• #define pwmChangePeriodI(pwmp, value)

Changes the period the PWM peripheral.

#define pwmEnableChannell(pwmp, channel, width)

Enables a PWM channel.

• #define pwmDisableChannell(pwmp, channel)

Disables a PWM channel.

#define pwmlsChannelEnabledI(pwmp, channel) (((pwmp)->enabled & ((pwmchnmsk\_t)1U << (pwmchnmsk\_t)(channel))) != 0U)</li>

Returns a PWM channel status.

• #define pwmEnablePeriodicNotificationI(pwmp) pwm\_lld\_enable\_periodic\_notification(pwmp)

Enables the periodic activation edge notification.

#define pwmDisablePeriodicNotificationI(pwmp) pwm\_lld\_disable\_periodic\_notification(pwmp)

Disables the periodic activation edge notification.

#define pwmEnableChannelNotificationI(pwmp, channel) pwm\_lld\_enable\_channel\_notification(pwmp, channel)

Enables a channel de-activation edge notification.

#define pwmDisableChannelNotificationI(pwmp, channel) pwm\_lld\_disable\_channel\_notification(pwmp, channel)

Disables a channel de-activation edge notification.

#### **Typedefs**

typedef struct PWMDriver PWMDriver

Type of a structure representing a PWM driver.

typedef void(\* pwmcallback\_t) (PWMDriver \*pwmp)

Type of a PWM notification callback.

### **Enumerations**

### **Functions**

void pwmInit (void)

PWM Driver initialization.

void pwmObjectInit (PWMDriver \*pwmp)

Initializes the standard part of a PWMDriver structure.

void pwmStart (PWMDriver \*pwmp, const PWMConfig \*config)

Configures and activates the PWM peripheral.

void pwmStop (PWMDriver \*pwmp)

Deactivates the PWM peripheral.

void pwmChangePeriod (PWMDriver \*pwmp, pwmcnt\_t period)

Changes the period the PWM peripheral.

• void pwmEnableChannel (PWMDriver \*pwmp, pwmchannel\_t channel, pwmcnt\_t width)

Enables a PWM channel.

void pwmDisableChannel (PWMDriver \*pwmp, pwmchannel t channel)

Disables a PWM channel and its notification.

void pwmEnablePeriodicNotification (PWMDriver \*pwmp)

Enables the periodic activation edge notification.

void pwmDisablePeriodicNotification (PWMDriver \*pwmp)

Disables the periodic activation edge notification.

void pwmEnableChannelNotification (PWMDriver \*pwmp, pwmchannel\_t channel)

Enables a channel de-activation edge notification.

• void pwmDisableChannelNotification (PWMDriver \*pwmp, pwmchannel\_t channel)

Disables a channel de-activation edge notification.

### 9.60.1 Detailed Description

PWM Driver macros and structures.

### 9.61 pwm\_Ild.c File Reference

PLATFORM PWM subsystem low level driver source.

```
#include "hal.h"
```

#### **Functions**

void pwm\_lld\_init (void)

Low level PWM driver initialization.

void pwm\_lld\_start (PWMDriver \*pwmp)

Configures and activates the PWM peripheral.

void pwm Ild stop (PWMDriver \*pwmp)

Deactivates the PWM peripheral.

void pwm\_lld\_enable\_channel (PWMDriver \*pwmp, pwmchannel\_t channel, pwmcnt\_t width)

Enables a PWM channel.

• void pwm\_lld\_disable\_channel (PWMDriver \*pwmp, pwmchannel\_t channel)

Disables a PWM channel and its notification.

void pwm IId enable periodic notification (PWMDriver \*pwmp)

Enables the periodic activation edge notification.

void pwm\_lld\_disable\_periodic\_notification (PWMDriver \*pwmp)

Disables the periodic activation edge notification.

• void pwm\_lld\_enable\_channel\_notification (PWMDriver \*pwmp, pwmchannel\_t channel)

Enables a channel de-activation edge notification.

• void pwm Ild disable channel notification (PWMDriver \*pwmp, pwmchannel t channel)

Disables a channel de-activation edge notification.

#### **Variables**

• PWMDriver PWMD1

PWMD1 driver identifier.

### 9.61.1 Detailed Description

PLATFORM PWM subsystem low level driver source.

### 9.62 pwm\_lld.h File Reference

PLATFORM PWM subsystem low level driver header.

#### **Data Structures**

· struct PWMChannelConfig

Type of a PWM driver channel configuration structure.

struct PWMConfig

Type of a PWM driver configuration structure.

struct PWMDriver

Structure representing a PWM driver.

### **Macros**

• #define PWM\_CHANNELS 4

Number of PWM channels per PWM driver.

#define pwm\_lld\_change\_period(pwmp, period)

Changes the period the PWM peripheral.

### **PLATFORM** configuration options

#define PLATFORM\_PWM\_USE\_PWM1 FALSE
 PWMD1 driver enable switch.

### **Typedefs**

• typedef uint32\_t pwmmode\_t

Type of a PWM mode.

• typedef uint8\_t pwmchannel\_t

Type of a PWM channel.

typedef uint32\_t pwmchnmsk\_t

Type of a channels mask.

typedef uint32\_t pwmcnt\_t

Type of a PWM counter.

#### **Functions**

void pwm lld init (void)

Low level PWM driver initialization.

void pwm\_lld\_start (PWMDriver \*pwmp)

Configures and activates the PWM peripheral.

void pwm\_lld\_stop (PWMDriver \*pwmp)

Deactivates the PWM peripheral.

void pwm Ild enable channel (PWMDriver \*pwmp, pwmchannel t channel, pwmcnt t width)

Enables a PWM channel.

void pwm\_lld\_disable\_channel (PWMDriver \*pwmp, pwmchannel\_t channel)

Disables a PWM channel and its notification.

void pwm\_lld\_enable\_periodic\_notification (PWMDriver \*pwmp)

Enables the periodic activation edge notification.

void pwm\_lld\_disable\_periodic\_notification (PWMDriver \*pwmp)

Disables the periodic activation edge notification.

• void pwm Ild enable channel notification (PWMDriver \*pwmp, pwmchannel t channel)

Enables a channel de-activation edge notification.

void pwm IId disable channel notification (PWMDriver \*pwmp, pwmchannel t channel)

Disables a channel de-activation edge notification.

### 9.62.1 Detailed Description

PLATFORM PWM subsystem low level driver header.

### 9.63 rtc.c File Reference

RTC Driver code.

```
#include "hal.h"
```

### **Functions**

· void rtcInit (void)

RTC Driver initialization.

void rtcObjectInit (RTCDriver \*rtcp)

Initializes a generic RTC driver object.

void rtcSetTime (RTCDriver \*rtcp, const RTCDateTime \*timespec)

Set current time.

void rtcGetTime (RTCDriver \*rtcp, RTCDateTime \*timespec)

Get current time.

• void rtcSetAlarm (RTCDriver \*rtcp, rtcalarm\_t alarm, const RTCAlarm \*alarmspec)

Set alarm time.

• void rtcGetAlarm (RTCDriver \*rtcp, rtcalarm\_t alarm, RTCAlarm \*alarmspec)

Get current alarm.

void rtcSetCallback (RTCDriver \*rtcp, rtccb\_t callback)

Enables or disables RTC callbacks.

void rtcConvertDateTimeToStructTm (const RTCDateTime \*timespec, struct tm \*timp, uint32\_t \*tv\_msec)

Convert RTCDateTime to broken-down time structure.

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• void rtcConvertStructTmToDateTime (const struct tm \*timp, uint32\_t tv\_msec, RTCDateTime \*timespec)

Convert broken-down time structure to RTCDateTime.

uint32 t rtcConvertDateTimeToFAT (const RTCDateTime \*timespec)

Get current time in format suitable for usage in FAT file system.

### 9.63.1 Detailed Description

RTC Driver code.

### 9.64 rtc.h File Reference

#### RTC Driver macros and structures.

```
#include <time.h>
#include "rtc_lld.h"
```

### **Data Structures**

struct RTCDateTime

Type of a structure representing an RTC date/time stamp.

#### **Macros**

#define RTC BASE YEAR 1980U

Base year of the calendar.

#### Date/Time bit masks for FAT format

- #define RTC\_FAT\_TIME\_SECONDS\_MASK 0x0000001FU
- #define RTC FAT TIME MINUTES MASK 0x000007E0U
- #define RTC\_FAT\_TIME\_HOURS\_MASK 0x0000F800U
- #define RTC FAT DATE DAYS MASK 0x001F0000U
- #define RTC\_FAT\_DATE\_MONTHS\_MASK 0x01E00000U
- #define RTC\_FAT\_DATE\_YEARS\_MASK 0xFE000000U

### Day of week encoding

- #define RTC DAY\_CATURDAY 0U
- #define RTC\_DAY\_MONDAY 1U
- #define RTC\_DAY\_TUESDAY 2U
- #define RTC\_DAY\_WEDNESDAY 3U
- #define RTC\_DAY\_THURSDAY 4U
- #define RTC\_DAY\_FRIDAY 5U
- #define RTC\_DAY\_SATURDAY 6U
- #define RTC\_DAY\_SUNDAY 7U

### **Typedefs**

• typedef struct RTCDriver RTCDriver

Type of a structure representing an RTC driver.

#### **Functions**

· void rtcInit (void)

RTC Driver initialization.

void rtcObjectInit (RTCDriver \*rtcp)

Initializes a generic RTC driver object.

void rtcSetTime (RTCDriver \*rtcp, const RTCDateTime \*timespec)

Set current time.

void rtcGetTime (RTCDriver \*rtcp, RTCDateTime \*timespec)

Get current time.

void rtcSetCallback (RTCDriver \*rtcp, rtccb\_t callback)

Enables or disables RTC callbacks.

• void rtcConvertDateTimeToStructTm (const RTCDateTime \*timespec, struct tm \*timp, uint32\_t \*tv\_msec)

Convert RTCDateTime to broken-down time structure.

void rtcConvertStructTmToDateTime (const struct tm \*timp, uint32 t tv msec, RTCDateTime \*timespec)

Convert broken-down time structure to RTCDateTime.

uint32\_t rtcConvertDateTimeToFAT (const RTCDateTime \*timespec)

Get current time in format suitable for usage in FAT file system.

### 9.64.1 Detailed Description

RTC Driver macros and structures.

### 9.65 rtc Ild.c File Reference

PLATFORM RTC subsystem low level driver source.

```
#include "hal.h"
```

### **Functions**

void rtc\_lld\_init (void)

Enable access to registers.

• void rtc\_lld\_set\_time (RTCDriver \*rtcp, const RTCDateTime \*timespec)

Set current time.

void rtc\_lld\_get\_time (RTCDriver \*rtcp, RTCDateTime \*timespec)

Get current time.

• void rtc\_lld\_set\_alarm (RTCDriver \*rtcp, rtcalarm\_t alarm, const RTCAlarm \*alarmspec)

Set alarm time

• void rtc lld get alarm (RTCDriver \*rtcp, rtcalarm t alarm, RTCAlarm \*alarmspec)

Get alarm time.

#### **Variables**

• RTCDriver RTCD1

RTC driver identifier.

#### 9.65.1 Detailed Description

PLATFORM RTC subsystem low level driver source.

### 9.66 rtc\_lld.h File Reference

PLATFORM RTC subsystem low level driver header.

#### **Data Structures**

struct RTCAlarm

Type of a structure representing an RTC alarm time stamp.

struct RTCDriverVMT

RTCDriver virtual methods table.

struct RTCDriver

Structure representing an RTC driver.

#### **Macros**

#define \_rtc\_driver\_methods \_file\_stream\_methods
 FileStream specific methods.

#### Implementation capabilities

- #define RTC SUPPORTS CALLBACKS TRUE
  - Callback support int the driver.
- #define RTC\_ALARMS 2

Number of alarms available.

• #define RTC\_HAS\_STORAGE FALSE

Presence of a local persistent storage.

### **PLATFORM** configuration options

• #define PLATFORM\_RTC\_USE\_RTC1 FALSE RTCD1 driver enable switch.

### **Typedefs**

• typedef uint32\_t rtcalarm\_t

Type of an RTC alarm number.

typedef void(\* rtccb\_t) (RTCDriver \*rtcp, rtcevent\_t event)

Type of a generic RTC callback.

#### **Enumerations**

#### **Functions**

• void rtc\_lld\_init (void)

Enable access to registers.

void rtc\_lld\_set\_time (RTCDriver \*rtcp, const RTCDateTime \*timespec)

Set current time.

• void rtc\_lld\_get\_time (RTCDriver \*rtcp, RTCDateTime \*timespec)

Get current time.

• void rtc\_lld\_set\_alarm (RTCDriver \*rtcp, rtcalarm\_t alarm, const RTCAlarm \*alarmspec)

Set alarm time.

void rtc\_lld\_get\_alarm (RTCDriver \*rtcp, rtcalarm\_t alarm, RTCAlarm \*alarmspec)

Get alarm time.

### 9.66.1 Detailed Description

PLATFORM RTC subsystem low level driver header.

#### 9.67 sdc.c File Reference

```
SDC Driver code.
```

```
#include <string.h>
#include "hal.h"
```

#### **Enumerations**

#### **Functions**

static bool mode\_detect (SDCDriver \*sdcp)

Detects card mode.

static bool mmc init (SDCDriver \*sdcp)

Init procedure for MMC.

static bool sdc\_init (SDCDriver \*sdcp)

Init procedure for SDC.

- static uint32\_t mmc\_cmd6\_construct (mmc\_switch\_t access, uint32\_t idx, uint32\_t value, uint32\_t cmd\_set)

  Constructs CMD6 argument for MMC.
- static uint32\_t sdc\_cmd6\_construct (sd\_switch\_t mode, sd\_switch\_function\_t function, uint32\_t value)

Constructs CMD6 argument for SDC.

• static uint16 t sdc cmd6 extract info (sd switch function t function, const uint8 t \*buf)

Extracts information from CMD6 answer.

static bool sdc\_cmd6\_check\_status (sd\_switch\_function\_t function, const uint8\_t \*buf)

Checks status after switching using CMD6.

static bool sdc detect bus clk (SDCDriver \*sdcp, sdcbusclk t \*clk)

Reads supported bus clock and switch SDC to appropriate mode.

static bool mmc\_detect\_bus\_clk (SDCDriver \*sdcp, sdcbusclk\_t \*clk)

Reads supported bus clock and switch MMC to appropriate mode.

static bool detect\_bus\_clk (SDCDriver \*sdcp, sdcbusclk\_t \*clk)

Reads supported bus clock and switch card to appropriate mode.

static bool sdc\_set\_bus\_width (SDCDriver \*sdcp)

Sets bus width for SDC.

static bool mmc\_set\_bus\_width (SDCDriver \*sdcp)

Sets bus width for MMC.

• bool sdc wait for transfer state (SDCDriver \*sdcp)

Wait for the card to complete pending operations.

· void sdcInit (void)

SDC Driver initialization.

void sdcObjectInit (SDCDriver \*sdcp)

Initializes the standard part of a SDCDriver structure.

void sdcStart (SDCDriver \*sdcp, const SDCConfig \*config)

Configures and activates the SDC peripheral.

void sdcStop (SDCDriver \*sdcp)

Deactivates the SDC peripheral.

• bool sdcConnect (SDCDriver \*sdcp)

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Performs the initialization procedure on the inserted card.

bool sdcDisconnect (SDCDriver \*sdcp)

Brings the driver in a state safe for card removal.

• bool sdcRead (SDCDriver \*sdcp, uint32 t startblk, uint8 t \*buf, uint32 t n)

Reads one or more blocks.

bool sdcWrite (SDCDriver \*sdcp, uint32 t startblk, const uint8 t \*buf, uint32 t n)

Writes one or more blocks.

sdcflags\_t sdcGetAndClearErrors (SDCDriver \*sdcp)

Returns the errors mask associated to the previous operation.

bool sdcSync (SDCDriver \*sdcp)

Waits for card idle condition.

• bool sdcGetInfo (SDCDriver \*sdcp, BlockDeviceInfo \*bdip)

Returns the media info.

• bool sdcErase (SDCDriver \*sdcp, uint32\_t startblk, uint32\_t endblk)

Erases the supplied blocks.

### **Variables**

static const struct SDCDriverVMT sdc vmt

Virtual methods table.

### 9.67.1 Detailed Description

SDC Driver code.

### sdc.h File Reference

SDC Driver macros and structures.

```
#include "sdc lld.h"
```

#### **Macros**

### SD card types

- #define **SDC\_MODE\_CARDTYPE\_MASK** 0xFU
- #define SDC\_MODE\_CARDTYPE\_SDV11 0U
- #define **SDC\_MODE\_CARDTYPE\_SDV20** 1U
- #define SDC\_MODE\_CARDTYPE\_MMC 2U
- #define SDC MODE HIGH CAPACITY 0x10U

#### SDC bus error conditions

- #define SDC NO ERROR 0U
- #define SDC\_CMD\_CRC\_ERROR 1U
- #define SDC\_DATA\_CRC\_ERROR 2U
- #define SDC\_DATA\_TIMEOUT 4U
- #define SDC\_COMMAND\_TIMEOUT 8U
- #define SDC\_TX\_UNDERRUN 16U
- #define **SDC\_RX\_OVERRUN** 32U
- #define SDC\_STARTBIT\_ERROR 64U
  #define SDC\_OVERFLOW\_ERROR 128U
- #define SDC\_UNHANDLED\_ERROR 0xFFFFFFFU

#### **SDC** configuration options

• #define SDC INIT RETRY 100

Number of initialization attempts before rejecting the card.

#define SDC\_MMC\_SUPPORT FALSE

Include support for MMC cards.

#define SDC\_NICE\_WAITING TRUE

Delays insertions.

#define SDC\_INIT\_OCR\_V20 0x50FF8000U

OCR initialization constant for V20 cards.

• #define SDC\_INIT\_OCR 0x80100000U

OCR initialization constant for non-V20 cards.

#### **Macro Functions**

• #define sdclsCardInserted(sdcp) (sdc lld is card inserted(sdcp))

Returns the card insertion status.

#define sdcIsWriteProtected(sdcp) (sdc\_Ild\_is\_write\_protected(sdcp))

Returns the write protect status

#### **Enumerations**

#### **Functions**

· void sdcInit (void)

SDC Driver initialization.

void sdcObjectInit (SDCDriver \*sdcp)

Initializes the standard part of a SDCDriver structure.

void sdcStart (SDCDriver \*sdcp, const SDCConfig \*config)

Configures and activates the SDC peripheral.

void sdcStop (SDCDriver \*sdcp)

Deactivates the SDC peripheral.

bool sdcConnect (SDCDriver \*sdcp)

Performs the initialization procedure on the inserted card.

bool sdcDisconnect (SDCDriver \*sdcp)

Brings the driver in a state safe for card removal.

• bool sdcRead (SDCDriver \*sdcp, uint32\_t startblk, uint8\_t \*buf, uint32\_t n)

Reads one or more blocks.

bool sdcWrite (SDCDriver \*sdcp, uint32\_t startblk, const uint8\_t \*buf, uint32\_t n)

Writes one or more blocks.

sdcflags t sdcGetAndClearErrors (SDCDriver \*sdcp)

Returns the errors mask associated to the previous operation.

bool sdcSync (SDCDriver \*sdcp)

Waits for card idle condition.

• bool sdcGetInfo (SDCDriver \*sdcp, BlockDeviceInfo \*bdip)

Returns the media info.

bool sdcErase (SDCDriver \*sdcp, uint32\_t startblk, uint32\_t endblk)

Erases the supplied blocks.

• bool \_sdc\_wait\_for\_transfer\_state (SDCDriver \*sdcp)

Wait for the card to complete pending operations.

#### 9.68.1 Detailed Description

SDC Driver macros and structures.

### 9.69 sdc Ild.c File Reference

PLATFORM SDC subsystem low level driver source.

```
#include "hal.h"
```

### **Functions**

void sdc Ild init (void)

Low level SDC driver initialization.

void sdc\_lld\_start (SDCDriver \*sdcp)

Configures and activates the SDC peripheral.

void sdc\_lld\_stop (SDCDriver \*sdcp)

Deactivates the SDC peripheral.

• void sdc\_lld\_start\_clk (SDCDriver \*sdcp)

Starts the SDIO clock and sets it to init mode (400kHz or less).

void sdc\_lld\_set\_data\_clk (SDCDriver \*sdcp, sdcbusclk\_t clk)

Sets the SDIO clock to data mode (25MHz or less).

void sdc\_lld\_stop\_clk (SDCDriver \*sdcp)

Stops the SDIO clock.

void sdc\_lld\_set\_bus\_mode (SDCDriver \*sdcp, sdcbusmode\_t mode)

Switches the bus to 4 bits mode.

void sdc\_lld\_send\_cmd\_none (SDCDriver \*sdcp, uint8\_t cmd, uint32\_t arg)

Sends an SDIO command with no response expected.

bool sdc\_lld\_send\_cmd\_short (SDCDriver \*sdcp, uint8\_t cmd, uint32\_t arg, uint32\_t \*resp)

Sends an SDIO command with a short response expected.

• bool sdc\_lld\_send\_cmd\_short\_crc (SDCDriver \*sdcp, uint8\_t cmd, uint32\_t arg, uint32\_t \*resp)

Sends an SDIO command with a short response expected and CRC.

bool sdc\_lld\_send\_cmd\_long\_crc (SDCDriver \*sdcp, uint8\_t cmd, uint32\_t arg, uint32\_t \*resp)

Sends an SDIO command with a long response expected and CRC.

• bool sdc\_lld\_read (SDCDriver \*sdcp, uint32\_t startblk, uint8\_t \*buf, uint32\_t n)

Reads one or more blocks.

• bool sdc\_lld\_write (SDCDriver \*sdcp, uint32\_t startblk, const uint8\_t \*buf, uint32\_t n)

Writes one or more blocks.

bool sdc\_lld\_sync (SDCDriver \*sdcp)

Waits for card idle condition.

### **Variables**

SDCDriver SDCD1

SDCD1 driver identifier.

### 9.69.1 Detailed Description

PLATFORM SDC subsystem low level driver source.

### 9.70 sdc\_lld.h File Reference

PLATFORM SDC subsystem low level driver header.

#### **Data Structures**

struct SDCConfig

Driver configuration structure.

struct SDCDriverVMT

SDCDriver virtual methods table.

struct SDCDriver

Structure representing an SDC driver.

#### **Macros**

#define \_sdc\_driver\_methods \_mmcsd\_block\_device\_methods
 SDCDriver specific methods.

### **PLATFORM** configuration options

#define PLATFORM\_SDC\_USE\_SDC1 FALSE
 PWMD1 driver enable switch.

### **Typedefs**

• typedef uint32\_t sdcmode\_t

Type of card flags.

typedef uint32\_t sdcflags\_t

SDC Driver condition flags type.

typedef struct SDCDriver SDCDriver

Type of a structure representing an SDC driver.

### **Functions**

void sdc Ild init (void)

Low level SDC driver initialization.

void sdc\_lld\_start (SDCDriver \*sdcp)

Configures and activates the SDC peripheral.

void sdc\_lld\_stop (SDCDriver \*sdcp)

Deactivates the SDC peripheral.

void sdc\_lld\_start\_clk (SDCDriver \*sdcp)

Starts the SDIO clock and sets it to init mode (400kHz or less).

void sdc\_lld\_set\_data\_clk (SDCDriver \*sdcp, sdcbusclk\_t clk)

Sets the SDIO clock to data mode (25MHz or less).

void sdc\_lld\_stop\_clk (SDCDriver \*sdcp)

Stops the SDIO clock.

• void sdc\_lld\_set\_bus\_mode (SDCDriver \*sdcp, sdcbusmode\_t mode)

Switches the bus to 4 bits mode.

void sdc Ild send cmd none (SDCDriver \*sdcp, uint8 t cmd, uint32 t arg)

Sends an SDIO command with no response expected.

bool sdc\_lld\_send\_cmd\_short (SDCDriver \*sdcp, uint8\_t cmd, uint32\_t arg, uint32\_t \*resp)

Sends an SDIO command with a short response expected.

bool sdc Ild send cmd short crc (SDCDriver \*sdcp, uint8 t cmd, uint32 t arg, uint32 t \*resp)

Sends an SDIO command with a short response expected and CRC.

bool sdc\_lld\_send\_cmd\_long\_crc (SDCDriver \*sdcp, uint8\_t cmd, uint32\_t arg, uint32\_t \*resp)

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Sends an SDIO command with a long response expected and CRC.

• bool sdc\_lld\_read (SDCDriver \*sdcp, uint32\_t startblk, uint8\_t \*buf, uint32\_t n)

Reads one or more blocks.

• bool sdc\_lld\_write (SDCDriver \*sdcp, uint32\_t startblk, const uint8\_t \*buf, uint32\_t n)

Writes one or more blocks.

bool sdc\_lld\_sync (SDCDriver \*sdcp)

Waits for card idle condition.

### 9.70.1 Detailed Description

PLATFORM SDC subsystem low level driver header.

### 9.71 serial.c File Reference

```
Serial Driver code.
```

```
#include "hal.h"
```

#### **Functions**

· void sdlnit (void)

Serial Driver initialization.

void sdObjectInit (SerialDriver \*sdp, qnotify\_t inotify, qnotify\_t onotify)

Initializes a generic full duplex driver object.

void sdStart (SerialDriver \*sdp, const SerialConfig \*config)

Configures and starts the driver.

void sdStop (SerialDriver \*sdp)

Stops the driver.

void sdlncomingDatal (SerialDriver \*sdp, uint8\_t b)

Handles incoming data.

msg\_t sdRequestDatal (SerialDriver \*sdp)

Handles outgoing data.

bool sdPutWouldBlock (SerialDriver \*sdp)

Direct output check on a SerialDriver.

bool sdGetWouldBlock (SerialDriver \*sdp)

Direct input check on a SerialDriver.

#### 9.71.1 Detailed Description

Serial Driver code.

### 9.72 serial.h File Reference

Serial Driver macros and structures.

```
#include "serial_lld.h"
```

#### **Data Structures**

struct SerialDriverVMT

SerialDriver virtual methods table.

struct SerialDriver

Full duplex serial driver class.

#### **Macros**

 $\bullet \ \ \hbox{\tt \#define \_serial\_driver\_methods \_base\_asynchronous\_channel\_methods}$ 

SerialDriver specific methods.

### Serial status flags

#define SD\_PARITY\_ERROR (eventflags\_t)32

Parity

• #define SD\_FRAMING\_ERROR (eventflags\_t)64

Framing.

• #define SD OVERRUN ERROR (eventflags t)128

Overflow.

#define SD\_NOISE\_ERROR (eventflags\_t)256

Line noise.

#define SD\_BREAK\_DETECTED (eventflags\_t)512

LIN Break.

#### Serial configuration options

• #define SERIAL\_DEFAULT\_BITRATE 38400

Default bit rate.

• #define SERIAL\_BUFFERS\_SIZE 16

Serial buffers size.

#### **Macro Functions**

#define sdPut(sdp, b) oqPut(&(sdp)->oqueue, b)

Direct write to a SerialDriver.

• #define sdPutTimeout(sdp, b, t) oqPutTimeout(&(sdp)->oqueue, b, t)

Direct write to a SerialDriver with timeout specification.

#define sdGet(sdp) iqGet(&(sdp)->iqueue)

Direct read from a SerialDriver.

 $\hbox{ * \#define sdGetTimeout(sdp, t) iqGetTimeout(\&(sdp)->iqueue, t) }\\$ 

Direct read from a SerialDriver with timeout specification.

#define sdWrite(sdp, b, n) oqWriteTimeout(&(sdp)->oqueue, b, n, TIME\_INFINITE)

Direct blocking write to a SerialDriver.

• #define sdWriteTimeout(sdp, b, n, t) oqWriteTimeout(&(sdp)->oqueue, b, n, t)

Direct blocking write to a SerialDriver with timeout specification.

 $\bullet \ \ \, \text{\#define sdAsynchronousWrite(sdp, b, n) oqWriteTimeout(\&(sdp)->oqueue, b, n, TIME\_IMMEDIATE)}\\$ 

• #define sdRead(sdp, b, n) iqReadTimeout(&(sdp)->iqueue, b, n, TIME\_INFINITE)

Direct blocking read from a SerialDriver.

Direct non-blocking write to a SerialDriver.

#define sdReadTimeout(sdp, b, n, t) iqReadTimeout(&(sdp)->iqueue, b, n, t)

Direct blocking read from a SerialDriver with timeout specification.

#define sdAsynchronousRead(sdp, b, n) igReadTimeout(&(sdp)->igueue, b, n, TIME IMMEDIATE)

Direct non-blocking read from a SerialDriver.

### **Typedefs**

typedef struct SerialDriver SerialDriver

Structure representing a serial driver.

### **Enumerations**

#### **Functions**

void sdlnit (void)

Serial Driver initialization.

void sdObjectInit (SerialDriver \*sdp, qnotify\_t inotify, qnotify\_t onotify)

Initializes a generic full duplex driver object.

void sdStart (SerialDriver \*sdp, const SerialConfig \*config)

Configures and starts the driver.

void sdStop (SerialDriver \*sdp)

Stops the driver.

void sdlncomingDatal (SerialDriver \*sdp, uint8\_t b)

Handles incoming data.

msg\_t sdRequestDatal (SerialDriver \*sdp)

Handles outgoing data.

bool sdPutWouldBlock (SerialDriver \*sdp)

Direct output check on a SerialDriver.

• bool sdGetWouldBlock (SerialDriver \*sdp)

Direct input check on a SerialDriver.

### 9.72.1 Detailed Description

Serial Driver macros and structures.

### 9.73 serial\_lld.c File Reference

PLATFORM serial subsystem low level driver source.

```
#include "hal.h"
```

#### **Functions**

void sd\_lld\_init (void)

Low level serial driver initialization.

void sd\_lld\_start (SerialDriver \*sdp, const SerialConfig \*config)

Low level serial driver configuration and (re)start.

void sd\_lld\_stop (SerialDriver \*sdp)

Low level serial driver stop.

#### **Variables**

SerialDriver SD1

USART1 serial driver identifier.

· static const SerialConfig default\_config

Driver default configuration.

### 9.73.1 Detailed Description

PLATFORM serial subsystem low level driver source.

### 9.74 serial\_lld.h File Reference

PLATFORM serial subsystem low level driver header.

#### **Data Structures**

· struct SerialConfig

PLATFORM Serial Driver configuration structure.

#### **Macros**

• #define \_serial\_driver\_data

SerialDriver specific data.

### **PLATFORM** configuration options

#define PLATFORM\_SERIAL\_USE\_USART1 FALSE
 USART1 driver enable switch.

#### **Functions**

• void sd\_lld\_init (void)

Low level serial driver initialization.

• void sd\_lld\_start (SerialDriver \*sdp, const SerialConfig \*config)

Low level serial driver configuration and (re)start.

void sd\_lld\_stop (SerialDriver \*sdp)

Low level serial driver stop.

### 9.74.1 Detailed Description

PLATFORM serial subsystem low level driver header.

### 9.75 serial\_usb.c File Reference

Serial over USB Driver code.

```
#include "hal.h"
```

### **Functions**

static void ibnotify (io\_buffers\_queue\_t \*bqp)

Notification of empty buffer released into the input buffers queue.

static void obnotify (io\_buffers\_queue\_t \*bqp)

Notification of filled buffer inserted into the output buffers queue.

· void sdulnit (void)

Serial Driver initialization.

void sduObjectInit (SerialUSBDriver \*sdup)

Initializes a generic full duplex driver object.

void sduStart (SerialUSBDriver \*sdup, const SerialUSBConfig \*config)

Configures and starts the driver.

void sduStop (SerialUSBDriver \*sdup)

Stops the driver.

void sduDisconnectl (SerialUSBDriver \*sdup)

USB device disconnection handler.

void sduConfigureHookI (SerialUSBDriver \*sdup)

USB device configured handler.

bool sduRequestsHook (USBDriver \*usbp)

Default requests hook.

void sduSOFHookl (SerialUSBDriver \*sdup)

SOF handler.

void sduDataTransmitted (USBDriver \*usbp, usbep t ep)

Default data transmitted callback.

void sduDataReceived (USBDriver \*usbp, usbep\_t ep)

Default data received callback.

void sduInterruptTransmitted (USBDriver \*usbp, usbep\_t ep)

Default data received callback.

### 9.75.1 Detailed Description

Serial over USB Driver code.

### 9.76 serial usb.h File Reference

Serial over USB Driver macros and structures.

```
#include "usb_cdc.h"
```

#### **Data Structures**

· struct SerialUSBConfig

Serial over USB Driver configuration structure.

struct SerialUSBDriverVMT

SerialDriver virtual methods table.

struct SerialUSBDriver

Full duplex serial driver class.

### Macros

• #define \_serial\_usb\_driver\_data

SerialDriver specific data.

• #define \_serial\_usb\_driver\_methods \_base\_asynchronous\_channel\_methods

SerialUSBDriver specific methods.

### SERIAL\_USB configuration options

• #define SERIAL USB BUFFERS SIZE 256

Serial over USB buffers size.

• #define SERIAL\_USB\_BUFFERS\_NUMBER 2

Serial over USB number of buffers.

### **Typedefs**

· typedef struct SerialUSBDriver SerialUSBDriver

Structure representing a serial over USB driver.

#### **Enumerations**

#### **Functions**

• void sdulnit (void)

Serial Driver initialization.

void sduObjectInit (SerialUSBDriver \*sdup)

Initializes a generic full duplex driver object.

void sduStart (SerialUSBDriver \*sdup, const SerialUSBConfig \*config)

Configures and starts the driver.

• void sduStop (SerialUSBDriver \*sdup)

Stops the driver.

void sduDisconnectI (SerialUSBDriver \*sdup)

USB device disconnection handler.

void sduConfigureHookI (SerialUSBDriver \*sdup)

USB device configured handler.

• bool sduRequestsHook (USBDriver \*usbp)

Default requests hook.

void sduSOFHookI (SerialUSBDriver \*sdup)

SOF handler.

void sduDataTransmitted (USBDriver \*usbp, usbep\_t ep)

Default data transmitted callback.

void sduDataReceived (USBDriver \*usbp, usbep\_t ep)

Default data received callback.

void sduInterruptTransmitted (USBDriver \*usbp, usbep\_t ep)

Default data received callback.

### 9.76.1 Detailed Description

Serial over USB Driver macros and structures.

### 9.77 spi.c File Reference

#### SPI Driver code.

#include "hal.h"

#### **Functions**

void spilnit (void)

SPI Driver initialization.

void spiObjectInit (SPIDriver \*spip)

Initializes the standard part of a SPIDriver structure.

void spiStart (SPIDriver \*spip, const SPIConfig \*config)

Configures and activates the SPI peripheral.

void spiStop (SPIDriver \*spip)

Deactivates the SPI peripheral.

void spiSelect (SPIDriver \*spip)

Asserts the slave select signal and prepares for transfers.

void spiUnselect (SPIDriver \*spip)

Deasserts the slave select signal.

• void spiStartIgnore (SPIDriver \*spip, size\_t n)

Ignores data on the SPI bus.

• void spiStartExchange (SPIDriver \*spip, size\_t n, const void \*txbuf, void \*rxbuf)

Exchanges data on the SPI bus.

void spiStartSend (SPIDriver \*spip, size\_t n, const void \*txbuf)

Sends data over the SPI bus.

void spiStartReceive (SPIDriver \*spip, size\_t n, void \*rxbuf)

Receives data from the SPI bus.

• void spilgnore (SPIDriver \*spip, size\_t n)

Ignores data on the SPI bus.

void spiExchange (SPIDriver \*spip, size t n, const void \*txbuf, void \*rxbuf)

Exchanges data on the SPI bus.

void spiSend (SPIDriver \*spip, size\_t n, const void \*txbuf)

Sends data over the SPI bus.

• void spiReceive (SPIDriver \*spip, size\_t n, void \*rxbuf)

Receives data from the SPI bus.

void spiAcquireBus (SPIDriver \*spip)

Gains exclusive access to the SPI bus.

void spiReleaseBus (SPIDriver \*spip)

Releases exclusive access to the SPI bus.

### 9.77.1 Detailed Description

SPI Driver code.

### 9.78 spi.h File Reference

SPI Driver macros and structures.

```
#include "spi_lld.h"
```

#### **Macros**

#### SPI configuration options

• #define SPI\_USE\_WAIT TRUE

Enables synchronous APIs.

#define SPI\_USE\_MUTUAL\_EXCLUSION TRUE

Enables the spiAcquireBus() and spiReleaseBus() APIs.

#### **Macro Functions**

• #define spiSelectI(spip)

Asserts the slave select signal and prepares for transfers.

• #define spiUnselectI(spip)

Deasserts the slave select signal.

#define spiStartIgnoreI(spip, n)

Ignores data on the SPI bus.

• #define spiStartExchangel(spip, n, txbuf, rxbuf)

Exchanges data on the SPI bus.

#define spiStartSendI(spip, n, txbuf)

Sends data over the SPI bus.

#define spiStartReceiveI(spip, n, rxbuf)

Receives data from the SPI bus.

• #define spiPolledExchange(spip, frame) spi\_lld\_polled\_exchange(spip, frame)

Exchanges one frame using a polled wait.

### Low level driver helper macros

• #define \_spi\_wakeup\_isr(spip)

Wakes up the waiting thread.

#define spi isr code(spip)

Common ISR code.

#### **Enumerations**

#### **Functions**

void spilnit (void)

SPI Driver initialization.

void spiObjectInit (SPIDriver \*spip)

Initializes the standard part of a SPIDriver structure.

void spiStart (SPIDriver \*spip, const SPIConfig \*config)

Configures and activates the SPI peripheral.

void spiStop (SPIDriver \*spip)

Deactivates the SPI peripheral.

void spiSelect (SPIDriver \*spip)

Asserts the slave select signal and prepares for transfers.

void spiUnselect (SPIDriver \*spip)

Deasserts the slave select signal.

• void spiStartIgnore (SPIDriver \*spip, size t n)

Ignores data on the SPI bus.

• void spiStartExchange (SPIDriver \*spip, size\_t n, const void \*txbuf, void \*rxbuf)

Exchanges data on the SPI bus.

void spiStartSend (SPIDriver \*spip, size\_t n, const void \*txbuf)

Sends data over the SPI bus.

• void spiStartReceive (SPIDriver \*spip, size\_t n, void \*rxbuf)

Receives data from the SPI bus.

• void spilgnore (SPIDriver \*spip, size t n)

Ignores data on the SPI bus.

• void spiExchange (SPIDriver \*spip, size\_t n, const void \*txbuf, void \*rxbuf)

Exchanges data on the SPI bus.

void spiSend (SPIDriver \*spip, size t n, const void \*txbuf)

Sends data over the SPI bus.

• void spiReceive (SPIDriver \*spip, size\_t n, void \*rxbuf)

Receives data from the SPI bus.

void spiAcquireBus (SPIDriver \*spip)

Gains exclusive access to the SPI bus.

• void spiReleaseBus (SPIDriver \*spip)

Releases exclusive access to the SPI bus.

### 9.78.1 Detailed Description

SPI Driver macros and structures.

### 9.79 spi\_lld.c File Reference

PLATFORM SPI subsystem low level driver source.

```
#include "hal.h"
```

#### **Functions**

void spi\_lld\_init (void)

Low level SPI driver initialization.

void spi\_lld\_start (SPIDriver \*spip)

Configures and activates the SPI peripheral.

void spi\_lld\_stop (SPIDriver \*spip)

Deactivates the SPI peripheral.

void spi\_lld\_select (SPIDriver \*spip)

Asserts the slave select signal and prepares for transfers.

• void spi\_lld\_unselect (SPIDriver \*spip)

Deasserts the slave select signal.

• void spi\_lld\_ignore (SPIDriver \*spip, size\_t n)

Ignores data on the SPI bus.

void spi\_lld\_exchange (SPIDriver \*spip, size\_t n, const void \*txbuf, void \*rxbuf)

Exchanges data on the SPI bus.

• void spi\_lld\_send (SPIDriver \*spip, size\_t n, const void \*txbuf)

Sends data over the SPI bus.

void spi\_lld\_receive (SPIDriver \*spip, size\_t n, void \*rxbuf)

Receives data from the SPI bus.

• uint16\_t spi\_lld\_polled\_exchange (SPIDriver \*spip, uint16\_t frame)

Exchanges one frame using a polled wait.

#### **Variables**

SPIDriver SPID1

SPI1 driver identifier.

### 9.79.1 Detailed Description

PLATFORM SPI subsystem low level driver source.

# 9.80 spi\_lld.h File Reference

PLATFORM SPI subsystem low level driver header.

### **Data Structures**

· struct SPIConfig

Driver configuration structure.

struct SPIDriver

Structure representing an SPI driver.

#### **Macros**

#### **PLATFORM** configuration options

#define PLATFORM\_SPI\_USE\_SPI1 FALSE
 SPI1 driver enable switch.

### **Typedefs**

· typedef struct SPIDriver SPIDriver

Type of a structure representing an SPI driver.

typedef void(\* spicallback\_t) (SPIDriver \*spip)

SPI notification callback type.

### **Functions**

void spi\_lld\_init (void)

Low level SPI driver initialization.

void spi\_lld\_start (SPIDriver \*spip)

Configures and activates the SPI peripheral.

void spi\_lld\_stop (SPIDriver \*spip)

Deactivates the SPI peripheral.

void spi\_lld\_select (SPIDriver \*spip)

Asserts the slave select signal and prepares for transfers.

• void spi\_lld\_unselect (SPIDriver \*spip)

Deasserts the slave select signal.

void spi\_lld\_ignore (SPIDriver \*spip, size\_t n)

Ignores data on the SPI bus.

• void spi\_lld\_exchange (SPIDriver \*spip, size\_t n, const void \*txbuf, void \*rxbuf)

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Exchanges data on the SPI bus.

• void spi\_lld\_send (SPIDriver \*spip, size\_t n, const void \*txbuf)

Sends data over the SPI bus.

void spi\_lld\_receive (SPIDriver \*spip, size\_t n, void \*rxbuf)

Receives data from the SPI bus.

uint16\_t spi\_lld\_polled\_exchange (SPIDriver \*spip, uint16\_t frame)

Exchanges one frame using a polled wait.

#### 9.80.1 Detailed Description

PLATFORM SPI subsystem low level driver header.

### 9.81 st.c File Reference

```
ST Driver code.
```

```
#include "hal.h"
```

### **Functions**

void stlnit (void)

ST Driver initialization.

void stStartAlarm (systime\_t abstime)

Starts the alarm.

void stStopAlarm (void)

Stops the alarm interrupt.

void stSetAlarm (systime\_t abstime)

Sets the alarm time.

systime\_t stGetAlarm (void)

Returns the current alarm time.

### 9.81.1 Detailed Description

ST Driver code.

### 9.82 st.h File Reference

ST Driver macros and structures.

```
#include "st_lld.h"
```

#### **Macros**

#### **Macro Functions**

• #define stGetCounter() st Ild get counter()

Returns the time counter value.

• #define stlsAlarmActive() st\_lld\_is\_alarm\_active()

Determines if the alarm is active.

#### **Functions**

void stlnit (void)

ST Driver initialization.

· void stStartAlarm (systime t abstime)

Starts the alarm.

void stStopAlarm (void)

Stops the alarm interrupt.

void stSetAlarm (systime\_t abstime)

Sets the alarm time.

• systime\_t stGetAlarm (void)

Returns the current alarm time.

### 9.82.1 Detailed Description

ST Driver macros and structures.

This header is designed to be include-able without having to include other files from the HAL.

# 9.83 st\_lld.c File Reference

PLATFORM ST subsystem low level driver source.

```
#include "hal.h"
```

### **Functions**

void st\_lld\_init (void)

Low level ST driver initialization.

### 9.83.1 Detailed Description

PLATFORM ST subsystem low level driver source.

### 9.84 st\_lld.h File Reference

PLATFORM ST subsystem low level driver header.

#### **Functions**

void st\_lld\_init (void)

Low level ST driver initialization.

• static systime\_t st\_lld\_get\_counter (void)

Returns the time counter value.

static void st\_lld\_start\_alarm (systime\_t abstime)

Starts the alarm.

• static void st\_lld\_stop\_alarm (void)

Stops the alarm interrupt.

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• static void st\_lld\_set\_alarm (systime\_t abstime)

Sets the alarm time.

static systime t st lld get alarm (void)

Returns the current alarm time.

static bool st\_lld\_is\_alarm\_active (void)

Determines if the alarm is active.

#### 9.84.1 Detailed Description

PLATFORM ST subsystem low level driver header.

This header is designed to be include-able without having to include other files from the HAL.

### 9.85 uart.c File Reference

```
UART Driver code.
```

```
#include "hal.h"
```

#### **Functions**

· void uartInit (void)

UART Driver initialization.

void uartObjectInit (UARTDriver \*uartp)

Initializes the standard part of a UARTDriver structure.

void uartStart (UARTDriver \*uartp, const UARTConfig \*config)

Configures and activates the UART peripheral.

void uartStop (UARTDriver \*uartp)

Deactivates the UART peripheral.

void uartStartSend (UARTDriver \*uartp, size\_t n, const void \*txbuf)

Starts a transmission on the UART peripheral.

void uartStartSendI (UARTDriver \*uartp, size\_t n, const void \*txbuf)

Starts a transmission on the UART peripheral.

size\_t uartStopSend (UARTDriver \*uartp)

Stops any ongoing transmission.

size\_t uartStopSendI (UARTDriver \*uartp)

Stops any ongoing transmission.

void uartStartReceive (UARTDriver \*uartp, size\_t n, void \*rxbuf)

Starts a receive operation on the UART peripheral.

• void uartStartReceivel (UARTDriver \*uartp, size\_t n, void \*rxbuf)

Starts a receive operation on the UART peripheral.

size\_t uartStopReceive (UARTDriver \*uartp)

Stops any ongoing receive operation.

size\_t uartStopReceiveI (UARTDriver \*uartp)

Stops any ongoing receive operation.

msg\_t uartSendTimeout (UARTDriver \*uartp, size\_t \*np, const void \*txbuf, systime\_t timeout)

Performs a transmission on the UART peripheral.

msg\_t\_uartSendFullTimeout (UARTDriver \*uartp, size\_t \*np, const void \*txbuf, systime\_t timeout)

Performs a transmission on the UART peripheral.

msg\_t uartReceiveTimeout (UARTDriver \*uartp, size\_t \*np, void \*rxbuf, systime\_t timeout)

Performs a receive operation on the UART peripheral.

void uartAcquireBus (UARTDriver \*uartp)

Gains exclusive access to the UART bus.

void uartReleaseBus (UARTDriver \*uartp)

Releases exclusive access to the UART bus.

### 9.85.1 Detailed Description

UART Driver code.

### 9.86 uart.h File Reference

UART Driver macros and structures.

```
#include "uart_lld.h"
```

#### **Macros**

#### **UART** status flags

• #define UART NO ERROR 0

No pending conditions.

• #define UART\_PARITY\_ERROR 4

Parity error happened.

#define UART\_FRAMING\_ERROR 8

Framing error happened.

• #define UART\_OVERRUN\_ERROR 16

Overflow happened.

• #define UART\_NOISE\_ERROR 32

Noise on the line.

#define UART\_BREAK\_DETECTED 64

Break detected.

### **UART** configuration options

• #define UART\_USE\_WAIT FALSE

Enables synchronous APIs.

• #define UART\_USE\_MUTUAL\_EXCLUSION FALSE

Enables the uartAcquireBus() and uartReleaseBus() APIs.

#### Low level driver helper macros

#define \_uart\_wakeup\_tx1\_isr(uartp)

Wakes up the waiting thread in case of early TX complete.

#define \_uart\_wakeup\_tx2\_isr(uartp)

Wakes up the waiting thread in case of late TX complete.

#define \_uart\_wakeup\_rx\_complete\_isr(uartp)

Wakes up the waiting thread in case of RX complete.

#define uart wakeup rx error isr(uartp)

Wakes up the waiting thread in case of RX error.

• #define \_uart\_tx1\_isr\_code(uartp)

Common ISR code for early TX.

#define \_uart\_tx2\_isr\_code(uartp)

Common ISR code for late TX.

9.86 uart.h File Reference 663

#define \_uart\_rx\_complete\_isr\_code(uartp)

Common ISR code for RX complete.

#define \_uart\_rx\_error\_isr\_code(uartp, errors)

Common ISR code for RX error.

#define \_uart\_rx\_idle\_code(uartp)

Common ISR code for RX on idle.

#### **Enumerations**

#### **Functions**

void uartInit (void)

UART Driver initialization.

void uartObjectInit (UARTDriver \*uartp)

Initializes the standard part of a UARTDriver structure.

void uartStart (UARTDriver \*uartp, const UARTConfig \*config)

Configures and activates the UART peripheral.

void uartStop (UARTDriver \*uartp)

Deactivates the UART peripheral.

void uartStartSend (UARTDriver \*uartp, size\_t n, const void \*txbuf)

Starts a transmission on the UART peripheral.

void uartStartSendI (UARTDriver \*uartp, size t n, const void \*txbuf)

Starts a transmission on the UART peripheral.

size t uartStopSend (UARTDriver \*uartp)

Stops any ongoing transmission.

size\_t uartStopSendI (UARTDriver \*uartp)

Stops any ongoing transmission.

• void uartStartReceive (UARTDriver \*uartp, size\_t n, void \*rxbuf)

Starts a receive operation on the UART peripheral.

void uartStartReceivel (UARTDriver \*uartp, size t n, void \*rxbuf)

Starts a receive operation on the UART peripheral.

• size\_t uartStopReceive (UARTDriver \*uartp)

Stops any ongoing receive operation.

size t uartStopReceivel (UARTDriver \*uartp)

Stops any ongoing receive operation.

msg\_t uartSendTimeout (UARTDriver \*uartp, size\_t \*np, const void \*txbuf, systime\_t timeout)

Performs a transmission on the UART peripheral.

• msg\_t uartSendFullTimeout (UARTDriver \*uartp, size\_t \*np, const void \*txbuf, systime\_t timeout)

Performs a transmission on the UART peripheral.

msg\_t uartReceiveTimeout (UARTDriver \*uartp, size\_t \*np, void \*rxbuf, systime\_t timeout)

Performs a receive operation on the UART peripheral.

void uartAcquireBus (UARTDriver \*uartp)

Gains exclusive access to the UART bus.

void uartReleaseBus (UARTDriver \*uartp)

Releases exclusive access to the UART bus.

### 9.86.1 Detailed Description

UART Driver macros and structures.

### 9.87 uart\_lld.c File Reference

PLATFORM UART subsystem low level driver source.

```
#include "hal.h"
```

### **Functions**

void uart Ild init (void)

Low level UART driver initialization.

void uart\_lld\_start (UARTDriver \*uartp)

Configures and activates the UART peripheral.

void uart\_lld\_stop (UARTDriver \*uartp)

Deactivates the UART peripheral.

void uart\_lld\_start\_send (UARTDriver \*uartp, size\_t n, const void \*txbuf)

Starts a transmission on the UART peripheral.

size\_t uart\_lld\_stop\_send (UARTDriver \*uartp)

Stops any ongoing transmission.

• void uart\_lld\_start\_receive (UARTDriver \*uartp, size\_t n, void \*rxbuf)

Starts a receive operation on the UART peripheral.

• size\_t uart\_lld\_stop\_receive (UARTDriver \*uartp)

Stops any ongoing receive operation.

### **Variables**

• UARTDriver UARTD1

UART1 driver identifier.

### 9.87.1 Detailed Description

PLATFORM UART subsystem low level driver source.

### 9.88 uart\_lld.h File Reference

PLATFORM UART subsystem low level driver header.

#### **Data Structures**

• struct UARTConfig

Driver configuration structure.

struct UARTDriver

Structure representing an UART driver.

### Macros

### **PLATFORM** configuration options

 #define PLATFORM\_UART\_USE\_UART1 FALSE UART driver enable switch. 9.89 usb.c File Reference 665

### **Typedefs**

· typedef uint32 t uartflags t

UART driver condition flags type.

typedef struct UARTDriver UARTDriver

Type of structure representing an UART driver.

typedef void(\* uartcb\_t) (UARTDriver \*uartp)

Generic UART notification callback type.

typedef void(\* uartccb t) (UARTDriver \*uartp, uint16 t c)

Character received UART notification callback type.

• typedef void(\* uartecb\_t) (UARTDriver \*uartp, uartflags\_t e)

Receive error UART notification callback type.

#### **Functions**

· void uart Ild init (void)

Low level UART driver initialization.

• void uart\_lld\_start (UARTDriver \*uartp)

Configures and activates the UART peripheral.

void uart\_lld\_stop (UARTDriver \*uartp)

Deactivates the UART peripheral.

void uart\_lld\_start\_send (UARTDriver \*uartp, size\_t n, const void \*txbuf)

Starts a transmission on the UART peripheral.

size\_t uart\_lld\_stop\_send (UARTDriver \*uartp)

Stops any ongoing transmission.

• void uart\_lld\_start\_receive (UARTDriver \*uartp, size\_t n, void \*rxbuf)

Starts a receive operation on the UART peripheral.

size\_t uart\_lld\_stop\_receive (UARTDriver \*uartp)

Stops any ongoing receive operation.

### 9.88.1 Detailed Description

PLATFORM UART subsystem low level driver header.

### 9.89 usb.c File Reference

### USB Driver code.

```
#include <string.h>
#include "hal.h"
```

### **Functions**

• static void set\_address (USBDriver \*usbp)

SET ADDRESS transaction callback.

• static bool default\_handler (USBDriver \*usbp)

Standard requests handler.

void usblnit (void)

USB Driver initialization.

```
    void usbObjectInit (USBDriver *usbp)

          Initializes the standard part of a USBDriver structure.

    void usbStart (USBDriver *usbp, const USBConfig *config)

          Configures and activates the USB peripheral.

    void usbStop (USBDriver *usbp)

          Deactivates the USB peripheral.

    void usblnitEndpointI (USBDriver *usbp, usbep_t ep, const USBEndpointConfig *epcp)

          Enables an endpoint.

    void usbDisableEndpointsI (USBDriver *usbp)

          Disables all the active endpoints.
    • void usbStartReceivel (USBDriver *usbp, usbep_t ep, uint8_t *buf, size_t n)
          Starts a receive transaction on an OUT endpoint.
    • void usbStartTransmitI (USBDriver *usbp, usbep_t ep, const uint8_t *buf, size_t n)
          Starts a transmit transaction on an IN endpoint.
    • msg_t usbReceive (USBDriver *usbp, usbep_t ep, uint8_t *buf, size_t n)
          Performs a receive transaction on an OUT endpoint.
    • msg_t usbTransmit (USBDriver *usbp, usbep_t ep, const uint8_t *buf, size_t n)
          Performs a transmit transaction on an IN endpoint.

    bool usbStallReceivel (USBDriver *usbp, usbep_t ep)

          Stalls an OUT endpoint.

    bool usbStallTransmitl (USBDriver *usbp, usbep_t ep)

          Stalls an IN endpoint.

    void <u>usb_reset</u> (USBDriver *usbp)

          USB reset routine.

    void <u>usb_suspend</u> (USBDriver *usbp)

          USB suspend routine.
    void <u>usb_wakeup</u> (USBDriver *usbp)
          USB wake-up routine.

    void _usb_ep0setup (USBDriver *usbp, usbep_t ep)

          Default EP0 SETUP callback.

    void _usb_ep0in (USBDriver *usbp, usbep_t ep)

          Default EP0 IN callback.
    • void _usb_ep0out (USBDriver *usbp, usbep_t ep)
          Default EP0 OUT callback.
9.89.1 Detailed Description
```

USB Driver code.

#### 9.90 usb.h File Reference

USB Driver macros and structures.

```
#include "usb_lld.h"
```

### **Data Structures**

struct USBDescriptor

Type of an USB descriptor.

9.90 usb.h File Reference 667

#### **Macros**

#define USB USE WAIT FALSE

Enables synchronous APIs.

### Helper macros for USB descriptors

#define USB DESC INDEX(i) ((uint8 t)(i))

Helper macro for index values into descriptor strings.

#define USB\_DESC\_BYTE(b) ((uint8\_t)(b))

Helper macro for byte values into descriptor strings.

#define USB\_DESC\_WORD(w)

Helper macro for word values into descriptor strings.

#define USB DESC BCD(bcd)

Helper macro for BCD values into descriptor strings.

- #define USB\_DESC\_DEVICE\_SIZE 18U
- #define USB\_DESC\_DEVICE(bcdUSB, bDeviceClass, bDeviceSubClass, bDeviceProtocol, b
   — MaxPacketSize, idVendor, idProduct, bcdDevice, iManufacturer, iProduct, iSerialNumber, bNum
   — Configurations)

Device Descriptor helper macro.

• #define USB\_DESC\_CONFIGURATION\_SIZE 9U

Configuration Descriptor size.

 #define USB\_DESC\_CONFIGURATION(wTotalLength, bNumInterfaces, bConfigurationValue, i← Configuration, bmAttributes, bMaxPower)

Configuration Descriptor helper macro.

• #define USB DESC INTERFACE SIZE 9U

Interface Descriptor size.

#define USB\_DESC\_INTERFACE(bInterfaceNumber, bAlternateSetting, bNumEndpoints, bInterface Class, bInterfaceSubClass, bInterfaceProtocol, iInterface)

Interface Descriptor helper macro.

#define USB\_DESC\_INTERFACE\_ASSOCIATION\_SIZE 8U

Interface Association Descriptor size.

#define USB\_DESC\_INTERFACE\_ASSOCIATION(bFirstInterface, bInterfaceCount, bFunctionClass, b←
 FunctionSubClass, bFunctionProcotol, iInterface)

Interface Association Descriptor helper macro.

• #define USB DESC ENDPOINT SIZE 7U

Endpoint Descriptor size.

#define USB\_DESC\_ENDPOINT(bEndpointAddress, bmAttributes, wMaxPacketSize, bInterval)

Endpoint Descriptor helper macro.

#### **Endpoint types and settings**

- #define USB\_EP\_MODE\_TYPE 0x0003U
- #define USB EP MODE TYPE CTRL 0x0000U
- #define USB\_EP\_MODE\_TYPE\_ISOC 0x0001U
- #define USB EP MODE TYPE BULK 0x0002U
- #define USB\_EP\_MODE\_TYPE\_INTR 0x0003U

### **Macro Functions**

• #define usbGetDriverStateI(usbp) ((usbp)->state)

Returns the driver state.

#define usbConnectBus(usbp) usb\_lld\_connect\_bus(usbp)

Connects the USB device.

#define usbDisconnectBus(usbp) usb\_lld\_disconnect\_bus(usbp)

Disconnect the USB device.

#define usbGetFrameNumberX(usbp) usb\_lld\_get\_frame\_number(usbp)

Returns the current frame number.

#define usbGetTransmitStatusI(usbp, ep) (((usbp)->transmitting & (uint16\_t)((unsigned)1U << (unsigned)(ep))) != 0U)</li>

Returns the status of an IN endpoint.

#define usbGetReceiveStatusI(usbp, ep) (((usbp)->receiving & (uint16\_t)((unsigned)1U << (unsigned)(ep))) != 0U)</li>

Returns the status of an OUT endpoint.

#define usbGetReceiveTransactionSizeX(usbp, ep) usb\_lld\_get\_transaction\_size(usbp, ep)

Returns the exact size of a receive transaction.

• #define usbSetupTransfer(usbp, buf, n, endcb)

Request transfer setup.

#define usbReadSetup(usbp, ep, buf) usb Ild read setup(usbp, ep, buf)

Reads a setup packet from the dedicated packet buffer.

#### Low level driver helper macros

#define \_usb\_isr\_invoke\_event\_cb(usbp, evt)

Common ISR code, usb event callback.

#define usb isr invoke sof cb(usbp)

Common ISR code, SOF callback.

#define \_usb\_isr\_invoke\_setup\_cb(usbp, ep)

Common ISR code, setup packet callback.

#define <u>usb\_isr\_invoke\_in\_cb</u>(usbp, ep)

Common ISR code, IN endpoint callback.

#define \_usb\_isr\_invoke\_out\_cb(usbp, ep)

Common ISR code, OUT endpoint event.

#### **Typedefs**

typedef struct USBDriver USBDriver

Type of a structure representing an USB driver.

typedef uint8\_t usbep\_t

Type of an endpoint identifier.

typedef void(\* usbcallback\_t) (USBDriver \*usbp)

Type of an USB generic notification callback.

• typedef void(\* usbepcallback\_t) (USBDriver \*usbp, usbep\_t ep)

Type of an USB endpoint callback.

• typedef void(\* usbeventcb\_t) (USBDriver \*usbp, usbevent\_t event)

Type of an USB event notification callback.

typedef bool(\* usbreqhandler\_t) (USBDriver \*usbp)

Type of a requests handler callback.

typedef const USBDescriptor \*(\* usbgetdescriptor\_t) (USBDriver \*usbp, uint8\_t dtype, uint8\_t dindex, uint16\_t lang)

Type of an USB descriptor-retrieving callback.

#### **Enumerations**

#### **Functions**

· void usblnit (void)

USB Driver initialization.

void usbObjectInit (USBDriver \*usbp)

Initializes the standard part of a USBDriver structure.

void usbStart (USBDriver \*usbp, const USBConfig \*config)

Configures and activates the USB peripheral.

void usbStop (USBDriver \*usbp)

Deactivates the USB peripheral.

void usbInitEndpointI (USBDriver \*usbp, usbep\_t ep, const USBEndpointConfig \*epcp)

Enables an endpoint.

void usbDisableEndpointsI (USBDriver \*usbp)

Disables all the active endpoints.

• void usbStartReceiveI (USBDriver \*usbp, usbep\_t ep, uint8\_t \*buf, size\_t n)

Starts a receive transaction on an OUT endpoint.

• void usbStartTransmitl (USBDriver \*usbp, usbep\_t ep, const uint8\_t \*buf, size\_t n)

Starts a transmit transaction on an IN endpoint.

• msg\_t usbReceive (USBDriver \*usbp, usbep\_t ep, uint8\_t \*buf, size\_t n)

Performs a receive transaction on an OUT endpoint.

msg\_t usbTransmit (USBDriver \*usbp, usbep\_t ep, const uint8\_t \*buf, size\_t n)

Performs a transmit transaction on an IN endpoint.

bool usbStallReceivel (USBDriver \*usbp, usbep t ep)

Stalls an OUT endpoint.

• bool usbStallTransmitl (USBDriver \*usbp, usbep\_t ep)

Stalls an IN endpoint.

void usb reset (USBDriver \*usbp)

USB reset routine.

• void \_usb\_suspend (USBDriver \*usbp)

USB suspend routine.

void \_usb\_wakeup (USBDriver \*usbp)

USB wake-up routine.

void \_usb\_ep0setup (USBDriver \*usbp, usbep\_t ep)

Default EP0 SETUP callback.

void \_usb\_ep0in (USBDriver \*usbp, usbep\_t ep)

Default EP0 IN callback.

void \_usb\_ep0out (USBDriver \*usbp, usbep\_t ep)

Default EP0 OUT callback.

# 9.90.1 Detailed Description

USB Driver macros and structures.

# 9.91 usb\_cdc.h File Reference

USB CDC macros and structures.

### **Data Structures**

· struct cdc linecoding t

Type of Line Coding structure.

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### **Macros**

### CDC specific messages.

- #define CDC\_SEND\_ENCAPSULATED\_COMMAND 0x00U
- #define CDC\_GET\_ENCAPSULATED\_RESPONSE 0x01U
- #define CDC\_SET\_COMM\_FEATURE 0x02U
- #define CDC\_GET\_COMM\_FEATURE 0x03U
- #define CDC\_CLEAR\_COMM\_FEATURE 0x04U
- #define CDC\_SET\_AUX\_LINE\_STATE 0x10U
- #define CDC\_SET\_HOOK\_STATE 0x11U
- #define CDC\_PULSE\_SETUP 0x12U
- #define CDC\_SEND\_PULSE 0x13U
- #define CDC\_SET\_PULSE\_TIME 0x14U
- #define CDC\_RING\_AUX\_JACK 0x15U
- #define CDC\_SET\_LINE\_CODING 0x20U
- #define CDC\_GET\_LINE\_CODING 0x21U
- #define CDC\_SET\_CONTROL\_LINE\_STATE 0x22U
- #define CDC SEND BREAK 0x23U
- #define CDC SET RINGER PARMS 0x30U
- #define CDC\_GET\_RINGER\_PARMS 0x31U
- #define CDC\_SET\_OPERATION\_PARMS 0x32U
- #define CDC GET OPERATION PARMS 0x33U

### **CDC** classes

- #define CDC\_COMMUNICATION\_INTERFACE\_CLASS 0x02U
- #define CDC\_DATA\_INTERFACE\_CLASS 0x0AU

### **CDC** subclasses

#define CDC\_ABSTRACT\_CONTROL\_MODEL 0x02U

### **CDC** descriptors

• #define CDC CS INTERFACE 0x24U

# **CDC** subdescriptors

- #define CDC HEADER 0x00U
- #define CDC\_CALL\_MANAGEMENT 0x01U
- #define CDC\_ABSTRACT\_CONTROL\_MANAGEMENT 0x02U
- #define CDC UNION 0x06U

### Line Control bit definitions.

- #define LC STOP 1 0U
- #define LC STOP 1P5 1U
- #define LC\_STOP\_2 2U
- #define LC\_PARITY\_NONE 0U
- #define LC\_PARITY\_ODD 1U
- #define LC\_PARITY\_EVEN 2U
- #define LC\_PARITY\_MARK 3U
- #define LC\_PARITY\_SPACE 4U

# 9.91.1 Detailed Description

USB CDC macros and structures.

# 9.92 usb Ild.c File Reference

```
PLATFORM USB subsystem low level driver source.
```

```
#include "hal.h"
```

### **Functions**

• void usb\_lld\_init (void)

Low level USB driver initialization.

void usb\_lld\_start (USBDriver \*usbp)

Configures and activates the USB peripheral.

void usb\_lld\_stop (USBDriver \*usbp)

Deactivates the USB peripheral.

void usb\_lld\_reset (USBDriver \*usbp)

USB low level reset routine.

void usb\_lld\_set\_address (USBDriver \*usbp)

Sets the USB address.

void usb\_lld\_init\_endpoint (USBDriver \*usbp, usbep\_t ep)

Enables an endpoint.

void usb\_lld\_disable\_endpoints (USBDriver \*usbp)

Disables all the active endpoints except the endpoint zero.

• usbepstatus\_t usb\_lld\_get\_status\_out (USBDriver \*usbp, usbep\_t ep)

Returns the status of an OUT endpoint.

• usbepstatus\_t usb\_lld\_get\_status\_in (USBDriver \*usbp, usbep\_t ep)

Returns the status of an IN endpoint.

void usb\_lld\_read\_setup (USBDriver \*usbp, usbep\_t ep, uint8\_t \*buf)

Reads a setup packet from the dedicated packet buffer.

void usb\_lld\_prepare\_receive (USBDriver \*usbp, usbep\_t ep)

Prepares for a receive operation.

• void usb\_lld\_prepare\_transmit (USBDriver \*usbp, usbep\_t ep)

Prepares for a transmit operation.

void usb Ild start out (USBDriver \*usbp, usbep t ep)

Starts a receive operation on an OUT endpoint.

void usb\_lld\_start\_in (USBDriver \*usbp, usbep\_t ep)

Starts a transmit operation on an IN endpoint.

• void usb\_lld\_stall\_out (USBDriver \*usbp, usbep\_t ep)

Brings an OUT endpoint in the stalled state.

• void usb\_lld\_stall\_in (USBDriver \*usbp, usbep\_t ep)

Brings an IN endpoint in the stalled state.

void usb\_lld\_clear\_out (USBDriver \*usbp, usbep\_t ep)

Brings an OUT endpoint in the active state.

• void usb Ild clear in (USBDriver \*usbp, usbep tep)

Brings an IN endpoint in the active state.

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# Variables

USBDriver USBD1

USB1 driver identifier.

union {
 USBInEndpointState in
 IN EP0 state.
 USBOutEndpointState out
 OUT EP0 state.
 } ep0\_state

EP0 state.

· static const USBEndpointConfig ep0config

EP0 initialization structure.

# 9.92.1 Detailed Description

PLATFORM USB subsystem low level driver source.

9.92.2 Variable Documentation

9.92.2.1 USBInEndpointState in

IN EP0 state.

9.92.2.2 USBOutEndpointState out

OUT EP0 state.

# 9.93 usb\_lld.h File Reference

PLATFORM USB subsystem low level driver header.

# **Data Structures**

• struct USBInEndpointState

Type of an IN endpoint state structure.

• struct USBOutEndpointState

Type of an OUT endpoint state structure.

• struct USBEndpointConfig

Type of an USB endpoint configuration structure.

· struct USBConfig

Type of an USB driver configuration structure.

• struct USBDriver

Structure representing an USB driver.

### **Macros**

#define USB MAX ENDPOINTS 4

Maximum endpoint address.

#define USB EP0 STATUS STAGE USB EP0 STATUS STAGE SW

Status stage handling method.

#define USB\_SET\_ADDRESS\_MODE USB\_LATE\_SET\_ADDRESS

The address can be changed immediately upon packet reception.

#define USB SET ADDRESS ACK HANDLING USB SET ADDRESS ACK SW

Method for set address acknowledge.

#define usb\_lld\_get\_frame\_number(usbp) 0

Returns the current frame number.

• #define usb\_lld\_get\_transaction\_size(usbp, ep) ((usbp)->epc[ep]->out\_state->rxcnt)

Returns the exact size of a receive transaction.

#define usb IId connect bus(usbp)

Connects the USB device.

#define usb\_lld\_disconnect\_bus(usbp)

Disconnect the USB device.

### **PLATFORM** configuration options

#define PLATFORM\_USB\_USE\_USB1 FALSE

USB driver enable switch.

### **Functions**

• void usb Ild init (void)

Low level USB driver initialization.

void usb\_lld\_start (USBDriver \*usbp)

Configures and activates the USB peripheral.

void usb Ild stop (USBDriver \*usbp)

Deactivates the USB peripheral.

void usb\_lld\_reset (USBDriver \*usbp)

USB low level reset routine.

void usb\_lld\_set\_address (USBDriver \*usbp)

Sets the USB address.

void usb\_lld\_init\_endpoint (USBDriver \*usbp, usbep\_t ep)

Enables an endpoint.

void usb IId disable endpoints (USBDriver \*usbp)

Disables all the active endpoints except the endpoint zero.

usbepstatus\_t usb\_lld\_get\_status\_in (USBDriver \*usbp, usbep\_t ep)

Returns the status of an IN endpoint.

usbepstatus\_t usb\_lld\_get\_status\_out (USBDriver \*usbp, usbep\_t ep)

Returns the status of an OUT endpoint.

void usb Ild read setup (USBDriver \*usbp, usbep t ep, uint8 t \*buf)

Reads a setup packet from the dedicated packet buffer.

void usb\_lld\_prepare\_receive (USBDriver \*usbp, usbep\_t ep)

Prepares for a receive operation.

void usb\_lld\_prepare\_transmit (USBDriver \*usbp, usbep\_t ep)

Prepares for a transmit operation.

void usb\_lld\_start\_out (USBDriver \*usbp, usbep\_t ep)

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Starts a receive operation on an OUT endpoint.

void usb\_lld\_start\_in (USBDriver \*usbp, usbep\_t ep)

Starts a transmit operation on an IN endpoint.

void usb\_lld\_stall\_out (USBDriver \*usbp, usbep\_t ep)

Brings an OUT endpoint in the stalled state.

void usb\_lld\_stall\_in (USBDriver \*usbp, usbep\_t ep)

Brings an IN endpoint in the stalled state.

void usb\_lld\_clear\_out (USBDriver \*usbp, usbep\_t ep)

Brings an OUT endpoint in the active state.

void usb\_lld\_clear\_in (USBDriver \*usbp, usbep\_t ep)

Brings an IN endpoint in the active state.

# 9.93.1 Detailed Description

PLATFORM USB subsystem low level driver header.

# 9.94 wdg.c File Reference

```
WDG Driver code.
```

```
#include "hal.h"
```

### **Functions**

void wdgInit (void)

WDG Driver initialization.

void wdgStart (WDGDriver \*wdgp, const WDGConfig \*config)

Configures and activates the WDG peripheral.

void wdgStop (WDGDriver \*wdgp)

Deactivates the WDG peripheral.

void wdgReset (WDGDriver \*wdgp)

Resets WDG's counter.

# 9.94.1 Detailed Description

WDG Driver code.

# 9.95 wdg.h File Reference

WDG Driver macros and structures.

```
#include "wdg_lld.h"
```

### Macros

#define wdgResetI(wdgp) wdg\_lld\_reset(wdgp)

Resets WDG's counter.

### **Enumerations**

### **Functions**

• void wdgInit (void)

WDG Driver initialization.

void wdgStart (WDGDriver \*wdgp, const WDGConfig \*config)

Configures and activates the WDG peripheral.

void wdgStop (WDGDriver \*wdgp)

Deactivates the WDG peripheral.

void wdgReset (WDGDriver \*wdgp)

Resets WDG's counter.

# 9.95.1 Detailed Description

WDG Driver macros and structures.

# 9.96 wdg\_lld.c File Reference

WDG Driver subsystem low level driver source template.

```
#include "hal.h"
```

### **Functions**

void wdg\_lld\_init (void)

Low level WDG driver initialization.

void wdg\_lld\_start (WDGDriver \*wdgp)

Configures and activates the WDG peripheral.

void wdg\_lld\_stop (WDGDriver \*wdgp)

Deactivates the WDG peripheral.

void wdg\_lld\_reset (WDGDriver \*wdgp)

Reloads WDG's counter.

# 9.96.1 Detailed Description

WDG Driver subsystem low level driver source template.

# 9.97 wdg\_lld.h File Reference

WDG Driver subsystem low level driver header template.

### **Data Structures**

• struct WDGConfig

Driver configuration structure.

struct WDGDriver

Structure representing an WDG driver.

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# **Macros**

# **Configuration options**

• #define PLATFORM\_WDG\_USE\_WDG1 FALSE WDG1 driver enable switch.

# **Typedefs**

• typedef struct WDGDriver WDGDriver

Type of a structure representing an WDG driver.

# **Functions**

void wdg\_lld\_init (void)

Low level WDG driver initialization.

void wdg\_lld\_start (WDGDriver \*wdgp)

Configures and activates the WDG peripheral.

void wdg\_lld\_stop (WDGDriver \*wdgp)

Deactivates the WDG peripheral.

void wdg\_lld\_reset (WDGDriver \*wdgp)

Reloads WDG's counter.

# 9.97.1 Detailed Description

WDG Driver subsystem low level driver header template.

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